XRT Timeline to be uploaded on 2008/07/03

Period: 2008/07/03 11:30:00 - 2008/07/08 10:48:00

* * * * * **Normal mode** * * * * *

XOB #1562: Synoptic Q90 2x2 - Al/mesh(256/2048) + Dark cal(512 Q98) + Ti-poly(512/5975) + G-band(16)

Term	Pointing (x, y)	Comment
07/03 15:52:00 - 07/03 15:59:54	Fixed (0.0, 0.0)	synoptic, shifted manually.
07/04 06:02:00 - 07/04 06:09:54	Fixed (0.0, 0.0)	* Sun-center pointing for axions. and synoptics scheduled at 06:00:00 UT and 16:30:00 UT.
07/04 16:32:00 - 07/04 16:39:54	Fixed (0.0, 0.0)	* Sun-center pointing for axions. and synoptics scheduled at 06:00:00 UT and 16:30:00 UT.
07/05 06:31:30 - 07/05 06:39:24	Fixed (0.0, 0.0)	synoptic, shifted 29.5 min

PROG= 16 1-time(s)
Subr= 1 1-time(s) 2.0sec
Seqn= 38 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0 2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0 2.0sec

Seqn= 72 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0 2.0sec

Seqn= 64 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0 2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0 2.0sec

Seqn= 46 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0 2.0sec

XOB #1579: Coronal cavity (Al/poly, Ti/poly, Thin Be)-DPCM 2x2-768X768-FOV offset for South-East -longer exposures

Term	Pointing (x, y)	Comment
07/03 18:32:26 - 07/03 22:00:00	Fixed (-787.0, -525.0)	# Pointing for cavity obs (official time: 18-22 UT).
07/04 18:00:26 - 07/04 21:40:00	Fixed (-787.0, -525.0)	# Pointing for cavity observations (official time: 18-22 UT).

PROG= 17 Inf.-time(s)
Subr= 1 1-time(s) 300.0sec
Seqn= 29 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	11.3s	Obs	2x2	768x768 (896, 904)	DPCM	0	0 2.0sec

Seqn= 66 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16.0s	Obs	2x2	768x768 (896, 904)	DPCM	0	0 2.0sec

Seqn= 33 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	45.2s	Obs	2x2	768x768 (896, 904)	DPCM	0	0 2.0sec

XOB #1577: Polar Plume - Al/Mesh, Al/Poly, C/Poly - 512x512 -3min cadence

Term	Pointing (x, y)	Comment
07/03 22:02:26 - 07/04 00:00:00	Fixed (0.0, -950.0)	* Multi-satellite S pole plumes study, final run.

PROG= 01 Inf.-time(s)
Subr= 1 1-time(s) 180.0sec
Seqn= 88 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0 36.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	32.0s	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0 36.0sec
C-poly/Open	thin-Be/Open	close	Safe	Norm	45.2s	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0 36.0sec

XOB #157B: Thermal analysis 8x8 2048FOV Al/p-C/p-Al/p+Ti/p-Thin/Be-Med/Al-Thick/Al-Thick/Be

Term	Pointing (x, y)	Comment
07/04 07:27:30 - 07/04 07:34:47	Fixed (0.0, 0.0)	* Sun-center pointing for axions. and synoptics scheduled at 06:00:00 UT and 16:30:00 UT.
07/04 13:20:13 - 07/04 13:27:30	Fixed (0.0, 0.0)	* Sun-center pointing for axions. and synoptics scheduled at 06:00:00 UT and 16:30:00 UT.

PROG= 11 1-time(s)
Subr= 1 1-time(s) 2.0sec
Seqn= 62 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	8x8	2048x2048 (1024, 1024)	Q=90	0	0 2.0sec
C-poly/Open	C-poly/Open	close	Safe	Norm	250ms	Obs	8x8	2048x2048 (1024, 1024)	Q=90	0	0 2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=90	0	0 2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	8x8	2048x2048 (1024, 1024)	Q=90	0	0 2.0sec

Seqn= 78 3-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
med-Al/Open	med-Al/Open	close	Safe	Norm	11.3s	Obs	8x8	2048x2048 (1024, 1024)	Q=90	0	0 2.0sec

Seqn=100 5-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/thick-Al	Open/thick-Al	close	Safe	Norm	22.6s	Obs	8x8	2048x2048 (1024, 1024)	Q=90	0	0 2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs	8x8	2048x2048 (1024, 1024)	Q=90	0	0 2.0sec

XOB #155F: Axion_Study_no-AEC_Med-Be-12s

Term	Pointing (x, y)	Comment
07/04 07:35:17 - 07/04 13:19:43	Fixed (0.0, 0.0)	* Sun-center pointing for axions. and synoptics scheduled at 06:00:00 UT and 16:30:00 UT.

PROG= 04 Inf.-time(s)
Subr= 1 1-time(s) 2.0sec
Seqn= 12 25-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
med-Be/Open	med-Be/Open	close	Safe	Norm	11.3s	Obs	8x8	2048x2048 (1024, 1024)	DPCM	0	0 2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 52 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Be/Open	med-Be/Open	close	Safe	Dark	11.3s	Obs	8x8	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec

XOB #1558: CH Boundary - Al/poly + C/poly + Thin-Be - AEC 1 - FOV 384x384 - 2 min cadence - Q90

Term	Pointing (x, y)	Comment
07/04 22:30:26 - 07/05 04:04:00	Track (44.7, 779.9) @ 07/04 22:00:00	* Track N polar CH boundary.

PROG= 06 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 16 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 93 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 77 30-time(s) 120.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	thin-Be/Open	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1024, 1024)	Q=90	1	0	2.0sec
C-poly/Open	thin-Be/Open	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1024, 1024)	Q=90	1	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	64.0s	Obs	1x1	384x384 (1024, 1024)	Q=90	0	0	2.0sec

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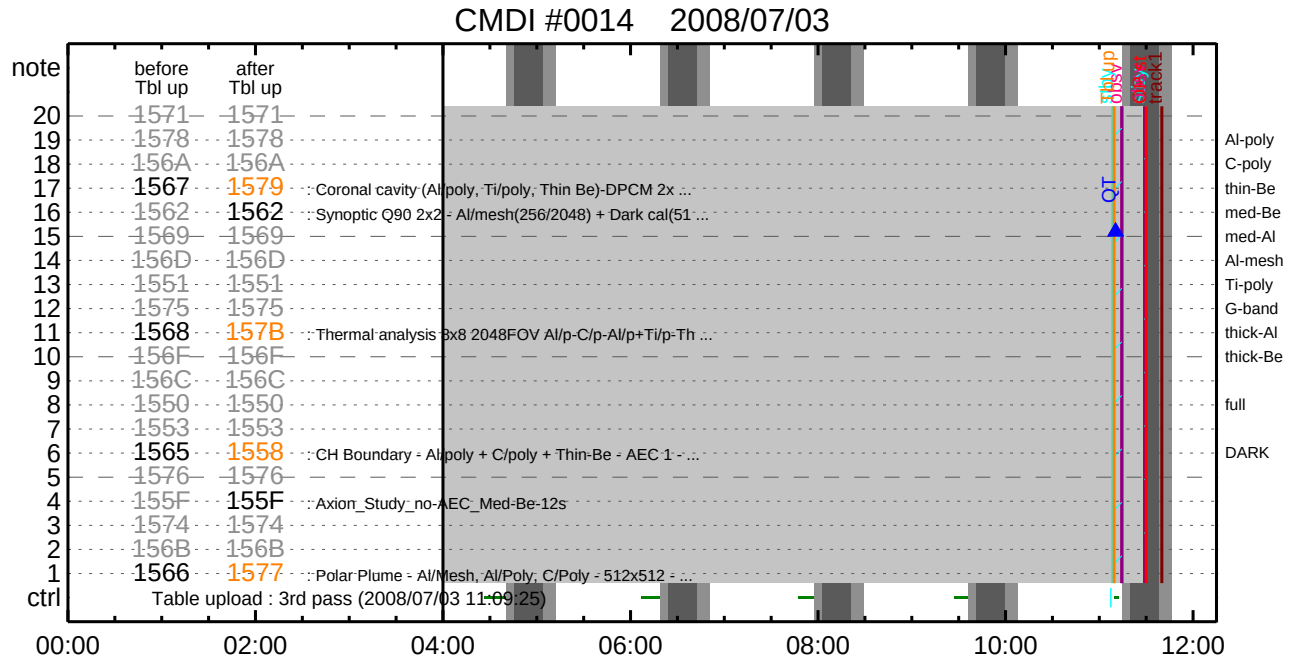
NOT USED


CMDI #0014 2008/07/03
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before Tbl up
after Tbl up
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18 156A 156A
17 1567 1579 : Coronal cavity (Al-poly, Ti/poly, Thin Be)-DPCM 2x ...
16 1562 1562 : Synoptic Q90 2x2 - Al/mesh(256/2048) + Dark cal(51 ...
15 1569 1569
14 156D 156D
13 1551 1551
12 1575 1575
11 1568 157B : Thermal analysis 8x8 2048FOV Al/p-C/p-Al/p+Ti/p-Th ...
10 156F 156F
9 156C 156C
8 1550 1550
7 1553 1553
6 1565 1558 : CH Boundary - Al-poly + C/poly + Thin-Be - AEC 1 - ...
5 1576 1576
4 155F 155F : Axion_Study_no-AEC_Med-Be-12s
3 1574 1574
2 156B 156B
1 1566 1577 : Polar Plume - Al/Mesh, Al/Poly, C/Poly - 512x512 - ...
ctrl Table upload : 3rd pass (2008/07/03 11:09:25)
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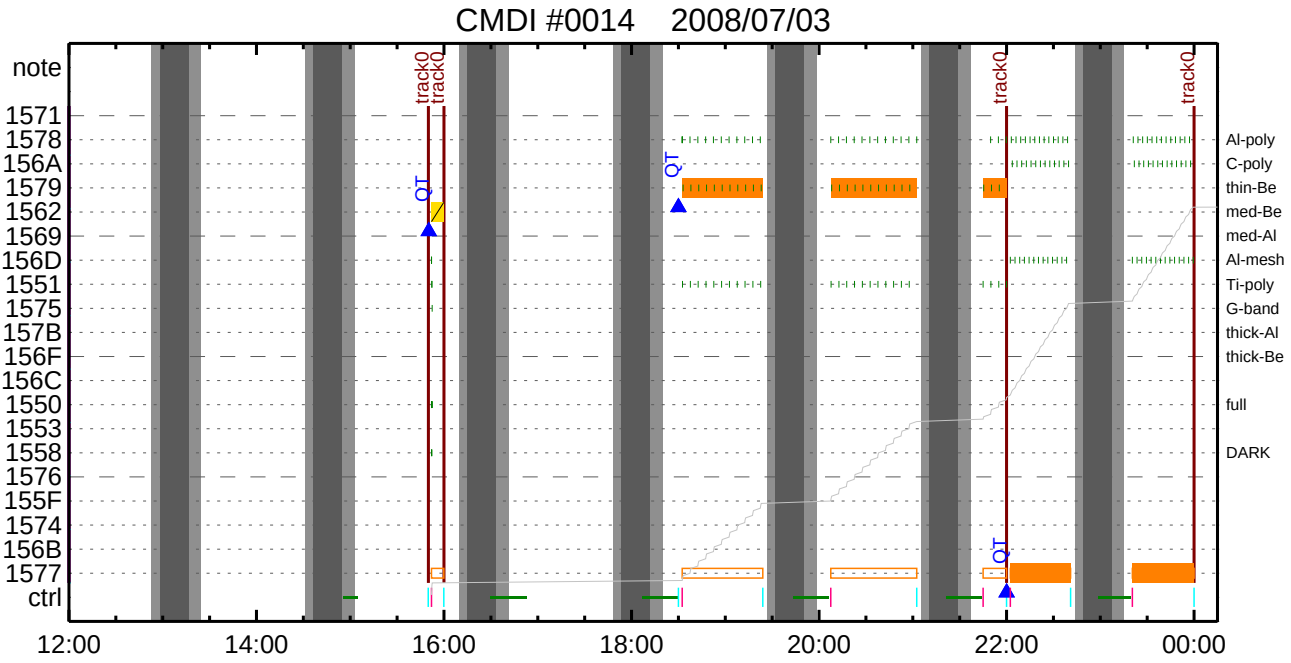
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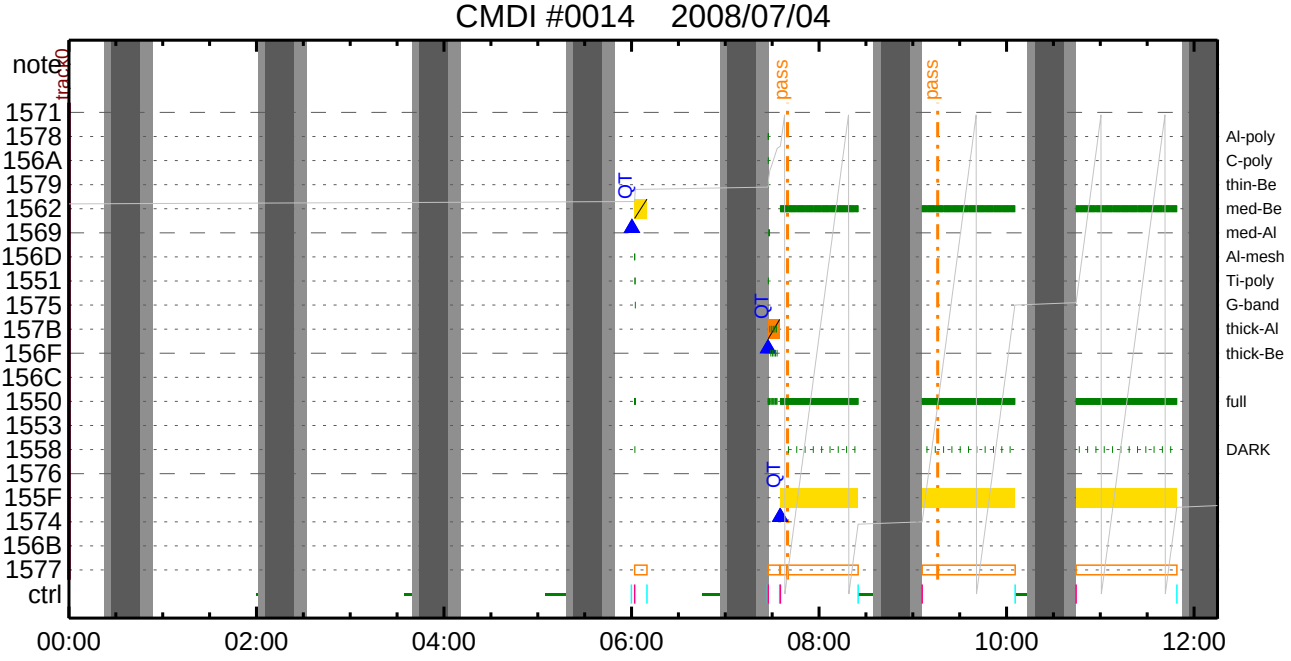
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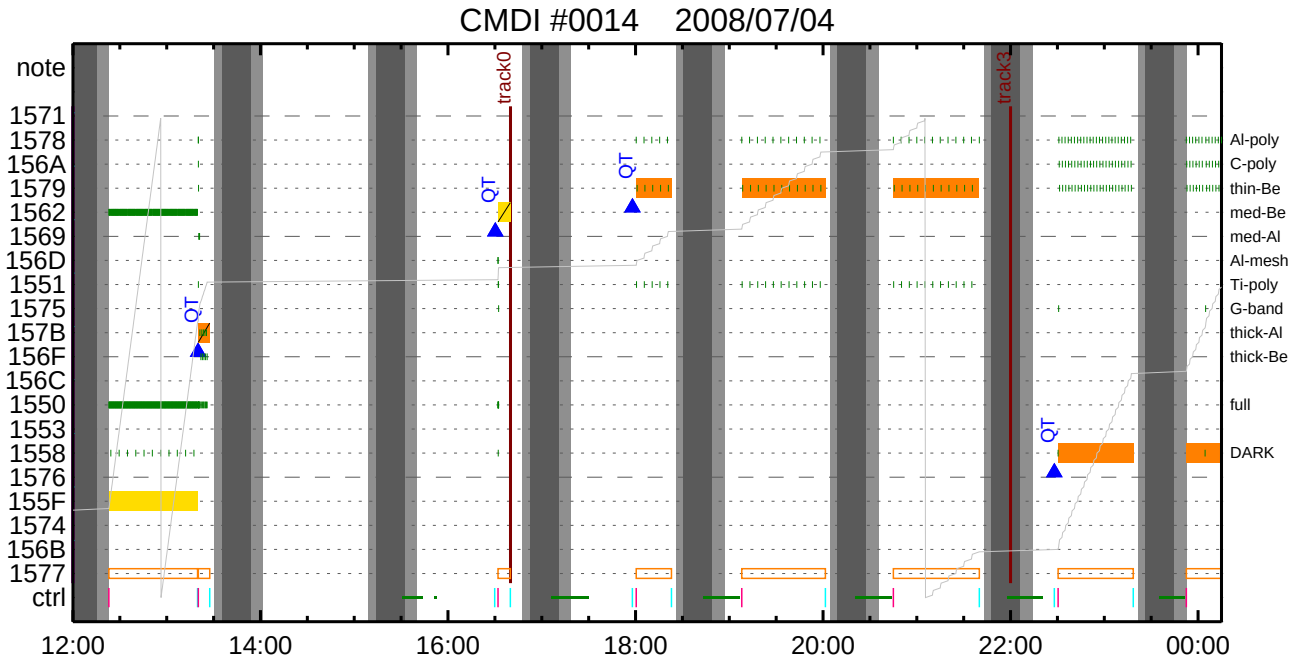
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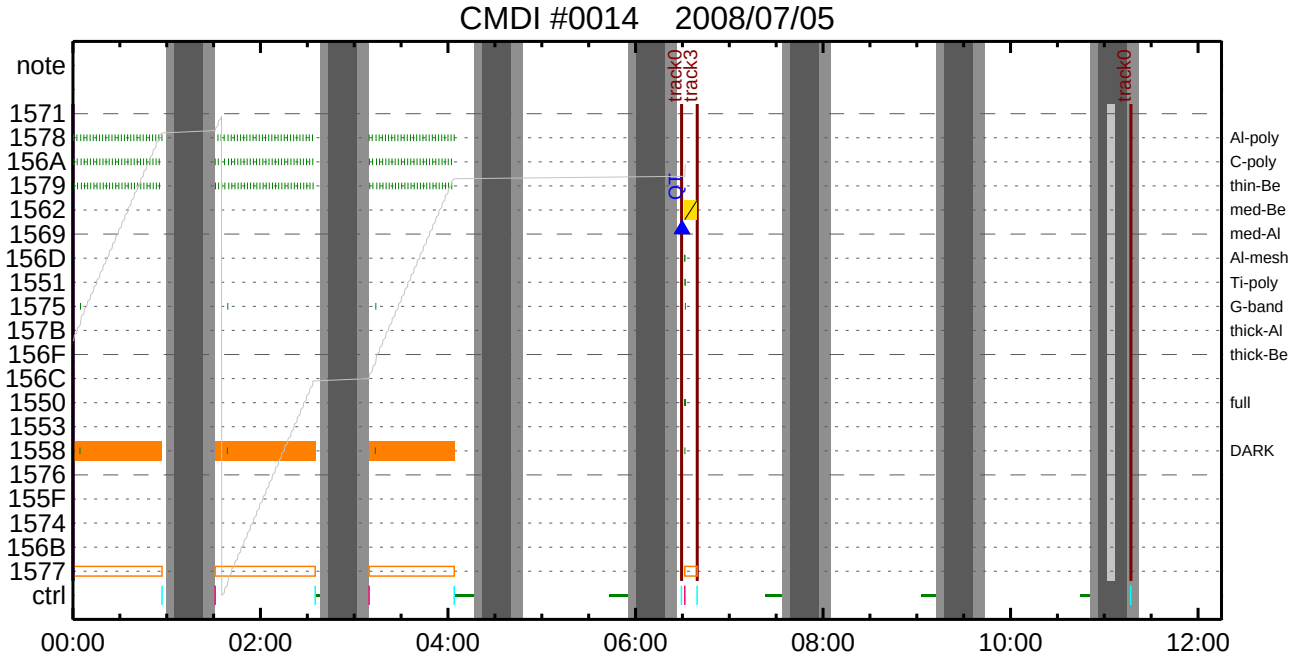
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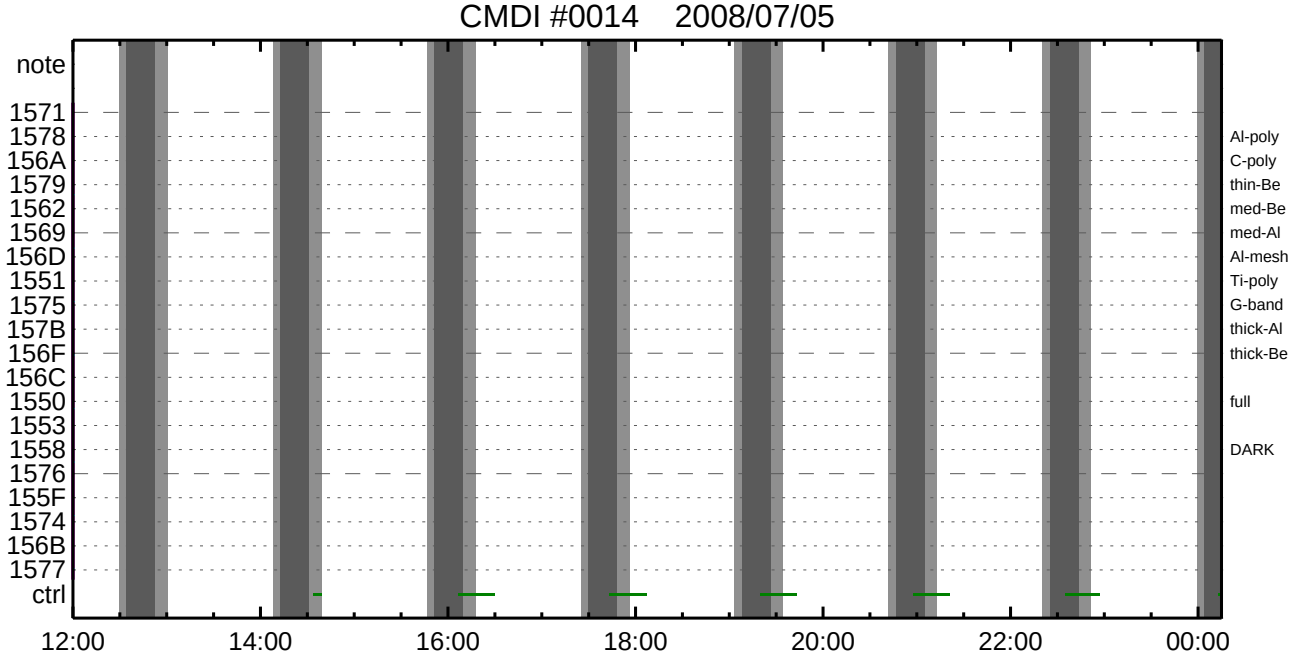
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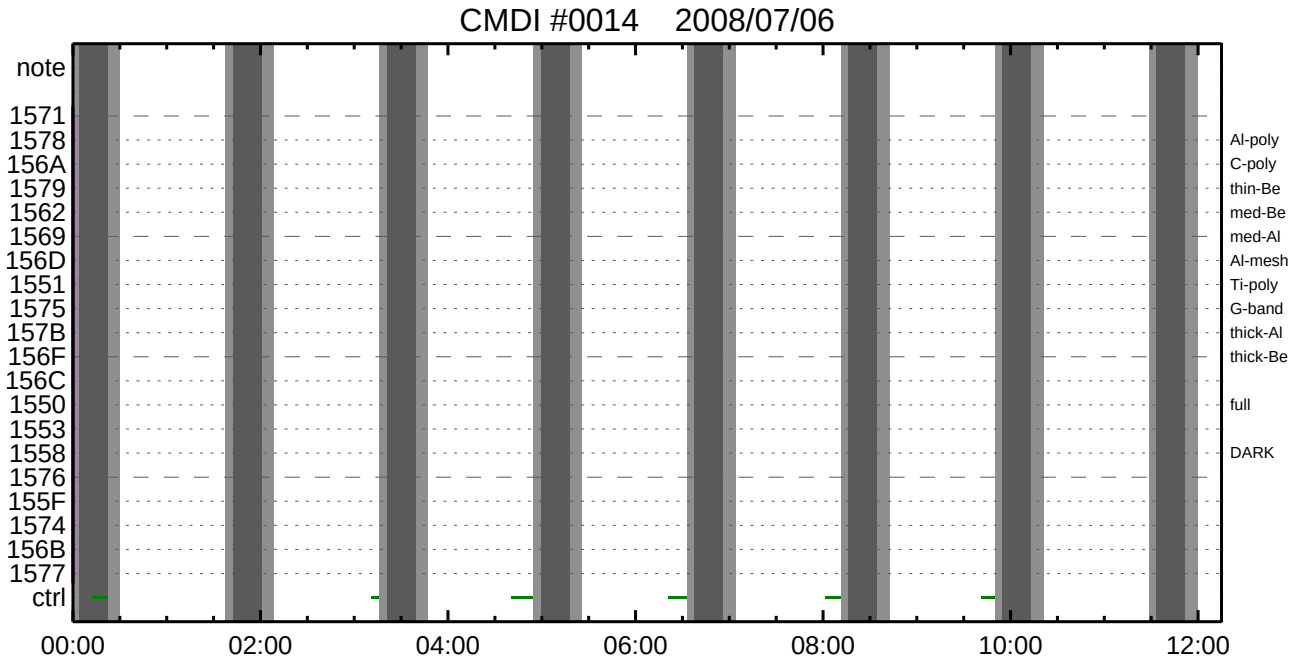
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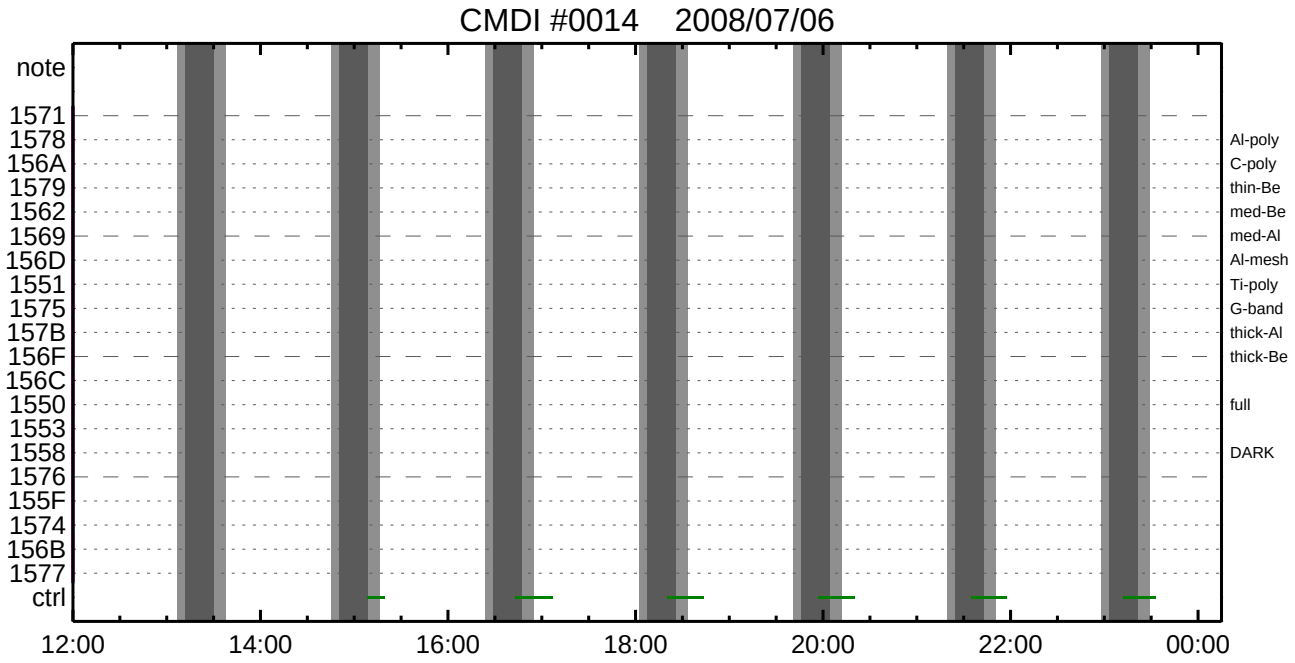
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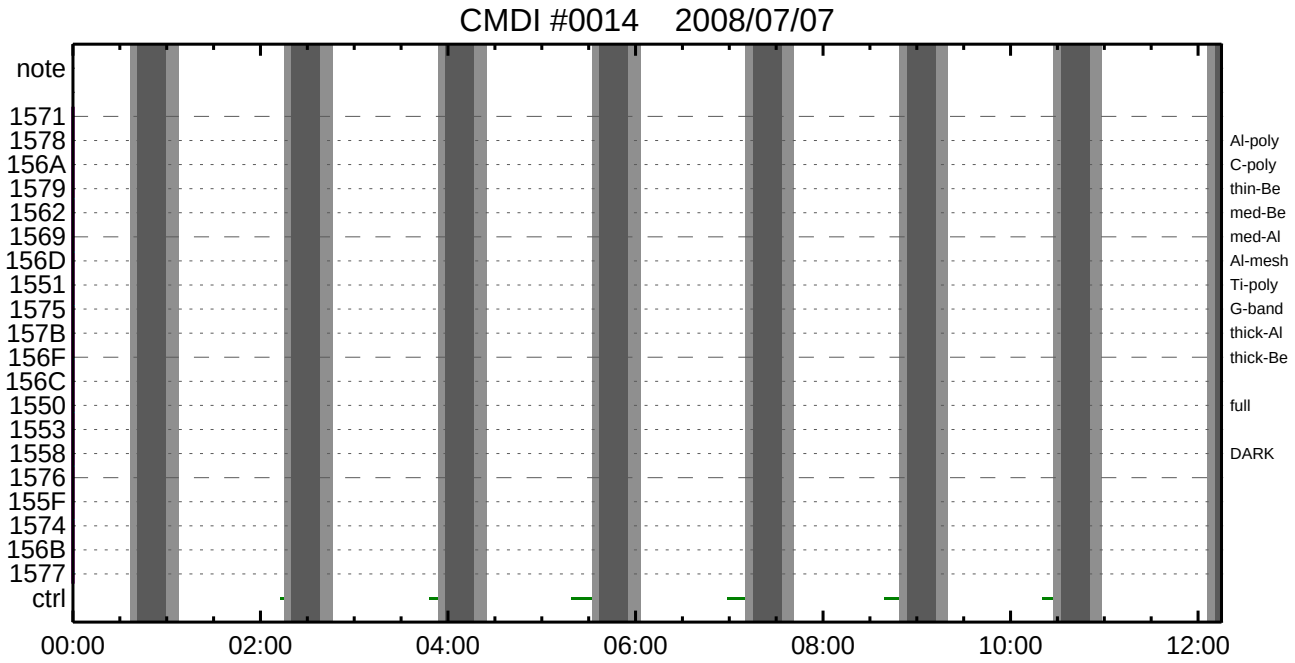
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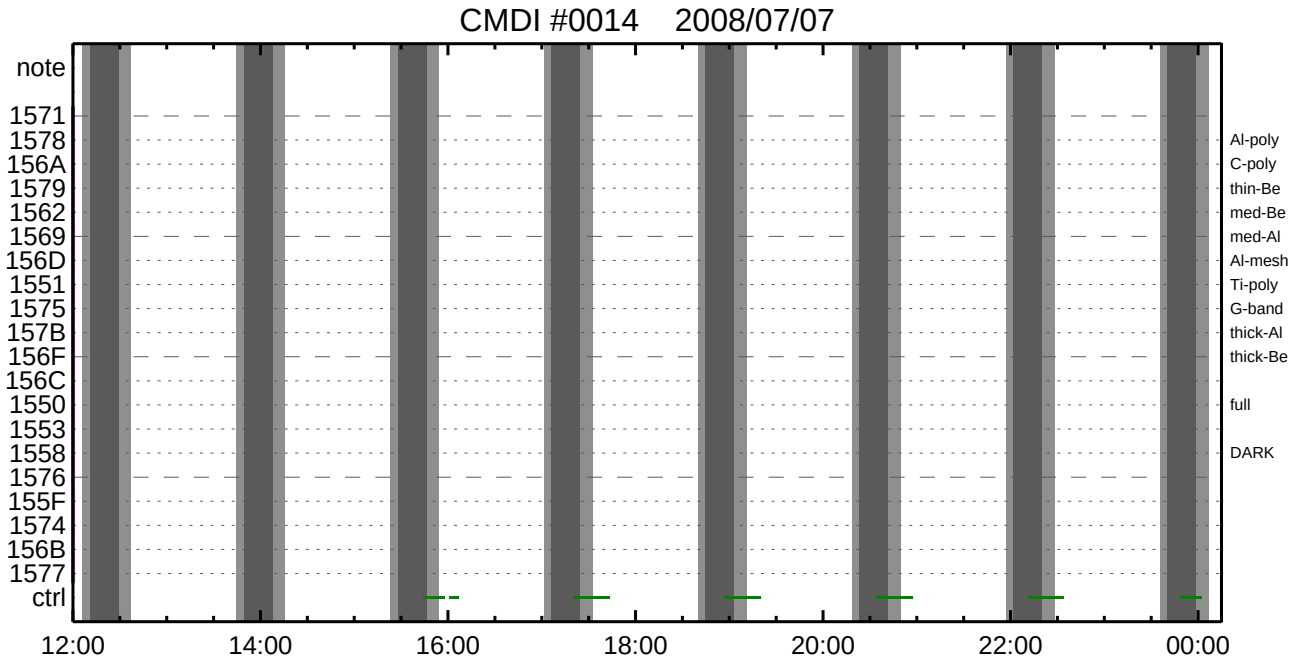
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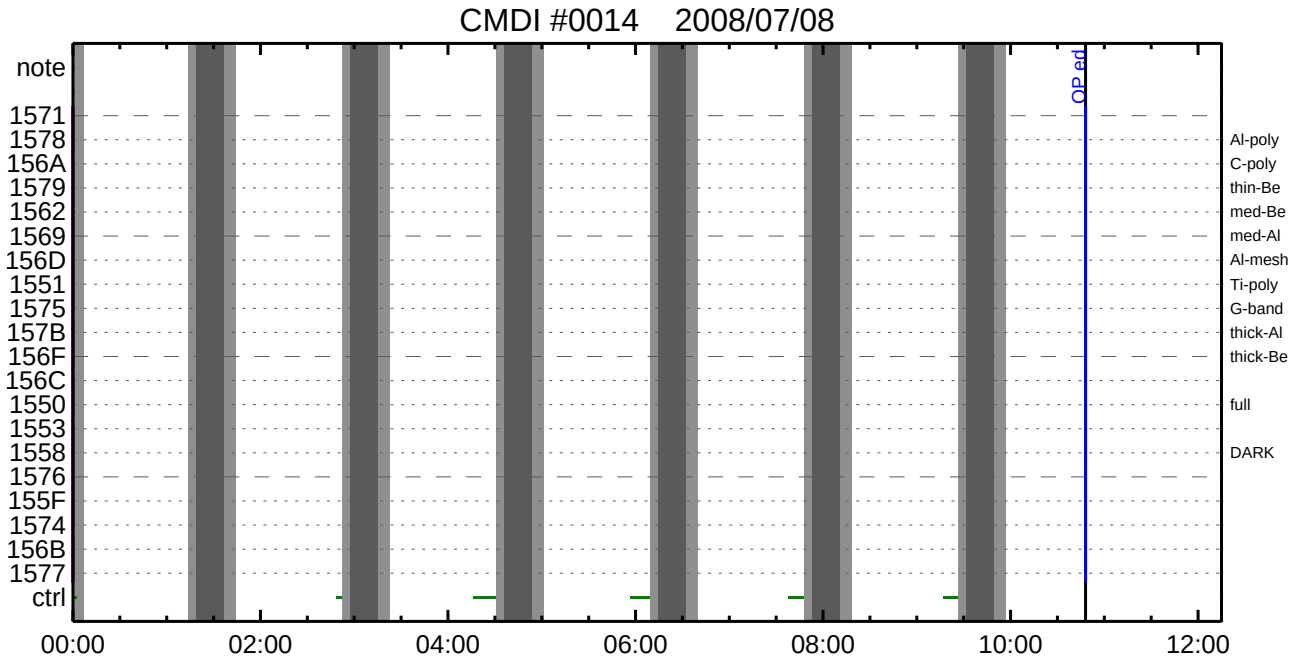
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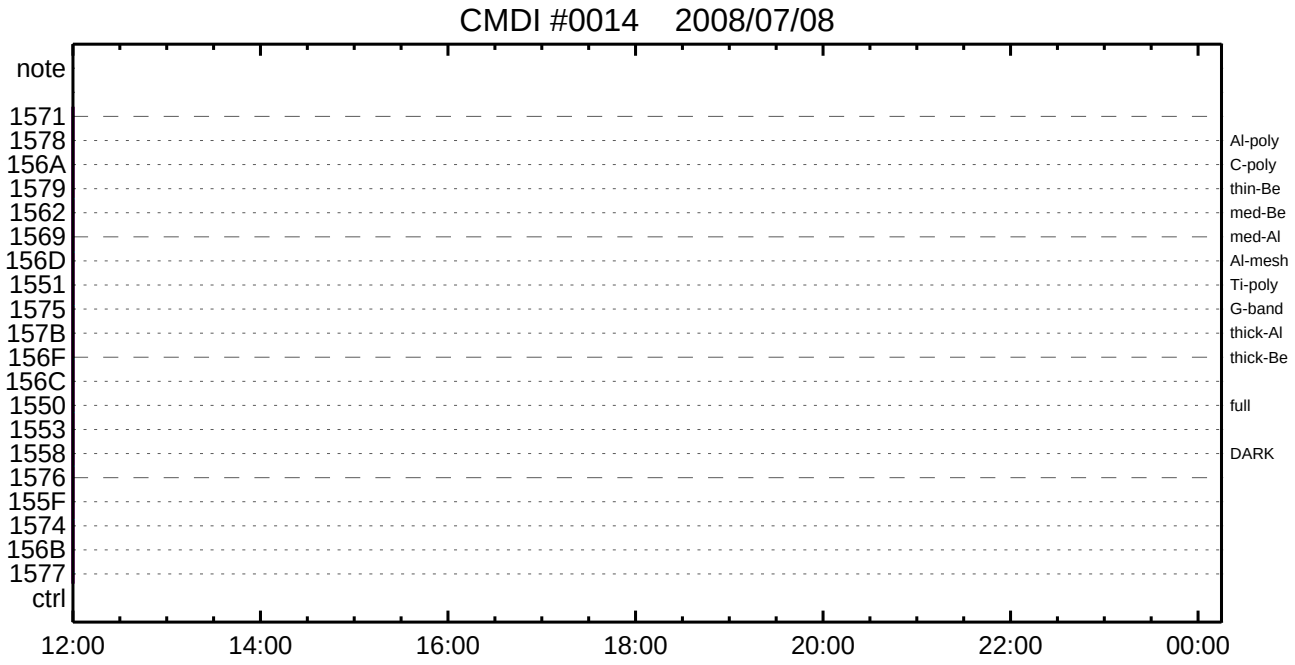
CMDI #0014 2008/07/08
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Al-mesh
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(a) Spacecraft Operation Procedure (real-commands)

main-962 2008-07-03 12:38:45 289 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇÎ»þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Äº²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
     C.
     C.
   . C. ****************************
     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
     C. ****************************
     C.
   . C. ¡ãOP/OG¥í¡¼¥É¡ä
   . S. OP    op-962:OP
     ()
   . S. OG    og-962:OG
     ()
     C.
   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
  +. DC 01-23 DHU_DMA_DMP_PRM_SET
     BC       (20 00 7f 01 02)
     C.                     ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
     C.                     ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
     C.                     ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
     C.                     ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
     C.                     ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
  +. DC 01-22 DHU_MODE_CHNG
     BC       (07 0b f8)
     C.                     ¢¢[HK1_PKT_FORM_NO]             EQ     7
     C.                     ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
     C.                     ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
     C.                     ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
     C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
     C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
     C.
     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
  +. DC 01-23 DHU_DMA_DMP_PRM_SET
     BC       (20 80 7f 01 02)
     C.                     ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
     C.                     ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
     C.                     ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
     C.                     ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
     C.                     ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
  +. DC 01-22 DHU_MODE_CHNG
     BC       (07 0b f8)
     C.                     ¢¢[HK1_PKT_FORM_NO]             EQ     7
     C.                     ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
     C.                     ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
     C.                     ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
     C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
     C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
     C.
     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
  +. DC 01-23 DHU_DMA_DMP_PRM_SET
     BC       (21 00 41 01 02)
     C.                     ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
     C.                     ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
     C.                     ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
     C.                     ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
     C.                     ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
  +. DC 01-22 DHU_MODE_CHNG
     BC       (07 0b f8)
     C.                     ¢¢[HK1_PKT_FORM_NO]             EQ     7
     C.                     ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
     C.                     ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
     C.                     ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
     C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
     C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
     C.
   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Çâ) *****
     C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤ÎÌá¤¹
  +. DC 01-22 DHU_MODE_CHNG
     BC       (02 0a f8)
     C.                     ¢¢[HK1_PKT_FORM_NO]             EQ     2
     C.                     ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
     C.                     ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
     C.                     ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
     C.
   . C. *********************************
     C. TI-CMD SET (OPOG STOP/COPY/START)
     C. *********************************
     C.
   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
     C.
   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
  +. TI 2008-07-03 11:25:00.0
     DC 01-B3 DHU_OP_STOP
     C.                     ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
     C.
  +. TI 2008-07-03 11:25:01.0
     DC 01-B4 DHU_OP_COPY
     C.                     ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
     C.
  +. TI 2008-07-03 11:25:01.0
     DC 01-B5 DHU_OPOG_COPY
     C.                     ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195   +. TI 2008-07-03 11:29:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238    . C. Stop EIS observation and temporarily disable EIS mode changes
0239     C.
0240     C.
0241     C. ********** Start EIS operation (TI set) **********
0242     C. Execute, after the success of OP upload.
0243     C. Set EIS TI-commands
0244   +. TI 2008-07-03 11:29:30.0
0245     DC 07-FC EIS_MODE_MANU
0246     BC       (21 02)
0247   +. TI 2008-07-03 11:29:40.0
0248     DC 07-FC EIS_MODE_CHG_DIS
0249     BC       (22)
0250    . C.                     [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0251     C. ************ End EIS operation (TI set) ************
0252     C.
0253     C.
0254     C. ********************************
0255     C.  SOT TI command set
0256     C. ********************************
0257     C. Execute, after the success of OP upload.
0258   +. TI 2008-07-03 11:29:16.0
0259     DC 07-F0 MDP_SOT_MODE_STBY
0260     BC       (41)
0261    . C. ------------------------------------
0262     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0263     C. ------------------------------------
0264     C. ************ SOT END ************
0265     C.
0266     C. ************ XRT START ************
0267     C. Execute, after the success of OP upload.
0268   +. TI 2008-07-03 11:29:00.0
0269     DC 07-F0 MDP_XRT_MODE_STBY
0270     BC       (c3)
0271    . C.                     [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0272     C.
0273     C. ************ XRT END ************
0274     C.
0275    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0276     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0277    . S. DC-BC dcbc-402:DCBC
0278     (MDP_known_event)
0279     C.
0280     C.
0281    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0282    . S. DC-BC dcbc-153:DCBC
0283     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284     C.
0285     C.
0286    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0287     C.
0288    . C. ********** LOS **********
0289     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-963 2008-07-03 12:38:45 82 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011  +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0015     C. Upload the Orbit Element and the Target Attitude
0016     C. RAM-ID:TARGET_ATT
0017   . S. RAM   ram-150:TARGET_ATT
0018     ()
0019     C.
0020     C.
0021     C. Set the dump memory area of TARGET_ATT
0022  +. DC 02-48 AOCU_DUMP_SET
0023     BC       (07 00 00 00 18 00)
0024     C.
0025     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026     C.
0027     C.
0028     C. Change the TLMFormatNo for the AOCS Dump Format
0029  +. DC 01-22 DHU_MODE_CHNG
0030     BC       (04 0b f8)
0031     C.
0032     C. Wait for AOCSDUMP to end
0033     C.
0034   . C. Check the dump memory
0035     C.
0036     C.                                      Result = OK [ ]
0037     C.
0038  +. DC 01-22 DHU_MODE_CHNG
0039     BC       (02 0a f8)
0040     C.
0041     C.                          <A_***>[TLM STS] FMT = 2 [ ]
0042     C.
0043  +. DC 02-8E AOCU_ORB_UPD
0044   . C.
0045   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046  +. DC 07-FC EIS_MODE_MANU
0047     BC       (21 02)
0048   . C. Verify EIS in MANUAL mode
0049   . C. Estimated OBSTBL upload time is 21s
0050     C. ********************************
0051     C. EIS START OBSTBL LOAD
0052     C. ********************************
0053   . S. RAM   ram-820:EIS_OBSTBL
0054     ()
0055  +. DC 07-FC EIS_DUMP_OBSTBL
0056     BC       (07 07 07 00 00 70 00)
0057     C.
0058     C. Execute, after the success of OBSTBL upload.
0059     C. Set EIS TI-commands
0060  +. TI 2008-07-03 11:29:50.0
0061     DC 07-FC EIS_MODE_CHG_ENA
0062     BC       (20)
0063   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0064     C. ********************************
0065     C. EIS END OBSTBL LOAD
0066     C. ********************************
0067     C.
0068   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0069     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0070   . S. DC-BC dcbc-402:DCBC
0071     (MDP_known_event)
0072     C.
0073     C.
0074   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0075   . S. DC-BC dcbc-153:DCBC
0076     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077     C.
0078     C.
0079   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0080     C.
0081   . C. *********** LOS ***********
0082     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-964 2008-07-03 12:38:45 138 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014     C.
0015     C. ************ XRT START ************
0016     C.
0017   +. DC 07-F0 MDP_XRT_CTRL_MANU
0018     BC       (c1)
0019   +  DC 07-F0 MDP_XRT_MODE_STBY
0020     BC       (c3)
0021   . C. ----------- Success Verify ?              OK / NG____
0022     C.
0023     C. XRT Obs. Table Upload
0024   . S. RAM   ram-291:MDP_OBS_X
0025     ()
0026     C.
0027   +. DC 07-F0 MDP_DUMP_XRTTBL
0028     BC       (84 07 00 00 00 3a d4)
0029   . C. ----------- Comparison Check ?                        OK / ERR ____
0030     C.
0031     C.
0032   +. DC 07-F0 MDP_XRT_ROI_SET
0033     BC       (cd 01 b1 b1 04 04)
0034   +  DC 07-F0 MDP_XRT_ROI_SET
0035     BC       (cd 02 b1 b1 08 08)
0036   +  DC 07-F0 MDP_XRT_ROI_SET
0037     BC       (cd 03 b1 b1 08 08)
0038   +  DC 07-F0 MDP_XRT_ROI_SET
0039     BC       (cd 04 b1 b1 06 06)
0040   +  DC 07-F0 MDP_XRT_ROI_SET
0041     BC       (cd 06 80 80 20 20)
0042   +  DC 07-F0 MDP_XRT_ROI_SET
0043     BC       (cd 07 70 71 0c 0c)
0044   +  DC 07-F0 MDP_XRT_ROI_SET
0045     BC       (cd 08 80 80 08 08)
0046   +  DC 07-F0 MDP_XRT_ROI_SET
0047     BC       (cd 09 80 80 06 06)
0048   +  DC 07-F0 MDP_XRT_ROI_SET
0049     BC       (cd 0f 80 80 06 06)
0050   +  DC 07-F0 MDP_XRT_ROI_SET
0051     BC       (cd 10 80 80 04 04)
0052   +. DC 07-F0 MDP_XRT_QT_PROG_SET
0053     BC       (c4 10)
0054   +  DC 07-F0 MDP_XRT_FLD_DIS
0055     BC       (d9)
0056   +  DC 07-F0 MDP_XRT_FLRCTRL_DIS
0057     BC       (c9)
0058   +  DC 07-F0 MDP_XRT_ARS_DIS
0059     BC       (d5)
0060   . C. ---------- Success Verify ?            OK / NG ____
0061     C.
0062     C.
0063   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0064     C.
0065   +. DC 07-F0 MDP_XRT_MODE_OBSV
0066     BC       (c2)
0067   +. TI 2008-07-03 11:29:02.0
0068     DC 07-F0 MDP_XRT_MODE_OBSV
0069     BC       (c2)
0070   . C. ---------- Success Verify ?            OK / NG ____
0071     C.
0072     C. ************ XRT END ************
0073   . C. ********************************
0074     C.  SOT table upload
0075     C. ********************************
0076   . C. < Stop FG table >
0077   +. DC 07-F0 MDP_FG_CTRL_MANU
0078     BC       (51)
0079   . C. ------------------------------------
0080     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0081     C. ------------------------------------
0082     C.
0083   . C. <Upload FG Observation Table>
0084   . S. RAM   ram-261:MDP_OBS_F
0085     ()
0086     C.
0087   . C. < Dump RAMID=MDP_OBS_F >
0088   +. DC 07-F0 MDP_DUMP_FGTBL
0089     BC       (82 07 00 00 00 38 b8)
0090     C. ------------------------------------
0091     C.  MDP_OBS_F verify = OK/NG        [ ]
0092     C. ------------------------------------
0093     C.
0094   . C. < Stop SP table >
0095   +. DC 07-F0 MDP_SP_CTRL_MANU
```

```
0096     BC       (61)
0097     C. ------------------------------------
0098     C.  MDP_SP_CTRL_MODE      = MANU    [ ]
0099     C. ------------------------------------
0100     C.
0101   . C. <Upload SP Observation Table>
0102   . S. RAM   ram-287:MDP_OBS_S
0103     ()
0104     C.
0105   . C. < Dump RAMID=MDP_OBS_S >
0106  +. DC 07-F0 MDP_DUMP_SPTBL
0107     BC       (83 07 00 00 00 38 b8)
0108     C. ------------------------------------
0109     C.  MDP_OBS_S verify = OK/NG        [ ]
0110     C. ------------------------------------
0111     C.
0112     C. ******************************
0113     C.  SOT TI command set
0114     C. ******************************
0115     C. Execute, after the success of TBL upload.
0116  +. TI 2008-07-03 11:29:18.0
0117     DC 07-F0 MDP_SOT_MODE_OBSV
0118     BC       (40)
0119   . C. ------------------------------------
0120     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0121     C. ------------------------------------
0122     C.
0123     C.
0124   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0125     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0126   . S. DC-BC dcbc-402:DCBC
0127     (MDP_known_event)
0128     C.
0129     C.
0130   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0131   . S. DC-BC dcbc-153:DCBC
0132     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0133     C.
0134     C.
0135   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0136     C.
0137   . C. ********** LOS **********
0138     C.
```

```
*** OP Sequence for XRT ***

2008/07/03  11:40:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM               5 02-76 01 00 00 00 00
2008/07/03  15:49:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2008/07/03  15:49:56.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                XRT_FOCUS_POSITION    4 07-F8 22 ff aa 00
2008/07/03  15:50:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM               5 02-76 00 00 00 00 00
2008/07/03  15:50:16.0  XRT_QT_PROG_SET_420_OG [0x1a4]
                                MDP_XRT_QT_PROG_SET   2 07-F0 c4 10
2008/07/03  15:50:18.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS       1 07-F0 d9
2008/07/03  15:50:20.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS   1 07-F0 c9
2008/07/03  15:50:22.0  XRT_ARS_DIS_427_OG [0x1ab]
                                MDP_XRT_ARS_DIS       1 07-F0 d5
2008/07/03  15:52:00.0  XRT_CTRL_AUTO_444_OG [0x1bc]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2008/07/03  15:59:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2008/07/03  16:00:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM               5 02-76 00 2e a7 45 f3
2008/07/03  18:30:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2008/07/03  18:30:02.0  XRT_QT_PROG_SET_423_OG [0x1a7]
                                MDP_XRT_QT_PROG_SET   2 07-F0 c4 11
2008/07/03  18:32:00.0  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                XRT_FOCUS_POSITION    4 07-F8 22 fe 97 00
2008/07/03  18:32:20.0  XRT_ARS_DIS_422_OG [0x1a6]
                                MDP_XRT_ARS_DIS       1 07-F0 d5
2008/07/03  18:32:22.0  XRT_FLD_DIS_445_OG [0x1bd]
                                MDP_XRT_FLD_DIS       1 07-F0 d9
2008/07/03  18:32:24.0  XRT_FLRCTRL_DIS_416_OG [0x1a0]
                                MDP_XRT_FLRCTRL_DIS   1 07-F0 c9
2008/07/03  18:32:26.0  XRT_CTRL_AUTO_403_OG [0x193]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2008/07/03  19:24:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2008/07/03  20:06:30.0  XRT_Custom_430_OG [0x1ae]
2008/07/03  20:07:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2008/07/03  21:02:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2008/07/03  21:44:00.0  XRT_Custom_430_OG [0x1ae]
2008/07/03  21:45:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2008/07/03  22:00:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM               5 02-76 00 54 72 00 00
2008/07/03  22:00:00.5  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2008/07/03  22:00:02.5  XRT_QT_PROG_SET_400_OG [0x190]
                                MDP_XRT_QT_PROG_SET   2 07-F0 c4 01
2008/07/03  22:02:00.5  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                XRT_FOCUS_POSITION    4 07-F8 22 fe 97 00
2008/07/03  22:02:20.5  XRT_ARS_DIS_422_OG [0x1a6]
                                MDP_XRT_ARS_DIS       1 07-F0 d5
2008/07/03  22:02:22.5  XRT_FLD_DIS_445_OG [0x1bd]
                                MDP_XRT_FLD_DIS       1 07-F0 d9
2008/07/03  22:02:24.5  XRT_FLRCTRL_DIS_416_OG [0x1a0]
                                MDP_XRT_FLRCTRL_DIS   1 07-F0 c9
2008/07/03  22:02:26.5  XRT_CTRL_AUTO_403_OG [0x193]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2008/07/03  22:41:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2008/07/03  23:19:30.0  XRT_Custom_430_OG [0x1ae]
2008/07/03  23:20:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2008/07/04  00:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM               5 02-76 00 00 00 00 00
2008/07/04  00:00:00.5  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2008/07/04  05:59:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2008/07/04  05:59:56.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                XRT_FOCUS_POSITION    4 07-F8 22 ff aa 00
2008/07/04  06:00:16.0  XRT_QT_PROG_SET_420_OG [0x1a4]
                                MDP_XRT_QT_PROG_SET   2 07-F0 c4 10
2008/07/04  06:00:18.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS       1 07-F0 d9
2008/07/04  06:00:20.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS   1 07-F0 c9
2008/07/04  06:00:22.0  XRT_ARS_DIS_427_OG [0x1ab]
                                MDP_XRT_ARS_DIS       1 07-F0 d5
2008/07/04  06:02:00.0  XRT_CTRL_AUTO_444_OG [0x1bc]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2008/07/04  06:09:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2008/07/04  07:27:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2008/07/04  07:27:02.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                XRT_FOCUS_POSITION    4 07-F8 22 ff aa 00
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2008/07/04  07:27:22.0  XRT_QT_PROG_SET_409_OG [0x199]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2008/07/04  07:27:24.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2008/07/04  07:27:26.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/07/04  07:27:28.0  XRT_ARS_DIS_422_OG [0x1a6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2008/07/04  07:27:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/07/04  07:34:47.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/07/04  07:34:49.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2008/07/04  07:35:09.0  XRT_QT_PROG_SET_446_OG [0x1be]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2008/07/04  07:35:11.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2008/07/04  07:35:13.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/07/04  07:35:15.0  XRT_ARS_DIS_422_OG [0x1a6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2008/07/04  07:35:17.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/07/04  08:25:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/07/04  08:58:00.0  XRT_Custom_421_OG [0x1a5]
2008/07/04  09:06:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/07/04  10:05:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/07/04  10:36:30.0  XRT_Custom_421_OG [0x1a5]
2008/07/04  10:44:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/07/04  11:49:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/07/04  12:15:00.0  XRT_Custom_421_OG [0x1a5]
2008/07/04  12:23:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/07/04  13:19:43.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/07/04  13:19:45.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2008/07/04  13:20:05.0  XRT_QT_PROG_SET_409_OG [0x199]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2008/07/04  13:20:07.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2008/07/04  13:20:09.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/07/04  13:20:11.0  XRT_ARS_DIS_422_OG [0x1a6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2008/07/04  13:20:13.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/07/04  13:27:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/07/04  16:29:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/07/04  16:29:56.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2008/07/04  16:30:16.0  XRT_QT_PROG_SET_420_OG [0x1a4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 10
2008/07/04  16:30:18.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2008/07/04  16:30:20.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/07/04  16:30:22.0  XRT_ARS_DIS_427_OG [0x1ab]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2008/07/04  16:32:00.0  XRT_CTRL_AUTO_444_OG [0x1bc]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/07/04  16:39:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/07/04  16:40:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 2e a7 45 f3
2008/07/04  17:58:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/07/04  17:58:02.0  XRT_QT_PROG_SET_423_OG [0x1a7]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2008/07/04  18:00:00.0  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2008/07/04  18:00:20.0  XRT_ARS_DIS_422_OG [0x1a6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2008/07/04  18:00:22.0  XRT_FLD_DIS_445_OG [0x1bd]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2008/07/04  18:00:24.0  XRT_FLRCTRL_DIS_416_OG [0x1a0]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/07/04  18:00:26.0  XRT_CTRL_AUTO_403_OG [0x193]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/07/04  18:23:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/07/04  19:07:00.0  XRT_Custom_430_OG [0x1ae]
2008/07/04  19:08:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/07/04  20:01:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
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                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/07/04  20:44:00.0  XRT_Custom_430_OG [0x1ae]
2008/07/04  20:45:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/07/04  21:40:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/07/04  22:00:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                        AOCU_NM              5 02-76 03 00 00 00 00
2008/07/04  22:28:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/07/04  22:28:02.0  XRT_QT_PROG_SET_431_OG [0x1af]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 06
2008/07/04  22:30:00.0  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2008/07/04  22:30:20.0  XRT_ARS_DIS_422_OG [0x1a6]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2008/07/04  22:30:22.0  XRT_FLD_DIS_445_OG [0x1bd]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2008/07/04  22:30:24.0  XRT_FLRCTRL_DIS_416_OG [0x1a0]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/07/04  22:30:26.0  XRT_CTRL_AUTO_403_OG [0x193]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/07/04  23:18:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/07/04  23:51:30.0  XRT_Custom_430_OG [0x1ae]
2008/07/04  23:52:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/07/05  00:57:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/07/05  01:23:00.0  XRT_Custom_421_OG [0x1a5]
2008/07/05  01:31:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/07/05  02:35:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/07/05  03:01:30.0  XRT_Custom_421_OG [0x1a5]
2008/07/05  03:09:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/07/05  04:04:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/07/05  06:29:24.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/07/05  06:29:26.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2008/07/05  06:29:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2008/07/05  06:29:46.0  XRT_QT_PROG_SET_420_OG [0x1a4]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 10
2008/07/05  06:29:48.0  XRT_FLD_DIS_419_OG [0x1a3]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2008/07/05  06:29:50.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/07/05  06:29:52.0  XRT_ARS_DIS_427_OG [0x1ab]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2008/07/05  06:31:30.0  XRT_CTRL_AUTO_444_OG [0x1bc]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/07/05  06:39:24.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/07/05  06:39:30.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                        AOCU_NM              5 02-76 03 00 00 00 00
2008/07/05  11:16:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/07/05  11:17:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
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