XRT Timeline to be uploaded on 2008/08/05

Period: 2008/08/05 10:14:00 - 2008/08/09 11:37:00

* * * * * **Normal mode** * * * * *

XOB #1563: CCD Monitor During Bakeout - G-band + dark - wide FOV

Term	Pointing (x, y)	Comment
08/05 10:38:10 - 08/05 12:58:00	Track (-0.9, 0.0) @ 08/05 10:24:00	HOP72 La Palma

PROG= 18 Inf.-time(s)
Subr= 1 1-time(s) 600.0sec
Seqn= 98 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec

XOB #1565: CCD Monitor During Bakeout - G-band + dark - wide FOV - lower cadence (30min)

Term	Pointing (x, y)	Comment
08/05 13:00:10 - 08/05 22:14:30	Track (-0.9, 0.0) @ 08/05 10:24:00	HOP72 La Palma
08/05 22:37:30 - 08/05 23:52:00	Fixed (0.0, 915.0)	HOP75 North pole
08/06 00:05:30 - 08/06 02:51:00	Fixed (950.0, 0.0)	HOP75 West limb

PROG= 09 Inf.-time(s)
Subr= 1 1-time(s) 1800.0sec
Seqn= 98 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec

XOB #1550: G-Band Alignment with North Pole Q90 2x2(G-band only) 8min Cadence - ROI For Limb Alignment-3

Term	Pointing (x, y)	Comment
08/06 12:15:00 - 08/06 14:00:00	Fixed (0.0, 945.0)	Co-alignment north

PROG= 06 1-time(s)
Subr= 1 1-time(s) 360.0sec
Seqn= 58 13-time(s) 480.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec

XOB #1551: G-Band Alignment with East limb Q90 2x2 (G-band only) 8min Cadence - ROI for Limb Alignment-3

Term	Pointing (x, y)	Comment
08/06 15:15:00 - 08/06 17:00:00	Fixed (-945.0, 0.0)	Co-alignment east

PROG= 01 1-time(s)
Subr= 1 1-time(s) 360.0sec
Seqn= 30 13-time(s) 480.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec

XOB #15A3: Synoptic Q95 2x2 - Al/poly(512/5795) + Dark cal(512 Q98) + Ti-poly(723/11571) + G-band(16)

Term	Pointing (x, y)	Comment
08/06 18:25:00 - 08/06 18:32:54	Fixed (0.0, 0.0)	synoptic, shifted 23.0 min

PROG= 11 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 18 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 72 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 76 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 92 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #15A8: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant

Term	Pointing (x, y)	Comment
08/06 18:36:01 - 08/06 18:42:54	Fixed (-528.4, -528.4)	1 quadrant

PROG= 14 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 28 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec

XOB #15A9: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant

Term	Pointing (x, y)	Comment
08/06 18:46:00 - 08/06 18:52:54	Fixed (528.4, -528.4)	2 quadrant

PROG= 20 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 41 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec

XOB #15AA: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant

Term	Pointing (x, y)	Comment
08/06 18:56:00 - 08/06 19:02:54	Fixed (528.4, 528.4)	3 quadrant

PROG= 03 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 8 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec

XOB #15AB: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant

Term	Pointing (x, y)	Comment
08/06 19:06:00 - 08/06 19:12:54	Fixed (-528.4, 528.4)	4 quadrant

PROG= 13 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 40 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec

XOB #15A4: Synoptic Q95 2x2 - Al/mesh(512/5795) + Dark cal(512 Q98) + Ti-poly(723/11571) + G-band(16)

Term	Pointing (x, y)	Comment
08/07 05:56:30 - 08/07 06:04:24	Fixed (0.0, 0.0)	synoptic, shifted -5.5 min

PROG= 12 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 87 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 72 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 76 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 92 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Flare mode

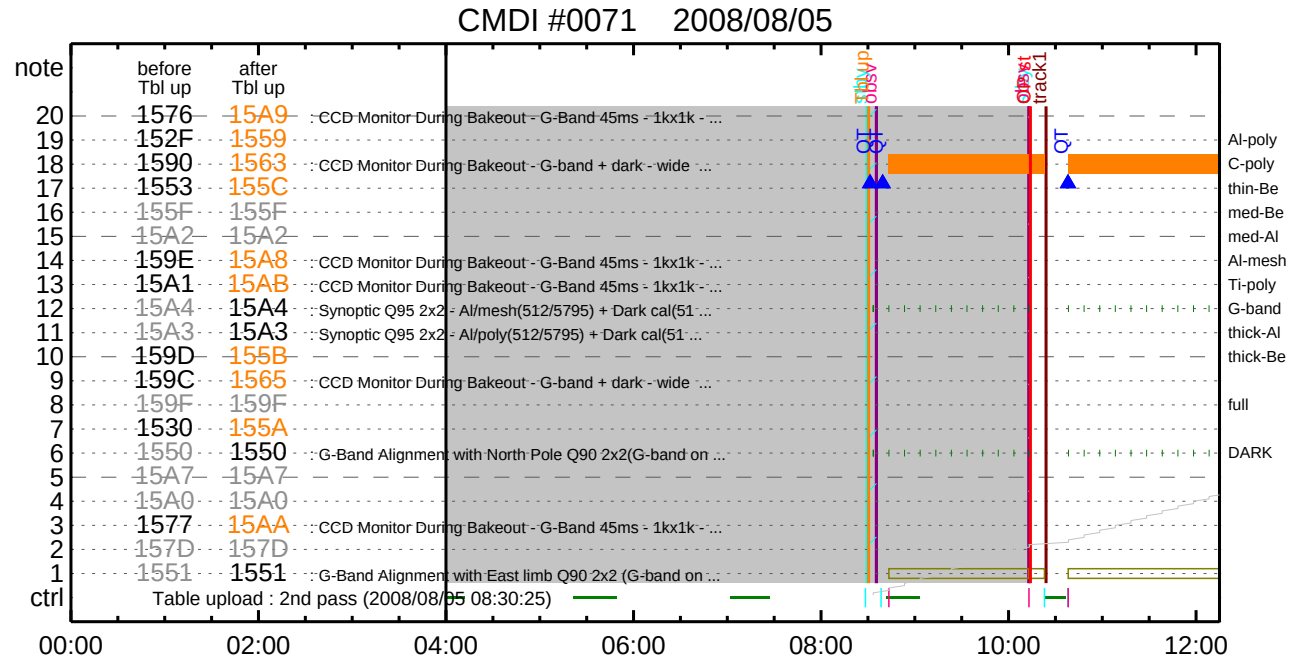
NOT USED

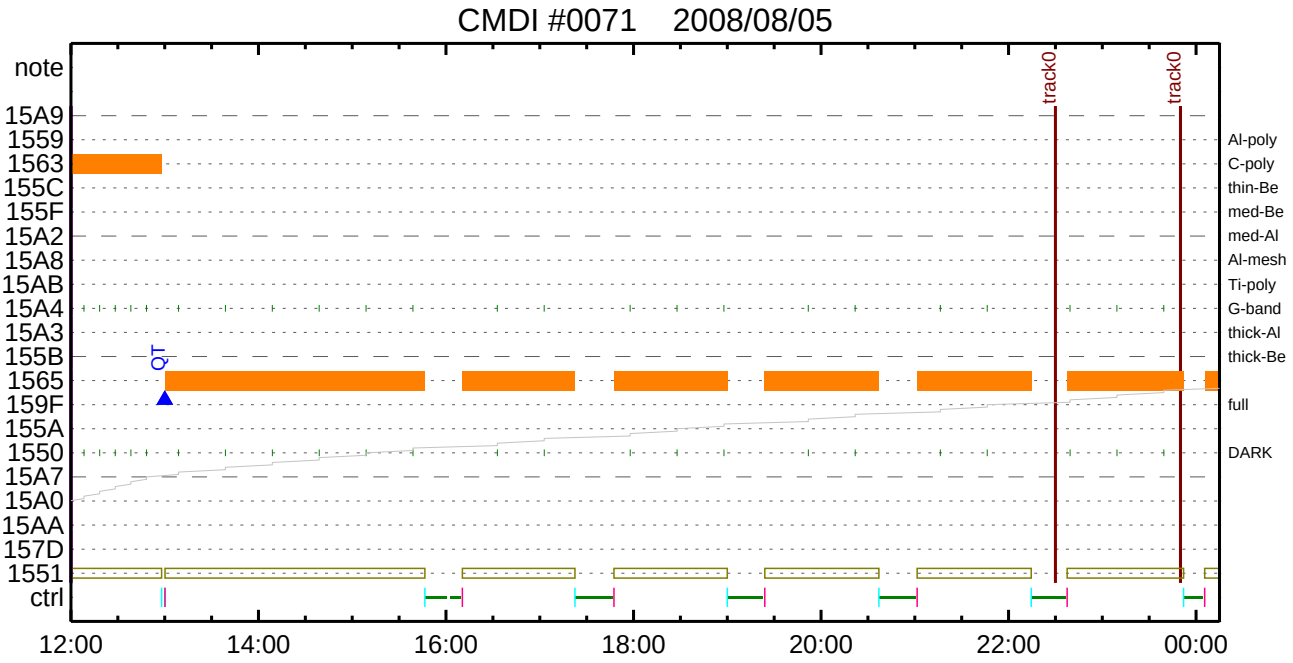
Active Region Search

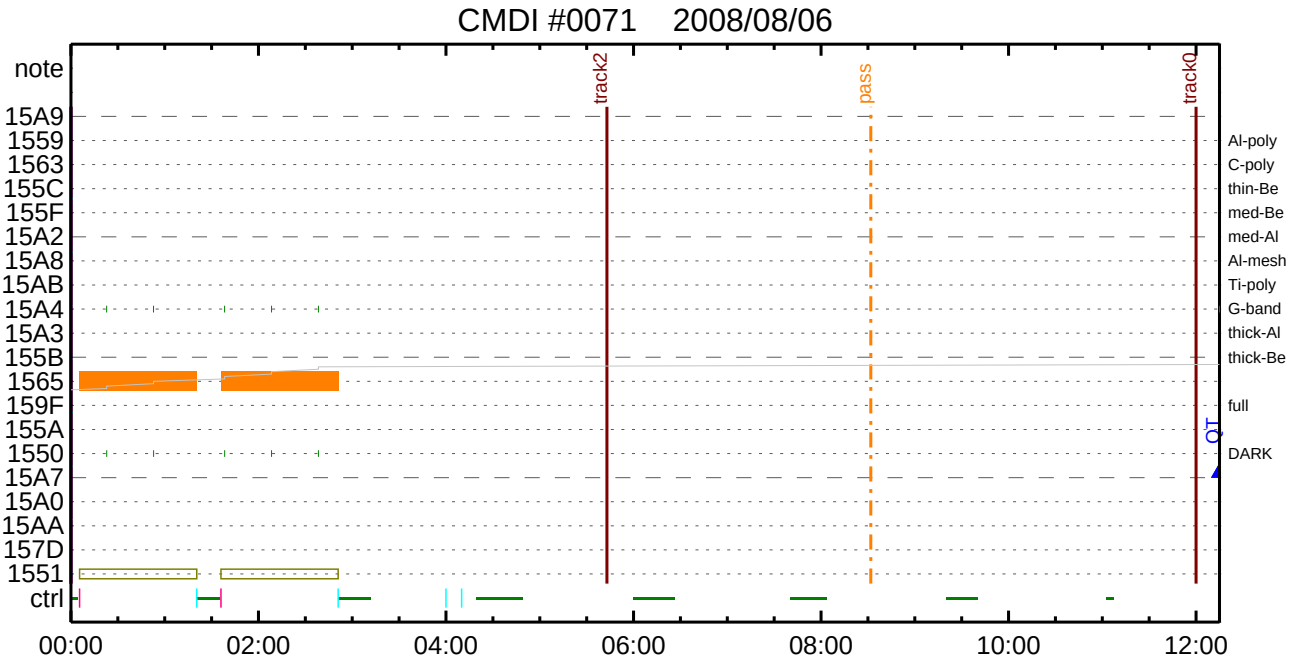
NOT USED

Flare Detection

NOT USED


CMDI #0071 2008/08/05
note
before Tbl up
after Tbl up
20 1576 15A9 : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
19 152F 1559
18 1590 1563 : CCD Monitor During Bakeout - G-band + dark - wide ...
17 1553 155C
16 155F 155F
15 15A2 15A2
14 159E 15A8 : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
13 15A1 15AB : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
12 15A4 15A4 : Synoptic Q95 2x2 - Al/mesh(512/5795) + Dark cal(51 ...
11 15A3 15A3 : Synoptic Q95 2x2 - Al/poly(512/5795) + Dark cal(51 ...
10 159D 155B
9 159C 1565 : CCD Monitor During Bakeout - G-band + dark - wide ...
8 159F 159F
7 1530 155A
6 1550 1550 : G-Band Alignment with North Pole Q90 2x2(G-band on ...
5 15A7 15A7
4 15A0 15A0
3 1577 15AA : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
2 157D 157D
1 1551 1551 : G-Band Alignment with East limb Q90 2x2 (G-band on ...
ctrl Table upload : 2nd pass (2008/08/05 08:30:25)
Tbl up
obsy
track1
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0071 2008/08/05
note
15A9
1559
1563
155C
155F
15A2
15A8
15AB
15A4
15A3
155B
1565
159F
155A
1550
15A7
15A0
15AA
157D
1551
ctrl
track0
track0
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0071 2008/08/06
note
15A9
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1563
155C
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15A2
15A8
15AB
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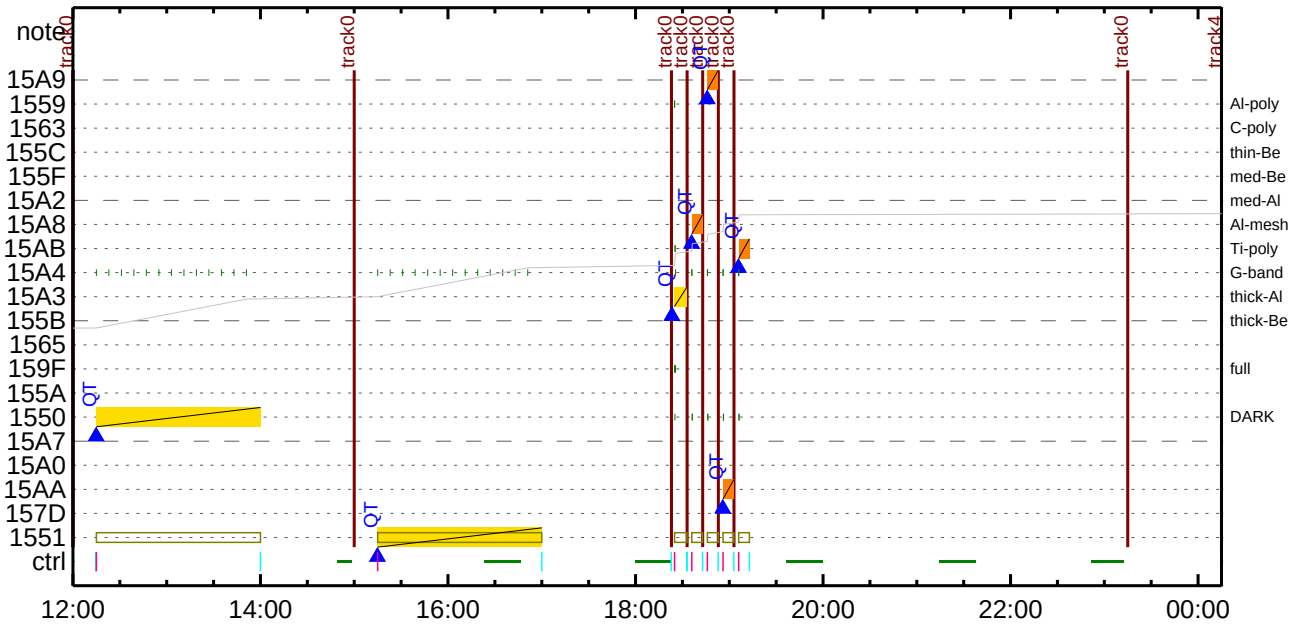
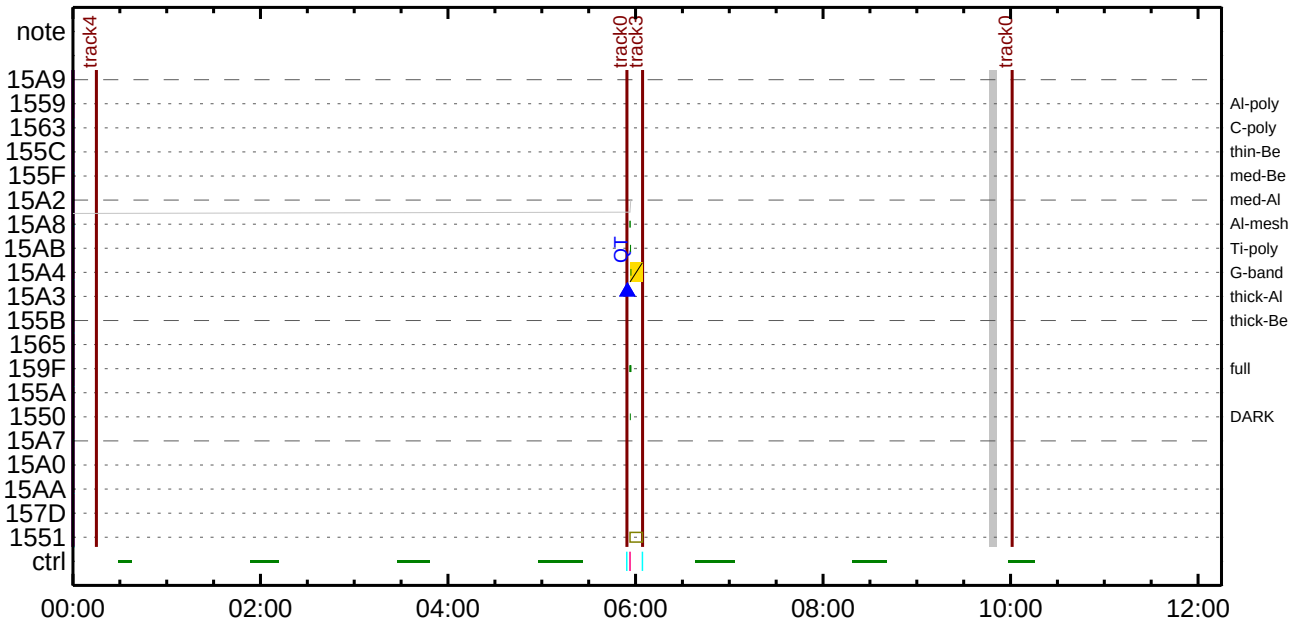
15A3
155B
1565
159F
155A
1550
15A7
15A0
15AA
157D
1551
ctrl
track2
pass
track0
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

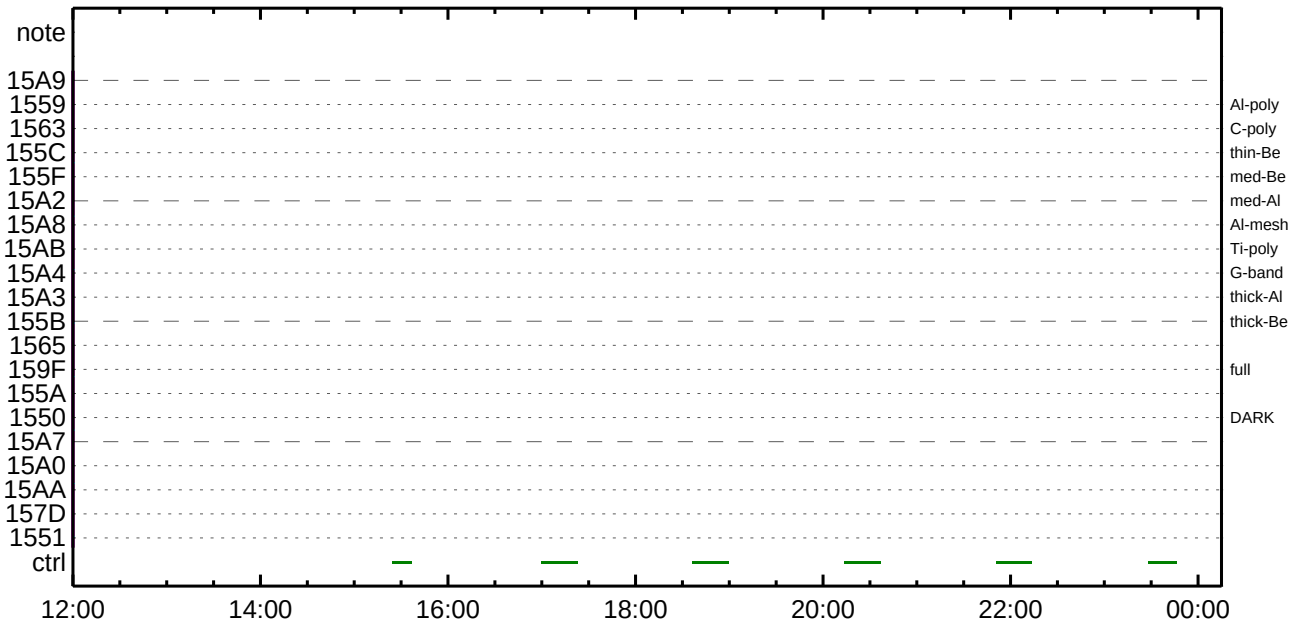
CMDI #0071 2008/08/06

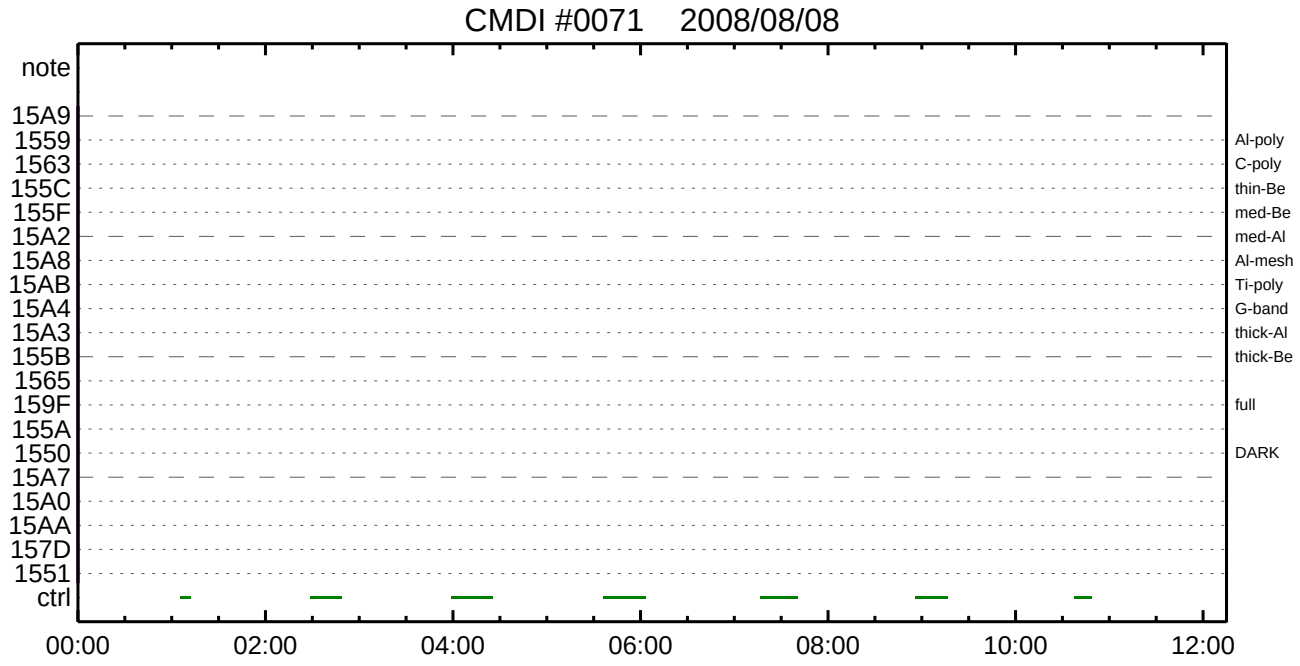


CMDI #0071 2008/08/07



CMDI #0071 2008/08/07




CMDI #0071 2008/08/08
note
15A9
1559
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155C
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15A8
15AB
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15A3
155B
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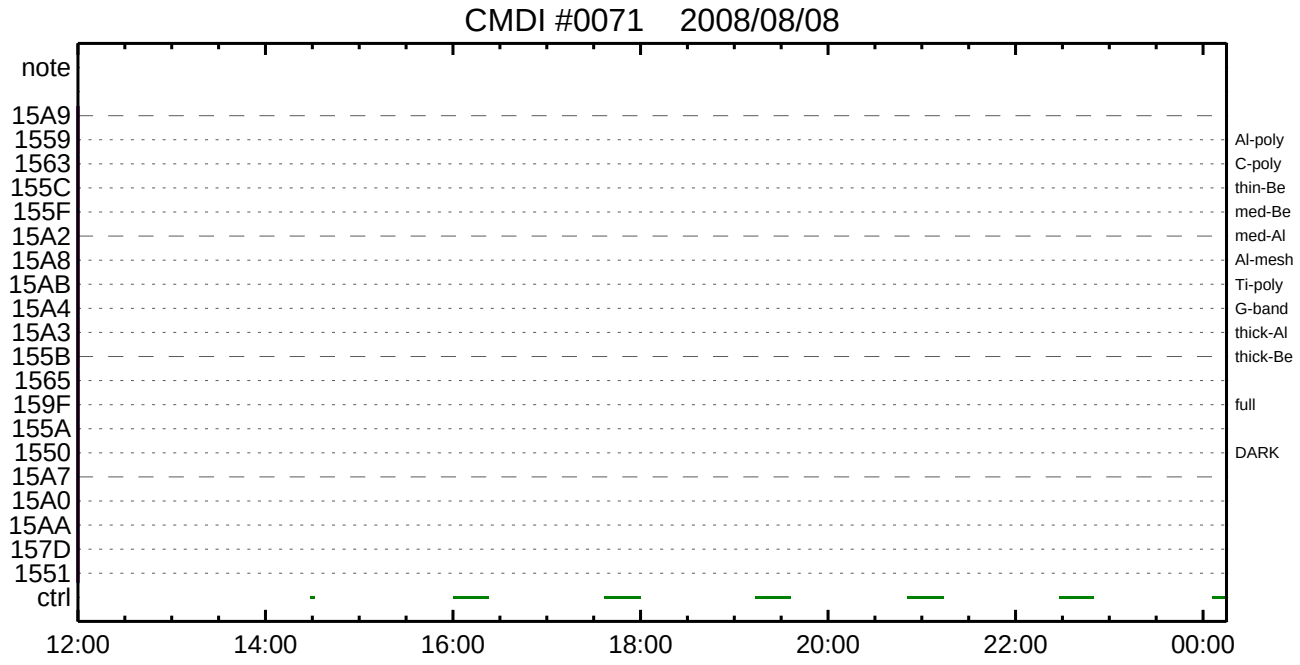
159F
155A
1550
15A7
15A0
15AA
157D
1551
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0071 2008/08/08
note
15A9
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15A8
15AB
15A4
15A3
155B
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159F
155A
1550
15A7
15A0
15AA
157D
1551
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
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14:00
16:00
18:00
20:00
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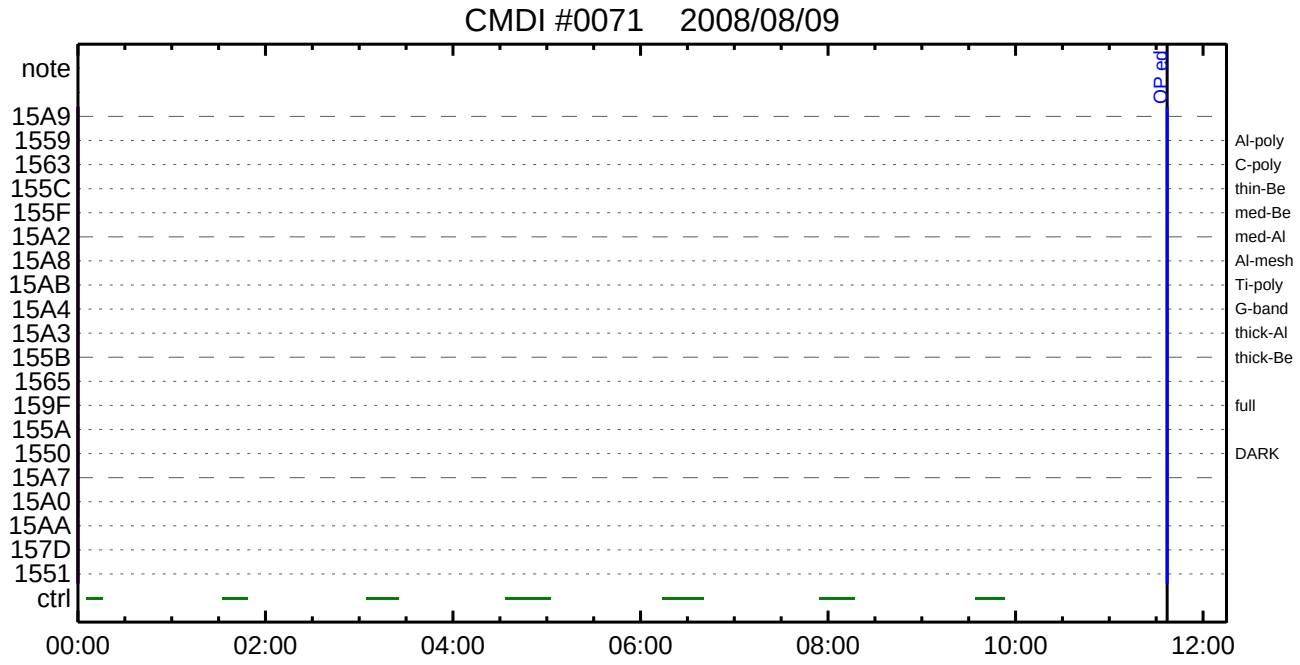
CMDI #0071 2008/08/09
note
OP ed
15A9
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155C
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15AB
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15A7
15A0
15AA
157D
1551
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
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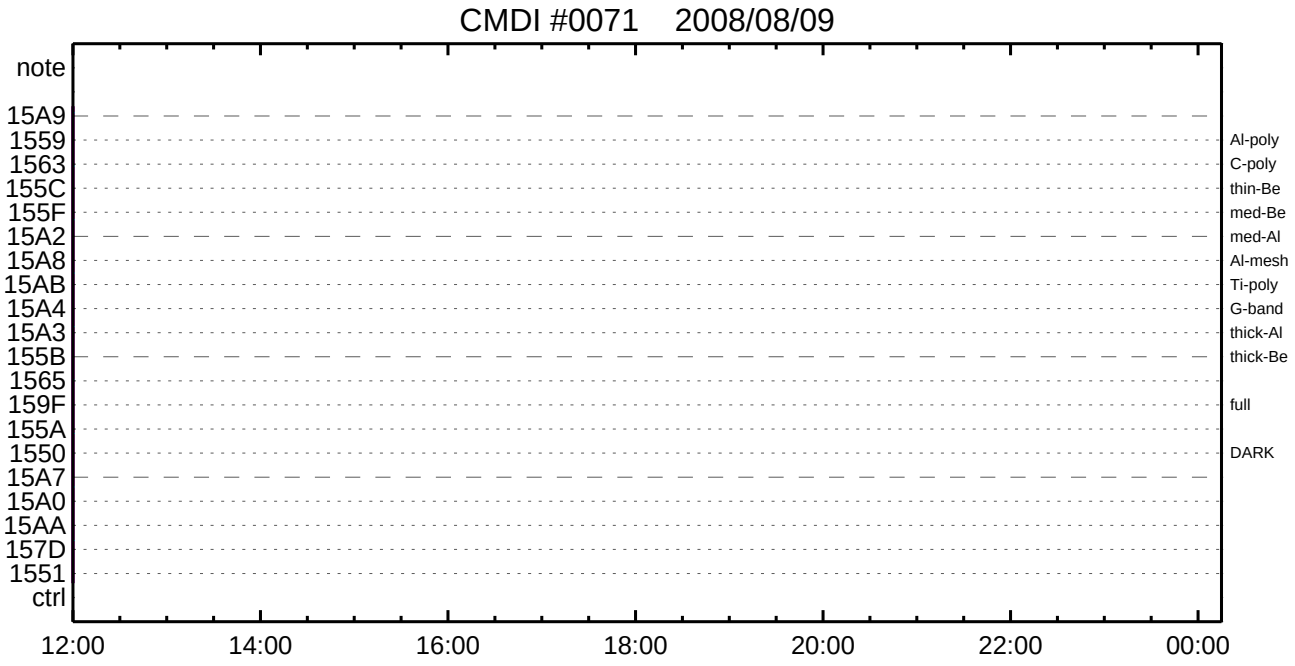
CMDI #0071 2008/08/09
note
15A9
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155C
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15A8
15AB
15A4
15A3
155B
1565
159F
155A
1550
15A7
15A0
15AA
157D
1551
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-048 2008-08-05 17:28:52 289 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ****************************
0099     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0100     C. ****************************
0101     C.
0102   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0103   . S. OP    op-048:OP
0104     ()
0105   . S. OG    og-048:OG
0106     ()
0107     C.
0108   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0109     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0110   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111     BC       (20 00 7f 01 02)
0112     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0113     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0114     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0115     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0116     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0117   +. DC 01-22 DHU_MODE_CHNG
0118     BC       (07 0b f8)
0119     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0120     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0122     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0123     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0124   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0125     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0126   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0127     C.
0128     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0129   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130     BC       (20 80 7f 01 02)
0131     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0132     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0133     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0134     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0135     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0136   +. DC 01-22 DHU_MODE_CHNG
0137     BC       (07 0b f8)
0138     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0139     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0141     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0142     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0143   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0144     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0145   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0146     C.
0147     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149     BC       (21 00 41 01 02)
0150     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0151     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0152     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0153     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0154     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0155   +. DC 01-22 DHU_MODE_CHNG
0156     BC       (07 0b f8)
0157     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0158     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0160     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0161     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0162   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0163     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0164   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0165     C.
0166   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0167     C. DHU¥â¡¼¥É¡Ê¼ý¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0168   +. DC 01-22 DHU_MODE_CHNG
0169     BC       (02 0a f8)
0170     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0171     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0172     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0173     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0174     C.
0175   . C. ********************************
0176     C. TI-CMD SET (OPOG STOP/COPY/START)
0177     C. ********************************
0178     C.
0179   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
0180     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¿¼Ç¹Ô¤¦¤³¤È¡£
0181     C.
0182   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0183   +. TI 2008-08-05 10:09:00.0
0184     DC 01-B3 DHU_OP_STOP
0185     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186     C.
0187   +. TI 2008-08-05 10:09:01.0
0188     DC 01-B4 DHU_OP_COPY
0189     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190     C.
0191   +. TI 2008-08-05 10:09:01.0
0192     DC 01-B5 DHU_OPOG_COPY
0193     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
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0194     C.
0195   +. TI 2008-08-05 10:13:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238     C. ********************************
0239     C.  SOT TI command set
0240     C. ********************************
0241     C. Execute, after the success of OP upload.
0242   +. TI 2008-08-05 10:13:16.0
0243     DC 07-F0 MDP_SOT_MODE_STBY
0244     BC       (41)
0245    . C. ------------------------------------
0246     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0247     C. ------------------------------------
0248     C. ************ SOT END ***********
0249    . C. Stop EIS observation and temporarily disable EIS mode changes
0250     C.
0251     C.
0252     C. ********** Start EIS operation (TI set) **********
0253     C. Execute, after the success of OP upload.
0254     C. Set EIS TI-commands
0255   +. TI 2008-08-05 10:13:30.0
0256     DC 07-FC EIS_MODE_MANU
0257     BC       (21 02)
0258   +. TI 2008-08-05 10:13:40.0
0259     DC 07-FC EIS_MODE_CHG_DIS
0260     BC       (22)
0261    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0262     C. ************ End EIS operation (TI set) ***********
0263     C.
0264     C.
0265     C.
0266     C. ************ XRT START ***********
0267     C. Execute, after the success of OP upload.
0268   +. TI 2008-08-05 10:13:00.0
0269     DC 07-F0 MDP_XRT_MODE_STBY
0270     BC       (c3)
0271    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0272     C.
0273     C. ************ XRT END ***********
0274     C.
0275    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0276     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0277    . S. DC-BC dcbc-402:DCBC
0278     (MDP_known_event)
0279     C.
0280     C.
0281    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0282    . S. DC-BC dcbc-153:DCBC
0283     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284     C.
0285     C.
0286    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0287     C.
0288    . C. ********** LOS **********
0289     C.
```

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(a) Spacecraft Operation Procedure (real-commands)

main-049 2008-08-05 17:28:53 174 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊÈ½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0015     C. Upload the Orbit Element and the Target Attitude
0016     C. RAM-ID:TARGET_ATT
0017   . S. RAM   ram-150:TARGET_ATT
0018     ()
0019     C.
0020     C.
0021     C. Set the dump memory area of TARGET_ATT
0022   +. DC 02-48 AOCU_DUMP_SET
0023     BC       (07 00 00 00 18 00)
0024     C.
0025     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026     C.
0027     C.
0028     C. Change the TLMFormatNo for the AOCS Dump Format
0029   +. DC 01-22 DHU_MODE_CHNG
0030     BC       (04 0b f8)
0031     C.
0032     C. Wait for AOCSDUMP to end
0033     C.
0034   . C. Check the dump memory
0035     C.
0036     C.                                      Result = OK [ ]
0037     C.
0038   +. DC 01-22 DHU_MODE_CHNG
0039     BC       (02 0a f8)
0040     C.
0041     C.                          <A_***>[TLM STS] FMT = 2 [ ]
0042     C.
0043   +. DC 02-8E AOCU_ORB_UPD
0044    . C.
0045   . C. ***** AOCS Commands (Orbital Element Update) *****
0046     C. Update the orbital element
0047   +. DC 02-50 AOCU_ORB_PRPGT_START
0048     BC       (16)
0049   +  DC 02-8E AOCU_ORB_UPD
0050     C.
0051     C.       <A_ORB>[ORBIT] EPC = 8263022.1 +- 1.0 (s) [ ]
0052     C.
0053   . C.
0054     C.
0055     C. ************ XRT START ************
0056     C.
0057   +. DC 07-F0 MDP_XRT_CTRL_MANU
0058     BC       (c1)
0059   +  DC 07-F0 MDP_XRT_MODE_STBY
0060     BC       (c3)
0061   . C. ------------ Success Verify ?             OK / NG____
0062     C.
0063     C. XRT Obs. Table Upload
0064   . S. RAM   ram-291:MDP_OBS_X
0065     ()
0066     C.
0067   +. DC 07-F0 MDP_DUMP_XRTTBL
0068     BC       (84 07 00 00 00 3a d4)
0069    . C. ------------ Comparison Check ?                        OK / ERR ____
0070     C.
0071     C.
0072   +. DC 07-F0 MDP_XRT_ROI_SET
0073     BC       (cd 01 b1 b1 04 04)
0074   +  DC 07-F0 MDP_XRT_ROI_SET
0075     BC       (cd 02 b1 b1 08 08)
0076   +  DC 07-F0 MDP_XRT_ROI_SET
0077     BC       (cd 03 b1 b1 08 08)
0078   +  DC 07-F0 MDP_XRT_ROI_SET
0079     BC       (cd 04 b1 b1 06 06)
0080   +  DC 07-F0 MDP_XRT_ROI_SET
0081     BC       (cd 06 80 80 20 04)
0082   +  DC 07-F0 MDP_XRT_ROI_SET
0083     BC       (cd 07 80 60 20 18)
0084   +  DC 07-F0 MDP_XRT_ROI_SET
0085     BC       (cd 08 a0 80 18 20)
0086   +  DC 07-F0 MDP_XRT_ROI_SET
0087     BC       (cd 09 80 80 20 20)
0088   +  DC 07-F0 MDP_XRT_ROI_SET
0089     BC       (cd 0a c0 c0 10 10)
0090   +  DC 07-F0 MDP_XRT_ROI_SET
0091     BC       (cd 0b 40 c0 10 10)
0092   +  DC 07-F0 MDP_XRT_ROI_SET
0093     BC       (cd 0c 40 40 10 10)
0094   +  DC 07-F0 MDP_XRT_ROI_SET
0095     BC       (cd 0d c0 40 10 10)
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0096  +  DC 07-F0 MDP_XRT_ROI_SET
0097     BC       (cd 0f 80 80 06 06)
0098  +  DC 07-F0 MDP_XRT_ROI_SET
0099     BC       (cd 10 80 80 04 04)
0100   . C. ---------- Success Verify ?            OK / NG ____
0101  +. DC 07-F0 MDP_XRT_QT_PROG_SET
0102     BC       (c4 12)
0103  +  DC 07-F0 MDP_XRT_ARS_DIS
0104     BC       (d5)
0105  +  DC 07-F0 MDP_XRT_FLD_DIS
0106     BC       (d9)
0107  +  DC 07-F0 MDP_XRT_FLRCTRL_DIS
0108     BC       (c9)
0109     C.
0110     C.
0111   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0112     C.
0113  +. DC 07-F0 MDP_XRT_MODE_OBSV
0114     BC       (c2)
0115  +. TI 2008-08-05 10:13:02.0
0116     DC 07-F0 MDP_XRT_MODE_OBSV
0117     BC       (c2)
0118  +. TI 2008-08-05 10:13:04.0
0119     DC 07-F0 MDP_XRT_CTRL_AUTO
0120     BC       (c0)
0121   . C. ---------- Success Verify ?            OK / NG ____
0122     C.
0123     C. ************ XRT END ***********
0124     C. ************************************************
0125     C.  START of XRT_CCD_HEATER_ON operation
0126     C. ************************************************
0127     C.
0128  +. DC 07-F0 MDP_XRT_CTRL_MANU
0129     BC       (c1)
0130     C. ---------- Success Verify ?           OK / NG;
0131     C.
0132  +. DC 04-BC TCIB_XRT_S_HTR_A_ENA
0133     C. ---------- Success Verify ?           OK / NG;
0134     C.
0135     C. ---------------------------------------
0136     C. If anomalous situation appeared, execute TCIB_XRT_S_HTR_A_DIS using DCBC-442 (line 24)
0137     C. ---------------------------------------
0138     C.
0139  +. DC 07-F0 MDP_XRT_QT_PROG_SET
0140     BC       (c4 12)
0141  +  DC 07-F0 MDP_XRT_FLD_DIS
0142     BC       (d9)
0143  +  DC 07-F0 MDP_XRT_FLRCTRL_DIS
0144     BC       (c9)
0145  +  DC 07-F0 MDP_XRT_ARS_DIS
0146     BC       (d5)
0147     C. ---------- Success Verify ?            OK / NG ____
0148     C.
0149     C.
0150     C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0151     C.
0152  +. DC 07-F0 MDP_XRT_CTRL_AUTO
0153     BC       (c0)
0154     C. ************************************************
0155     C.  END of XRT_CCD_HEATER_ON operation
0156     C. ************************************************
0157     C.
0158     C.
0159     C.
0160   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0161     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0162   . S. DC-BC dcbc-402:DCBC
0163     (MDP_known_event)
0164     C.
0165     C.
0166   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0167   . S. DC-BC dcbc-153:DCBC
0168     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0169     C.
0170     C.
0171   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0172     C.
0173   . C. *********** LOS ***********
0174     C.
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(a) Spacecraft Operation Procedure (real-commands)

main-050 2008-08-05 17:28:53 209 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇÎ»þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0099  +. DC 07-FC EIS_MODE_MANU
0100     BC       (21 02)
0101   . C. Verify EIS in MANUAL mode
0102   . C. Estimated OBSTBL upload time is 22s
0103     C. ********************************
0104     C. EIS START OBSTBL LOAD
0105     C. ********************************
0106   . S. RAM   ram-820:EIS_OBSTBL
0107     ()
0108  +. DC 07-FC EIS_DUMP_OBSTBL
0109     BC       (07 07 07 00 00 70 00)
0110     C.
0111     C. Execute, after the success of OBSTBL upload.
0112     C. Set EIS TI-commands
0113  +. TI 2008-08-05 10:13:50.0
0114     DC 07-FC EIS_MODE_CHG_ENA
0115     BC       (20)
0116   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0117     C. ********************************
0118     C. EIS END OBSTBL LOAD
0119     C. ********************************
0120   . C. ********************************
0121     C.  SOT table upload
0122     C. ********************************
0123   . C. < Stop FG table >
0124  +. DC 07-F0 MDP_FG_CTRL_MANU
0125     BC       (51)
0126   . C. ------------------------------------
0127     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0128     C. ------------------------------------
0129     C.
0130   . C. <Upload FG Observation Table>
0131   . S. RAM   ram-266:MDP_OBS_F
0132     ()
0133     C.
0134   . C. < Dump RAMID=MDP_OBS_F >
0135  +. DC 07-F0 MDP_DUMP_FGTBL
0136     BC       (82 07 00 00 00 38 b8)
0137     C. ------------------------------------
0138     C.  MDP_OBS_F verify = OK/NG        [ ]
0139     C. ------------------------------------
0140     C.
0141   . C. < Stop SP table >
0142  +. DC 07-F0 MDP_SP_CTRL_MANU
0143     BC       (61)
0144     C. ------------------------------------
0145     C.  MDP_SP_CTRL_MODE      = MANU    [ ]
0146     C. ------------------------------------
0147     C.
0148   . C. <Upload SP Observation Table>
0149   . S. RAM   ram-288:MDP_OBS_S
0150     ()
0151     C.
0152   . C. < Dump RAMID=MDP_OBS_S >
0153  +. DC 07-F0 MDP_DUMP_SPTBL
0154     BC       (83 07 00 00 00 38 b8)
0155     C. ------------------------------------
0156     C.  MDP_OBS_S verify = OK/NG        [ ]
0157     C. ------------------------------------
0158     C.
0159   . C. < Upload DPL table >
0160     C.
0161     C. ¥¢¥Ã¥×¥í¡¼¥É¤ÎÁ°¤ËSTS_CHK¤òOFF¤Ë¤¹¤ë
0162     C.
0163   . S. RAM   ram-271:MDP_DPL
0164     ()
0165     C.
0166   . C. < Dump RAMID=MDP_DPL >
0167  +. DC 07-F0 MDP_DUMP_FGTBL
0168     BC       (82 07 00 38 b8 00 40)
0169     C. ------------------------------------
0170     C.  MDP_DPL verify          = OK    [ ]
0171     C. ------------------------------------
0172     C.
0173     C. STS_CHK¤òON¤Ë¤¹¤ë
0174     C.
0175   . C. < Update MDP DSC PAR1 >
0176  +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0177     BC       (4c)
0178     C.  MDP_CMD_CODE          = F04C0700[ ]
0179     C.  MDP_CMD_CNT        (count-up 1) [ ]
0180     C. ------------------------------------
0181     C.
0182   . C.
0183     C. ********************************
0184     C.  SOT TI command set
0185     C. ********************************
0186     C. Execute, after the success of TBL upload.
0187  +. TI 2008-08-05 10:13:18.0
0188     DC 07-F0 MDP_SOT_MODE_OBSV
0189     BC       (40)
0190   . C. ------------------------------------
0191     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0192     C. ------------------------------------
0193     C.
```

```
0194     C.
0195   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0196     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0197   . S. DC-BC dcbc-402:DCBC
0198     (MDP_known_event)
0199     C.
0200     C.
0201   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0202   . S. DC-BC dcbc-153:DCBC
0203     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0204     C.
0205     C.
0206   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0207     C.
0208   . C. ********** LOS **********
0209     C.
```

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*** OP Sequence for XRT ***

2008/08/05  10:23:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/05  10:24:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                    AOCU_NM              5 02-76 01 00 00 00 00
2008/08/05  10:38:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/05  10:38:02.0  XRT_QT_PROG_SET_424_OG [0x1a8]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 12
2008/08/05  10:38:04.0  XRT_ARS_DIS_422_OG [0x1a6]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2008/08/05  10:38:06.0  XRT_FLD_DIS_445_OG [0x1bd]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2008/08/05  10:38:08.0  XRT_FLRCTRL_DIS_416_OG [0x1a0]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/08/05  10:38:10.0  XRT_CTRL_AUTO_403_OG [0x193]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/08/05  12:58:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/05  13:00:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/05  13:00:02.0  XRT_QT_PROG_SET_407_OG [0x197]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 09
2008/08/05  13:00:04.0  XRT_ARS_DIS_422_OG [0x1a6]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2008/08/05  13:00:06.0  XRT_FLD_DIS_445_OG [0x1bd]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2008/08/05  13:00:08.0  XRT_FLRCTRL_DIS_416_OG [0x1a0]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/08/05  13:00:10.0  XRT_CTRL_AUTO_403_OG [0x193]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/08/05  15:46:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/05  16:09:30.0  XRT_Custom_430_OG [0x1ae]
2008/08/05  16:10:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/08/05  17:22:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/05  17:46:30.0  XRT_Custom_430_OG [0x1ae]
2008/08/05  17:47:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/08/05  19:00:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/05  19:23:00.0  XRT_Custom_430_OG [0x1ae]
2008/08/05  19:24:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/08/05  20:37:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/05  21:00:30.0  XRT_Custom_430_OG [0x1ae]
2008/08/05  21:01:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/08/05  22:14:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/05  22:30:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                    AOCU_NM              5 02-76 00 ae a8 00 00
2008/08/05  22:36:30.0  XRT_Custom_430_OG [0x1ae]
2008/08/05  22:37:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/08/05  23:50:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                    AOCU_NM              5 02-76 00 00 00 ab 8e
2008/08/05  23:52:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/06  00:04:30.0  XRT_Custom_430_OG [0x1ae]
2008/08/06  00:05:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/08/06  01:20:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/06  01:35:00.0  XRT_Custom_430_OG [0x1ae]
2008/08/06  01:36:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/08/06  02:51:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/06  04:00:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/06  04:00:02.0  XRT_TCIB_XRT_S_HTR_A_DIS_433_OG [0x1b1]
                                    TCIB_XRT_S_HTR_A_DIS 0 04-C0
2008/08/06  04:10:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/06  05:43:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                    AOCU_NM              5 02-76 02 00 00 00 00
2008/08/06  12:00:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                    AOCU_NM              5 02-76 00 ac 00 00 00
2008/08/06  12:14:30.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/06  12:14:32.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2008/08/06  12:14:52.0  XRT_QT_PROG_SET_431_OG [0x1af]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 06
2008/08/06  12:14:54.0  XRT_FLD_DIS_419_OG [0x1a3]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2008/08/06  12:14:56.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
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2008/08/06  12:14:58.0  XRT_ARS_DIS_418_OG [0x1a2]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2008/08/06  12:15:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/08/06  14:00:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/06  15:00:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM              5 02-76 00 00 00 54 00
2008/08/06  15:14:30.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2008/08/06  15:14:50.0  XRT_QT_PROG_SET_400_OG [0x190]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 01
2008/08/06  15:14:54.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2008/08/06  15:14:56.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/08/06  15:14:58.0  XRT_ARS_DIS_418_OG [0x1a2]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2008/08/06  15:15:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/08/06  17:00:00.5  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/06  18:22:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/06  18:22:56.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2008/08/06  18:23:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                AOCU_NM              5 02-76 00 00 00 00 00
2008/08/06  18:23:16.0  XRT_QT_PROG_SET_409_OG [0x199]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2008/08/06  18:23:18.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2008/08/06  18:23:20.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/08/06  18:23:22.0  XRT_ARS_DIS_427_OG [0x1ab]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2008/08/06  18:25:00.0  XRT_CTRL_AUTO_444_OG [0x1bc]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/08/06  18:32:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/06  18:32:55.0  XRT_CTRL_MANU_405_OG [0x195]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/06  18:33:00.0  AOCS_ORe-point_Start_8_OG [0x09e]
                                AOCU_NM              5 02-76 00 2e f9 2e f9
2008/08/06  18:35:33.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2008/08/06  18:35:53.0  XRT_QT_PROG_SET_411_OG [0x19b]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0e
2008/08/06  18:35:55.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2008/08/06  18:35:57.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/08/06  18:35:59.0  XRT_ARS_DIS_418_OG [0x1a2]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2008/08/06  18:36:01.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/08/06  18:42:54.0  XRT_CTRL_MANU_405_OG [0x195]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/06  18:43:00.0  AOCS_ORe-point_Start_9_OG [0x09f]
                                AOCU_NM              5 02-76 00 2e f9 d1 07
2008/08/06  18:45:32.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2008/08/06  18:45:52.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 14
2008/08/06  18:45:54.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2008/08/06  18:45:56.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/08/06  18:45:58.0  XRT_ARS_DIS_418_OG [0x1a2]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2008/08/06  18:46:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/08/06  18:52:54.0  XRT_CTRL_MANU_405_OG [0x195]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/06  18:53:00.0  AOCS_ORe-point_Start_10_OG [0x0a0]
                                AOCU_NM              5 02-76 00 d1 07 d1 07
2008/08/06  18:55:32.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2008/08/06  18:55:52.0  XRT_QT_PROG_SET_425_OG [0x1a9]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2008/08/06  18:55:54.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2008/08/06  18:55:56.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/08/06  18:55:58.0  XRT_ARS_DIS_418_OG [0x1a2]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2008/08/06  18:56:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/08/06  19:02:54.0  XRT_CTRL_MANU_405_OG [0x195]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/06  19:03:00.0  AOCS_ORe-point_Start_11_OG [0x0a1]
                                AOCU_NM              5 02-76 00 d1 07 2e f9
2008/08/06  19:05:32.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
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                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2008/08/06  19:05:52.0  XRT_QT_PROG_SET_436_OG [0x1b4]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2008/08/06  19:05:54.0  XRT_FLD_DIS_419_OG [0x1a3]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2008/08/06  19:05:56.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/08/06  19:05:58.0  XRT_ARS_DIS_418_OG [0x1a2]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2008/08/06  19:06:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/08/06  19:12:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/06  23:15:00.0  AOCS_ORe-point_Start_12_OG [0x0a2]
                                    AOCU_NM              5 02-76 00 54 00 00 00
2008/08/07  00:15:00.0  AOCS_ORe-point_Start_13_OG [0x0a3]
                                    AOCU_NM              5 02-76 04 00 00 00 00
2008/08/07  05:54:24.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/07  05:54:26.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2008/08/07  05:54:30.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2008/08/07  05:54:46.0  XRT_QT_PROG_SET_429_OG [0x1ad]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2008/08/07  05:54:48.0  XRT_FLD_DIS_419_OG [0x1a3]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2008/08/07  05:54:50.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/08/07  05:54:52.0  XRT_ARS_DIS_427_OG [0x1ab]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2008/08/07  05:56:30.0  XRT_CTRL_AUTO_444_OG [0x1bc]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/08/07  06:04:24.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/07  06:04:30.0  AOCS_ORe-point_Start_14_OG [0x0a4]
                                    AOCU_NM              5 02-76 03 00 00 00 00
2008/08/07  10:01:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                    AOCU_NM              5 02-76 00 00 00 00 00
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