

XRT Timeline to be uploaded on 2008/08/07

Period: 2008/08/07 09:51:00 - 2008/08/12 10:45:00

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Normal mode

* * * * *

XOB #15AE: CH - Al/Mesh (16s) + Ti/Poly (32s) - 512x512 - Q95 - with half-res. full frame													
Term	Pointing (x, y)	Comment											
08/07 12:02:00 - 08/07 12:59:54	Track (80.3, -157.3) @ 08/07 10:01:00	# OP start + 10min, first pointing for HOP 76 (officially 12 - 15 UT), equatorial CH.											
08/07 13:02:00 - 08/07 13:59:54	Track (-92.4, -367.1) @ 08/07 13:00:00	* HOP 76, cont. with new pointing inb CH.											
08/07 14:02:00 - 08/07 15:00:00	Track (-402.0, -446.6) @ 08/07 14:00:00	* HOP76, cont., new pointing in ondisk CH.											
PROG= 15 Inf.-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 87 1-time(s) 4.0sec													
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Seqn= 72 1-time(s) 2.0sec													
└─ Open/Al-mesh Open/Al-mesh close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─ Seqn= 76 1-time(s) 4.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Subr= 2 3-time(s) 1200.0sec													
└─ Seqn= 79 1-time(s) 4.0sec													
└─ Open/Al-mesh Open/Al-mesh close Safe Dark 16.0s Obs 1x1 512x512 (1024, 1024) Q=98 0 0 2.0sec													
└─ Seqn= 2 10-time(s) 120.0sec													
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 16.0s Obs 1x1 512x512 (1024, 1024) Q=95 0 0 0.5sec													
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 32.0s Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec													
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #15A3: Synoptic Q95 2x2 - Al/poly(512/5795) + Dark cal(512 Q98) + Ti-poly(723/11571) + G-band(16)													
Term	Pointing (x, y)	Comment											
08/07 18:02:00 - 08/07 18:09:54	Fixed (0.0, 0.0)	synoptic, and Sun-center pointing; conserve memory until next HOP.											
08/08 18:02:00 - 08/08 18:09:54	Fixed (0.0, 0.0)	synoptic, and Sun-center pointing; conserve memory until next HOP.											
PROG= 11 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 18 1-time(s) 4.0sec													
└─ Al-poly/Open Al-poly/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Seqn= 72 1-time(s) 2.0sec													
└─ Open/Al-mesh Open/Al-mesh close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─ Seqn= 76 1-time(s) 4.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Seqn= 92 1-time(s) 2.0sec													
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #158D: Dither Pointing Q1 5x-ray NO GBAND-long exposure													
Term	Pointing (x, y)	Comment											
08/08 02:55:00 - 08/08 02:59:54	Fixed (-528.4, -528.4)	* XRT dither obs.											
08/08 03:03:00 - 08/08 03:07:54	Fixed (-541.8, -505.7)	* Cont.											
08/08 03:11:00 - 08/08 03:15:54	Fixed (-553.2, -539.7)	* Cont.											
PROG= 05 1-time(s)													
└─ Subr= 1 1-time(s) 30.0sec													
└─ Seqn= 97 1-time(s) 30.0sec													
└─ thin-Be/Open thin-Be/Open close Safe Norm 64.0s Obs 1x1 1024x1024 (1536, 1536) DPCM 0 0 6.0sec													
└─ C-poly/Open thin-Be/Open close Safe Norm 16.0s Obs 1x1 1024x1024 (1536, 1536) DPCM 0 0 6.0sec													
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 16.0s Obs 1x1 1024x1024 (1536, 1536) DPCM 0 0 6.0sec													
└─ Al-poly/Open Al-poly/Open close Safe Norm 11.3s Obs 1x1 1024x1024 (1536, 1536) DPCM 0 0 6.0sec													
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 5.66s Obs 1x1 1024x1024 (1536, 1536) DPCM 0 0 6.0sec													
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #158E: Dither Pointing Q2 5x-ray NO GBAND-long exposure													
Term	Pointing (x, y)	Comment											
08/08 03:19:00 - 08/08 03:23:54	Fixed (528.4, -528.4)	* Cont.											
08/08 03:27:00 - 08/08 03:31:54	Fixed (523.1, -516.8)	* Cont.											
08/08 03:35:00 - 08/08 03:40:00	Fixed (531.5, -554.2)	* Last dither pointing for this period.											
PROG= 02 1-time(s)													
└─ Subr= 1 1-time(s) 30.0sec													
└─ Seqn= 61 1-time(s) 30.0sec													
└─ thin-Be/Open thin-Be/Open close Safe Norm 64.0s Obs 1x1 1024x1024 (512, 1536) DPCM 0 0 6.0sec													
└─ C-poly/Open thin-Be/Open close Safe Norm 16.0s Obs 1x1 1024x1024 (512, 1536) DPCM 0 0 6.0sec													
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 16.0s Obs 1x1 1024x1024 (512, 1536) DPCM 0 0 6.0sec													
└─ Al-poly/Open Al-poly/Open close Safe Norm 11.3s Obs 1x1 1024x1024 (512, 1536) DPCM 0 0 6.0sec													
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 5.66s Obs 1x1 1024x1024 (512, 1536) DPCM 0 0 6.0sec													
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #15A4: Synoptic Q95 2x2 - Al/mesh(512/5795) + Dark cal(512 Q98) + Ti-poly(723/11571) + G-band(16)												
Term		Pointing (x, y)				Comment						
08/08 06:15:00 - 08/08 06:22:54		Fixed (0.0, 0.0)				synoptic, shifted 13.0 min						
08/09 05:48:30 - 08/09 05:56:24		Fixed (0.0, 0.0)				synoptic, shifted -13.5 min						
PROG= 12 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 87 1-time(s) 4.0sec												
└─┬─ Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ Open/Al-mesh Open/Ti-poly close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 72 1-time(s) 2.0sec												
└─┬─ Open/Al-mesh Open/Al-mesh close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 76 1-time(s) 4.0sec												
└─┬─ Open/Ti-poly Open/thick-Al close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ Open/Ti-poly Open/thick-Al close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 92 1-time(s) 2.0sec												
└─┬─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #15AF: XBP Q90 Al/poly (AEC1) + Ti/poly (AEC1) + Thin-Be (AEC0)-high cadence												
Term		Pointing (x, y)				Comment						
08/08 22:52:00 - 08/09 01:00:00		Fixed (950.0, 0.0)				* HOP 75, west-limb spicules (Hida).						
PROG= 16 Inf.-time(s)												
└─ Subr= 1 1-time(s) 400.0sec												
└─ Seqn= 21 10-time(s) 40.0sec												
└─┬─ Al-poly/Open C-poly/Open close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=90 1 0 2.0sec												
└─┬─ Open/Ti-poly Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=90 1 0 2.0sec												
└─ Subr= 2 1-time(s) 4.0sec												
└─ Seqn= 43 1-time(s) 4.0sec												
└─┬─ thin-Be/Open thin-Be/Open close Safe Norm 64.0s Obs 1x1 384x384 (1024, 1024) Q=90 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

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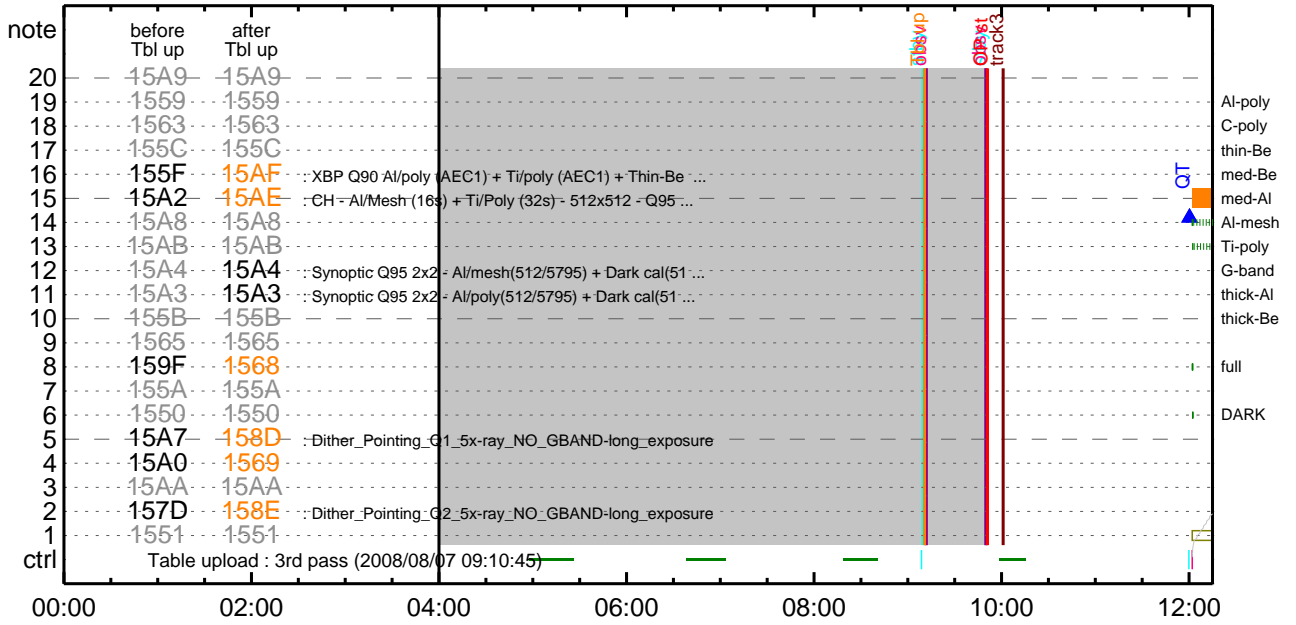
* * * * *
                                Flare mode
                                * * * * *
                                NOT USED

* * * * *
                                Active Region Search
                                * * * * *
                                NOT USED

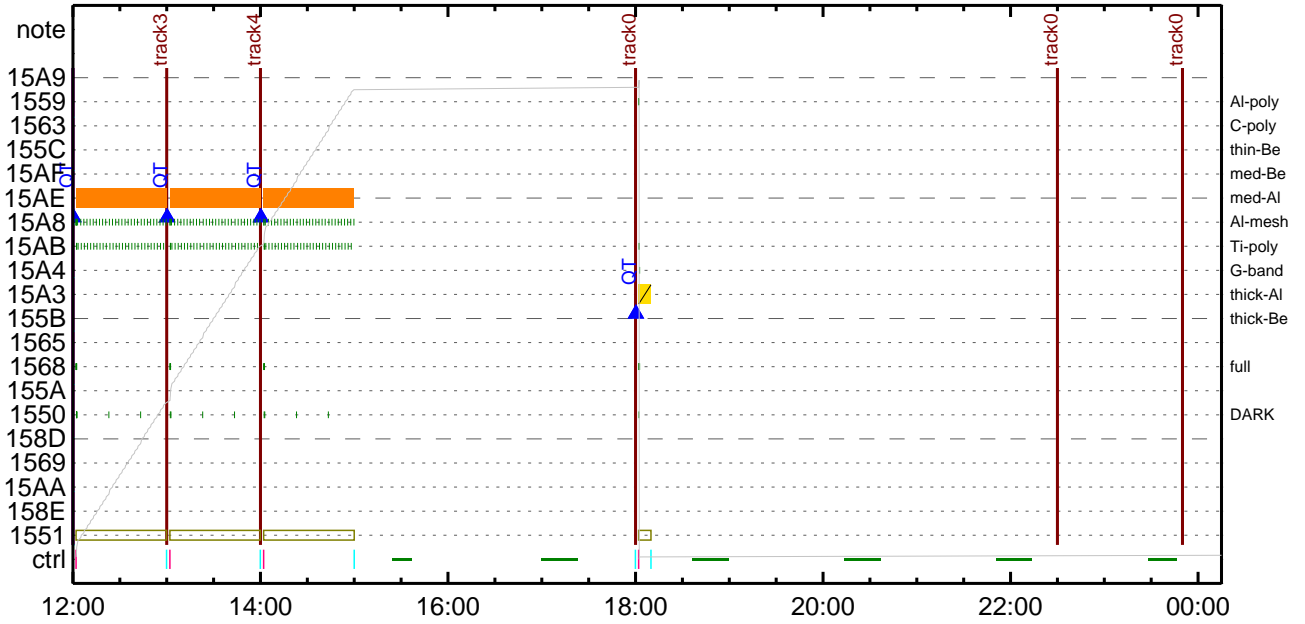
* * * * *
                                Flare Detection
                                * * * * *
                                NOT USED

```

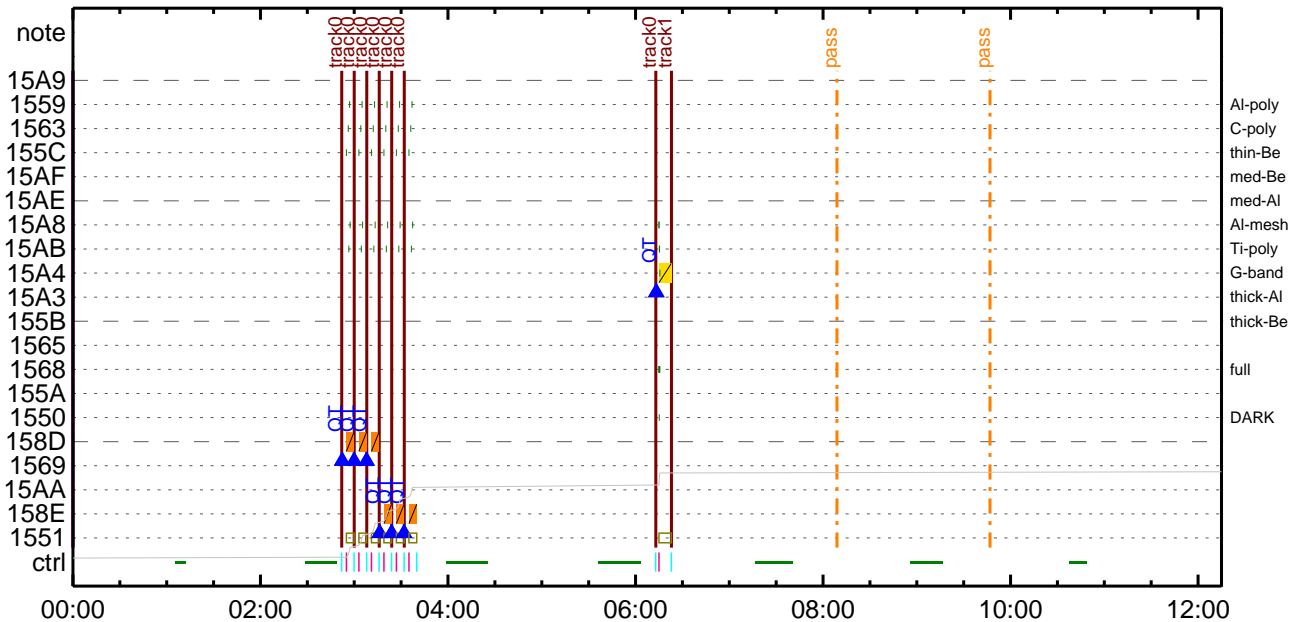
CMDI #0074 2008/08/07



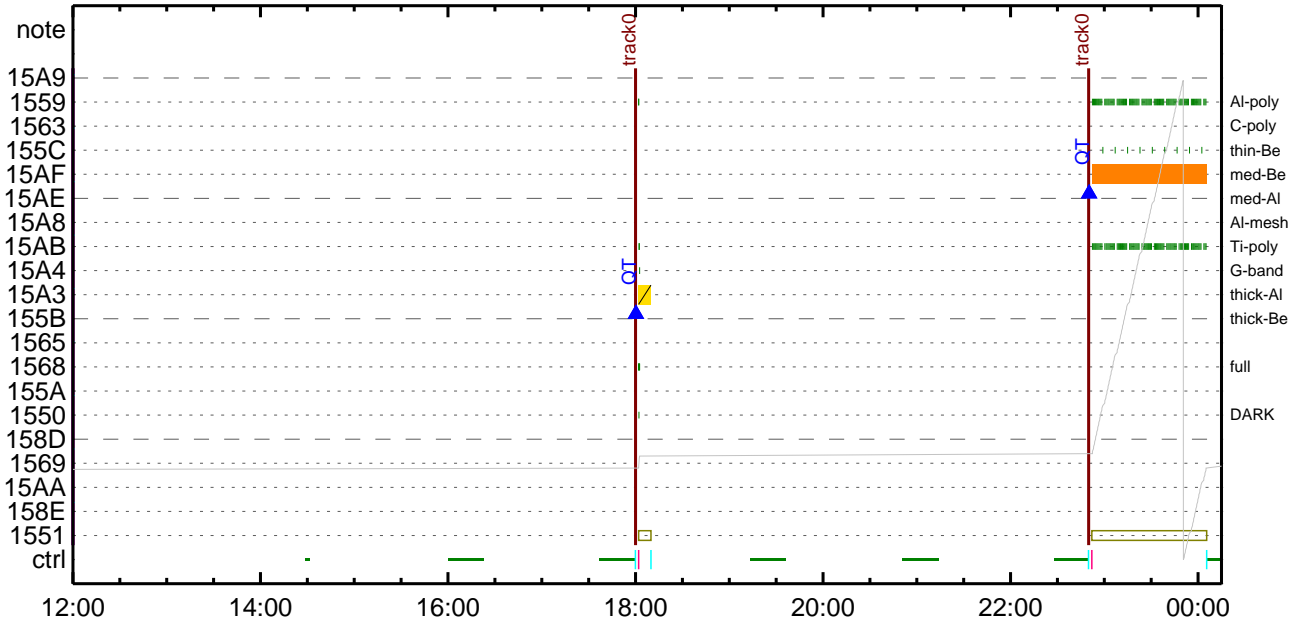
CMDI #0074 2008/08/07



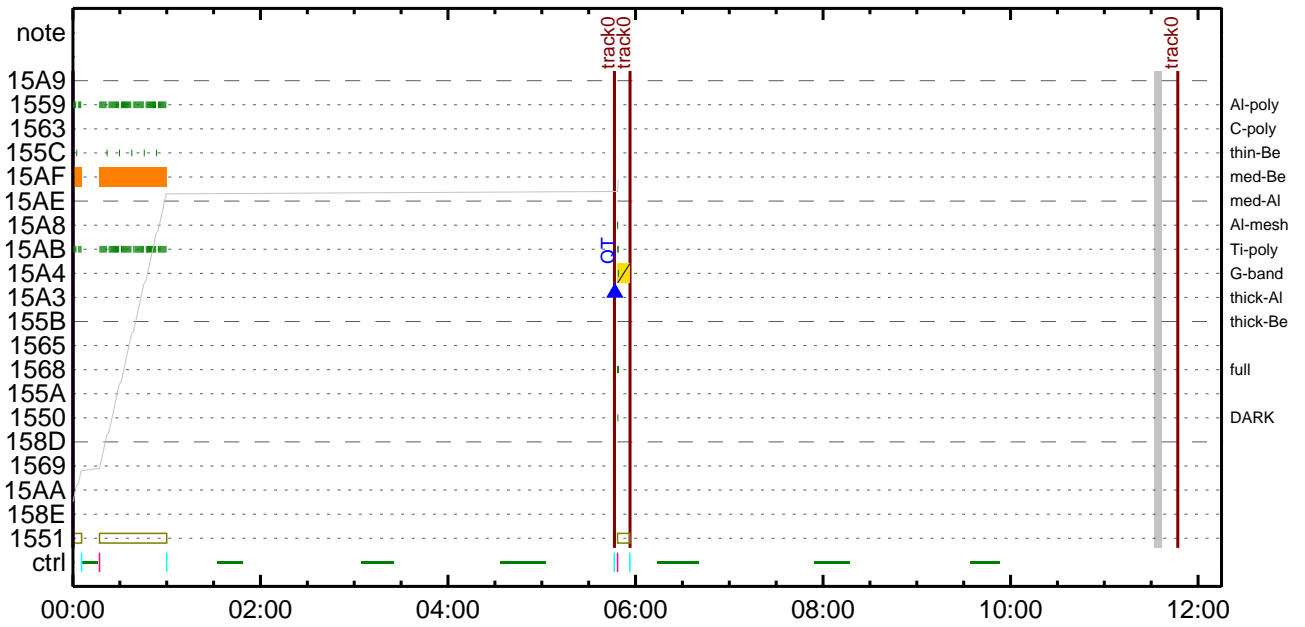
CMDI #0074 2008/08/08



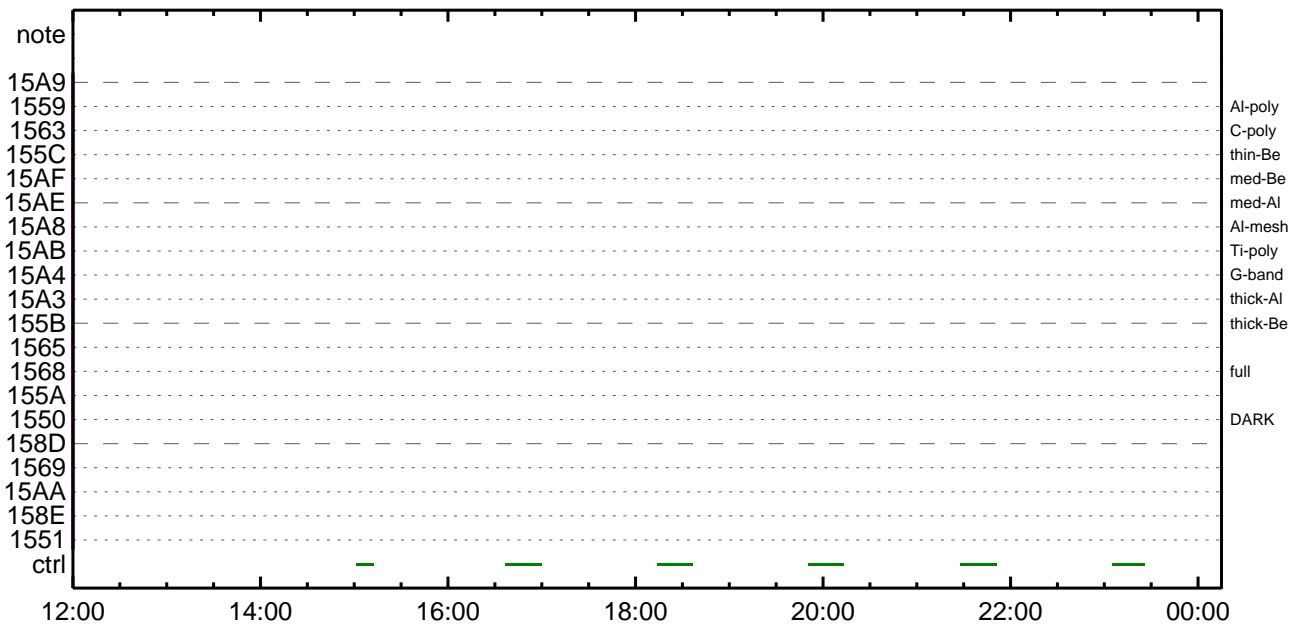
CMDI #0074 2008/08/08



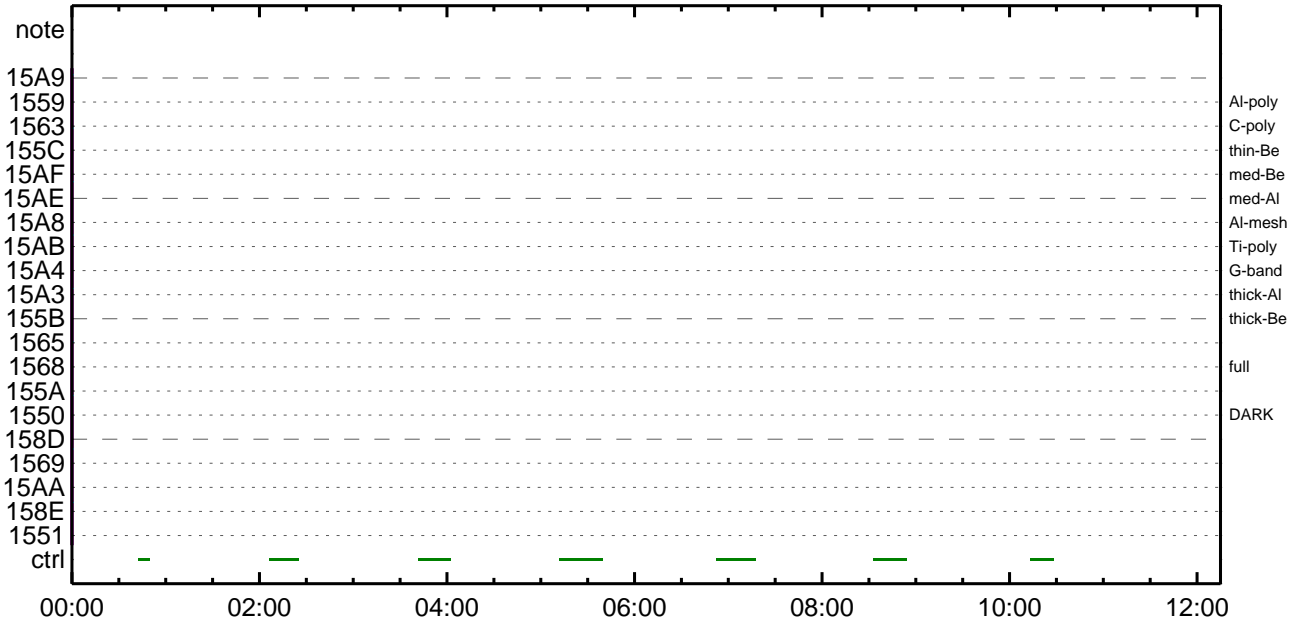
CMDI #0074 2008/08/09



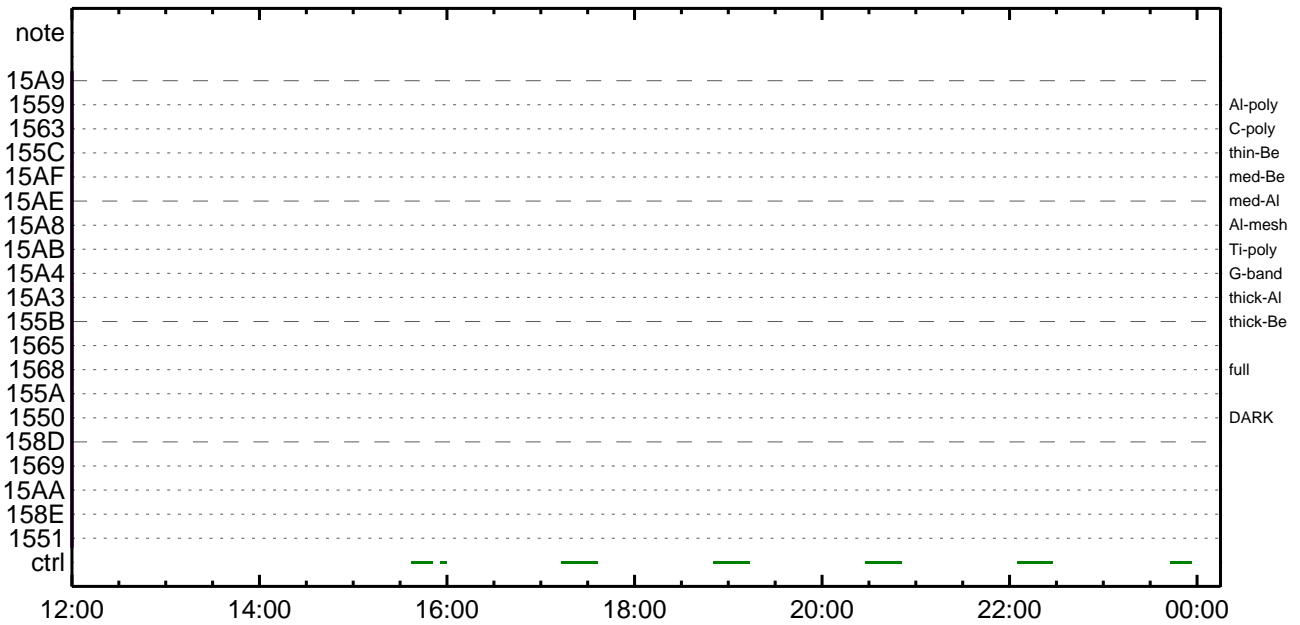
CMDI #0074 2008/08/09



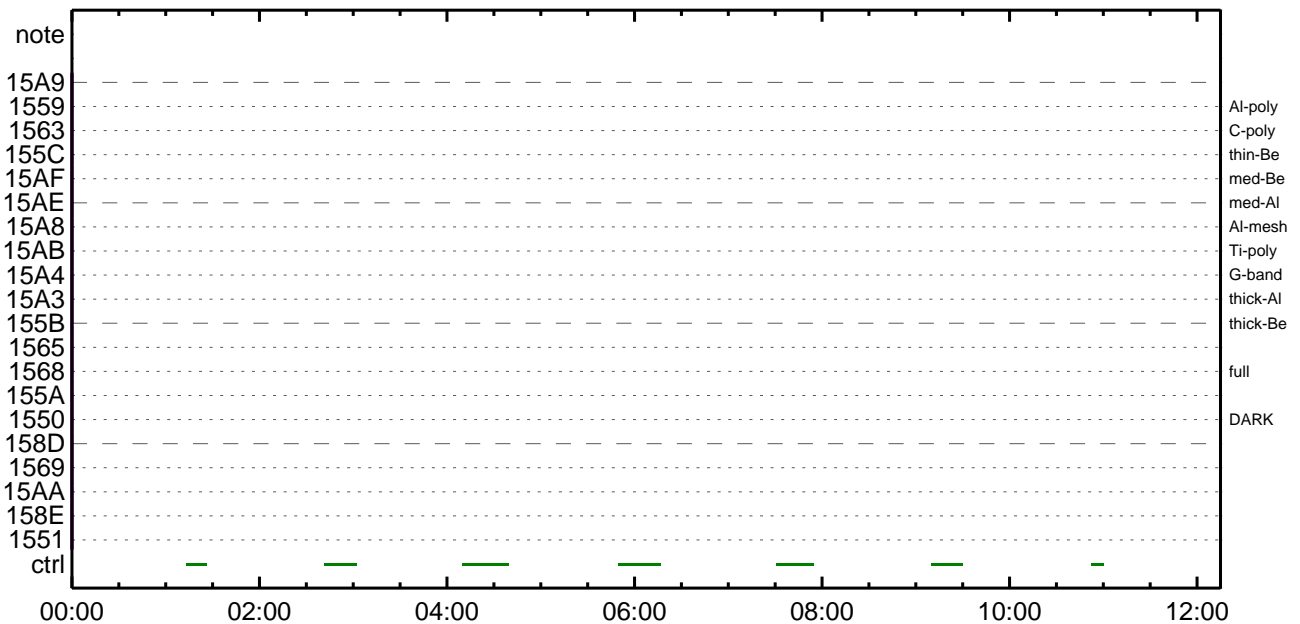
CMDI #0074 2008/08/10



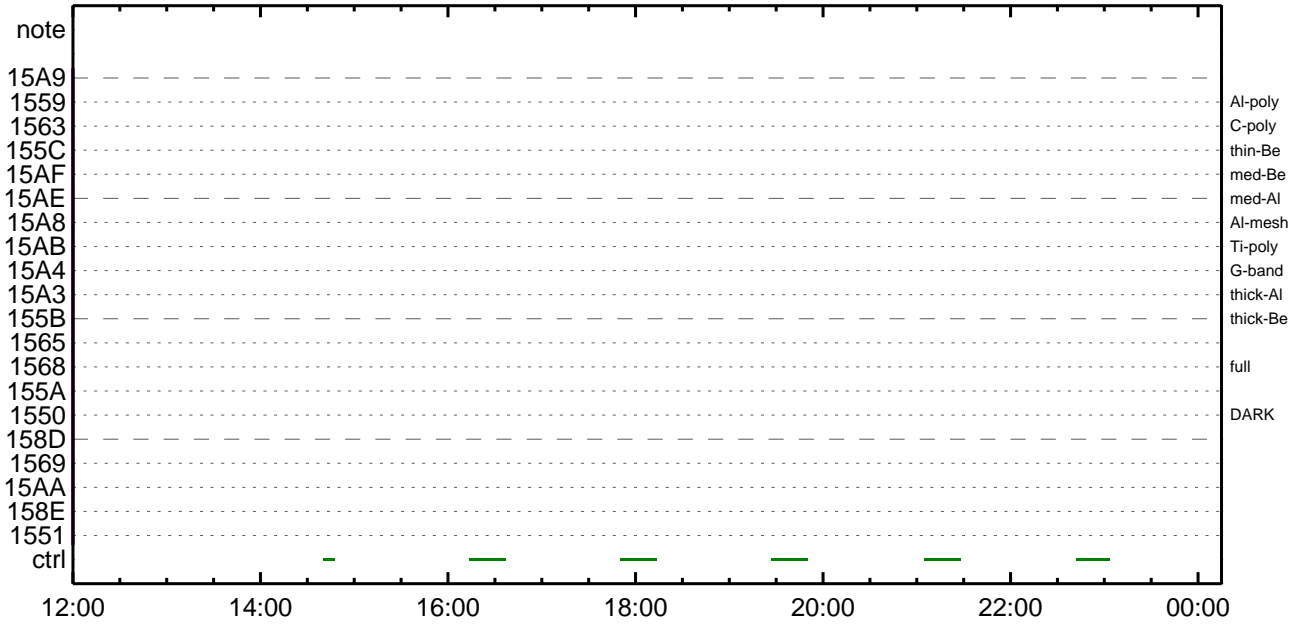
CMDI #0074 2008/08/10



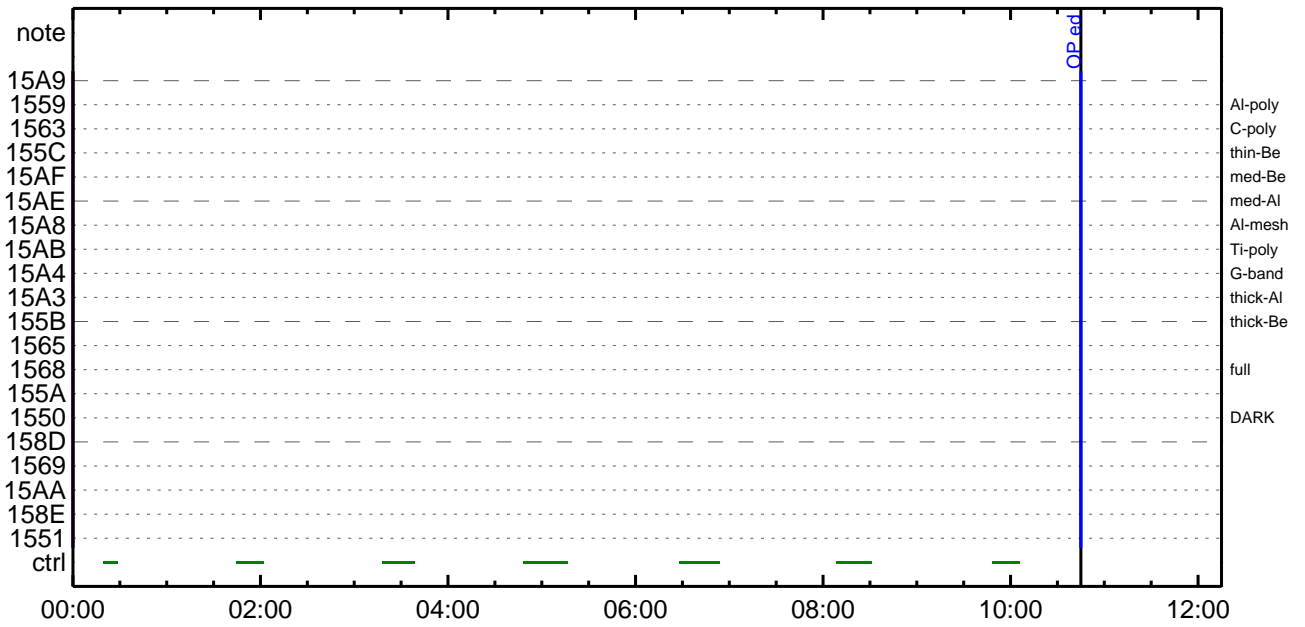
CMDI #0074 2008/08/11



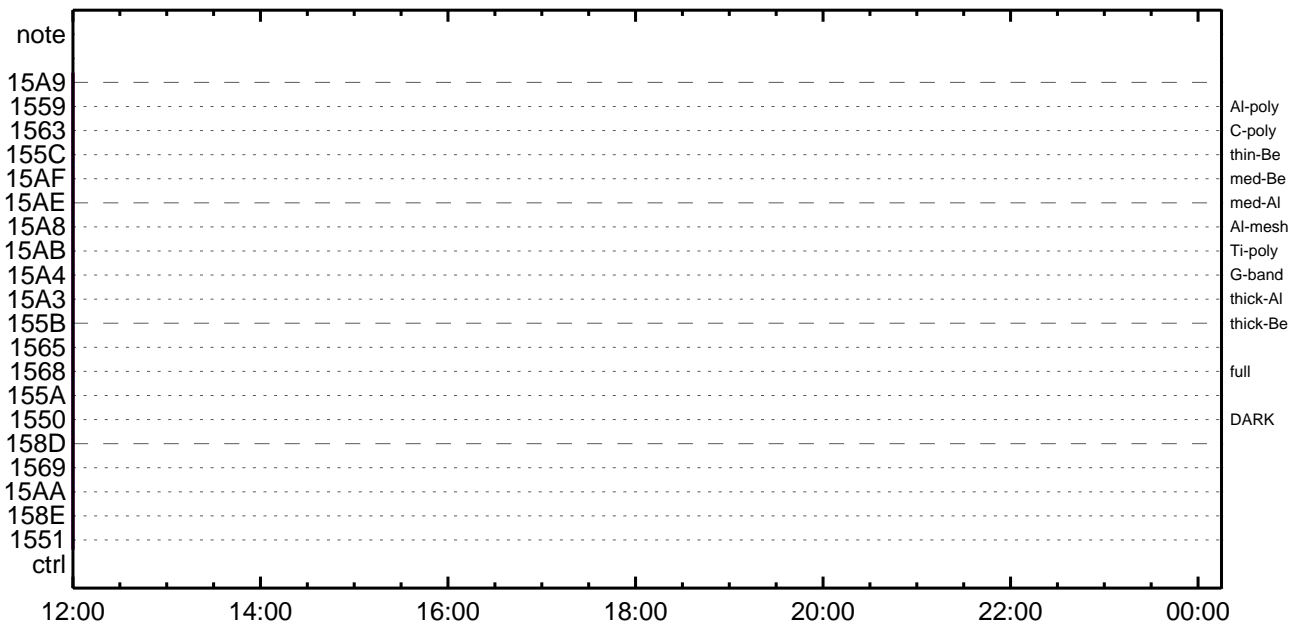
CMDI #0074 2008/08/11



CMDI #0074 2008/08/12



CMDI #0074 2008/08/12



(a) Spacecraft Operation Procedure (real-commands)

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main-054 2008-08-07 12:10:54 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY^-¼Á»Ü;ä
0005 C.
0006 C. YÀYË;¼Y³YFÿóYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ÁÁµ°È»Í×ÁÇ¿ÍYÇYÁY×Yí;¼YÉ;ÈÈ¿µ•íÉ;ÈÈ¼°ÇÓµ•µ¿¼l¹¿µÍ;ÇÀ®, ù¹µÈµµÇÁ+¿®µ•µÈµµ³µÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ;|YÁYóY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ä
0019 . S. OP op-054:OP
0020 ()
0021 . S. OG og-054:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPí°èYÁYóY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C.             ¿¿[HK1_DMP_TOP_ADRS_1]          EQ         40
0029 C.             ¿¿[HK1_DMP_TOP_ADRS_0]          EQ           0
0030 C.             ¿¿[HK1_DMP_BLOCK_NUM]           EQ        127
0031 C.             ¿¿[HK1_DMP_REPEAT_NUM]         EQ           0
0032 C.             ¿¿[HK1_DMA_DMP_PIM]            EQ         DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C.             ¿¿[HK1_PKT_FORM_NO]             EQ           7
0036 C.             ¿¿[HK1_PKT_GEN_TIME]            EQ        0.25 s
0037 C.             ¿¿[HK1_S_TLM_BIT_RATE]          EQ        32k
0038 C.             ¿¿[HK1_X_TLM_BIT_RATE]          EQ         4M
0039 C.             ¿¿[HK1_DMP_CHK_FLG]             EQ        EXEC
0040 . C. YÀYóY×¼ªâ¿µð¿ÍÇ§
0041 C.             ¿¿[HK1_DMP_CHK_FLG]            EQ        NON
0042 . C. RAM ID=NMOGµÍ¼È¹¿•ë²ÍOKµð¿ÍÇ§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C.             ¿¿[HK1_DMP_TOP_ADRS_1]          EQ         41
0048 C.             ¿¿[HK1_DMP_TOP_ADRS_0]          EQ           0
0049 C.             ¿¿[HK1_DMP_BLOCK_NUM]           EQ        127
0050 C.             ¿¿[HK1_DMP_REPEAT_NUM]         EQ           0
0051 C.             ¿¿[HK1_DMA_DMP_PIM]            EQ         DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C.             ¿¿[HK1_PKT_FORM_NO]             EQ           7
0055 C.             ¿¿[HK1_PKT_GEN_TIME]            EQ        0.25 s
0056 C.             ¿¿[HK1_S_TLM_BIT_RATE]          EQ        32k
0057 C.             ¿¿[HK1_X_TLM_BIT_RATE]          EQ         4M
0058 C.             ¿¿[HK1_DMP_CHK_FLG]             EQ        EXEC
0059 . C. YÀYóY×¼ªâ¿µð¿ÍÇ§
0060 C.             ¿¿[HK1_DMP_CHK_FLG]            EQ        NON
0061 . C. RAM ID=NMOGµÍ¼È¹¿•ë²ÍOKµð¿ÍÇ§
0062 C.
0063 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C.             ¿¿[HK1_DMP_TOP_ADRS_1]          EQ         42
0067 C.             ¿¿[HK1_DMP_TOP_ADRS_0]          EQ           0
0068 C.             ¿¿[HK1_DMP_BLOCK_NUM]           EQ         65
0069 C.             ¿¿[HK1_DMP_REPEAT_NUM]         EQ           0
0070 C.             ¿¿[HK1_DMA_DMP_PIM]            EQ         DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C.             ¿¿[HK1_PKT_FORM_NO]             EQ           7
0074 C.             ¿¿[HK1_PKT_GEN_TIME]            EQ        0.25 s
0075 C.             ¿¿[HK1_S_TLM_BIT_RATE]          EQ        32k
0076 C.             ¿¿[HK1_X_TLM_BIT_RATE]          EQ         4M
0077 C.             ¿¿[HK1_DMP_CHK_FLG]             EQ        EXEC
0078 . C. YÀYóY×¼ªâ¿µð¿ÍÇ§
0079 C.             ¿¿[HK1_DMP_CHK_FLG]            EQ        NON
0080 . C. RAM ID=NMOG, RAM ID=OPµÍ¼È¹¿•ë²ÍOKµð¿ÍÇ§
0081 C.
0082 . C. ***** °È²¼µí¼Á´¶Í°µÈÈ-µ°Á+¿® (¼åµ-YÁYóY×¼µ¿µµðÁÖÁæµÇª°µ°-µ¿¼í¹¿µÇµâ) *****
0083 C. DHUÝä;¼YÉ;È¼Y¼;Yí;¼YÉ;ÈµðÍáµ¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C.             ¿¿[HK1_PKT_FORM_NO]             EQ           2
0087 C.             ¿¿[HK1_PKT_GEN_TIME]            EQ        0.5S
0088 C.             ¿¿[HK1_S_TLM_BIT_RATE]          EQ        32k
0089 C.             ¿¿[HK1_X_TLM_BIT_RATE]          EQ         4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;§ OPOG UPLOADµ-Á+¿®NGµÍ¼¹¿¿;¿°È²¼µí¼TI-CMDÁ+¿®µí¼Á¹Ôµ•µÈµµ³µÈ;£

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0096 C.      SET EDUMP I±°iYÑY¹aÇ¹Ôa|a³aE;f
0097 C.
0098 C.      TIY³YFÿYÖYÉaòdÀDî¿ (UT)
0099 +. TI 2008-08-07 09:46:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.      çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0102 C.
0103 +. TI 2008-08-07 09:46:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.      çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0106 C.
0107 +. TI 2008-08-07 09:46:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.      çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0110 C.
0111 +. TI 2008-08-07 09:50:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.      çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0114 C.
0115 C.      °È²¼aîÃè%îíÑaîYÁY§YÁY-¹àîÛ
0116 C.      çç[HK1_TI_CMD_ENA/DIS] EQ ENA
0117 C.      çç[HK1_TI_CMD_NUM] EQ 4
0118 C.      çç[HK1_NEXT_EXEC_PIM] EQ DHU
0119 C.      çç[HK1_NEXT_EXEC_DC] EQ 0xB3
0120 C.
0121 C.      *****
0122 C.      TIîî°èYÁYÖY×
0123 C.      *****
0124 C.
0125 C.      TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.      çç[HK1_DMP_TOP_ADRS_1] EQ 07
0129 C.      çç[HK1_DMP_TOP_ADRS_0] EQ 2B
0130 C.      çç[HK1_DMP_BLOCK_NUM] EQ 3
0131 C.      çç[HK1_DMP_REPEAT_NUM] EQ 0
0132 C.      çç[HK1_DMA_DMP_PIM] EQ DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.      çç[HK1_PKT_FORM_NO] EQ 7
0136 C.      çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0137 C.      çç[HK1_S_TLM_BIT_RATE] EQ 32k
0138 C.      çç[HK1_X_TLM_BIT_RATE] EQ 4M
0139 C.      çç[HK1_DMP_CHK_FLG] EQ EXEC
0140 C.
0141 C.      YÁYÖY×½ªî»aò³îÇ§
0142 C.      çç[HK1_DMP_CHK_FLG] EQ NON
0143 C.
0144 C.      RAM ID=TI_TBLaîE¹Ç•è²îOKaò³îÇ§
0145 C.
0146 C.      DHUÿâ;¼YÉ;È¼Y½,¥î;¼YÈ;Ëaòîáa¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.      çç[HK1_PKT_FORM_NO] EQ 2
0150 C.      çç[HK1_PKT_GEN_TIME] EQ 0.5S
0151 C.      çç[HK1_S_TLM_BIT_RATE] EQ 32K
0152 C.      çç[HK1_X_TLM_BIT_RATE] EQ 4M
0153 C.
0154 C.      *****
0155 C.      SOT TI command set
0156 C.      *****
0157 C.      Execute, after the success of OP upload.
0158 +. TI 2008-08-07 09:50:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 C.      -----
0162 C.      HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C.      -----
0164 C.      ***** SOT END *****
0165 C.      Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C.      ***** Start EIS operation (TI set) *****
0169 C.      Execute, after the success of OP upload.
0170 C.      Set EIS TI-commands
0171 +. TI 2008-08-07 09:50:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2008-08-07 09:50:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 C.      [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0178 C.      ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C.      ***** XRT START *****
0183 C.      Execute, after the success of OP upload.
0184 +. TI 2008-08-07 09:50:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 C.      [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0188 C.
0189 C.      ***** XRT END *****
0190 C.
0191 C.      ***** MDP `úÃîaî»ö¼YòÈÄa¹aèDCBC•×²è *****
0192 C.      (¼a°îYÖYÁYÈYÞYÉYÁYÇYÈaÈ¼a¼A»Ûa¹aè)
0193 C.      DC-BC dcbc-402:DCBC

```



```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOSŸÁŸSŸÃŸ~¼Â»Û;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```

0096 C.
0097 C.
0098 C. *****
0099 C. SOT: Test of blocker stow
0100 C. *****
0101 C. •x²ê³°αÇMDP_SOT_TLM_LMT_SETαδîÑ°õ
0102 C.
0103 C. < Enable safety blocker stow>
0104 +. DC 07-F2 FPP_OP_PARAM
0105 BC (bc 07 44 00 00 01)
0106 C. -----
0107 C. FPP_CMD_OPCODE = BC [ ]
0108 C. FPP_CMD_LENGTH = 7 [ ]
0109 C. FPP_CMD_COUNT = +1 [ ]
0110 C. -----
0111 C.
0112 C. < Set TLM_LMT lower >
0113 C. (ÄîiîÑîÑîÄîÄîE°0%î°αPα¹)
0114 C. MDP_SOT_TLM_LMT_SET xx xx xx
0115 C. -----
0116 C. MDP_SOT_TLM_LIM = xxxxxx [ ]
0117 C. MDP_SOT_TLM_CNT (memo) [ ]
0118 C. -----
0119 C.
0120 C. < Run FG table>
0121 +. DC 07-F0 MDP_FG_QT_PROG_SET
0122 BC (52 10)
0123 +. DC 07-F0 MDP_FG_CTRL_AUTO
0124 BC (50)
0125 C. -----
0126 C. MDP_FG_QT_PROG_NO = 16 [ ]
0127 C. MDP_FG_CTRL_MODE = AUTO [ ]
0128 C. -----
0129 C.
0130 C. (monitor the status, it takes a few minutes)
0131 C.
0132 C. MDP_IMG_SOT_BUSY = BUSY [ ]
0133 C. FPP_FG_STATE = 01->00 [ ]
0134 C. FPP_NFI_BLKFLT_POS = 38->118 [ ]
0135 C. FPP_ERR_CODE = 8002 [ ]
0136 C. -----
0137 C.
0138 C. < Stop FG table>
0139 +. DC 07-F0 MDP_FG_CTRL_MANU
0140 BC (51)
0141 C. -----
0142 C. MDP_FG_CTRL_MODE = MANU [ ]
0143 C. -----
0144 C.
0145 C. < Resume TLM_LMT >
0146 C. MDP_SOT_TLM_LMT_SET 0C EA 00
0147 C. -----
0148 C. MDP_SOT_TLM_LIM = 846336 [ ]
0149 C. -----
0150 C.
0151 C. *****
0152 C. END of SOT sequence
0153 C. *****
0154 C. *****
0155 C. SOT table upload
0156 C. *****
0157 C. < Stop FG table >
0158 +. DC 07-F0 MDP_FG_CTRL_MANU
0159 BC (51)
0160 C. -----
0161 C. MDP_FG_CTRL_MODE = MANU [ ]
0162 C. -----
0163 C.
0164 C. <Upload FG Observation Table>
0165 S. RAM ram-267:MDP_OBS_F
0166 ( )
0167 C.
0168 C. < Dump RAMID=MDP_OBS_F >
0169 +. DC 07-F0 MDP_DUMP_FGTBL
0170 BC (82 07 00 00 00 38 b8)
0171 C. -----
0172 C. MDP_OBS_F verify = OK/NG [ ]
0173 C. -----
0174 C.
0175 C. < Stop SP table >
0176 +. DC 07-F0 MDP_SP_CTRL_MANU
0177 BC (61)
0178 C. -----
0179 C. MDP_SP_CTRL_MODE = MANU [ ]
0180 C. -----
0181 C.
0182 C. <Upload SP Observation Table>
0183 S. RAM ram-289:MDP_OBS_S
0184 ( )
0185 C.
0186 C. < Dump RAMID=MDP_OBS_S >
0187 +. DC 07-F0 MDP_DUMP_SPTBL
0188 BC (83 07 00 00 00 38 b8)
0189 C. -----
0190 C. MDP_OBS_S verify = OK/NG [ ]
0191 C. -----
0192 C.
0193 C. *****

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```
0194 C. SOT TI command set
0195 C. *****
0196 C. Execute, after the success of TBL upload.
0197 +. TI 2008-08-07 09:50:18.0
0198 DC 07-F0 MDP_SOT_MODE_OBSV
0199 BC (40)
0200 C. -----
0201 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0202 C. -----
0203 C.
0204 C.
0205 C. ***** XRT START *****
0206 C.
0207 +. DC 07-F0 MDP_XRT_CTRL_MANU
0208 BC (c1)
0209 + DC 07-F0 MDP_XRT_MODE_STBY
0210 BC (c3)
0211 . C. ----- Success Verify ? OK / NG ____
0212 C.
0213 C. XRT Obs. Table Upload
0214 . S. RAM ram-291:MDP_OBS_X
0215 ( )
0216 C.
0217 +. DC 07-F0 MDP_DUMP_XRTTBL
0218 BC (84 07 00 00 00 3a d4)
0219 . C. ----- Comparison Check ? OK / ERR ____
0220 C.
0221 C.
0222 +. DC 07-F0 MDP_XRT_ROI_SET
0223 BC (cd 01 b1 b1 04 04)
0224 + DC 07-F0 MDP_XRT_ROI_SET
0225 BC (cd 02 b1 b1 08 08)
0226 + DC 07-F0 MDP_XRT_ROI_SET
0227 BC (cd 03 b1 b1 08 08)
0228 + DC 07-F0 MDP_XRT_ROI_SET
0229 BC (cd 04 b1 b1 06 06)
0230 + DC 07-F0 MDP_XRT_ROI_SET
0231 BC (cd 06 80 80 20 20)
0232 + DC 07-F0 MDP_XRT_ROI_SET
0233 BC (cd 07 80 80 08 08)
0234 + DC 07-F0 MDP_XRT_ROI_SET
0235 BC (cd 08 c0 c0 10 10)
0236 + DC 07-F0 MDP_XRT_ROI_SET
0237 BC (cd 09 40 c0 10 10)
0238 + DC 07-F0 MDP_XRT_ROI_SET
0239 BC (cd 0a 80 80 06 06)
0240 + DC 07-F0 MDP_XRT_ROI_SET
0241 BC (cd 0f 80 80 06 06)
0242 + DC 07-F0 MDP_XRT_ROI_SET
0243 BC (cd 10 80 80 04 04)
0244 . C. ----- Success Verify ? OK / NG ____
0245 C.
0246 C.
0247 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0248 C.
0249 +. DC 07-F0 MDP_XRT_MODE_OBSV
0250 BC (c2)
0251 +. TI 2008-08-07 09:50:02.0
0252 DC 07-F0 MDP_XRT_MODE_OBSV
0253 BC (c2)
0254 . C. ----- Success Verify ? OK / NG ____
0255 C.
0256 C. ***** XRT END *****
0257 C.
0258 . C. ***** MDP `uAÎuÎ»ô%YôEÂÐu¹eÐCBC•x²è *****
0259 C. (%â°iYôYÄYÉYÞYËYáYçYèe%¼u¼Ä»Ûu¹eè)
0260 . S. DC-BC dcbc-402:DCBC
0261 (MDP_known_event)
0262 C.
0263 C.
0264 . C. ***** YÐY¹•Ï Daily±;iÎÑe`Øu¹eÐCBC•x²è *****
0265 . S. DC-BC dcbc-153:DCBC
0266 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0267 C.
0268 C.
0269 . C. ;ãLOSŸÁYŞYÄY~¼Ä»Û;ã
0270 C.
0271 . C. ***** LOS *****
0272 C.
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*** OP Sequence for XRT ***

2008/08/07	10:01:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03	00	00	00	00
2008/08/07	11:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/07	11:59:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2008/08/07	12:00:16.0	XRT_QT_PROG_SET_415_OG [0x19f]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2008/08/07	12:01:54.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/08/07	12:01:56.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/08/07	12:01:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/08/07	12:02:00.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/08/07	12:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/07	12:59:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2008/08/07	13:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03	12	a7	11	ca
2008/08/07	13:00:16.0	XRT_QT_PROG_SET_415_OG [0x19f]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2008/08/07	13:01:54.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/08/07	13:01:56.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/08/07	13:01:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/08/07	13:02:00.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/08/07	13:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/07	13:59:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2008/08/07	14:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	04	00	00	00	00
2008/08/07	14:00:16.0	XRT_QT_PROG_SET_415_OG [0x19f]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2008/08/07	14:01:54.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/08/07	14:01:56.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/08/07	14:01:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/08/07	14:02:00.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/08/07	15:00:00.5	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/07	17:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/07	17:59:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2008/08/07	18:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	00	00	00	00
2008/08/07	18:00:16.0	XRT_QT_PROG_SET_409_OG [0x199]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2008/08/07	18:00:18.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/08/07	18:00:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/08/07	18:00:22.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/08/07	18:02:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/08/07	18:09:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/07	22:30:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	ad	f4	00	00
2008/08/07	23:50:00.0	AOCS_Ore-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	00	00	ab	8e
2008/08/08	02:51:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/08	02:51:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2008/08/08	02:52:00.0	AOCS_Ore-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	2e	f9	2e	f9
2008/08/08	02:52:16.0	XRT_QT_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2008/08/08	02:54:54.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/08/08	02:54:56.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/08/08	02:54:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/08/08	02:55:00.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/08/08	02:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/08	02:59:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]							

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2008/08/08	03:00:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05
		AOCU_NM		5	02-76	00 2c	f5 30 28
2008/08/08	03:02:54.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2008/08/08	03:02:56.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2008/08/08	03:02:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2008/08/08	03:03:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2008/08/08	03:07:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2008/08/08	03:07:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05
2008/08/08	03:08:00.0	AOCS_ORe-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00 2f	f7 31 2f
2008/08/08	03:10:54.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2008/08/08	03:10:56.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2008/08/08	03:10:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2008/08/08	03:11:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2008/08/08	03:15:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2008/08/08	03:15:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff	aa 00
2008/08/08	03:16:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	00 2e	f9 d1 07
2008/08/08	03:16:16.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02
2008/08/08	03:18:54.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2008/08/08	03:18:56.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2008/08/08	03:18:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2008/08/08	03:19:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2008/08/08	03:23:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2008/08/08	03:23:56.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02
2008/08/08	03:24:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	00 2d	f3 d1 82
2008/08/08	03:26:54.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2008/08/08	03:26:56.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2008/08/08	03:26:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2008/08/08	03:27:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2008/08/08	03:31:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2008/08/08	03:31:56.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02
2008/08/08	03:32:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00 31	3f d0 c5
2008/08/08	03:34:54.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2008/08/08	03:34:56.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2008/08/08	03:34:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2008/08/08	03:35:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2008/08/08	03:40:00.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2008/08/08	06:12:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2008/08/08	06:12:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff	aa 00
2008/08/08	06:13:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00	00 00 00
2008/08/08	06:13:16.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
2008/08/08	06:13:18.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2008/08/08	06:13:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2008/08/08	06:13:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2008/08/08	06:15:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2008/08/08	06:22:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2008/08/08	06:23:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	01 00	00 00 00
2008/08/08	17:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2008/08/08	17:59:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2008/08/08	18:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a] AOCU_NM	5	02-76	00 00 00 00 00
2008/08/08	18:00:16.0	XRT_QT_PROG_SET_409_OG [0x199] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2008/08/08	18:00:18.0	XRT_FLD_DIS_419_OG [0x1a3] MDP_XRT_FLD_DIS	1	07-F0	d9
2008/08/08	18:00:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2008/08/08	18:00:22.0	XRT_ARS_DIS_427_OG [0x1ab] MDP_XRT_ARS_DIS	1	07-F0	d5
2008/08/08	18:02:00.0	XRT_CTRL_AUTO_444_OG [0x1bc] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/08/08	18:09:54.0	XRT_CTRL_MANU_428_OG [0x1ac] MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/08/08	22:49:54.0	XRT_CTRL_MANU_428_OG [0x1ac] MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/08/08	22:49:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2008/08/08	22:50:00.0	AOCS_ORe-point_Start_6_OG [0x09c] AOCU_NM	5	02-76	00 00 00 ab 8e
2008/08/08	22:50:16.0	XRT_QT_PROG_SET_420_OG [0x1a4] MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2008/08/08	22:51:54.0	XRT_ARS_DIS_422_OG [0x1a6] MDP_XRT_ARS_DIS	1	07-F0	d5
2008/08/08	22:51:56.0	XRT_FLD_DIS_445_OG [0x1bd] MDP_XRT_FLD_DIS	1	07-F0	d9
2008/08/08	22:51:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2008/08/08	22:52:00.0	XRT_CTRL_AUTO_403_OG [0x193] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/08/09	00:05:30.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/08/09	00:16:00.0	XRT_Custom_430_OG [0x1ae]			
2008/08/09	00:17:00.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/08/09	01:00:00.0	XRT_CTRL_MANU_428_OG [0x1ac] MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/08/09	05:46:24.0	XRT_CTRL_MANU_428_OG [0x1ac] MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/08/09	05:46:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2008/08/09	05:46:30.0	AOCS_ORe-point_Start_4_OG [0x09a] AOCU_NM	5	02-76	00 00 00 00 00
2008/08/09	05:46:46.0	XRT_QT_PROG_SET_429_OG [0x1ad] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2008/08/09	05:46:48.0	XRT_FLD_DIS_419_OG [0x1a3] MDP_XRT_FLD_DIS	1	07-F0	d9
2008/08/09	05:46:50.0	XRT_FLRCTRL_DIS_447_OG [0x1bf] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2008/08/09	05:46:52.0	XRT_ARS_DIS_427_OG [0x1ab] MDP_XRT_ARS_DIS	1	07-F0	d5
2008/08/09	05:48:30.0	XRT_CTRL_AUTO_444_OG [0x1bc] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/08/09	05:56:24.0	XRT_CTRL_MANU_428_OG [0x1ac] MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/08/09	05:56:30.0	AOCS_ORe-point_Start_6_OG [0x09c] AOCU_NM	5	02-76	00 00 00 ab 8e
2008/08/09	11:47:00.0	AOCS_ORe-point_Start_4_OG [0x09a] AOCU_NM	5	02-76	00 00 00 00 00