XRT Timeline to be uploaded on 2008/08/14

Period: 2008/08/14 11:27:00 - 2008/08/19 10:58:00

* * * * * **Normal mode** * * * * *

XOB #15AE: CH - Al/Mesh (16s) + Ti/Poly (32s) - 512x512 - Q95 - with half-res. full frame

Term	Pointing (x, y)	Comment
08/14 11:39:00 - 08/14 12:35:00	Fixed (850.0, -360.0)	# OP start + 10min, SP deep scan of CH at W limb.

PROG= 15 Inf.-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 87 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 72 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 76 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 3-time(s) 1200.0sec

Seqn= 79 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 2 10-time(s) 120.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	0.5sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32.0s	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec

XOB #15A3: Synoptic Q95 2x2 - Al/poly(512/5795) + Dark cal(512 Q98) + Ti-poly(723/11571) + G-band(16)

Term	Pointing (x, y)	Comment
08/14 17:42:00 - 08/14 17:49:54	Fixed (0.0, 0.0)	synoptic, shifted -20.0 min, and Sun-center pointing (conserve telemetry).
08/15 18:02:00 - 08/15 18:09:54	Fixed (0.0, 0.0)	synoptic.

PROG= 11 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 18 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 72 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 76 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 92 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #15AF: XBP Q90 Al/poly (AEC1) + Ti/poly (AEC1) + Thin-Be (AEC0)-high cadence

Term	Pointing (x, y)	Comment
08/14 23:17:30 - 08/15 00:20:00	Fixed (-10.0, 902.0)	* HOP 75 with Hida, polar jets study (official time: 23:10 - 00:10 UT).
08/15 23:50:30 - 08/16 00:55:00	Track (-10.9, 782.0) @ 08/15 23:30:00	* HOP 75, with Hida, QS chorom jets (23:45 - 00:45 UT).

PROG= 16 Inf.-time(s)

Subr= 1 1-time(s) 400.0sec

Seqn= 21 10-time(s) 40.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	C-poly/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=90	1	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=90	1	0	2.0sec

Subr= 2 1-time(s) 4.0sec

Seqn= 43 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	64.0s	Obs	1x1	384x384 (1024, 1024)	Q=90	0	0	2.0sec

XOB #15A4: Synoptic Q95 2x2 - Al/mesh(512/5795) + Dark cal(512 Q98) + Ti-poly(723/11571) + G-band(16)

Term	Pointing (x, y)	Comment
08/15 06:00:30 - 08/15 06:08:24	Fixed (0.0, 0.0)	synoptic, shifted -1.5 min.
08/16 06:19:00 - 08/16 06:26:54	Fixed (0.0, 0.0)	synoptic, shifted 17.0 min, and Sun-center pointing until end of plan.

PROG= 12 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 87 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 72 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 76 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 92 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #15B1: XBP Q90 Al/poly (AEC1) + Ti/poly (AEC1) + Thin-Be (AEC0)- med cadence -FOV512

Term	Pointing (x, y)	Comment
08/15 18:12:00 - 08/15 23:29:00	Track (0.0, -0.0) @ 08/15 18:10:00	* XRT XBP study.

PROG= 09 Inf.-time(s)

Subr= 1 1-time(s) 25.0sec

Seqn= 48 1-time(s) 25.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	C-poly/Open	close	Safe	Norm	250ms	Obs	1x1	512x512 (1024, 1024)	Q=90	1	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1024, 1024)	Q=90	1	0	2.0sec

Subr= 2 1-time(s) 4.0sec

Seqn= 56 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	64.0s	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec

* * * * * **Flare mode** * * * * *

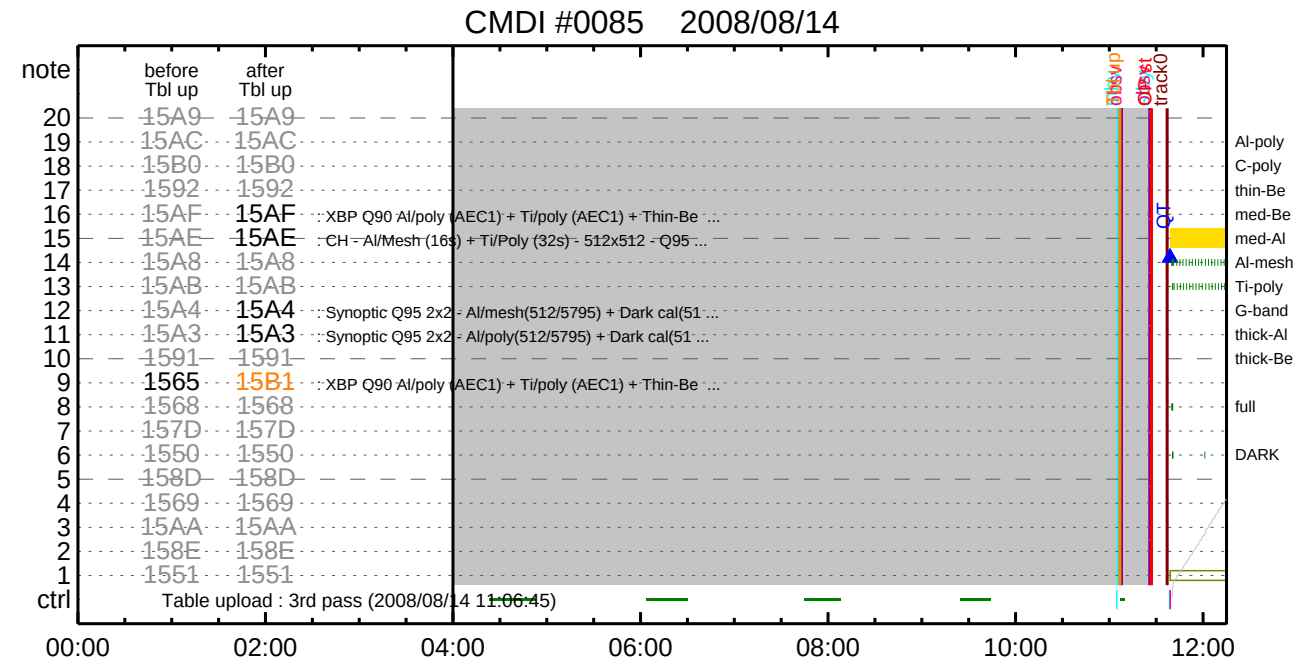
NOT USED

* * * * * **Active Region Search** * * * * *

NOT USED

* * * * * **Flare Detection** * * * * *

NOT USED


CMDI #0085 2008/08/14
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before Tbl up
after Tbl up
20 15A9 15A9
19 15AC 15AC
18 15B0 15B0
17 1592 1592
16 15AF 15AF : XBP Q90 Al/poly (AEC1) + Ti/poly (AEC1) + Thin-Be ...
15 15AE 15AE : CH - Al/Mesh (16s) + Ti/Poly (32s) - 512x512 - Q95 ...
14 15A8 15A8
13 15AB 15AB
12 15A4 15A4 : Synoptic Q95 2x2 - Al/mesh(512/5795) + Dark cal(51 ...
11 15A3 15A3 : Synoptic Q95 2x2 - Al/poly(512/5795) + Dark cal(51 ...
10 1591 1591
9 1565 15B1 : XBP Q90 Al/poly (AEC1) + Ti/poly (AEC1) + Thin-Be ...
8 1568 1568
7 157D 157D
6 1550 1550
5 158D 158D
4 1569 1569
3 15AA 15AA
2 158E 158E
1 1551 1551
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Al-poly
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med-Be
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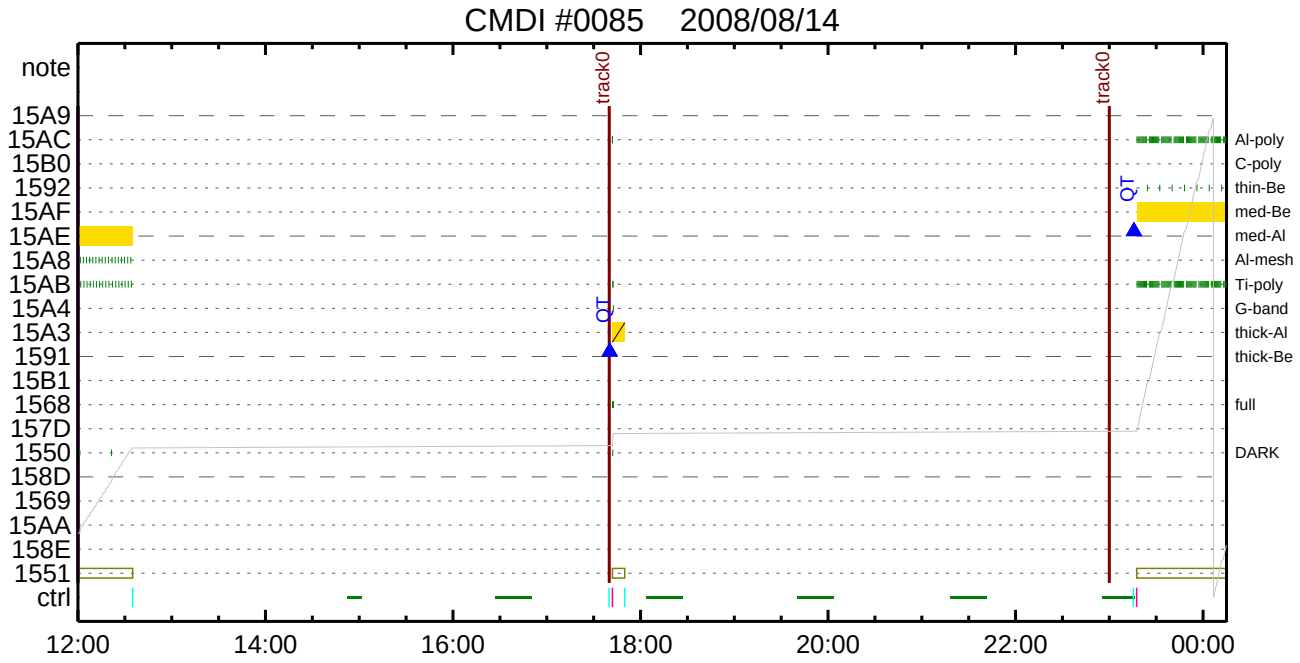
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Al-mesh
Ti-poly
G-band
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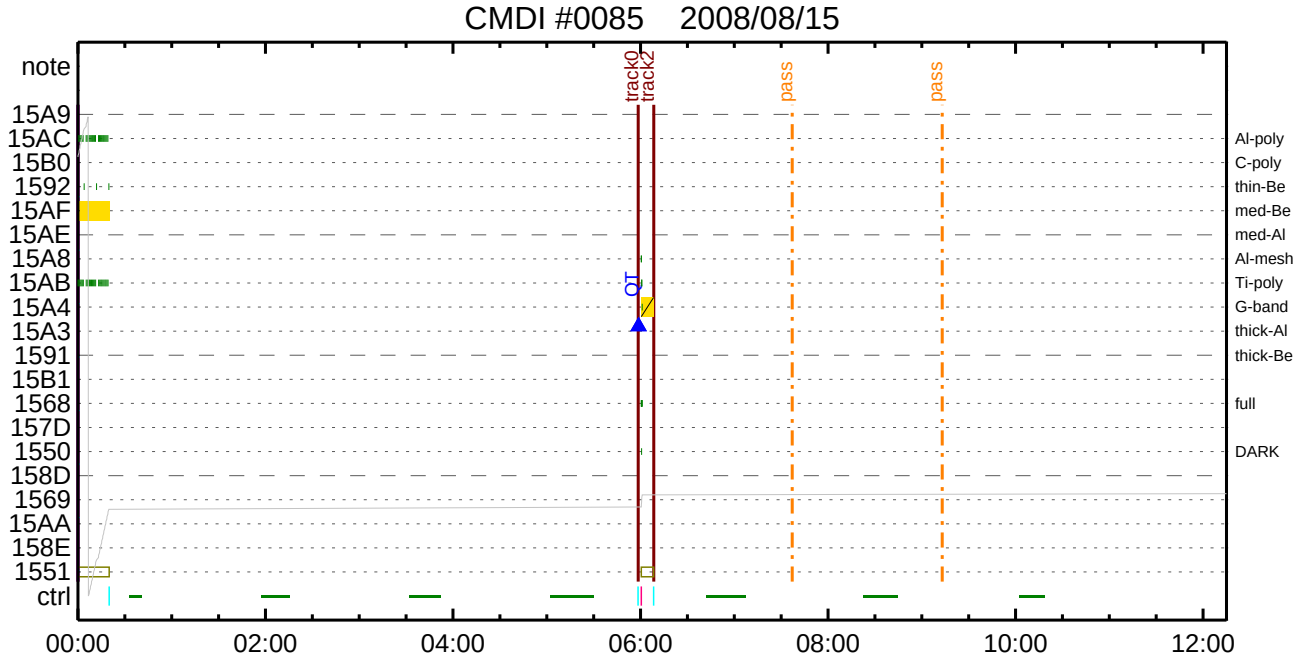
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Ti-poly
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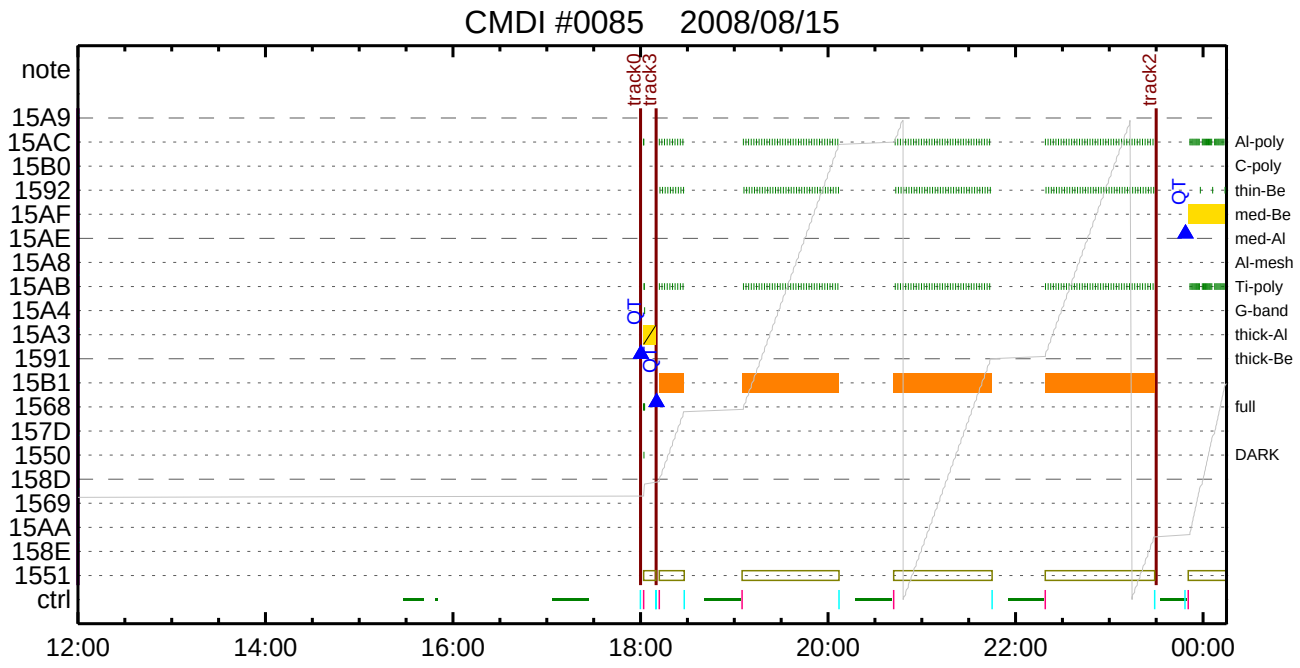
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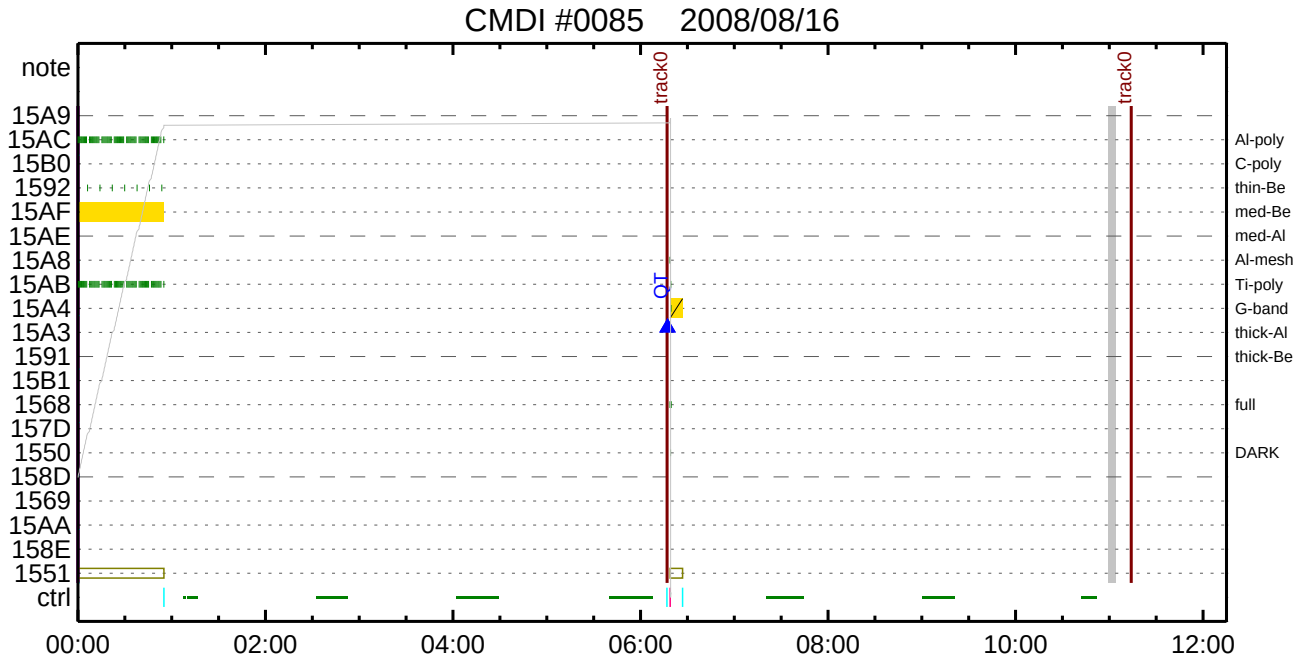
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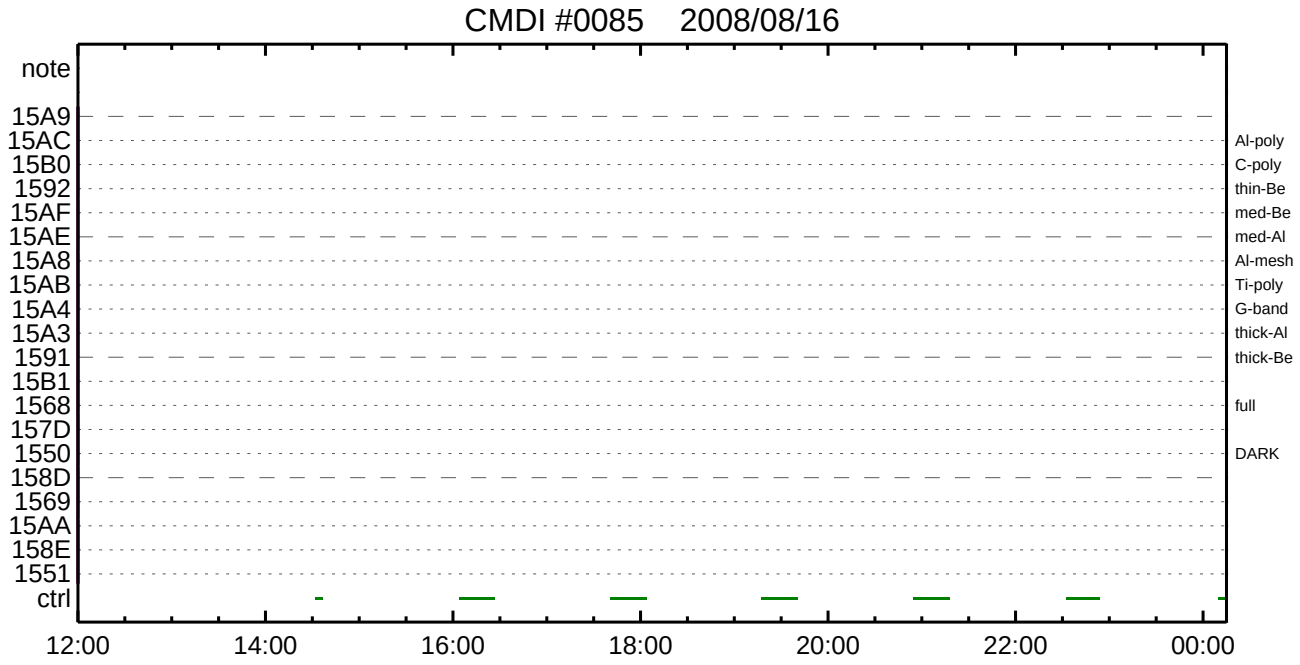
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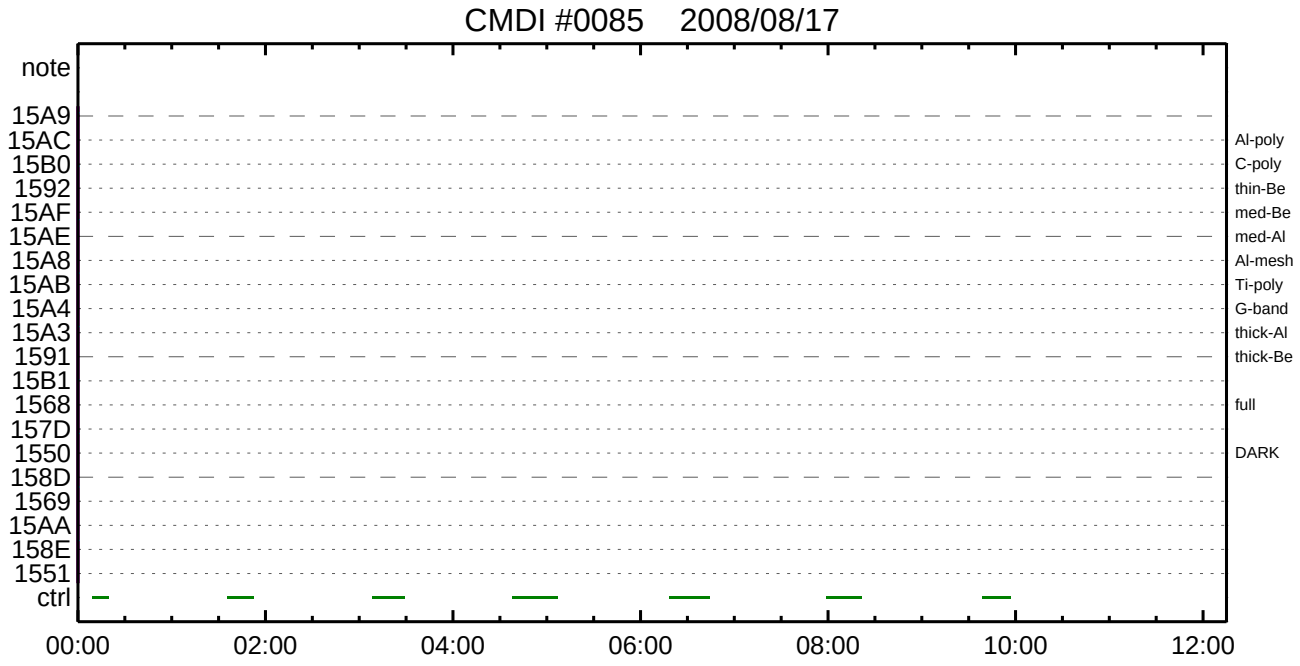
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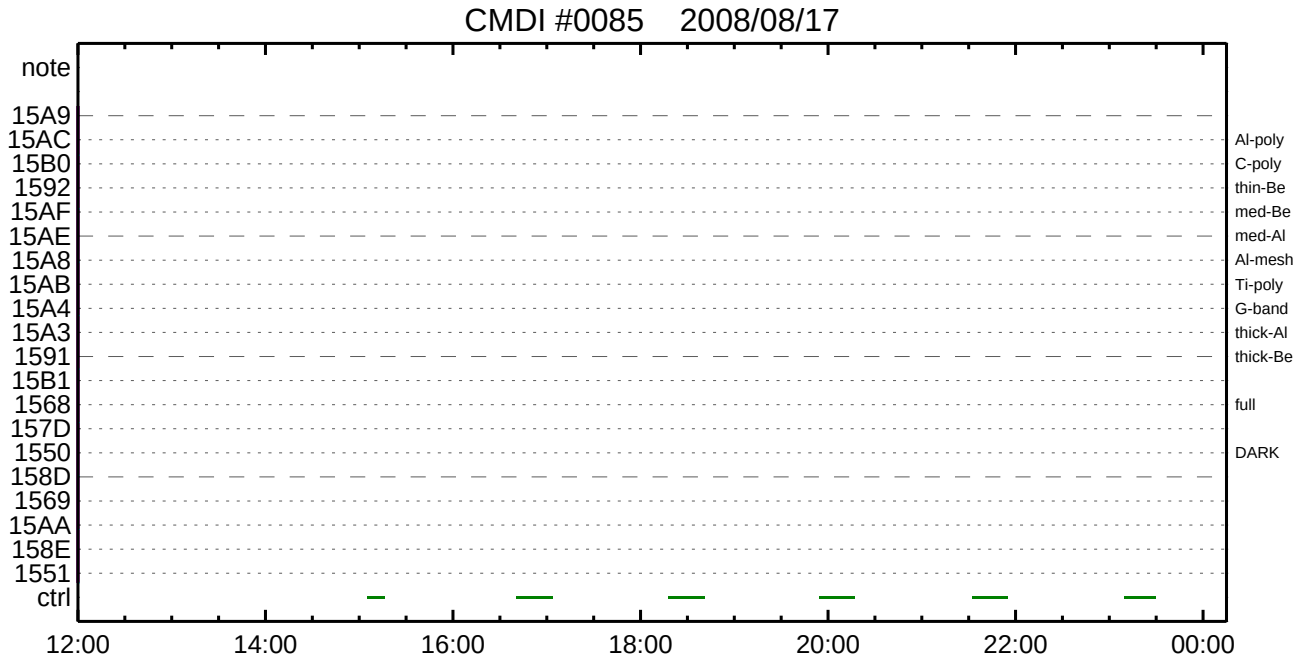
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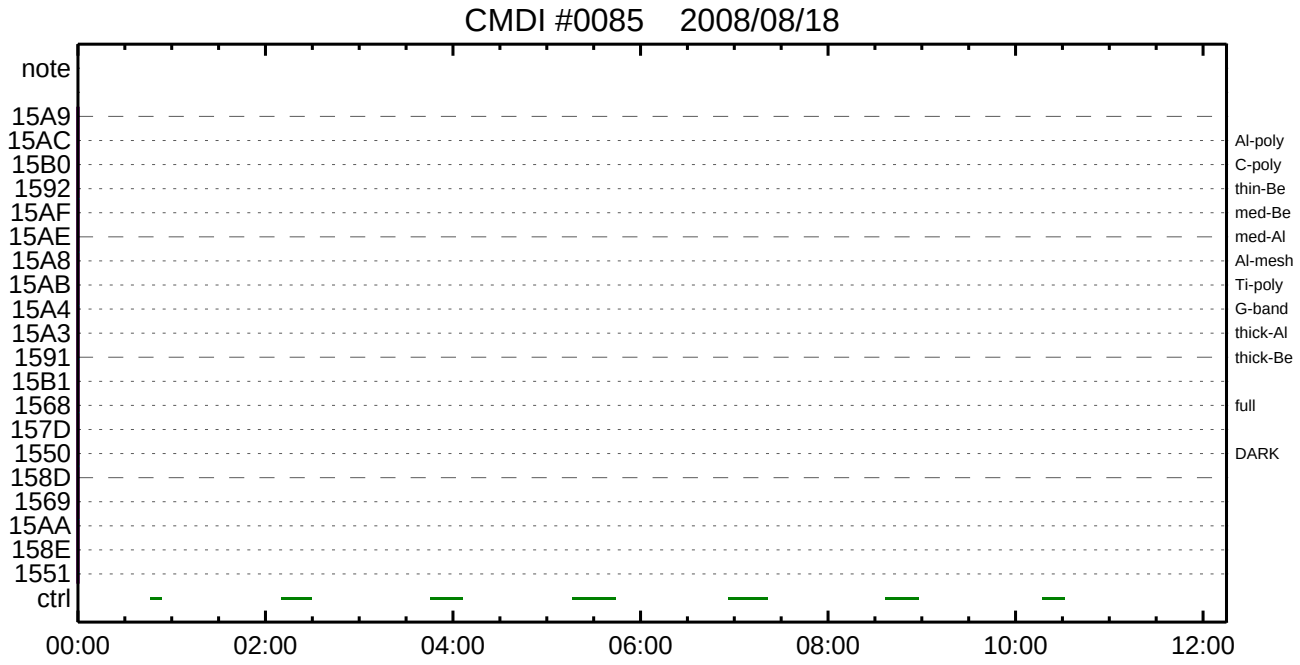
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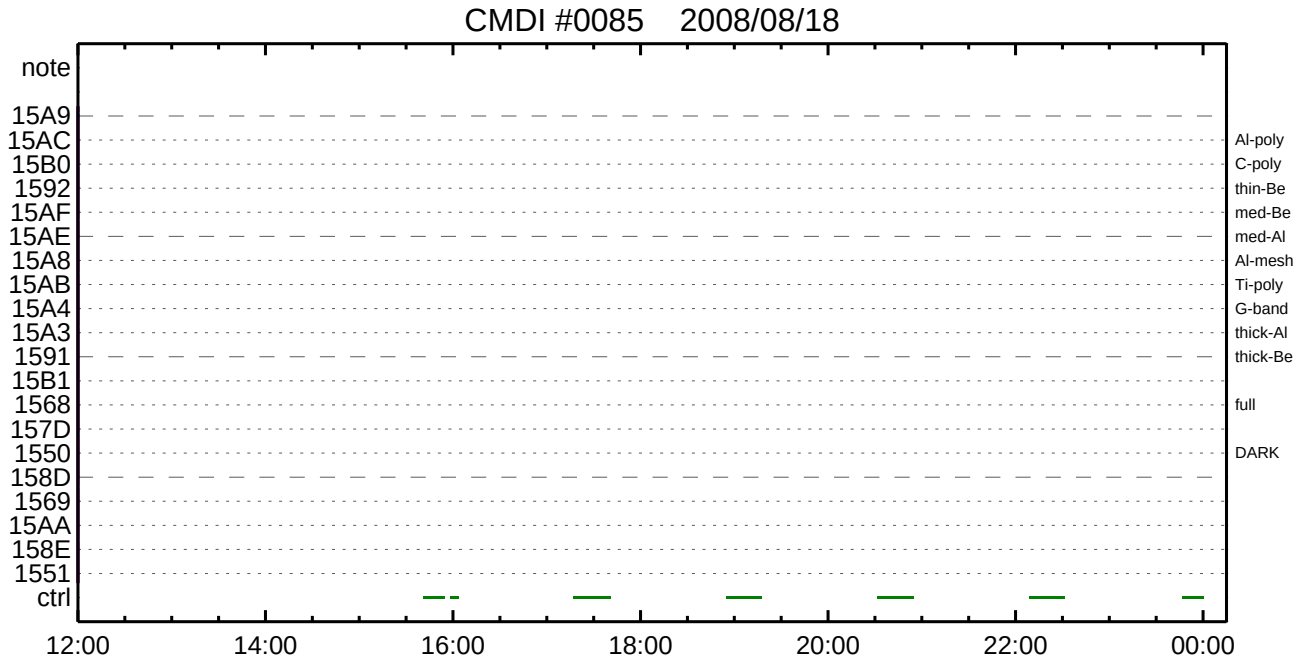
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Al-mesh
Ti-poly
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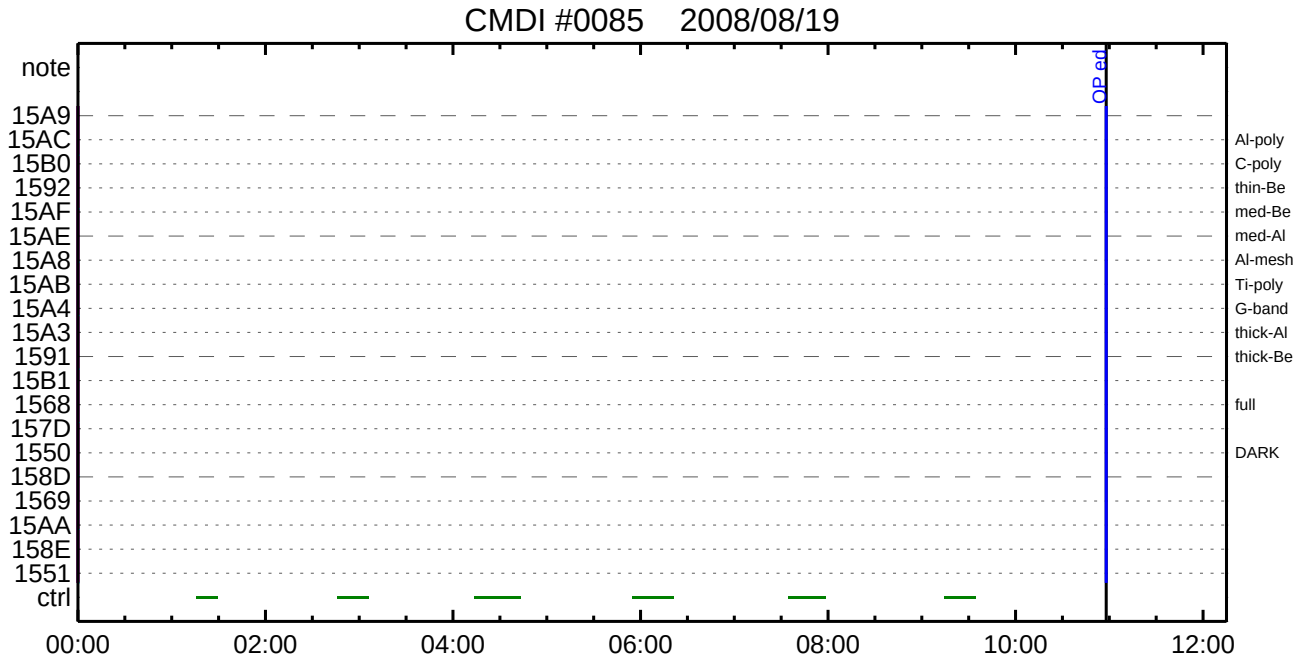
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Al-mesh
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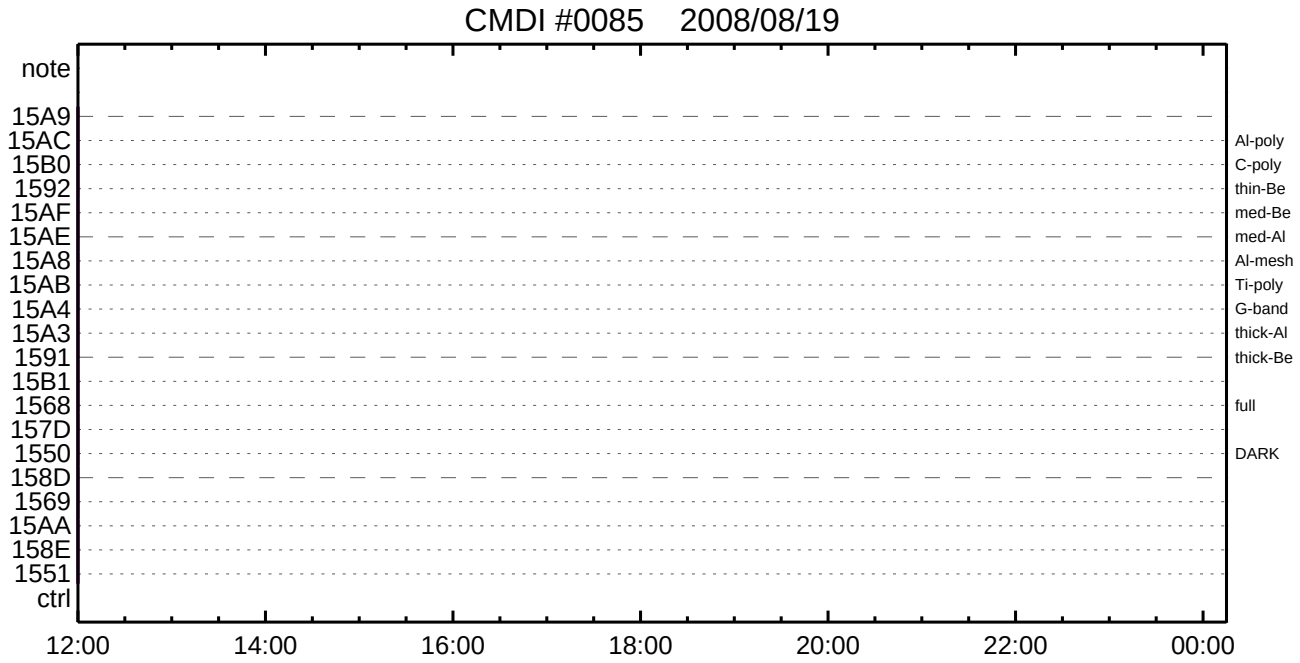
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Al-mesh
Ti-poly
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DARK
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```
(a) Spacecraft Operation Procedure (real-commands)

main-069 2008-08-14 12:20:34 205 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0016     C. ****************************
0017     C.
0018   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0019   . S. OP    op-069:OP
0020     ()
0021   . S. OG    og-069:OG
0022     ()
0023     C.
0024   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0025     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0026   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027     BC       (20 00 7f 01 02)
0028     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0029     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0030     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0031     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0032     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0033   +. DC 01-22 DHU_MODE_CHNG
0034     BC       (07 0b f8)
0035     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0036     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0037     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0038     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0039     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0040   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0041     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0042   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0043     C.
0044     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0045   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046     BC       (20 80 7f 01 02)
0047     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0048     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0049     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0050     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0051     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0052   +. DC 01-22 DHU_MODE_CHNG
0053     BC       (07 0b f8)
0054     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0055     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0056     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0057     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0058     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0059   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0060     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0061   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0062     C.
0063     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065     BC       (21 00 41 01 02)
0066     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0067     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0068     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0069     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0070     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0071   +. DC 01-22 DHU_MODE_CHNG
0072     BC       (07 0b f8)
0073     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0074     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0075     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0076     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0077     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0078   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0079     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0080   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0081     C.
0082   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0083     C. DHU¥â¡¼¥É¡Ê¼ý¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0084   +. DC 01-22 DHU_MODE_CHNG
0085     BC       (02 0a f8)
0086     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0087     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0088     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0089     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0090     C.
0091   . C. ********************************
0092     C. TI-CMD SET (OPOG STOP/COPY/START)
0093     C. ********************************
0094     C.
0095   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
```

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0096     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
0097     C.
0098   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0099   +. TI 2008-08-14 11:22:00.0
0100     DC 01-B3 DHU_OP_STOP
0101     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0102     C.
0103   +. TI 2008-08-14 11:22:01.0
0104     DC 01-B4 DHU_OP_COPY
0105     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0106     C.
0107   +. TI 2008-08-14 11:22:01.0
0108     DC 01-B5 DHU_OPOG_COPY
0109     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0110     C.
0111   +. TI 2008-08-14 11:26:59.5
0112     DC 01-B2 DHU_OP_START
0113     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0114     C.
0115     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0116     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0117     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0118     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0119     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0120     C.
0121   . C. ***************************
0122     C. TIÎÎ°è¥À¥ó¥×
0123     C. ***************************
0124     C.
0125     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0126   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127     BC       (03 ab 03 01 02)
0128     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0129     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0130     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0131     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0132     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0133   +. DC 01-22 DHU_MODE_CHNG
0134     BC       (07 0b f8)
0135     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0136     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0137     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0138     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0139     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0140     C.
0141   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0142     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0143     C.
0144   . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0145     C.
0146   . C. DHU¥â¡¼¥É¡Ê¼ý¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0147   +. DC 01-22 DHU_MODE_CHNG
0148     BC       (02 0a f8)
0149     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0150     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0151     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0152     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0153     C.
0154     C. *******************************
0155     C.  SOT TI command set
0156     C. *******************************
0157     C. Execute, after the success of OP upload.
0158   +. TI 2008-08-14 11:26:16.0
0159     DC 07-F0 MDP_SOT_MODE_STBY
0160     BC       (41)
0161   . C. ------------------------------------
0162     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0163     C. ------------------------------------
0164     C. ************ SOT END ************
0165   . C. Stop EIS observation and temporarily disable EIS mode changes
0166     C.
0167     C.
0168     C. ********** Start EIS operation (TI set) **********
0169     C. Execute, after the success of OP upload.
0170     C. Set EIS TI-commands
0171   +. TI 2008-08-14 11:26:30.0
0172     DC 07-FC EIS_MODE_MANU
0173     BC       (21 02)
0174   +. TI 2008-08-14 11:26:40.0
0175     DC 07-FC EIS_MODE_CHG_DIS
0176     BC       (22)
0177   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0178     C. ************ End EIS operation (TI set) ************
0179     C.
0180     C.
0181     C.
0182     C. ************ XRT START ************
0183     C. Execute, after the success of OP upload.
0184   +. TI 2008-08-14 11:26:00.0
0185     DC 07-F0 MDP_XRT_MODE_STBY
0186     BC       (c3)
0187   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0188     C.
0189     C. ************ XRT END ************
0190     C.
0191   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0192     C. (¾å°Ì¥Ó¥Ã¥Ë¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0193   . S. DC-BC dcbc-402:DCBC
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0194     (MDP_known_event)
0195     C.
0196     C.
0197   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0198   . S. DC-BC dcbc-153:DCBC
0199     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200     C.
0201     C.
0202   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0203     C.
0204   . C. ********** LOS **********
0205     C.
```

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(a) Spacecraft Operation Procedure (real-commands)

main-070 2008-08-14 12:20:34 166 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106   +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113   +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                      Result = OK [ ]
0121     C.
0122   +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                         <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127   +. DC 02-8E AOCU_ORB_UPD
0128    . C.
0129    . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130   +. DC 07-FC EIS_MODE_MANU
0131     BC       (21 02)
0132    . C. Verify EIS in MANUAL mode
0133    . C. Estimated OBSTBL upload time is 11s
0134     C. ********************************
0135     C. EIS START OBSTBL LOAD
0136     C. ********************************
0137    . S. RAM   ram-820:EIS_OBSTBL
0138     ()
0139   +. DC 07-FC EIS_DUMP_OBSTBL
0140     BC       (07 07 07 00 00 70 00)
0141     C.
0142     C. Execute, after the success of OBSTBL upload.
0143     C. Set EIS TI-commands
0144   +. TI 2008-08-14 11:26:50.0
0145     DC 07-FC EIS_MODE_CHG_ENA
0146     BC       (20)
0147    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0148     C. ********************************
0149     C. EIS END OBSTBL LOAD
0150     C. ********************************
0151     C.
0152    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0153     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0154    . S. DC-BC dcbc-402:DCBC
0155     (MDP_known_event)
0156     C.
0157     C.
0158    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0159    . S. DC-BC dcbc-153:DCBC
0160     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161     C.
0162     C.
0163    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0164     C.
0165    . C. ********** LOS **********
0166     C.
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(a) Spacecraft Operation Procedure (real-commands)

main-071 2008-08-14 12:20:34 128 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011  +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014     C.
0015     C. ************ XRT START ************
0016     C.
0017  +. DC 07-F0 MDP_XRT_CTRL_MANU
0018     BC       (c1)
0019  +  DC 07-F0 MDP_XRT_MODE_STBY
0020     BC       (c3)
0021   . C. ----------- Success Verify ?              OK / NG____
0022     C.
0023     C. XRT Obs. Table Upload
0024   . S. RAM   ram-291:MDP_OBS_X
0025     ()
0026     C.
0027  +. DC 07-F0 MDP_DUMP_XRTTBL
0028     BC       (84 07 00 00 00 3a d4)
0029   . C. ----------- Comparison Check ?                        OK / ERR ____
0030     C.
0031     C.
0032  +. DC 07-F0 MDP_XRT_ROI_SET
0033     BC       (cd 01 b1 b1 04 04)
0034  +  DC 07-F0 MDP_XRT_ROI_SET
0035     BC       (cd 02 b1 b1 08 08)
0036  +  DC 07-F0 MDP_XRT_ROI_SET
0037     BC       (cd 03 b1 b1 08 08)
0038  +  DC 07-F0 MDP_XRT_ROI_SET
0039     BC       (cd 04 b1 b1 06 06)
0040  +  DC 07-F0 MDP_XRT_ROI_SET
0041     BC       (cd 06 80 80 20 20)
0042  +  DC 07-F0 MDP_XRT_ROI_SET
0043     BC       (cd 07 80 80 08 08)
0044  +  DC 07-F0 MDP_XRT_ROI_SET
0045     BC       (cd 08 80 80 06 06)
0046  +  DC 07-F0 MDP_XRT_ROI_SET
0047     BC       (cd 0f 80 80 06 06)
0048  +  DC 07-F0 MDP_XRT_ROI_SET
0049     BC       (cd 10 80 80 04 04)
0050   . C. ---------- Success Verify ?            OK / NG ____
0051     C.
0052     C.
0053   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0054     C.
0055  +. DC 07-F0 MDP_XRT_MODE_OBSV
0056     BC       (c2)
0057  +. TI 2008-08-14 11:26:02.0
0058     DC 07-F0 MDP_XRT_MODE_OBSV
0059     BC       (c2)
0060   . C. ---------- Success Verify ?            OK / NG ____
0061     C.
0062     C. ************ XRT END ***********
0063   . C. ******************************
0064     C.  SOT table upload
0065     C. ******************************
0066   . C. < Stop FG table >
0067  +. DC 07-F0 MDP_FG_CTRL_MANU
0068     BC       (51)
0069   . C. ------------------------------------
0070     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0071     C. ------------------------------------
0072     C.
0073   . C. <Upload FG Observation Table>
0074   . S. RAM   ram-269:MDP_OBS_F
0075     ()
0076     C.
0077   . C. < Dump RAMID=MDP_OBS_F >
0078  +. DC 07-F0 MDP_DUMP_FGTBL
0079     BC       (82 07 00 00 00 38 b8)
0080     C. ------------------------------------
0081     C.  MDP_OBS_F verify = OK/NG        [ ]
0082     C. ------------------------------------
0083     C.
0084   . C. < Stop SP table >
0085  +. DC 07-F0 MDP_SP_CTRL_MANU
0086     BC       (61)
0087     C. ------------------------------------
0088     C.  MDP_SP_CTRL_MODE      = MANU    [ ]
0089     C. ------------------------------------
0090     C.
0091   . C. <Upload SP Observation Table>
0092   . S. RAM   ram-282:MDP_OBS_S
0093     ()
0094     C.
0095   . C. < Dump RAMID=MDP_OBS_S >
```

```
0096  +. DC 07-F0 MDP_DUMP_SPTBL
0097     BC       (83 07 00 00 00 38 b8)
0098     C. ------------------------------------
0099     C.  MDP_OBS_S verify = OK/NG        [ ]
0100     C. ------------------------------------
0101     C.
0102     C. ******************************
0103     C.  SOT TI command set
0104     C. ******************************
0105     C. Execute, after the success of TBL upload.
0106  +. TI 2008-08-14 11:26:18.0
0107     DC 07-F0 MDP_SOT_MODE_OBSV
0108     BC       (40)
0109   . C. ------------------------------------
0110     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0111     C. ------------------------------------
0112     C.
0113     C.
0114   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0115     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0116   . S. DC-BC dcbc-402:DCBC
0117     (MDP_known_event)
0118     C.
0119     C.
0120   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0121   . S. DC-BC dcbc-153:DCBC
0122     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0123     C.
0124     C.
0125   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0126     C.
0127   . C. ********** LOS **********
0128     C.
```

```
*** OP Sequence for XRT ***

2008/08/14  11:37:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM               5 02-76 00 20 00 b4 73
2008/08/14  11:38:30.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2008/08/14  11:38:32.0  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                XRT_FOCUS_POSITION    4 07-F8 22 fe 97 00
2008/08/14  11:38:52.0  XRT_QT_PROG_SET_415_OG [0x19f]
                                MDP_XRT_QT_PROG_SET   2 07-F0 c4 0f
2008/08/14  11:38:54.0  XRT_ARS_DIS_422_OG [0x1a6]
                                MDP_XRT_ARS_DIS       1 07-F0 d5
2008/08/14  11:38:56.0  XRT_FLD_DIS_445_OG [0x1bd]
                                MDP_XRT_FLD_DIS       1 07-F0 d9
2008/08/14  11:38:58.0  XRT_FLRCTRL_DIS_416_OG [0x1a0]
                                MDP_XRT_FLRCTRL_DIS   1 07-F0 c9
2008/08/14  11:39:00.0  XRT_CTRL_AUTO_403_OG [0x193]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2008/08/14  12:35:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2008/08/14  17:39:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2008/08/14  17:39:56.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                XRT_FOCUS_POSITION    4 07-F8 22 ff aa 00
2008/08/14  17:40:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM               5 02-76 00 00 00 00 00
2008/08/14  17:40:16.0  XRT_QT_PROG_SET_409_OG [0x199]
                                MDP_XRT_QT_PROG_SET   2 07-F0 c4 0b
2008/08/14  17:40:18.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS       1 07-F0 d9
2008/08/14  17:40:20.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS   1 07-F0 c9
2008/08/14  17:40:22.0  XRT_ARS_DIS_427_OG [0x1ab]
                                MDP_XRT_ARS_DIS       1 07-F0 d5
2008/08/14  17:42:00.0  XRT_CTRL_AUTO_444_OG [0x1bc]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2008/08/14  17:49:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2008/08/14  23:00:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM               5 02-76 00 af cf 00 e5
2008/08/14  23:15:24.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2008/08/14  23:15:26.0  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                XRT_FOCUS_POSITION    4 07-F8 22 fe 97 00
2008/08/14  23:15:46.0  XRT_QT_PROG_SET_420_OG [0x1a4]
                                MDP_XRT_QT_PROG_SET   2 07-F0 c4 10
2008/08/14  23:17:24.0  XRT_ARS_DIS_422_OG [0x1a6]
                                MDP_XRT_ARS_DIS       1 07-F0 d5
2008/08/14  23:17:26.0  XRT_FLD_DIS_445_OG [0x1bd]
                                MDP_XRT_FLD_DIS       1 07-F0 d9
2008/08/14  23:17:28.0  XRT_FLRCTRL_DIS_416_OG [0x1a0]
                                MDP_XRT_FLRCTRL_DIS   1 07-F0 c9
2008/08/14  23:17:30.0  XRT_CTRL_AUTO_403_OG [0x193]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2008/08/15  00:20:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2008/08/15  05:58:24.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2008/08/15  05:58:26.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                XRT_FOCUS_POSITION    4 07-F8 22 ff aa 00
2008/08/15  05:58:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM               5 02-76 00 00 00 00 00
2008/08/15  05:58:46.0  XRT_QT_PROG_SET_429_OG [0x1ad]
                                MDP_XRT_QT_PROG_SET   2 07-F0 c4 0c
2008/08/15  05:58:48.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS       1 07-F0 d9
2008/08/15  05:58:50.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS   1 07-F0 c9
2008/08/15  05:58:52.0  XRT_ARS_DIS_427_OG [0x1ab]
                                MDP_XRT_ARS_DIS       1 07-F0 d5
2008/08/15  06:00:30.0  XRT_CTRL_AUTO_444_OG [0x1bc]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2008/08/15  06:08:24.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2008/08/15  06:08:30.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM               5 02-76 02 00 00 00 00
2008/08/15  17:59:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2008/08/15  17:59:56.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                XRT_FOCUS_POSITION    4 07-F8 22 ff aa 00
2008/08/15  18:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM               5 02-76 00 00 00 00 00
2008/08/15  18:00:16.0  XRT_QT_PROG_SET_409_OG [0x199]
                                MDP_XRT_QT_PROG_SET   2 07-F0 c4 0b
2008/08/15  18:00:18.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS       1 07-F0 d9
2008/08/15  18:00:20.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS   1 07-F0 c9
2008/08/15  18:00:22.0  XRT_ARS_DIS_427_OG [0x1ab]
                                MDP_XRT_ARS_DIS       1 07-F0 d5
2008/08/15  18:02:00.0  XRT_CTRL_AUTO_444_OG [0x1bc]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2008/08/15  18:09:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
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                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/15  18:09:54.5  XRT_CTRL_MANU_428_OG [0x1ac]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/15  18:09:56.5  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2008/08/15  18:10:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                        AOCU_NM              5 02-76 03 00 00 00 00
2008/08/15  18:10:16.5  XRT_QT_PROG_SET_407_OG [0x197]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 09
2008/08/15  18:11:54.5  XRT_ARS_DIS_422_OG [0x1a6]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2008/08/15  18:11:56.5  XRT_FLD_DIS_445_OG [0x1bd]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2008/08/15  18:11:58.5  XRT_FLRCTRL_DIS_416_OG [0x1a0]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/08/15  18:12:00.5  XRT_CTRL_AUTO_403_OG [0x193]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/08/15  18:28:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/15  19:04:00.0  XRT_Custom_430_OG [0x1ae]
2008/08/15  19:05:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/08/15  20:07:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/15  20:41:00.0  XRT_Custom_430_OG [0x1ae]
2008/08/15  20:42:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/08/15  21:45:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/15  22:18:00.0  XRT_Custom_430_OG [0x1ae]
2008/08/15  22:19:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/08/15  23:29:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/15  23:30:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                        AOCU_NM              5 02-76 02 00 00 00 00
2008/08/15  23:48:24.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/15  23:48:26.0  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2008/08/15  23:48:46.0  XRT_QT_PROG_SET_420_OG [0x1a4]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 10
2008/08/15  23:50:24.0  XRT_ARS_DIS_422_OG [0x1a6]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2008/08/15  23:50:26.0  XRT_FLD_DIS_445_OG [0x1bd]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2008/08/15  23:50:28.0  XRT_FLRCTRL_DIS_416_OG [0x1a0]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/08/15  23:50:30.0  XRT_CTRL_AUTO_403_OG [0x193]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/08/16  00:55:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/16  06:16:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/16  06:16:56.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2008/08/16  06:17:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2008/08/16  06:17:16.0  XRT_QT_PROG_SET_429_OG [0x1ad]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2008/08/16  06:17:18.0  XRT_FLD_DIS_419_OG [0x1a3]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2008/08/16  06:17:20.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/08/16  06:17:22.0  XRT_ARS_DIS_427_OG [0x1ab]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2008/08/16  06:19:00.0  XRT_CTRL_AUTO_444_OG [0x1bc]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/08/16  06:26:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/08/16  11:14:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
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