

# XRT Timeline to be uploaded on 2008/08/21

Period: 2008/08/21 10:34:00 - 2008/08/25 10:08:00

\* \* \* \* \*

Normal mode

\* \* \* \* \*

XOB #15B7: XBP Q95 Al/poly (AEC4) + Ti/poly (AEC4) + Thin-Be (AEC0)- med cadence -FOV512																								
Term	Pointing (x, y)	Comment																						
08/21 11:05:06 - 08/21 12:21:00	Track ( -4.4, -8.1) @ 08/21 10:44:00	# OP start + 10min, HOP 74, with SST.																						
08/22 08:25:06 - 08/22 11:09:54	Track ( -29.4, -7.9) @ 08/22 08:00:00	HOP 74, with SST.																						
08/23 08:02:00 - 08/23 10:07:00	Track ( -29.4, -7.9) @ 08/23 08:00:00	HOP 74, with SST.																						
<b>PROG= 08 Inf.-time(s)</b>																								
└─ Subr= 1 1-time(s) 120.0sec																								
└─ Seqn= 77 10-time(s) 60.0sec																								
└─ Al-poly/Open C-poly/Open close Safe Norm 8.00s Obs 1x1 512x512 (1024, 1024) Q=95 4 0 2.0sec																								
└─ Open/Ti-poly Open/thick-Al close Safe Norm 8.00s Obs 1x1 512x512 (1024, 1024) Q=95 4 0 2.0sec																								
└─ Subr= 2 1-time(s) 4.0sec																								
└─ Seqn= 56 1-time(s) 4.0sec																								
└─ thin-Be/Open thin-Be/Open close Safe Norm 64.0s Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec																								
<table style="width: 100%; border: none;"> <tr> <td style="width: 10%;">Default Filter</td> <td style="width: 10%;">Thicker Filter</td> <td style="width: 10%;">VLS</td> <td style="width: 10%;">mode</td> <td style="width: 10%;">image</td> <td style="width: 10%;">Exp.</td> <td style="width: 10%;">CCD</td> <td style="width: 10%;">Bin</td> <td style="width: 10%;">ROI: size (center)</td> <td style="width: 10%;">Comp.</td> <td style="width: 10%;">AEC Buffer</td> <td style="width: 10%;">Interval</td> </tr> </table>													Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval													

XOB #15AE: CH - Al/Mesh (16s) + Ti/Poly (32s) - 512x512 - Q95 - with half-res. full frame																								
Term	Pointing (x, y)	Comment																						
08/21 12:43:06 - 08/21 17:55:54	Fixed ( 0.0, 922.0)	* N pole (EIS Peter Young density, etc.).																						
<b>PROG= 15 Inf.-time(s)</b>																								
└─ Subr= 1 1-time(s) 12.0sec																								
└─ Seqn= 87 1-time(s) 4.0sec																								
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
└─ Seqn= 72 1-time(s) 2.0sec																								
└─ Open/Al-mesh Open/Al-mesh close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec																								
└─ Seqn= 76 1-time(s) 4.0sec																								
└─ Open/Ti-poly Open/thick-Al close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
└─ Open/Ti-poly Open/thick-Al close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
└─ Subr= 2 3-time(s) 1200.0sec																								
└─ Seqn= 79 1-time(s) 4.0sec																								
└─ Open/Al-mesh Open/Al-mesh close Safe Dark 16.0s Obs 1x1 512x512 (1024, 1024) Q=98 0 0 2.0sec																								
└─ Seqn= 2 10-time(s) 120.0sec																								
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 16.0s Obs 1x1 512x512 (1024, 1024) Q=95 0 0 0.5sec																								
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 32.0s Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec																								
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Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval													

XOB #15B3: Synoptic Q95 2x2 - Al/poly(1024/11571) + Dark cal(512 Q98) + Ti-poly(2048/23142) + G-band(16)																								
Term	Pointing (x, y)	Comment																						
08/21 17:58:00 - 08/21 18:05:54	Fixed ( 0.0, 0.0)	synoptic, shifted -4.0 min																						
08/22 17:46:00 - 08/22 17:53:54	Fixed ( 0.0, 0.0)	synoptic, shifted -16.0 min																						
<b>PROG= 20 1-time(s)</b>																								
└─ Subr= 1 1-time(s) 12.0sec																								
└─ Seqn= 6 1-time(s) 4.0sec																								
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
└─ Seqn= 72 1-time(s) 2.0sec																								
└─ Open/Al-mesh Open/Al-mesh close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec																								
└─ Seqn= 89 1-time(s) 4.0sec																								
└─ Open/Ti-poly Open/thick-Al close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
└─ Open/Ti-poly Open/thick-Al close Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
└─ Seqn= 92 1-time(s) 2.0sec																								
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
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Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval													

XOB #15B4: Synoptic Q95 2x2 - Al/mesh(1024/11571) + Dark cal(512 Q98) + Ti-poly(2048/23142) + G-band(16)																								
Term	Pointing (x, y)	Comment																						
08/22 05:43:00 - 08/22 05:50:54	Fixed ( 0.0, 0.0)	synoptic, shifted -19.0 min																						
08/23 06:02:00 - 08/23 06:09:54	Fixed ( 0.0, 0.0)	synoptic																						
<b>PROG= 03 1-time(s)</b>																								
└─ Subr= 1 1-time(s) 12.0sec																								
└─ Seqn= 23 1-time(s) 4.0sec																								
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
└─ Seqn= 72 1-time(s) 2.0sec																								
└─ Open/Al-mesh Open/Al-mesh close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec																								
└─ Seqn= 89 1-time(s) 4.0sec																								
└─ Open/Ti-poly Open/thick-Al close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
└─ Open/Ti-poly Open/thick-Al close Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
└─ Seqn= 92 1-time(s) 2.0sec																								
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																								
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Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval													

**XOB #15B2: XBP Q90 Al/poly (AEC4) + Ti/poly (AEC4) + Thin-Be (AEC0)-high cadence**

Term	Pointing (x, y)	Comment
08/22 11:40:00 - 08/22 17:43:54	Track ( 97.3, 207.2) @ 08/22 11:10:00	* EIS cancelation study.
<b>PROG= 14 Inf.-time(s)</b>		
Subr= 1	1-time(s) 400.0sec	
└─ Seqn= 93	10-time(s) 60.0sec	
└─ Al-poly/Open	C-poly/Open close Safe Norm 250ms	Obs 1x1 384x384 (1024, 1024) Q=90 4 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close Safe Norm 1.00s	Obs 1x1 384x384 (1024, 1024) Q=90 4 0 2.0sec
Subr= 2	1-time(s) 4.0sec	
└─ Seqn= 43	1-time(s) 4.0sec	
└─ thin-Be/Open	thin-Be/Open close Safe Norm 64.0s	Obs 1x1 384x384 (1024, 1024) Q=90 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin	ROI: size (center) Comp. AEC Buffer Interval

\* \* \* \* \*

**Flare mode**

\* \* \* \* \*

NOT USED

\* \* \* \* \*

**Active Region Search**

\* \* \* \* \*

NOT USED

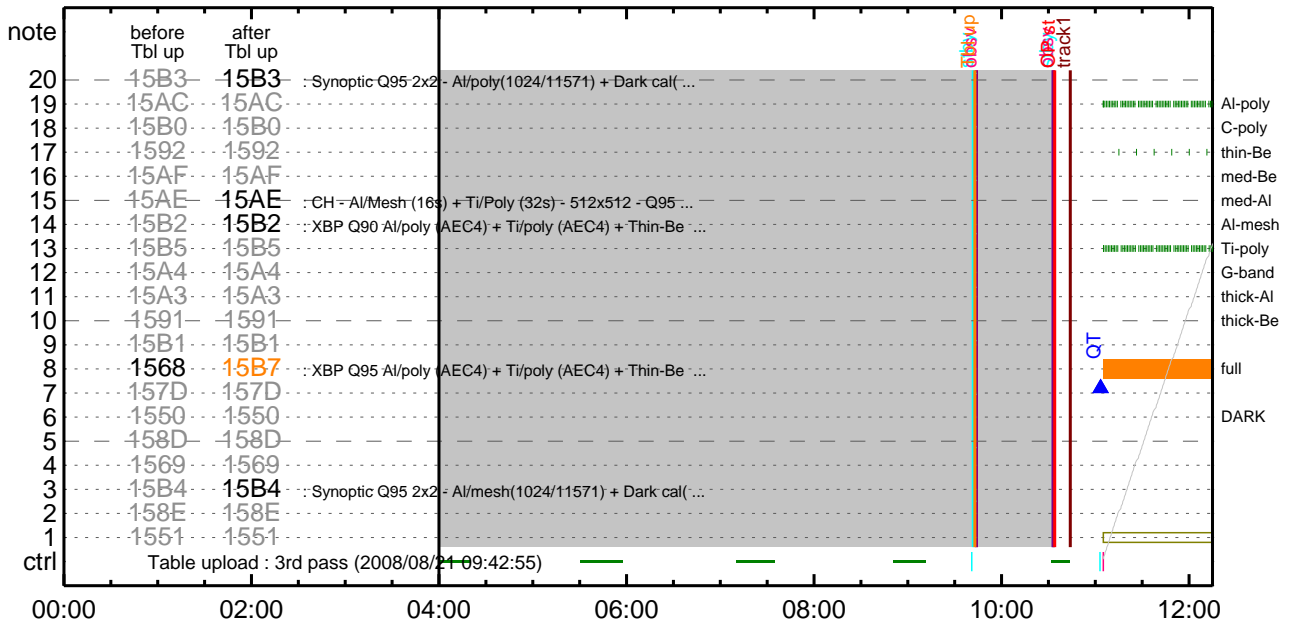
\* \* \* \* \*

**Flare Detection**

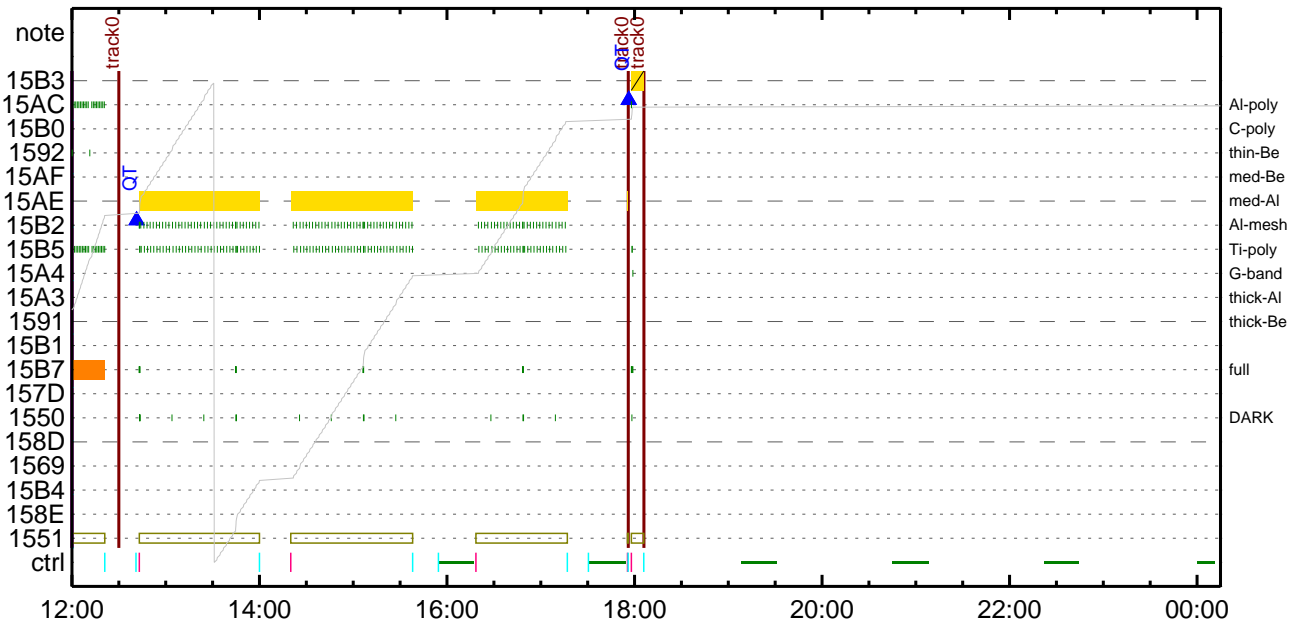
\* \* \* \* \*

NOT USED

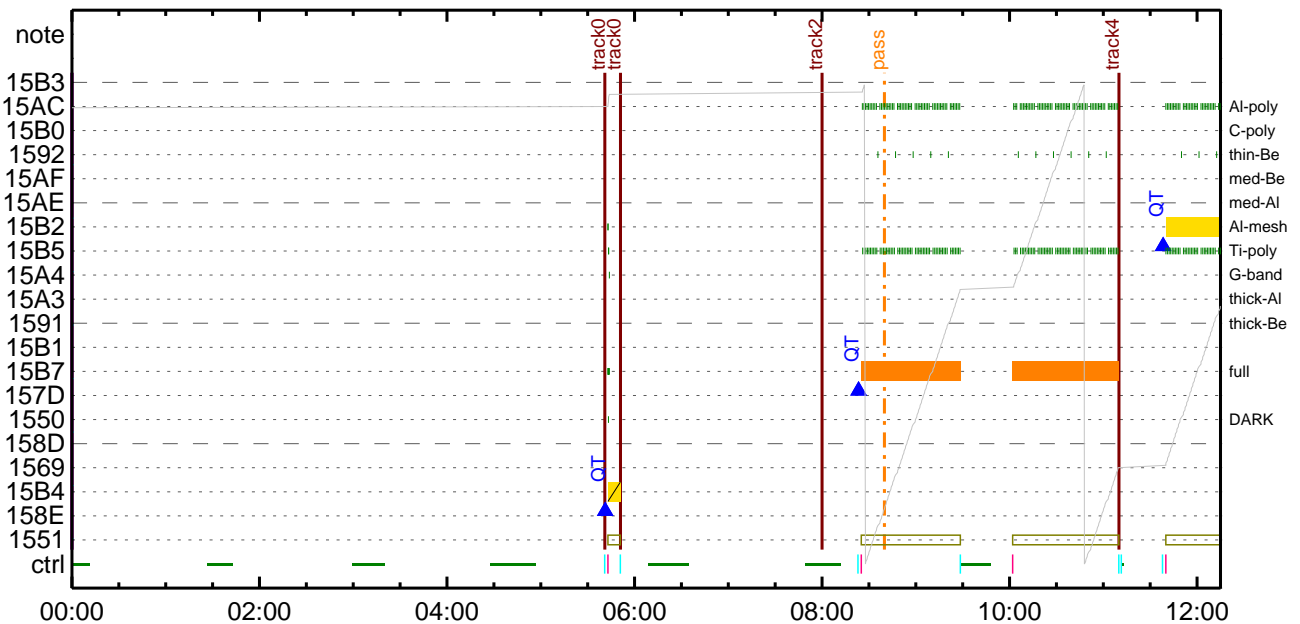
### CMDI #0096 2008/08/21



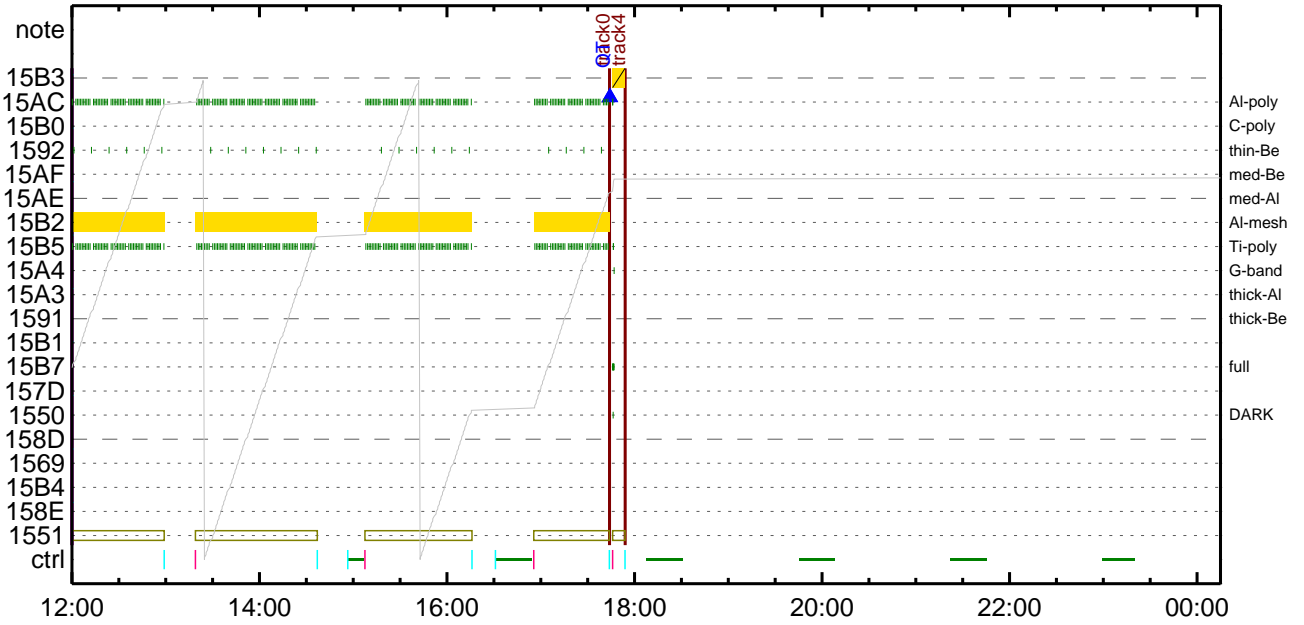
### CMDI #0096 2008/08/21



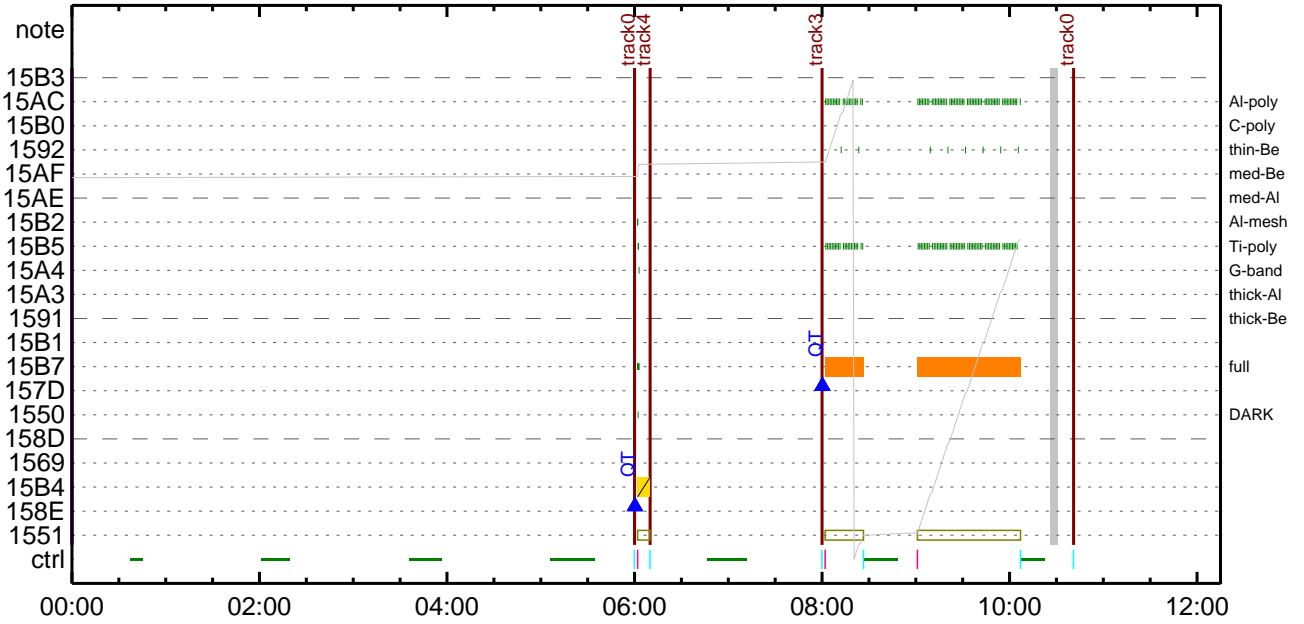
### CMDI #0096 2008/08/22



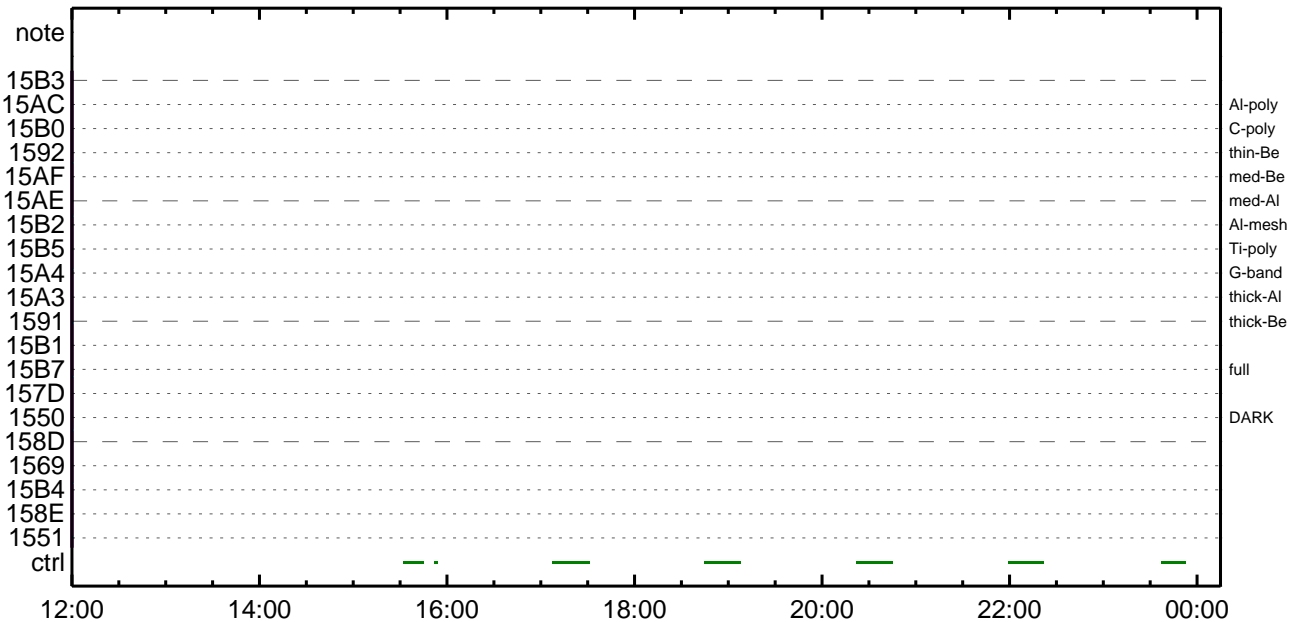
CMDI #0096 2008/08/22



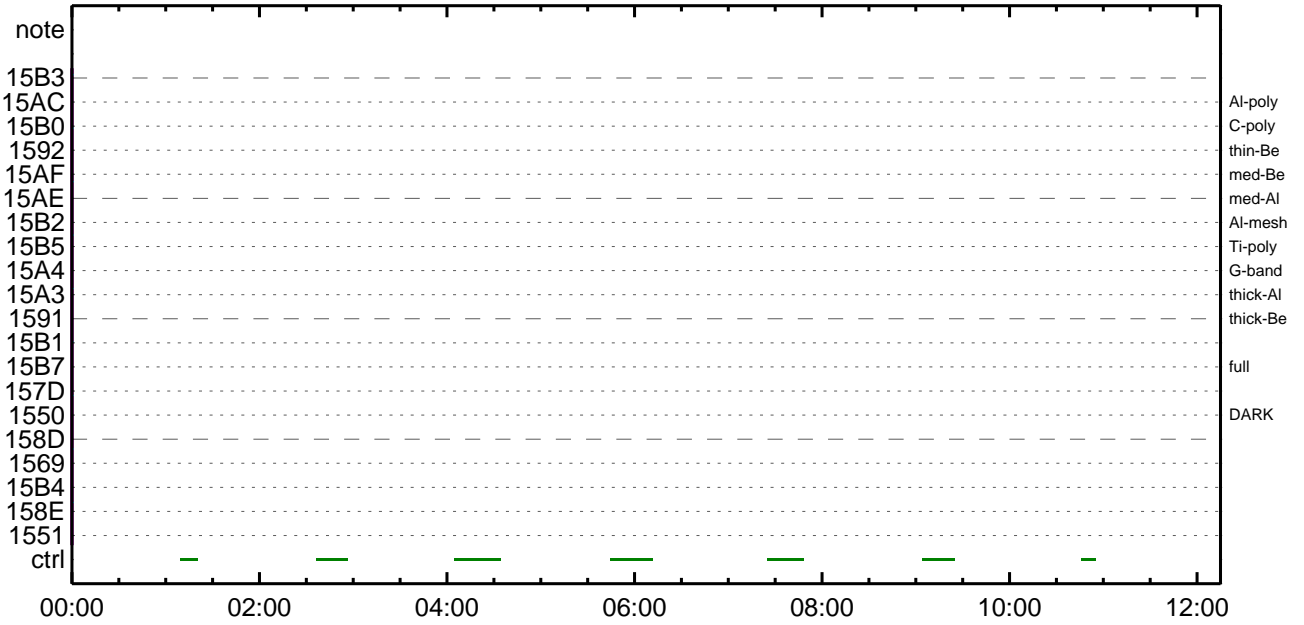
CMDI #0096 2008/08/23



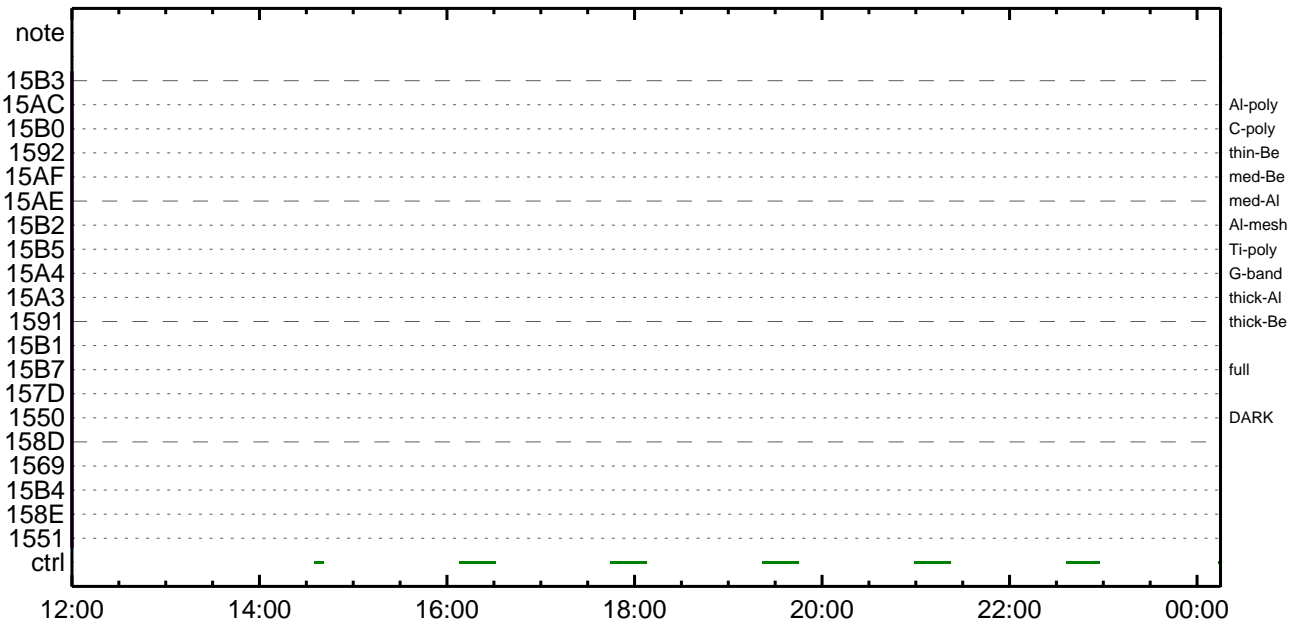
CMDI #0096 2008/08/23



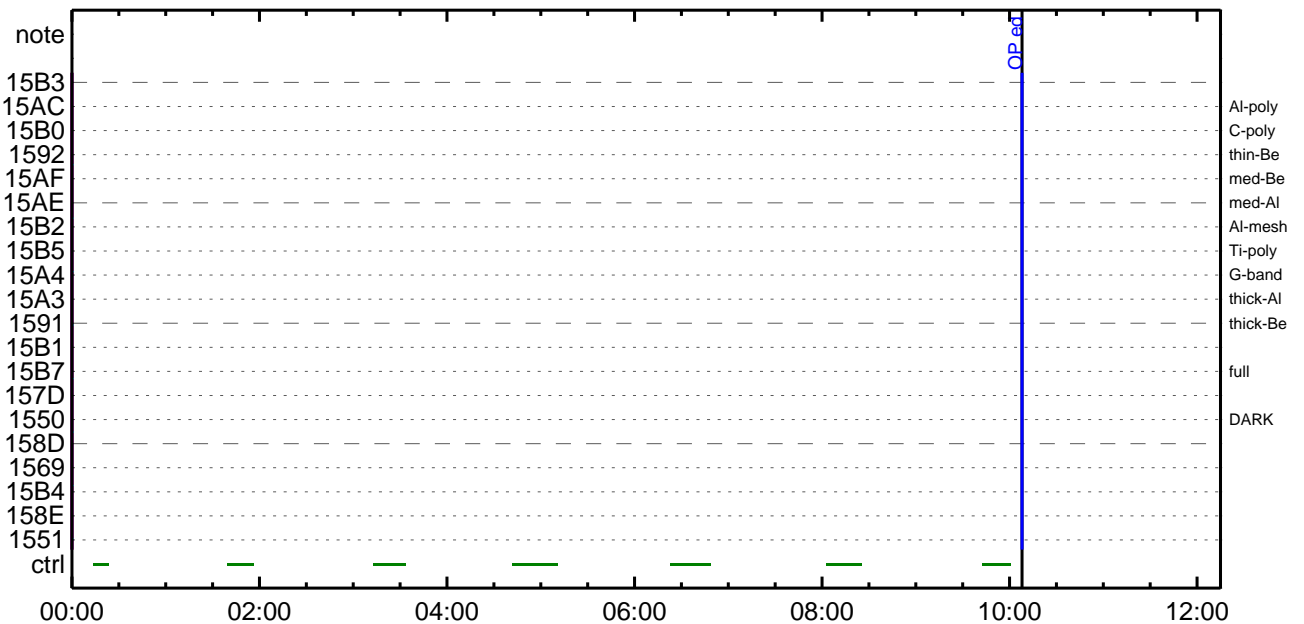
CMDI #0096 2008/08/24

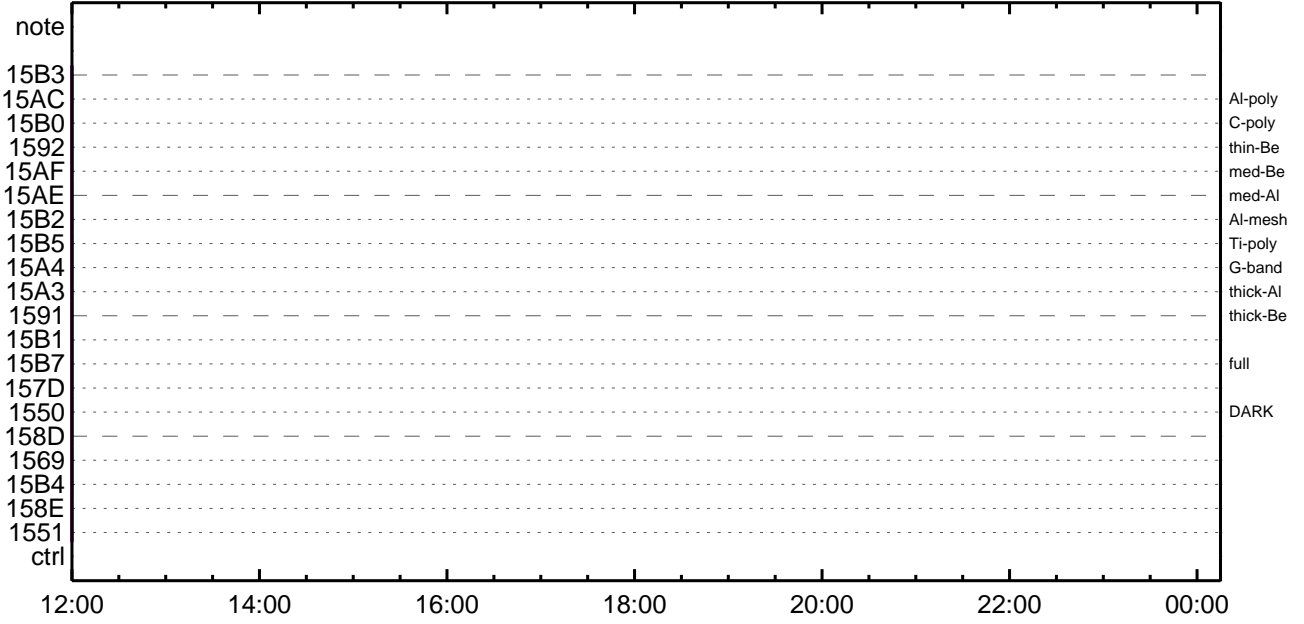


CMDI #0096 2008/08/24



CMDI #0096 2008/08/25





(a) Spacecraft Operation Procedure (real-commands)

```
main-084 2008-08-21 12:44:15 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YFYóYÉA+z©
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ëð¿ðÁð•µ°Ë»Í×ÁÇóÍYçYÁY×YÍ;¼YÉ;ËËè¼µ•íÉ;ËðÉ¼°ÇÓð•µ¿¼í¹çðÍ;çÁ®, ù¹ðèððçÁ+z©ð•ðËðð³ðÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ;|YÁYóY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ä
0019 . S. OP op-084:OP
0020 ()
0021 . S. OG og-084:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPí°èYÁYóY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÁYóY×¼ªª î»ðð³ îÇ§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGðí¼È¹ç•ë² İOKðð³ îÇ§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÁYóY×¼ªª î»ðð³ îÇ§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGðí¼È¹ç•ë² İOKðð³ îÇ§
0062 C.
0063 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÁYóY×¼ªª î»ðð³ îÇ§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OPðí¼È¹ç•ë² İOKðð³ îÇ§
0081 C.
0082 . C. ***** øÈ²¼øÍ¼Ã´ ¶ÍºøÈÈ¬øÁ+z© (¼ªµ-YÁYóY×¼ªªçððÁÖÃæç¼ªª ð²ð¼¼í¹çðçðå) *****
0083 C. DHUYá;¼YÉ;Ë¼Y¼; Yí;¼YÉ;Ëððíá¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;§ OPOG UPLOADð¬Á+z©NGðí¼È¹ç;ç°È²¼øÍ¼TI-CMDÁ+z©ðí¼Á¹Ôð•ðÈðð³ðÈ;f
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0096 C.                0300; SET0EDUMP0I00iYNY10C100|030E; E
0097 C.
0098 C. TIY3YBY0Y0E000ADi0 (UT)
0099 +. TI 2008-08-21 10:29:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                00[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0102 C.
0103 +. TI 2008-08-21 10:29:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                00[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0106 C.
0107 +. TI 2008-08-21 10:29:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                00[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0110 C.
0111 +. TI 2008-08-21 10:33:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                00[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0114 C.
0115 C.                0E2%0IAE%iIN0IYAY$YAY-1aIU
0116 C.                00[HK1_TI_CMD_ENA/DIS]        EQ      ENA
0117 C.                00[HK1_TI_CMD_NUM]           EQ      4
0118 C.                00[HK1_NEXT_EXEC_PIM]        EQ      DHU
0119 C.                00[HK1_NEXT_EXEC_DC]         EQ      0xB3
0120 C.
0121 C. *****
0122 C. TI00EYAY0Yx
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; $ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                00[HK1_DMP_TOP_ADRS_1]        EQ      07
0129 C.                00[HK1_DMP_TOP_ADRS_0]        EQ      2B
0130 C.                00[HK1_DMP_BLOCK_NUM]         EQ      3
0131 C.                00[HK1_DMP_REPEAT_NUM]        EQ      0
0132 C.                00[HK1_DMA_DMP_PIM]          EQ      DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                00[HK1_PKT_FORM_NO]           EQ      7
0136 C.                00[HK1_PKT_GEN_TIME]          EQ      0.25 s
0137 C.                00[HK1_S_TLM_BIT_RATE]        EQ      32k
0138 C.                00[HK1_X_TLM_BIT_RATE]       EQ      4M
0139 C.                00[HK1_DMP_CHK_FLG]          EQ      EXEC
0140 C.
0141 C.                YAY0Yx%aI»003IC$
0142 C.                00[HK1_DMP_CHK_FLG]          EQ      NON
0143 C.
0144 C. RAM ID=TI_TBL0I%E1C.e2IOK003IC$
0145 C.
0146 C. DHUYa;Y%E;E%Y%,Yi;Y%E;E00IA01
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                00[HK1_PKT_FORM_NO]           EQ      2
0150 C.                00[HK1_PKT_GEN_TIME]          EQ      0.5S
0151 C.                00[HK1_S_TLM_BIT_RATE]        EQ      32K
0152 C.                00[HK1_X_TLM_BIT_RATE]       EQ      4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2008-08-21 10:33:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 C. -----
0162 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2008-08-21 10:33:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2008-08-21 10:33:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 C.                [ ] [HK1_TI_CMD_NUM]          EQ      2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2008-08-21 10:33:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 C.                [ ] [HK1_TI_CMD_NUM]          EQ      1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 C. ***** MDP `uAI0I»0%Y0EAD010EDCBC.x2e *****
0192 C. (%a0iY0YAYEY0Y0YAY0Y0Y0E%000%AY»U010e)
0193 C. DC-BC dcbc-402:DCBC

```



```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```







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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-263:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Stop SP table >
0120 +. DC 07-F0 MDP_SP_CTRL_MANU
0121 BC (61)
0122 C. -----
0123 C. MDP_SP_CTRL_MODE = MANU [ ]
0124 C. -----
0125 C.
0126 . C. <Upload SP Observation Table>
0127 . S. RAM ram-283:MDP_OBS_S
0128 ( )
0129 C.
0130 . C. < Dump RAMID=MDP_OBS_S >
0131 +. DC 07-F0 MDP_DUMP_SPTBL
0132 BC (83 07 00 00 00 38 b8)
0133 C. -----
0134 C. MDP_OBS_S verify = OK/NG [ ]
0135 C. -----
0136 C.
0137 C. *****
0138 C. SOT TI command set
0139 C. *****
0140 C. Execute, after the success of TBL upload.
0141 +. TI 2008-08-21 10:33:18.0
0142 DC 07-F0 MDP_SOT_MODE_OBSV
0143 BC (40)
0144 . C. -----
0145 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0146 C. -----
0147 C.
0148 C.
0149 C. ***** XRT START *****
0150 C.
0151 +. DC 07-F0 MDP_XRT_CTRL_MANU
0152 BC (c1)
0153 + DC 07-F0 MDP_XRT_MODE_STBY
0154 BC (c3)
0155 . C. ----- Success Verify ? OK / NG____
0156 C.
0157 C. XRT Obs. Table Upload
0158 . S. RAM ram-291:MDP_OBS_X
0159 ( )
0160 C.
0161 +. DC 07-F0 MDP_DUMP_XRTTBL
0162 BC (84 07 00 00 00 3a d4)
0163 . C. ----- Comparison Check ? OK / ERR ____
0164 C.
0165 C.
0166 +. DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 01 b1 b1 04 04)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 02 b1 b1 08 08)
0170 + DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 03 b1 b1 08 08)
0172 + DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 04 b1 b1 06 06)
0174 + DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 06 80 80 08 08)
0176 + DC 07-F0 MDP_XRT_ROI_SET
0177 BC (cd 07 80 80 20 20)
0178 + DC 07-F0 MDP_XRT_ROI_SET
0179 BC (cd 08 80 80 06 06)
0180 + DC 07-F0 MDP_XRT_ROI_SET
0181 BC (cd 0f 80 80 06 06)
0182 + DC 07-F0 MDP_XRT_ROI_SET
0183 BC (cd 10 80 80 04 04)
0184 . C. ----- Success Verify ? OK / NG ____
0185 C.
0186 C.
0187 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0188 C.
0189 +. DC 07-F0 MDP_XRT_MODE_OBSV
0190 BC (c2)
0191 +. TI 2008-08-21 10:33:02.0
0192 DC 07-F0 MDP_XRT_MODE_OBSV
0193 BC (c2)

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0194 . C. ----- Success Verify ? OK / NG \_\_\_\_  
0195 C.  
0196 C. \*\*\*\*\* XRT END \*\*\*\*\*  
0197 C.  
0198 . C. \*\*\*\*\* MDP 'ûÃïáî»ô¼ŷâÈÄð¹âëDCBC•x²è \*\*\*\*\*  
0199 C. (¼â°îŷÔŷÃŷÈŷPŷÈŷáŷçŷéâÉ¼â¼Ä»Û¹âé)  
0200 . S. DC-BC dcbc-402:DCBC  
0201 (MDP\_known\_event)  
0202 C.  
0203 C.  
0204 . C. \*\*\*\*\* ŷDŷ¹•İ Daily±;îŃâÈ'Ø¹âëDCBC•x²è \*\*\*\*\*  
0205 . S. DC-BC dcbc-153:DCBC  
0206 (SPECIAL-CMD\_DAILY\_OPERATIN\_DCB)  
0207 C.  
0208 C.  
0209 . C. ;ãLOŷÃŷŶŷÃŷ-¼Ä»Û;ã  
0210 C.  
0211 . C. \*\*\*\*\* LOS \*\*\*\*\*  
0212 C.

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\*\*\* OP Sequence for XRT \*\*\*

2008/08/21	10:44:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01	00	00	00	00
2008/08/21	11:03:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/21	11:03:02.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2008/08/21	11:03:22.0	XRT_QT_PROG_SET_412_OG [0x19c]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	08			
2008/08/21	11:05:00.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/08/21	11:05:02.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/08/21	11:05:04.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/08/21	11:05:06.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/08/21	12:21:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/21	12:30:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	ae	0d	00	00
2008/08/21	12:41:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/21	12:41:02.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2008/08/21	12:41:22.0	XRT_QT_PROG_SET_415_OG [0x19f]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2008/08/21	12:43:00.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/08/21	12:43:02.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/08/21	12:43:04.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/08/21	12:43:06.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/08/21	14:00:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/21	14:20:00.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/08/21	15:38:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/21	15:54:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/21	16:17:30.0	XRT_Custom_430_OG [0x1ae]							
2008/08/21	16:18:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/08/21	17:17:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/21	17:30:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/21	17:54:30.0	XRT_Custom_430_OG [0x1ae]							
2008/08/21	17:55:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/08/21	17:55:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/21	17:55:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2008/08/21	17:56:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2008/08/21	17:56:16.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2008/08/21	17:56:18.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/08/21	17:56:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/08/21	17:56:22.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/08/21	17:58:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/08/21	18:05:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/21	18:06:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	ae	0d	00	00
2008/08/22	05:40:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/22	05:40:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2008/08/22	05:41:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2008/08/22	05:41:16.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2008/08/22	05:41:18.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/08/22	05:41:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/08/22	05:41:22.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/08/22	05:43:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/08/22	05:50:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/22	05:51:00.0	AOCS_Ore-point_Start_2_OG [0x098]							

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2008/08/22	08:00:00.0	AOCs_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	ae	0d	00	00
			AOCU_NM	5	02-76	02	00	00	00	00
2008/08/22	08:23:00.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2008/08/22	08:23:02.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2008/08/22	08:23:22.0	XRT_QT_PROG_SET_412_OG [0x19c]	MDP_XRT_QT_PROG_SET	2	07-F0		c4		08	
2008/08/22	08:25:00.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0		d5			
2008/08/22	08:25:02.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0		d9			
2008/08/22	08:25:04.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]	MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2008/08/22	08:25:06.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2008/08/22	09:28:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2008/08/22	10:02:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2008/08/22	11:09:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2008/08/22	11:10:00.0	AOCs_OrE-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	04	00	00	00	00
2008/08/22	11:11:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2008/08/22	11:37:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2008/08/22	11:37:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2008/08/22	11:38:16.0	XRT_QT_PROG_SET_411_OG [0x19b]	MDP_XRT_QT_PROG_SET	2	07-F0		c4		0e	
2008/08/22	11:39:54.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0		d5			
2008/08/22	11:39:56.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0		d9			
2008/08/22	11:39:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]	MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2008/08/22	11:40:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2008/08/22	12:59:00.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2008/08/22	13:19:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2008/08/22	14:37:00.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2008/08/22	14:56:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2008/08/22	15:06:30.0	XRT_Custom_430_OG [0x1ae]								
2008/08/22	15:07:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2008/08/22	16:16:00.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2008/08/22	16:31:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2008/08/22	16:54:30.0	XRT_Custom_430_OG [0x1ae]								
2008/08/22	16:55:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2008/08/22	17:43:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2008/08/22	17:43:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2008/08/22	17:44:00.0	AOCs_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00	00
2008/08/22	17:44:16.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0		c4		14	
2008/08/22	17:44:18.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0		d9			
2008/08/22	17:44:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2008/08/22	17:44:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0		d5			
2008/08/22	17:46:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2008/08/22	17:53:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2008/08/22	17:54:00.0	AOCs_OrE-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	04	00	00	00	00
2008/08/23	05:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2008/08/23	05:59:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2008/08/23	06:00:00.0	AOCs_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00	00
2008/08/23	06:00:16.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0		c4		03	
2008/08/23	06:00:18.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0		d9			
2008/08/23	06:00:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2008/08/23	06:00:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0		d5			



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2008/08/23	06:02:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/08/23	06:09:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/23	06:10:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2008/08/23	07:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/23	07:59:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2008/08/23	08:00:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2008/08/23	08:00:16.0	XRT_QT_PROG_SET_412_OG [0x19c]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2008/08/23	08:01:54.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/08/23	08:01:56.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/08/23	08:01:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/08/23	08:02:00.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/08/23	08:26:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/23	09:01:00.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/08/23	10:07:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/23	10:40:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/23	10:41:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				