

# XRT Timeline to be uploaded on 2008/08/31

Period: 2008/08/31 10:36:00 - 2008/09/04 11:26:00

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Normal mode

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XOB #15B7: XBP Q95 Al/poly (AEC4) + Ti/poly (AEC4) + Thin-Be (AEC0)- med cadence -FOV512													
Term	Pointing (x, y)							Comment					
08/31 10:39:07 - 08/31 12:00:00	Fixed ( , )												
09/01 08:02:00 - 09/01 11:55:30	Track ( -20.1, -8.0) @ 09/01 08:00:00							* HOP 74, tracking disk center QS with SST.					
<b>PROG= 08 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 120.0sec													
└─ Seqn= 77 10-time(s) 60.0sec													
└─ Al-poly/Open C-poly/Open close Safe Norm 8.00s Obs 1x1 512x512 (1024, 1024) Q=95 4 0 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Norm 8.00s Obs 1x1 512x512 (1024, 1024) Q=95 4 0 2.0sec													
└─ Subr= 2 1-time(s) 4.0sec													
└─ Seqn= 56 1-time(s) 4.0sec													
└─ thin-Be/Open thin-Be/Open close Safe Norm 64.0s Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec													
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #15BC: twilight monitor - 128x128													
Term	Pointing (x, y)							Comment					
08/31 13:43:36 - 08/31 13:58:30	Fixed ( , )												
09/01 02:52:36 - 09/01 03:05:30	Track ( 193.8, -278.3) @ 08/31 18:16:30							# Track bright point.					
<b>PROG= 01 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 30.0sec													
└─ Seqn= 78 1-time(s) 2.0sec													
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 16.0s Obs 1x1 128x128 (1024, 1024) Q=90 0 0 2.0sec													
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #15A3: Synoptic Q95 2x2 - Al/poly(512/5795) + Dark cal(512 Q98) + Ti-poly(723/11571) + G-band(16)													
Term	Pointing (x, y)							Comment					
08/31 18:08:30 - 09/01 02:50:30	Fixed ( 0.0, 0.0)							synoptic, shifted 6.5 min					
09/01 18:14:00 - 09/02 05:58:54	Fixed ( 0.0, 0.0)							synoptic, shifted 12.0 min					
<b>PROG= 11 1-time(s)</b>													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 18 1-time(s) 4.0sec													
└─ Al-poly/Open Al-poly/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Seqn= 72 1-time(s) 2.0sec													
└─ Open/Al-mesh Open/Al-mesh close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─ Seqn= 76 1-time(s) 4.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Seqn= 92 1-time(s) 2.0sec													
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #15A4: Synoptic Q95 2x2 - Al/mesh(512/5795) + Dark cal(512 Q98) + Ti-poly(723/11571) + G-band(16)													
Term	Pointing (x, y)							Comment					
09/01 06:27:00 - 09/01 07:59:54	Fixed ( 0.0, 0.0)							synoptic, shifted 25.0 min					
09/02 06:01:00 - 09/02 07:59:54	Fixed ( 0.0, 0.0)							synoptic, shifted -1.0 min					
<b>PROG= 12 1-time(s)</b>													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 87 1-time(s) 4.0sec													
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Seqn= 72 1-time(s) 2.0sec													
└─ Open/Al-mesh Open/Al-mesh close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─ Seqn= 76 1-time(s) 4.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Seqn= 92 1-time(s) 2.0sec													
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #15B2: XBP Q90 Al/poly (AEC4) + Ti/poly (AEC4) + Thin-Be (AEC0)-high cadence													
Term	Pointing (x, y)							Comment					
09/02 08:02:00 - 09/02 10:12:00	Track ( -20.1, -7.9) @ 09/02 08:00:00							* HOP 74, disk center tracking of QS with SST.					
<b>PROG= 14 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 400.0sec													
└─ Seqn= 93 10-time(s) 60.0sec													
└─ Al-poly/Open C-poly/Open close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=90 4 0 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=90 4 0 2.0sec													
└─ Subr= 2 1-time(s) 4.0sec													
└─ Seqn= 43 1-time(s) 4.0sec													

L	thin-Be/Open	thin-Be/Open	close	Safe	Norm	64.0s	Obs	1x1	384x384 (1024, 1024)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Flare mode**

\* \* \* \* \*

NOT USED

\* \* \* \* \*

**Active Region Search**

\* \* \* \* \*

NOT USED

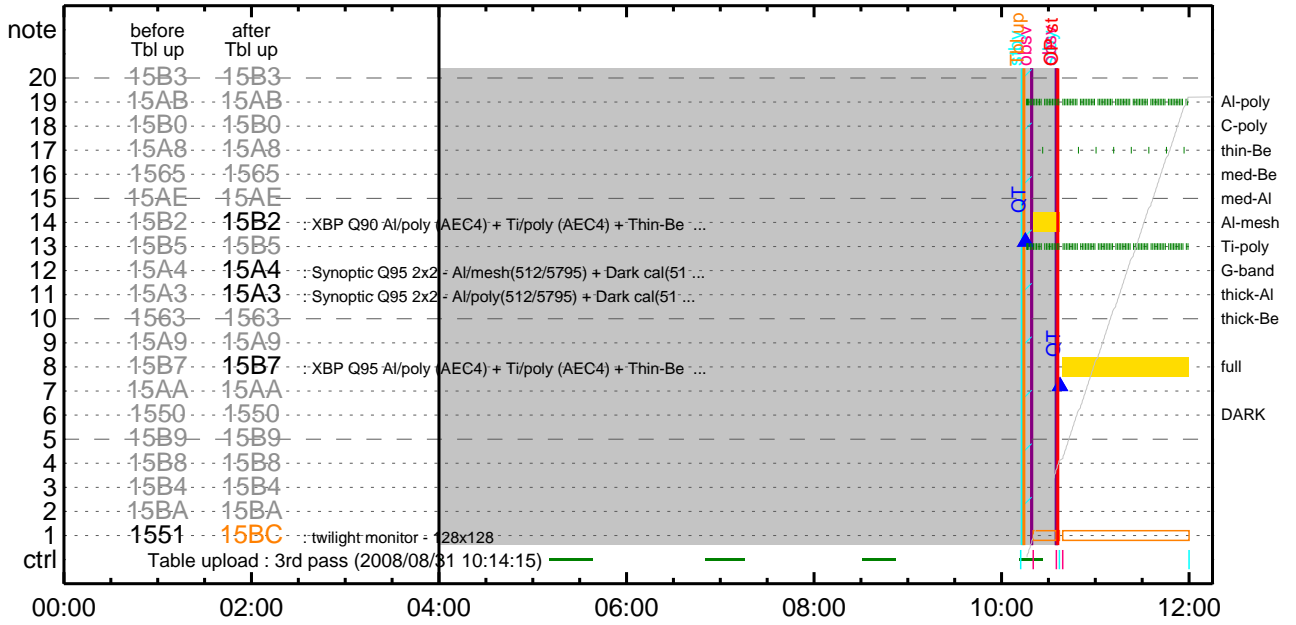
\* \* \* \* \*

**Flare Detection**

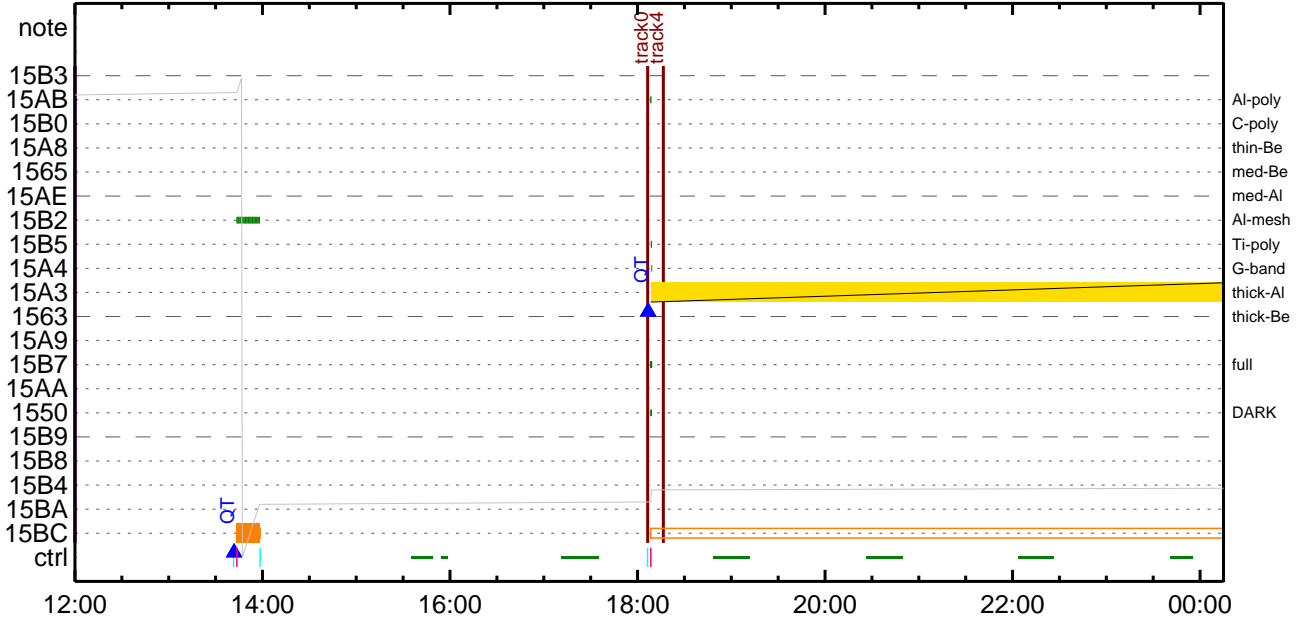
\* \* \* \* \*

NOT USED

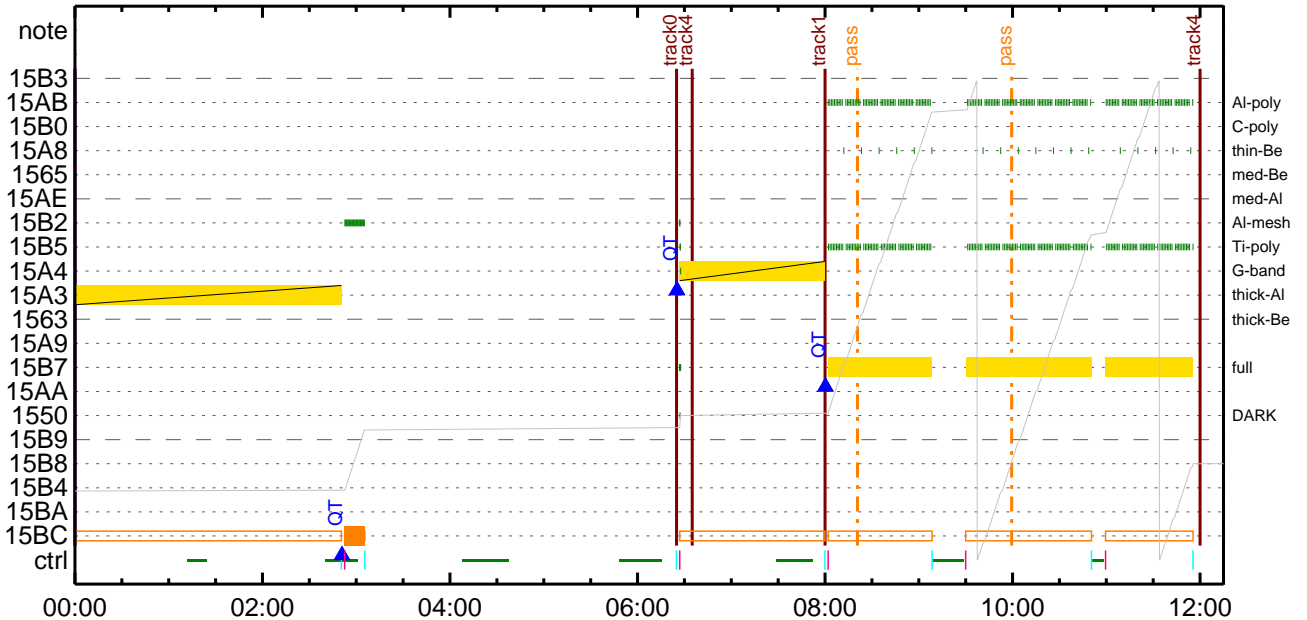
### CMDI #0109 2008/08/31



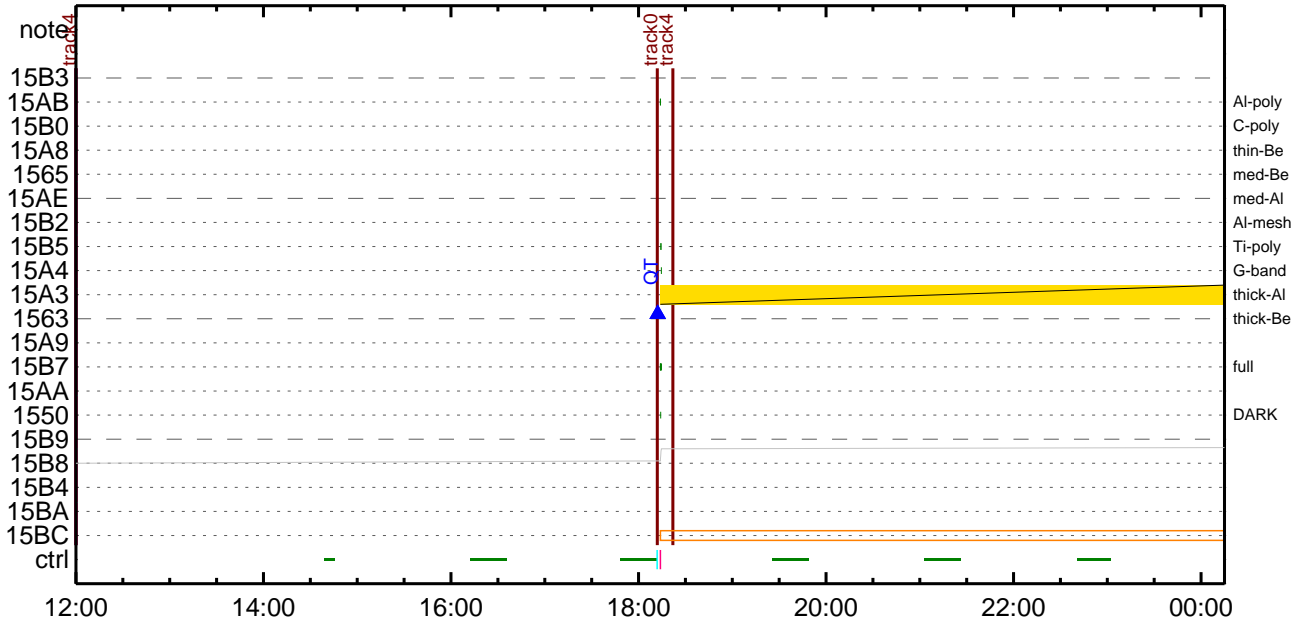
### CMDI #0109 2008/08/31



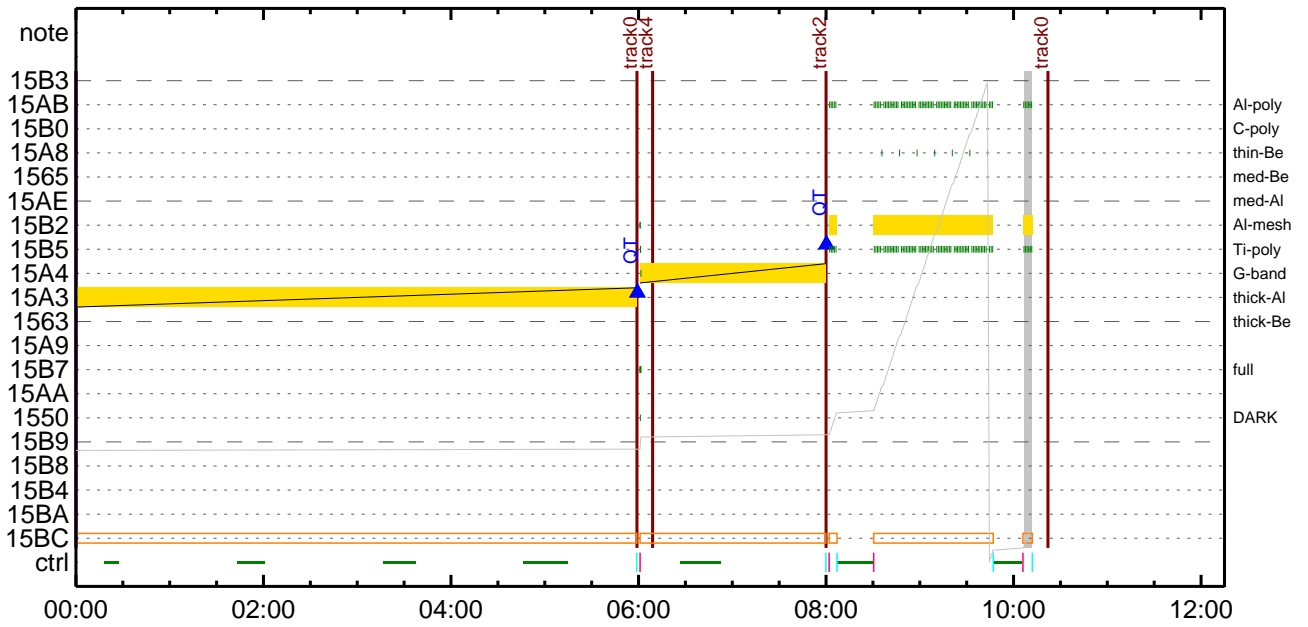
### CMDI #0109 2008/09/01



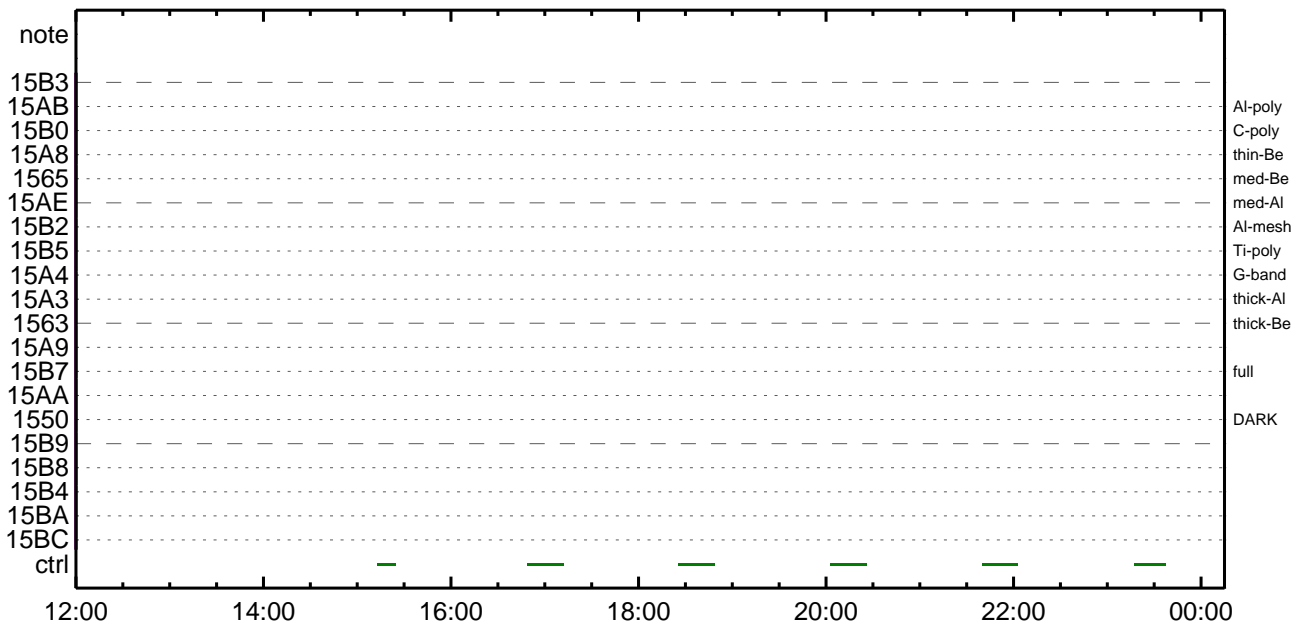
### CMDI #0109 2008/09/01



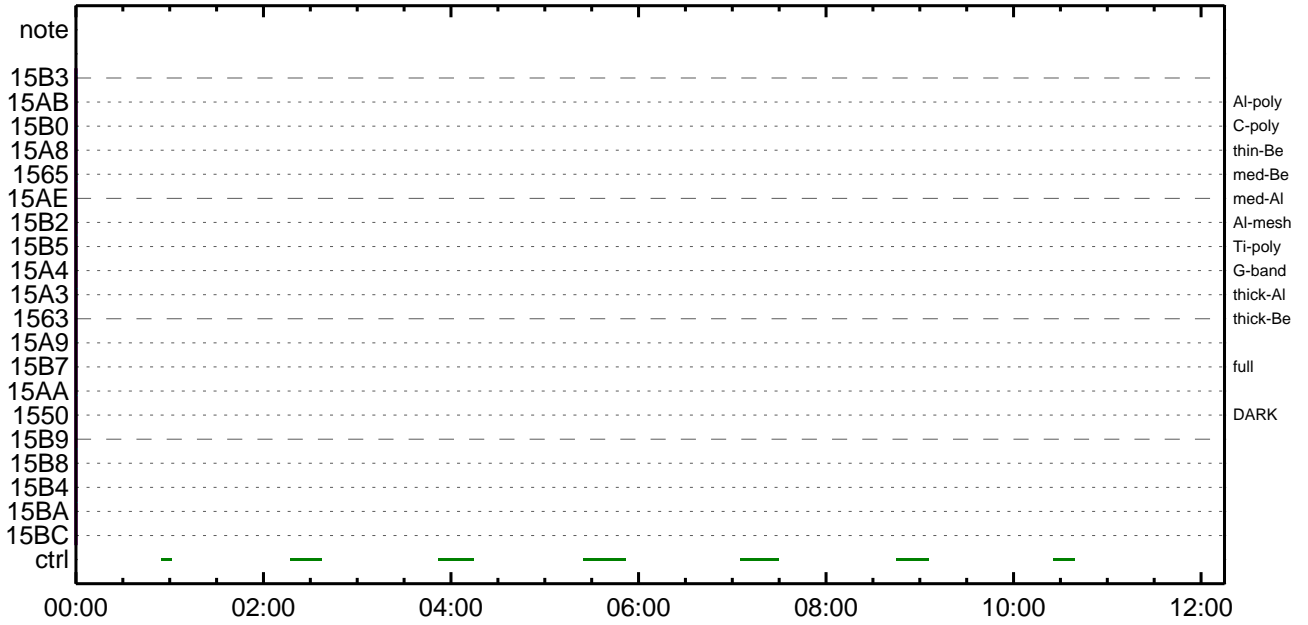
### CMDI #0109 2008/09/02



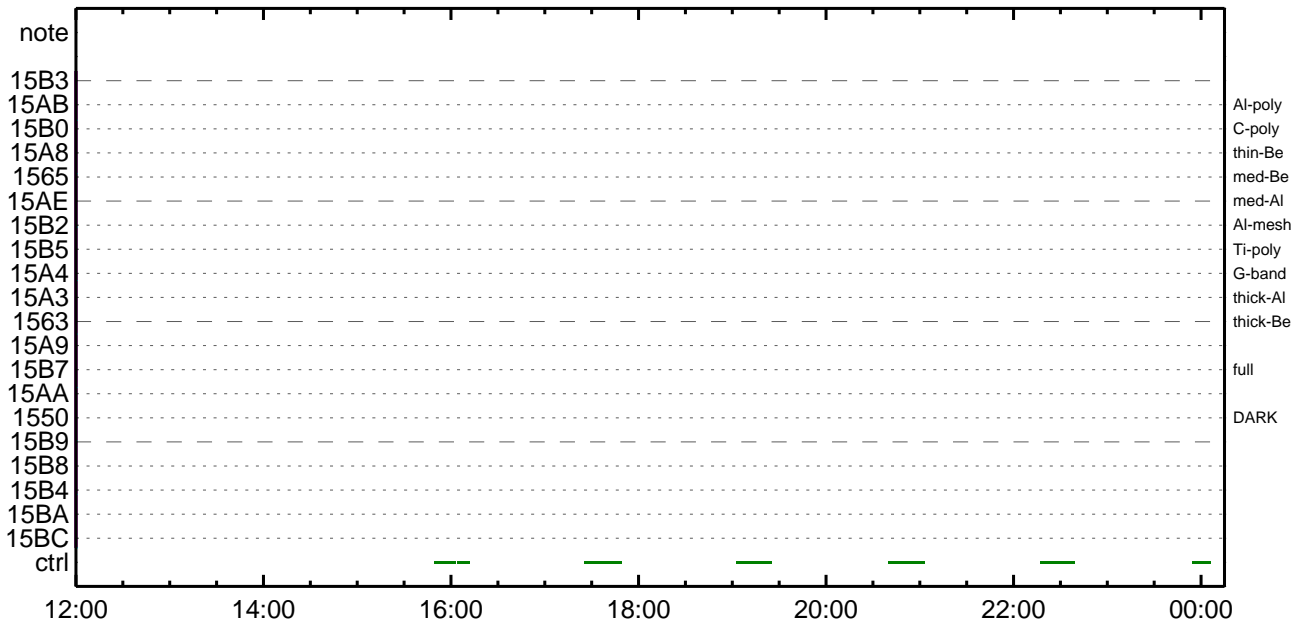
### CMDI #0109 2008/09/02



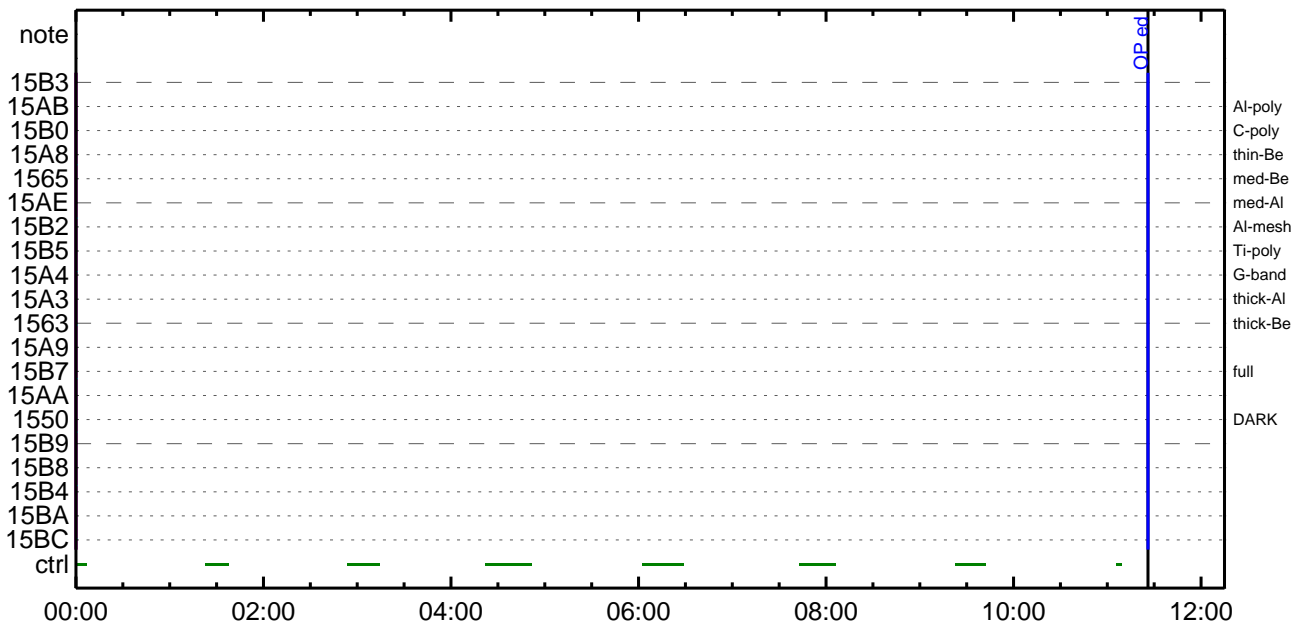
CMDI #0109 2008/09/03



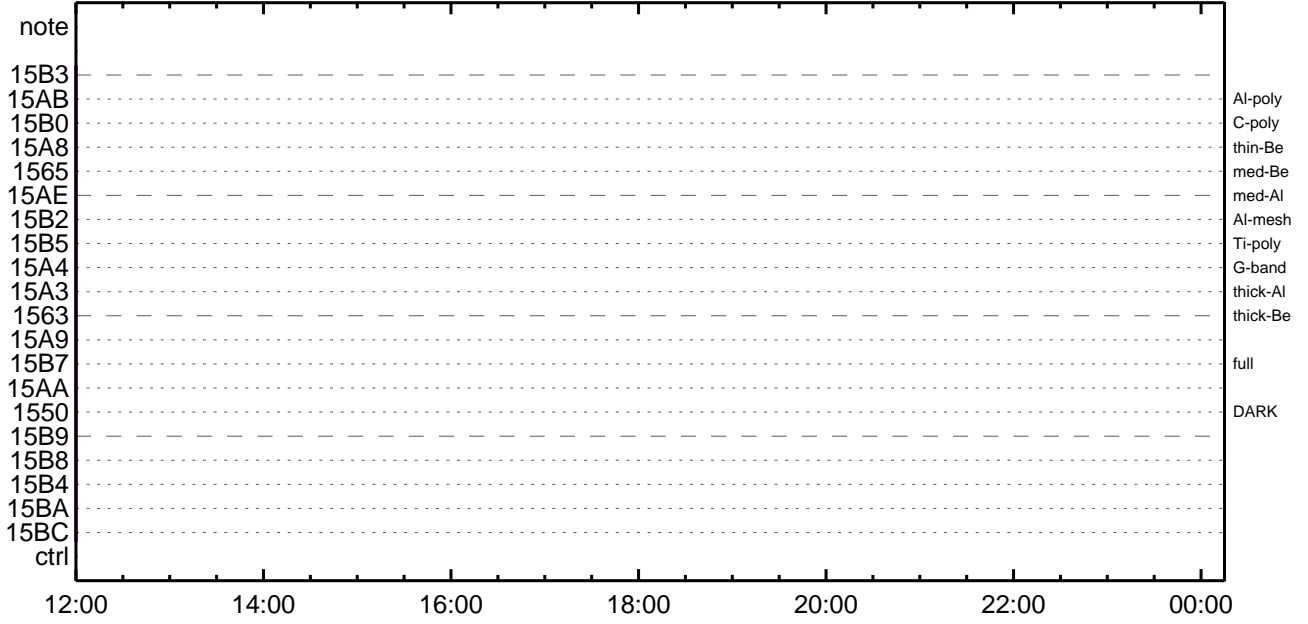
CMDI #0109 2008/09/03



CMDI #0109 2008/09/04



CMDI #0109 2008/09/04





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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ä
0100 C. *****
0101 C.
0102 C. ;äOP/OGY1;4YE;ä
0103 S. OP op-103:OP
0104 ( )
0105 S. OG og-103:OG
0106 ( )
0107 C.
0108 C. ;äNMOG&OPfî°èYÄYóYx;ä
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YÄYóYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YÄYóYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YÄYóYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ä´¶Á°òEÉ-ò°Á÷¿@ (¼âµ-YÄYóYx½ê½çòðÁÔæòÇ¼ª°"òè¼i¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½i¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½Á¹Ôª°²E²²²³òE;f
0180 C. ²²²¿;çSET²E²DUMP²îE±²iYÑY¹²ç¹Ôª|²³²E;f
0181 C.
0182 C. TIY³Y²YóYÉ²òðÁDî¿(UT)
0183 +. TI 2008-08-31 10:31:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2008-08-31 10:31:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2008-08-31 10:31:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0096 C. MDP_OBS_F verify = OK/NG [ ]
0097 C. -----
0098 C.
0099 . C. < Upload DPL table >
0100 C.
0101 C. ¼ç¥Ã¥×¥í;¼¥É°îÁ°¤ESTS_CHK¤ðOFF¤È¤¹¤è
0102 C.
0103 . S. RAM ram-271:MDP_DPL
0104 ( )
0105 C.
0106 . C. < Dump RAMID=MDP_DPL >
0107 +. DC 07-F0 MDP_DUMP_FGTBL
0108 BC (82 07 00 38 b8 00 40)
0109 C. -----
0110 C. MDP_DPL verify = OK [ ]
0111 C. -----
0112 C.
0113 C. STS_CHK¤ðON¤È¤¹¤è
0114 C.
0115 . C. < Update MDP DSC PAR1 >
0116 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0117 BC (4c)
0118 C. MDP_CMD_CODE = F04C0700 [ ]
0119 C. MDP_CMD_CNT (count-up 1) [ ]
0120 C. -----
0121 C.
0122 . C.
0123 . C. < Resume FG table (auto mode) >
0124 +. DC 07-F0 MDP_FG_CTRL_AUTO
0125 BC (50)
0126 . C. -----
0127 C. MDP_FG_CTRL_MODE = AUTO [ ]
0128 C. -----
0129 C.
0130 C. *****
0131 C. SOT TI command set
0132 C. *****
0133 C. Execute, after the success of TBL upload.
0134 +. TI 2008-08-31 10:35:18.0
0135 DC 07-F0 MDP_SOT_MODE_OBSV
0136 BC (40)
0137 . C. -----
0138 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0139 C. -----
0140 C.
0141 C. Only when FG_CTRL_AUTO is used in RT.
0142 +. TI 2008-08-31 10:35:20.0
0143 DC 07-F0 MDP_FG_CTRL_AUTO
0144 BC (50)
0145 . C. -----
0146 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0147 C. -----
0148 C. ***** SOT END *****
0149 C.
0150 . C. ***** MDP `ûÃî¤î»ö¼Ý¤È¤Ê¤¹¤èDCBC•x²è *****
0151 C. (¼á°î¼¥Ã¥¤È¥¤¥¥¼¥ç¥è¤È¼¤¤¼¤¤»Û¤¹¤è)
0152 . S. DC-BC dcbc-402:DCBC
0153 (MDP_known_event)
0154 C.
0155 C.
0156 . C. ***** ¥¤¥¹•î Daily±¿îÑ¤È´Ø¤¹¤èDCBC•x²è *****
0157 . S. DC-BC dcbc-153:DCBC
0158 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0159 C.
0160 C.
0161 . C. ¡ãLOS¥Ã¥¤¥¥¼¥»¼¤¤»Û¤¹¤è
0162 C.
0163 . C. ***** LOS *****
0164 C.

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Aug 31, 08 11:37

XRT\_OGLIST\_0109.chk

Page 1/2

\*\*\* OP Sequence for XRT \*\*\*

2008/08/31	10:37:00.5	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/31	10:37:03.5	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2008/08/31	10:37:23.5	XRT_QT_PROG_SET_412_OG [0x19c]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2008/08/31	10:39:01.5	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/08/31	10:39:03.5	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/08/31	10:39:05.5	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/08/31	10:39:07.5	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/08/31	12:00:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/31	13:41:30.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/31	13:41:32.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2008/08/31	13:41:52.0	XRT_QT_PROG_SET_400_OG [0x190]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2008/08/31	13:43:30.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/08/31	13:43:32.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/08/31	13:43:34.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/08/31	13:43:36.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/08/31	13:58:30.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/31	18:06:24.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/08/31	18:06:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2008/08/31	18:06:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2008/08/31	18:06:46.0	XRT_QT_PROG_SET_409_OG [0x199]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2008/08/31	18:06:48.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/08/31	18:06:50.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/08/31	18:06:52.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/08/31	18:08:30.0	XRT_CTRL_AUTO_444_OG [0x1bc]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/08/31	18:16:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2008/09/01	02:50:30.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/01	02:50:32.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2008/09/01	02:50:52.0	XRT_QT_PROG_SET_400_OG [0x190]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2008/09/01	02:52:30.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/09/01	02:52:32.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/09/01	02:52:34.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/09/01	02:52:36.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/09/01	03:05:30.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/01	06:24:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/01	06:24:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2008/09/01	06:25:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2008/09/01	06:25:16.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2008/09/01	06:25:18.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/09/01	06:25:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/09/01	06:25:22.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/09/01	06:27:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/09/01	06:35:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2008/09/01	07:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/01	07:59:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2008/09/01	08:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2008/09/01	08:00:16.0	XRT_QT_PROG_SET_412_OG [0x19c]							

Monday September 01, 2008

1/2

Aug 31, 08 11:37

## XRT\_OGLIST\_0109.chk

Page 2/2

2008/09/01	08:01:54.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2008/09/01	08:01:56.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2008/09/01	08:01:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2008/09/01	08:02:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2008/09/01	09:08:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2008/09/01	09:29:01.0	XRT_Custom_430_OG [0x1ae]					
2008/09/01	09:30:01.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2008/09/01	10:50:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2008/09/01	10:58:30.0	XRT_Custom_430_OG [0x1ae]					
2008/09/01	10:59:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2008/09/01	11:55:30.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2008/09/01	12:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	04 00 00 00 00	
2008/09/01	18:11:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2008/09/01	18:11:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2008/09/01	18:12:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00	
2008/09/01	18:12:16.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2008/09/01	18:12:18.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2008/09/01	18:12:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2008/09/01	18:12:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2008/09/01	18:14:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2008/09/01	18:22:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	04 00 00 00 00	
2008/09/02	05:58:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2008/09/02	05:58:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2008/09/02	05:59:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00	
2008/09/02	05:59:16.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c	
2008/09/02	05:59:18.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2008/09/02	05:59:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2008/09/02	05:59:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2008/09/02	06:01:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2008/09/02	06:09:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	04 00 00 00 00	
2008/09/02	07:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2008/09/02	07:59:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2008/09/02	08:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	02 00 00 00 00	
2008/09/02	08:00:16.0	XRT_QT_PROG_SET_411_OG [0x19b]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2008/09/02	08:01:54.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2008/09/02	08:01:56.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2008/09/02	08:01:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2008/09/02	08:02:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2008/09/02	08:07:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2008/09/02	08:29:30.0	XRT_Custom_430_OG [0x1ae]					
2008/09/02	08:30:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2008/09/02	09:47:00.5	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2008/09/02	10:05:00.0	XRT_Custom_430_OG [0x1ae]					
2008/09/02	10:06:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2008/09/02	10:12:00.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2008/09/02	10:22:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00	