XRT Timeline to be uploaded on 2008/09/02

Period: 2008/09/02 10:12:00 - 2008/09/06 10:30:00

* * * * * **Normal mode** * * * * *

XOB #15B7: XBP Q95 Al/poly (AEC4) + Ti/poly (AEC4) + Thin-Be (AEC0)- med cadence -FOV512

Term	Pointing (x, y)	Comment
09/02 10:18:06 - 09/02 11:59:54	Fixed (,)	

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
PROG= 08 Inf.-time(s)												
Subr= 1 1-time(s) 120.0sec												
Seqn= 77 10-time(s) 60.0sec												
Al-poly/Open	C-poly/Open	close	Safe	Norm	8.00s	Obs	1x1	512x512 (1024, 1024)	Q=95	4	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	1x1	512x512 (1024, 1024)	Q=95	4	0	2.0sec
Subr= 2 1-time(s) 4.0sec												
Seqn= 56 1-time(s) 4.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	64.0s	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec

XOB #15A3: Synoptic Q95 2x2 - Al/poly(512/5795) + Dark cal(512 Q98) + Ti-poly(723/11571) + G-band(16)

Term	Pointing (x, y)	Comment
09/02 18:02:00 - 09/02 18:49:54	Fixed (0.0, 0.0)	synoptic
09/03 18:06:30 - 09/04 05:35:24	Fixed (0.0, 0.0)	synoptic, shifted 4.5 min

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
PROG= 11 1-time(s)												
Subr= 1 1-time(s) 12.0sec												
Seqn= 18 1-time(s) 4.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 72 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 76 1-time(s) 4.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 92 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #15BD: CH - Al/Mesh (32s) + Ti/Poly (64s) - 512x512 - Q95 - with half-res. full frame

Term	Pointing (x, y)	Comment
09/02 18:52:00 - 09/02 20:29:54	Track (-44.0, -177.5) @ 09/02 18:50:00	HOP76 S1-dash
09/02 20:32:00 - 09/02 22:04:54	Track (-139.0, -247.8) @ 09/02 20:30:00	HOP76 S1
09/02 22:07:00 - 09/02 23:59:54	Track (-224.6, -348.1) @ 09/02 22:05:00	HOP76 S2

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
PROG= 13 Inf.-time(s)												
Subr= 1 1-time(s) 12.0sec												
Seqn= 87 1-time(s) 4.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 72 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 76 1-time(s) 4.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 6-time(s) 1200.0sec												
Seqn= 62 1-time(s) 4.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	32.0s	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 64 10-time(s) 120.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	32.0s	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	1.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	64.0s	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1550: G-Band Alignment with North Pole Q90 2x2(G-band only) 8min Cadence - ROI For Limb Alignment-3

Term	Pointing (x, y)	Comment
09/03 00:02:00 - 09/03 01:59:54	Fixed (0.0, 945.0)	Coalignment N-limb

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
PROG= 06 1-time(s)												
Subr= 1 1-time(s) 360.0sec												
Seqn= 58 13-time(s) 480.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec

XOB #1551: G-Band Alignment with East limb Q90 2x2 (G-band only) 8min Cadence - ROI for Limb Alignment-3

Term	Pointing (x, y)	Comment
09/03 02:02:00 - 09/03 03:51:30	Fixed (-945.0, 0.0)	Coalignment E-limb

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
PROG= 18 1-time(s)												
Subr= 1 1-time(s) 360.0sec												
Seqn= 30 13-time(s) 480.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec

	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval

XOB #15A4: Synoptic Q95 2x2 - Al/mesh(512/5795) + Dark cal(512 Q98) + Ti-poly(723/11571) + G-band(16)

Term	Pointing (x, y)	Comment
09/03 06:03:30 - 09/03 07:59:54	Fixed (0.0, 0.0)	synoptic, shifted 1.5 min
09/04 05:37:30 - 09/04 07:59:54	Fixed (0.0, 0.0)	synoptic, shifted -24.5 min

PROG= 12 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 87 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 72 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 76 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 92 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval

XOB #15B2: XBP Q90 Al/poly (AEC4) + Ti/poly (AEC4) + Thin-Be (AEC0)-high cadence

Term	Pointing (x, y)	Comment
09/03 08:02:00 - 09/03 11:59:54	Track (-20.1, -7.9) @ 09/03 08:00:00	HOP74 with SST
09/04 08:02:00 - 09/04 11:15:00	Track (-20.1, -7.9) @ 09/04 08:00:00	HOP74 with SST

PROG= 14 Inf.-time(s)

Subr= 1 1-time(s) 400.0sec

Seqn= 93 10-time(s) 60.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval
Al-poly/Open	C-poly/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=90	4	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=90	4	0	2.0sec

Subr= 2 1-time(s) 4.0sec

Seqn= 43 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	64.0s	Obs	1x1	384x384 (1024, 1024)	Q=90	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval

* * * * * **Flare mode** * * * * *

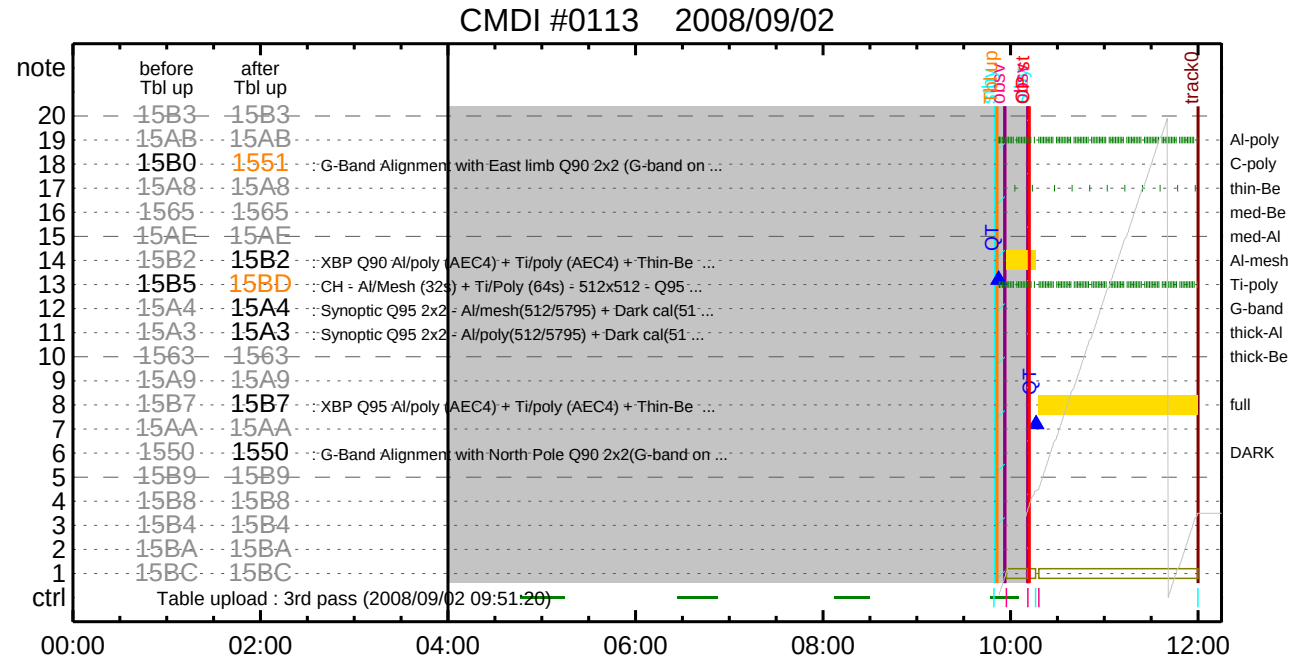
NOT USED

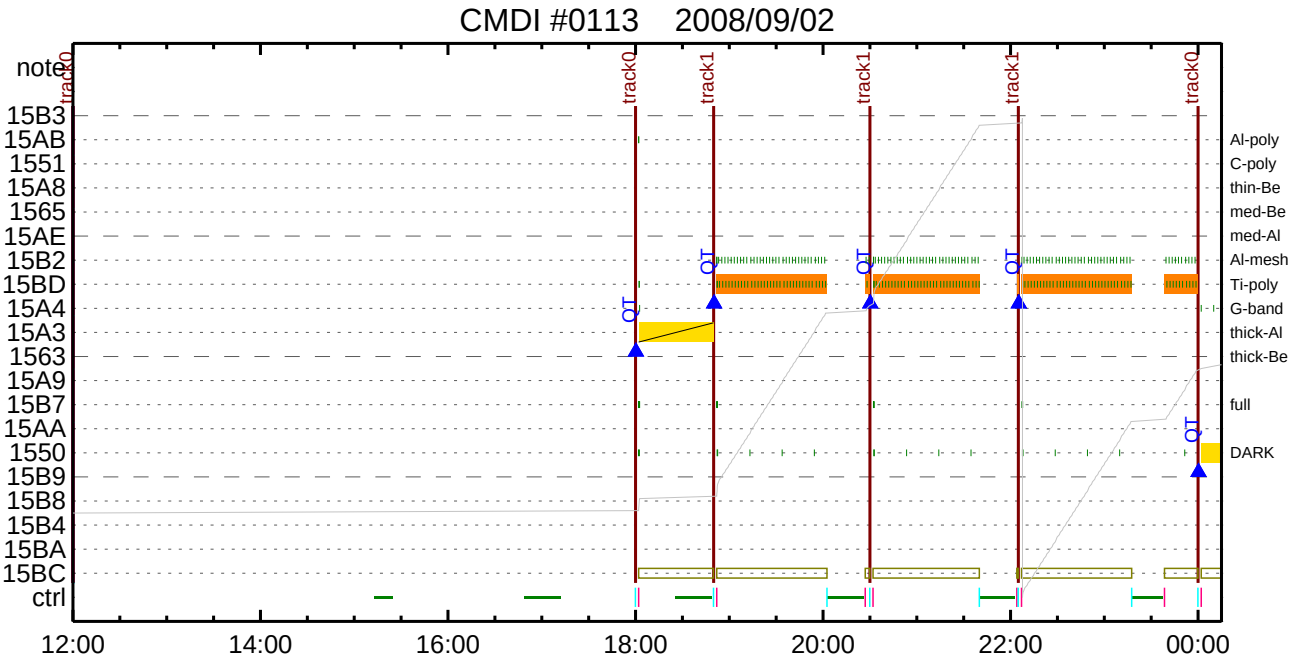
* * * * * **Active Region Search** * * * * *

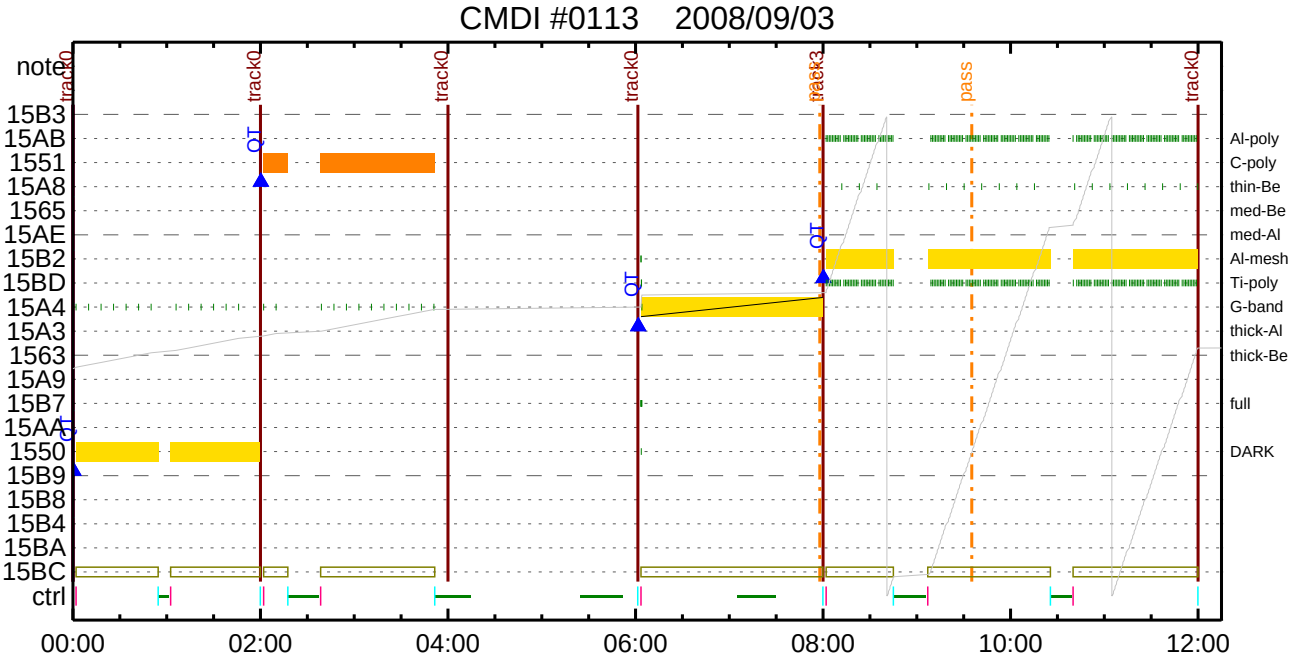
NOT USED

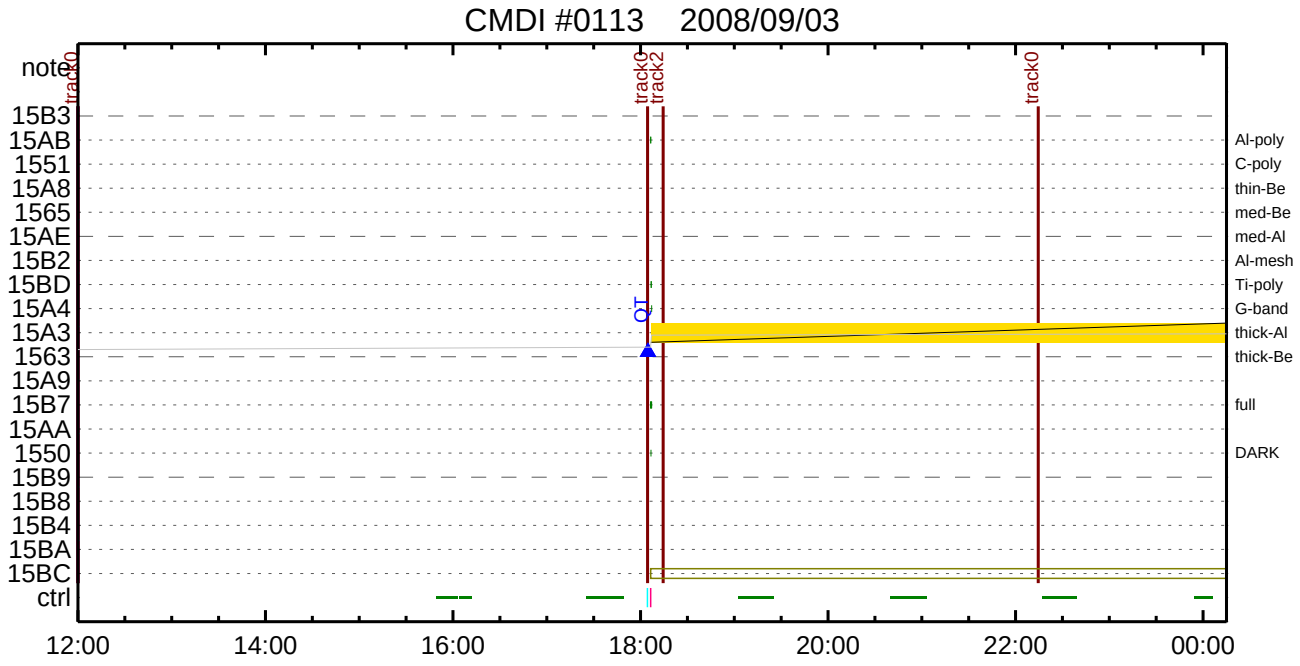
* * * * * **Flare Detection** * * * * *

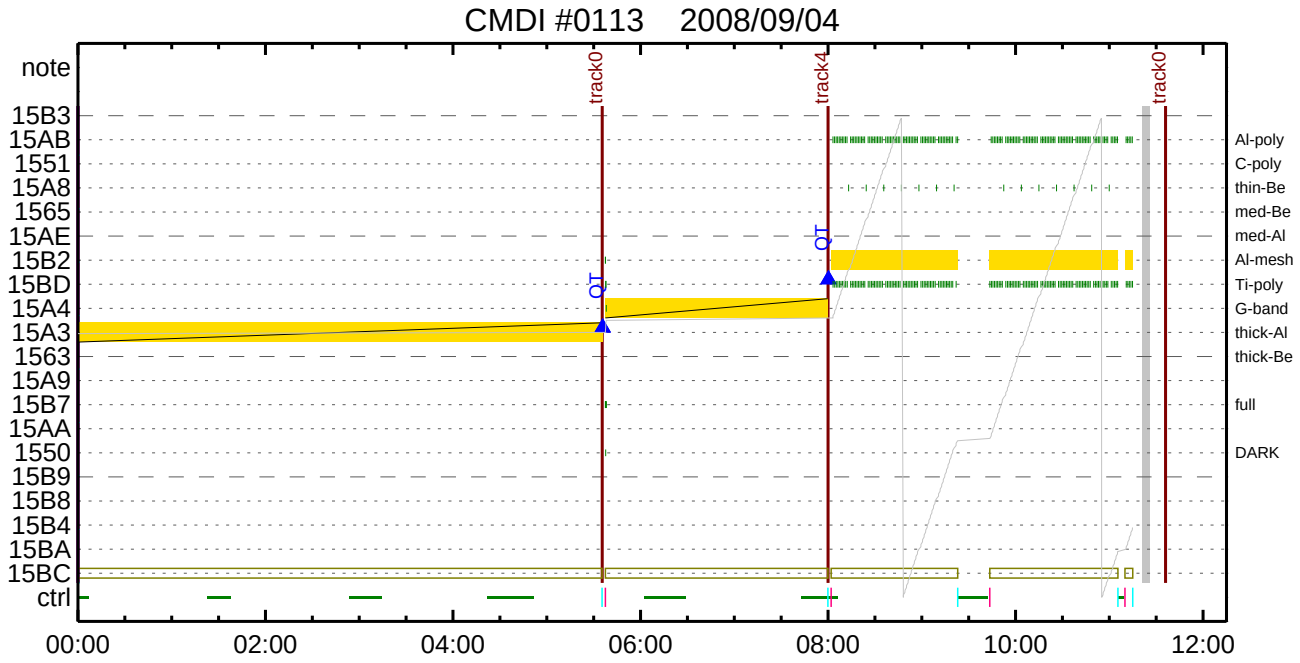
NOT USED


CMDI #0113 2008/09/02
note
before Tbl up
after Tbl up
15B3 15B3
15AB 15AB
15B0 1551
15A8 15A8
1565 1565
15AE 15AE
15B2 15B2
15B5 15BD
15A4 15A4
15A3 15A3
1563 1563
15A9 15A9
15B7 15B7
15AA 15AA
1550 1550
15B9 15B9
15B8 15B8
15B4 15B4
15BA 15BA
15BC 15BC
ctrl
: G-Band Alignment with East limb Q90 2x2 (G-band on ...
: XBP Q90 Al/poly (AEC4) + Ti/poly (AEC4) + Thin-Be ...
: CH - Al/Mesh (32s) + Ti/Poly (64s) - 512x512 - Q95 ...
: Synoptic Q95 2x2 - Al/mesh(512/5795) + Dark cal(51 ...
: Synoptic Q95 2x2 - Al/poly(512/5795) + Dark cal(51 ...
: XBP Q95 Al/poly (AEC4) + Ti/poly (AEC4) + Thin-Be ...
: G-Band Alignment with North Pole Q90 2x2(G-band on ...
Table upload : 3rd pass (2008/09/02 09:51:20)
track0
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0113 2008/09/02
note
track0
track1
QT
15B3
15AB
1551
15A8
1565
15AE
15B2
15BD
15A4
15A3
1563
15A9
15B7
15AA
1550
15B9
15B8
15B4
15BA
15BC
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0113 2008/09/03
note
track0
track3
pass
QT
15B3
15AB
1551
15A8
1565
15AE
15B2
15BD
15A4
15A3
1563
15A9
15B7
15AA
1550
15B9
15B8
15B4
15BA
15BC
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0113 2008/09/03
note
15B3
15AB
1551
15A8
1565
15AE
15B2
15BD
15A4
15A3
1563
15A9
15B7
15AA
1550
15B9
15B8
15B4
15BA
15BC
ctrl
track0
track0
track2
track0
QT
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK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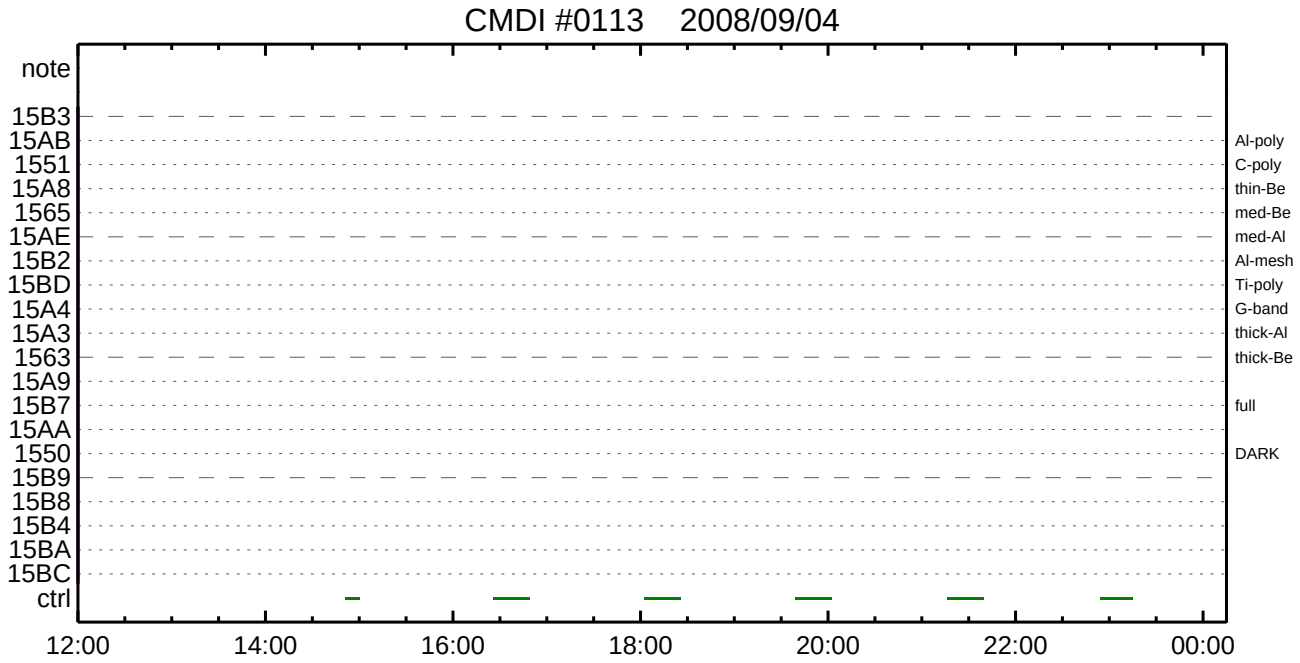
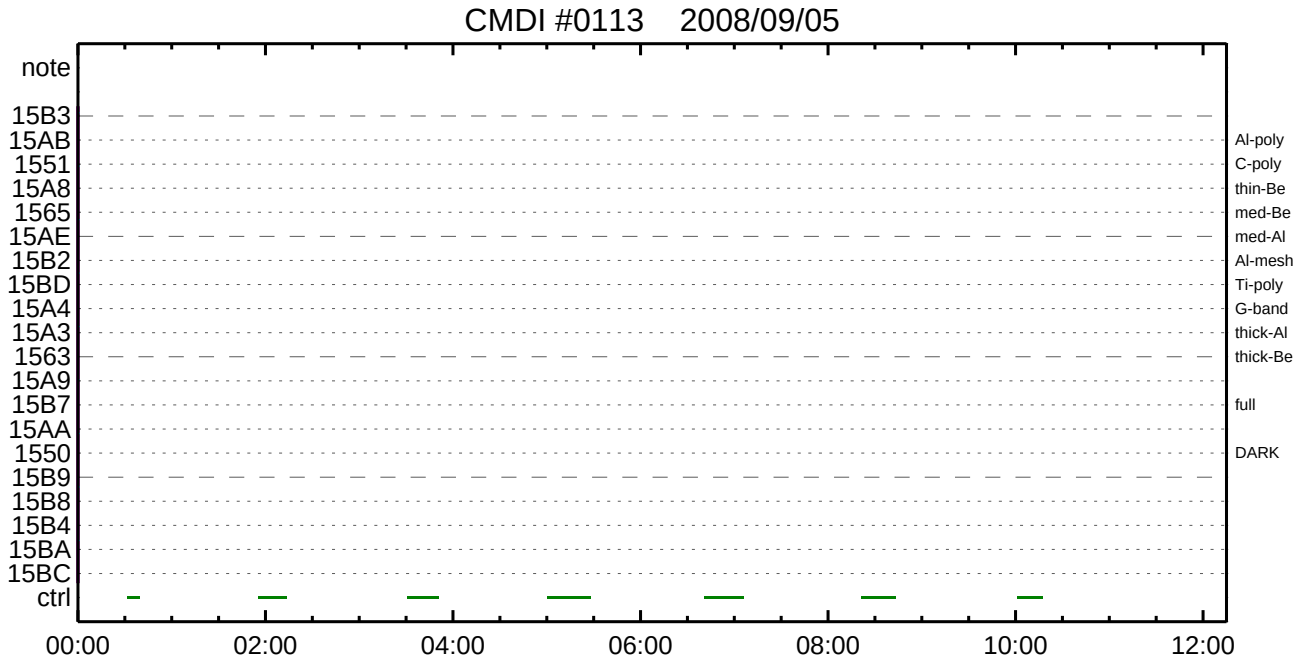
note
15B3
15AB
1551
15A8
1565
15AE
15B2
15BD
15A4
15A3
1563
15A9
15B7
15AA
1550
15B9
15B8
15B4
15BA
15BC
ctrl
track0
track4
track0
QT
QT
00:00
02:00
04:00
06:00
08:00
10:00
12:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0113 2008/09/04
note
15B3
15AB
1551
15A8
1565
15AE
15B2
15BD
15A4
15A3
1563
15A9
15B7
15AA
1550
15B9
15B8
15B4
15BA
15BC
ctrl
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK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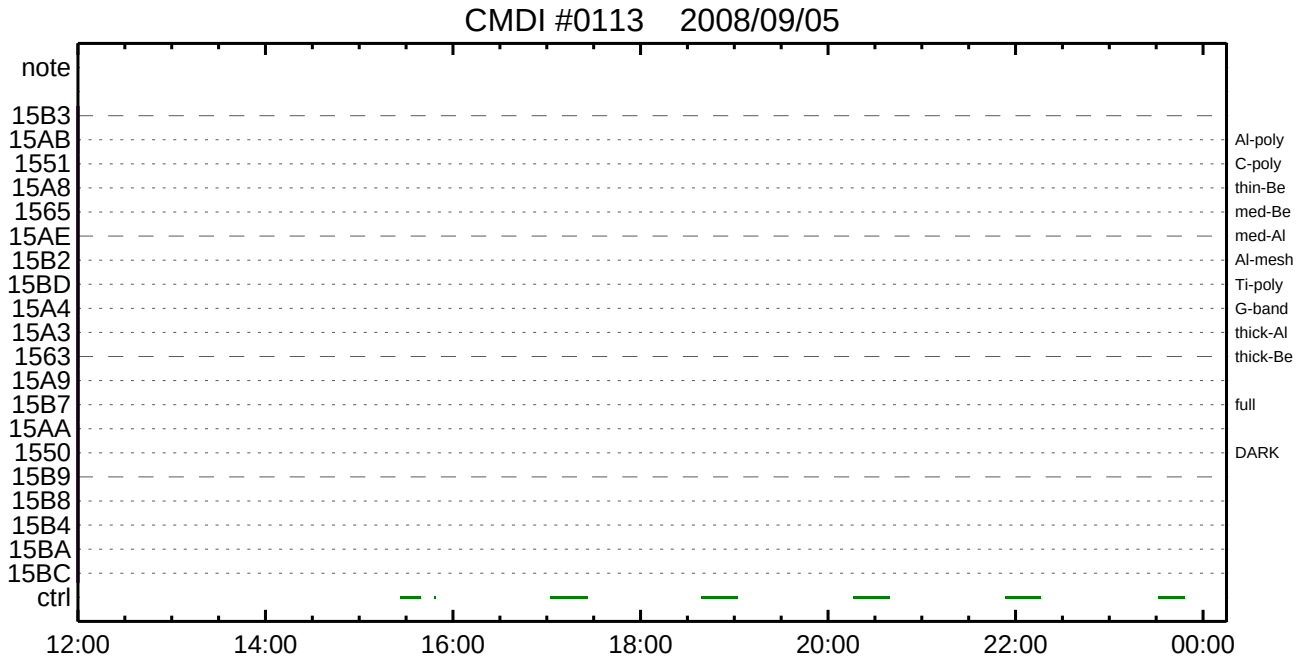
note
15B3
15AB
1551
15A8
1565
15AE
15B2
15BD
15A4
15A3
1563
15A9
15B7
15AA
1550
15B9
15B8
15B4
15BA
15BC
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00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note
15B3
15AB
1551
15A8
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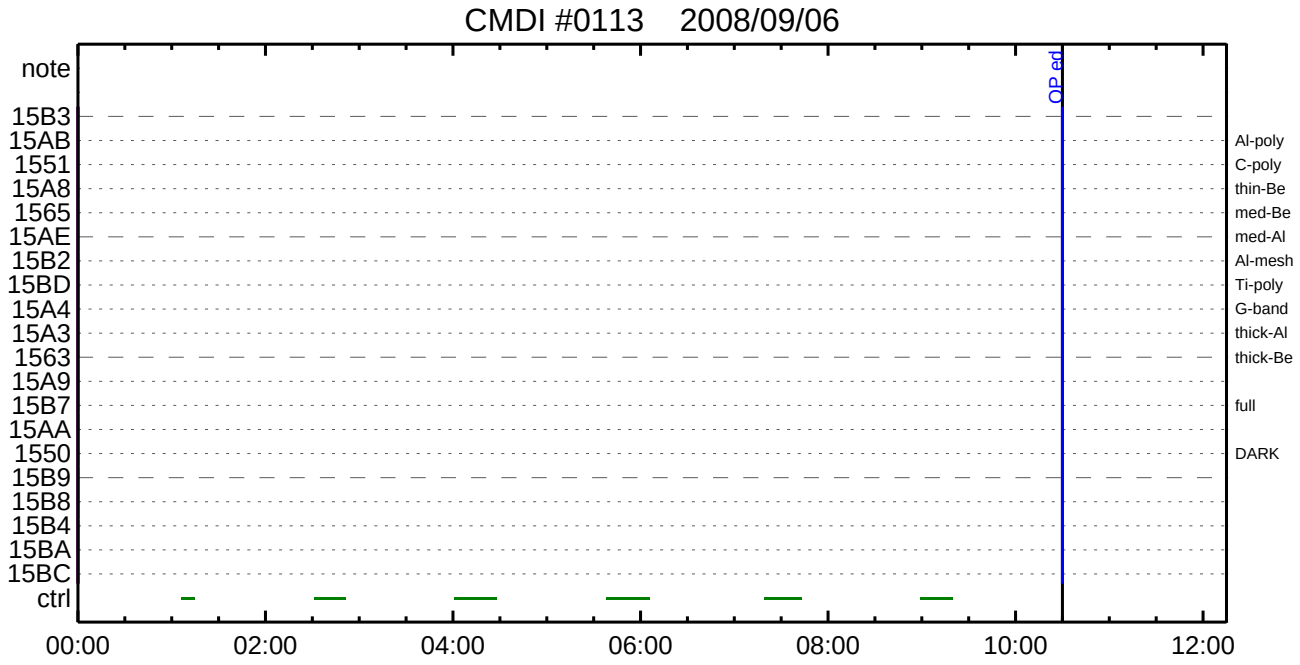
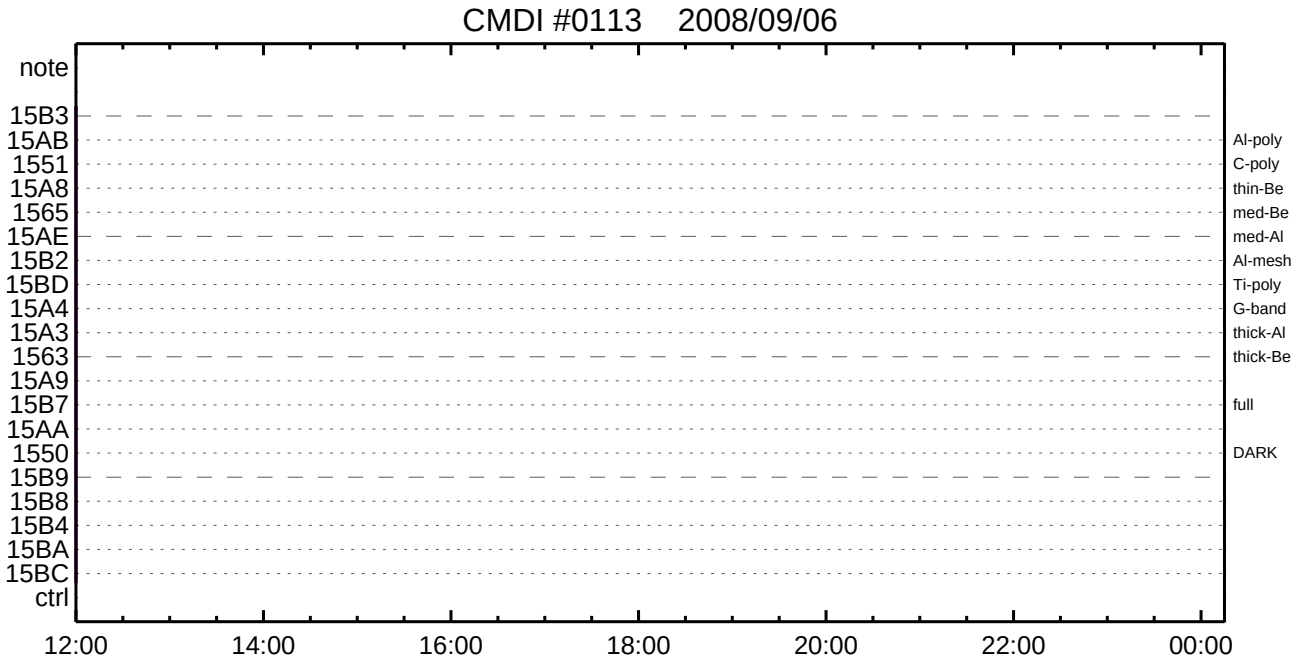
15AE
15B2
15BD
15A4
15A3
1563
15A9
15B7
15AA
1550
15B9
15B8
15B4
15BA
15BC
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0113 2008/09/06
OP end
note
15B3
15AB
1551
15A8
1565
15AE
15B2
15BD
15A4
15A3
1563
15A9
15B7
15AA
1550
15B9
15B8
15B4
15BA
15BC
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0113 2008/09/06
note
15B3
15AB
1551
15A8
1565
15AE
15B2
15BD
15A4
15A3
1563
15A9
15B7
15AA
1550
15B9
15B8
15B4
15BA
15BC
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-108 2008-09-02 13:39:49 289 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Äº²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***************************
0099     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0100     C. ***************************
0101     C.
0102   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0103   . S. OP    op-108:OP
0104     ()
0105   . S. OG    og-108:OG
0106     ()
0107     C.
0108   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0109     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0110  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111     BC       (20 00 7f 01 02)
0112     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0113     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0114     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0115     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0116     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0117  +. DC 01-22 DHU_MODE_CHNG
0118     BC       (07 0b f8)
0119     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0120     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0122     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0123     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0124   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0125     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0126   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0127     C.
0128     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0129  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130     BC       (20 80 7f 01 02)
0131     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0132     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0133     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0134     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0135     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0136  +. DC 01-22 DHU_MODE_CHNG
0137     BC       (07 0b f8)
0138     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0139     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0141     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0142     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0143   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0144     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0145   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0146     C.
0147     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149     BC       (21 00 41 01 02)
0150     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0151     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0152     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0153     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0154     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0155  +. DC 01-22 DHU_MODE_CHNG
0156     BC       (07 0b f8)
0157     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0158     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0160     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0161     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0162   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0163     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0164   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0165     C.
0166   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0167     C. DHU¥â¡¼¥É¡Ê¼ý¸¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0168  +. DC 01-22 DHU_MODE_CHNG
0169     BC       (02 0a f8)
0170     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0171     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0172     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0173     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0174     C.
0175   . C. ********************************
0176     C. TI-CMD SET (OPOG STOP/COPY/START)
0177     C. ********************************
0178     C.
0179   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
0180     C.           ¤Þ¤¿¡¢¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¿¼Ç¹Ô¤¦¤³¤È¡£
0181     C.
0182   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0183  +. TI 2008-09-02 10:07:00.0
0184     DC 01-B3 DHU_OP_STOP
0185     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186     C.
0187  +. TI 2008-09-02 10:07:01.0
0188     DC 01-B4 DHU_OP_COPY
0189     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190     C.
0191  +. TI 2008-09-02 10:07:01.0
0192     DC 01-B5 DHU_OPOG_COPY
0193     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195   +. TI 2008-09-02 10:11:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238     C. ********************************
0239     C.  SOT TI command set
0240     C. ********************************
0241     C. Execute, after the success of OP upload.
0242   +. TI 2008-09-02 10:11:16.0
0243     DC 07-F0 MDP_SOT_MODE_STBY
0244     BC       (41)
0245    . C. ------------------------------------
0246     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0247     C. ------------------------------------
0248     C. ************ SOT END ***********
0249    . C. Stop EIS observation and temporarily disable EIS mode changes
0250     C.
0251     C.
0252     C. ********** Start EIS operation (TI set) **********
0253     C. Execute, after the success of OP upload.
0254     C. Set EIS TI-commands
0255   +. TI 2008-09-02 10:11:30.0
0256     DC 07-FC EIS_MODE_MANU
0257     BC       (21 02)
0258   +. TI 2008-09-02 10:11:40.0
0259     DC 07-FC EIS_MODE_CHG_DIS
0260     BC       (22)
0261    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0262     C. ************ End EIS operation (TI set) ***********
0263     C.
0264     C.
0265     C.
0266     C. ************ XRT START ***********
0267     C. Execute, after the success of OP upload.
0268   +. TI 2008-09-02 10:11:00.0
0269     DC 07-F0 MDP_XRT_MODE_STBY
0270     BC       (c3)
0271    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0272     C.
0273     C. ************ XRT END ***********
0274     C.
0275    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0276     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0277    . S. DC-BC dcbc-402:DCBC
0278     (MDP_known_event)
0279     C.
0280     C.
0281    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0282    . S. DC-BC dcbc-153:DCBC
0283     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284     C.
0285     C.
0286    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0287     C.
0288    . C. ********** LOS **********
0289     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-109 2008-09-02 13:39:49 175 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Äº²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106   +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113   +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                      Result = OK [ ]
0121     C.
0122   +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                         <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127   +. DC 02-8E AOCU_ORB_UPD
0128   . C.
0129   . C. ***** AOCS Commands (Orbital Element Update) *****
0130     C. Update the orbital element
0131   +. DC 02-50 AOCU_ORB_PRPGT_START
0132     BC       (16)
0133   +  DC 02-8E AOCU_ORB_UPD
0134     C.
0135     C.       <A_ORB>[ORBIT] EPC = 2297231.2 +- 1.0 (s) [ ]
0136     C.
0137   . C.
0138   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139   +. DC 07-FC EIS_MODE_MANU
0140     BC       (21 02)
0141   . C. Verify EIS in MANUAL mode
0142   . C. Estimated OBSTBL upload time is 37s
0143     C. ********************************
0144     C. EIS START OBSTBL LOAD
0145     C. ********************************
0146   . S. RAM   ram-820:EIS_OBSTBL
0147     ()
0148   +. DC 07-FC EIS_DUMP_OBSTBL
0149     BC       (07 07 07 00 00 70 00)
0150     C.
0151     C. Execute, after the success of OBSTBL upload.
0152     C. Set EIS TI-commands
0153   +. TI 2008-09-02 10:11:50.0
0154     DC 07-FC EIS_MODE_CHG_ENA
0155     BC       (20)
0156   . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0157     C. ********************************
0158     C. EIS END OBSTBL LOAD
0159     C. ********************************
0160     C.
0161   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0162     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0163   . S. DC-BC dcbc-402:DCBC
0164     (MDP_known_event)
0165     C.
0166     C.
0167   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0168   . S. DC-BC dcbc-153:DCBC
0169     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0170     C.
0171     C.
0172   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0173     C.
0174   . C. ********** LOS **********
0175     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-110 2008-09-02 13:39:49 142 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014     C.
0015     C. ************ XRT START ************
0016     C.
0017   +. DC 07-F0 MDP_XRT_CTRL_MANU
0018     BC       (c1)
0019   +  DC 07-F0 MDP_XRT_MODE_STBY
0020     BC       (c3)
0021   . C. ----------- Success Verify ?              OK / NG____
0022     C.
0023     C. XRT Obs. Table Upload
0024   . S. RAM   ram-291:MDP_OBS_X
0025     ()
0026     C.
0027   +. DC 07-F0 MDP_DUMP_XRTTBL
0028     BC       (84 07 00 00 00 3a d4)
0029   . C. ----------- Comparison Check ?                       OK / ERR ____
0030     C.
0031     C.
0032   +. DC 07-F0 MDP_XRT_ROI_SET
0033     BC       (cd 01 b1 b1 04 04)
0034   +  DC 07-F0 MDP_XRT_ROI_SET
0035     BC       (cd 02 b1 b1 08 08)
0036   +  DC 07-F0 MDP_XRT_ROI_SET
0037     BC       (cd 03 b1 b1 08 08)
0038   +  DC 07-F0 MDP_XRT_ROI_SET
0039     BC       (cd 04 b1 b1 06 06)
0040   +  DC 07-F0 MDP_XRT_ROI_SET
0041     BC       (cd 06 80 80 06 06)
0042   +  DC 07-F0 MDP_XRT_ROI_SET
0043     BC       (cd 07 80 80 08 08)
0044   +  DC 07-F0 MDP_XRT_ROI_SET
0045     BC       (cd 08 80 80 20 20)
0046   +  DC 07-F0 MDP_XRT_ROI_SET
0047     BC       (cd 09 80 60 20 18)
0048   +  DC 07-F0 MDP_XRT_ROI_SET
0049     BC       (cd 0a a0 80 18 20)
0050   +  DC 07-F0 MDP_XRT_ROI_SET
0051     BC       (cd 0f 80 80 06 06)
0052   +  DC 07-F0 MDP_XRT_ROI_SET
0053     BC       (cd 10 80 80 04 04)
0054   +. DC 07-F0 MDP_XRT_QT_PROG_SET
0055     BC       (c4 0e)
0056   +  DC 07-F0 MDP_XRT_ARS_DIS
0057     BC       (d5)
0058   +  DC 07-F0 MDP_XRT_FLD_DIS
0059     BC       (d9)
0060   +  DC 07-F0 MDP_XRT_FLRCTRL_DIS
0061     BC       (c9)
0062   . C. ---------- Success Verify ?           OK / NG ____
0063     C.
0064     C.
0065   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0066     C.
0067   +. DC 07-F0 MDP_XRT_MODE_OBSV
0068     BC       (c2)
0069   +. DC 07-F0 MDP_XRT_CTRL_AUTO
0070     BC       (c0)
0071   +. TI 2008-09-02 10:11:02.0
0072     DC 07-F0 MDP_XRT_MODE_OBSV
0073     BC       (c2)
0074   +. TI 2008-09-02 10:11:04.0
0075     DC 07-F0 MDP_XRT_CTRL_AUTO
0076     BC       (c0)
0077   . C. ---------- Success Verify ?           OK / NG ____
0078     C.
0079     C. ************ XRT END ***********
0080   . C. ********************************
0081     C.  SOT table upload
0082     C. ********************************
0083   . C. < Stop FG table >
0084   +. DC 07-F0 MDP_FG_CTRL_MANU
0085     BC       (51)
0086   . C. ------------------------------------
0087     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0088     C. ------------------------------------
0089     C.
0090   . C. <Upload FG Observation Table>
0091   . S. RAM   ram-268:MDP_OBS_F
0092     ()
0093     C.
0094   . C. < Dump RAMID=MDP_OBS_F >
0095   +. DC 07-F0 MDP_DUMP_FGTBL
```

```
0096     BC       (82 07 00 00 00 38 b8)
0097     C. ------------------------------------
0098     C.  MDP_OBS_F verify = OK/NG        [ ]
0099     C. ------------------------------------
0100     C.
0101   . C. < Resume FG table (auto mode) >
0102   +. DC 07-F0 MDP_FG_CTRL_AUTO
0103     BC       (50)
0104   . C. ------------------------------------
0105     C.  MDP_FG_CTRL_MODE      = AUTO    [ ]
0106     C. ------------------------------------
0107     C.
0108     C. ******************************
0109     C.  SOT TI command set
0110     C. ******************************
0111     C. Execute, after the success of TBL upload.
0112   +. TI 2008-09-02 10:11:18.0
0113     DC 07-F0 MDP_SOT_MODE_OBSV
0114     BC       (40)
0115   . C. ------------------------------------
0116     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0117     C. ------------------------------------
0118     C.
0119     C. Only when FG_CTRL_AUTO is used in RT.
0120   +. TI 2008-09-02 10:11:20.0
0121     DC 07-F0 MDP_FG_CTRL_AUTO
0122     BC       (50)
0123   . C. ------------------------------------
0124     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0125     C. ------------------------------------
0126     C. ************ SOT END ***********
0127     C.
0128   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0129     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0130   . S. DC-BC dcbc-402:DCBC
0131     (MDP_known_event)
0132     C.
0133     C.
0134   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0135   . S. DC-BC dcbc-153:DCBC
0136     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0137     C.
0138     C.
0139   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0140     C.
0141   . C. *********** LOS ***********
0142     C.
```

```
*** OP Sequence for XRT ***

2008/09/02  10:16:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/02  10:16:02.0  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2008/09/02  10:16:22.0  XRT_QT_PROG_SET_412_OG [0x19c]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 08
2008/09/02  10:18:00.0  XRT_ARS_DIS_422_OG [0x1a6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2008/09/02  10:18:02.0  XRT_FLD_DIS_445_OG [0x1bd]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2008/09/02  10:18:04.0  XRT_FLRCTRL_DIS_416_OG [0x1a0]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/09/02  10:18:06.0  XRT_CTRL_AUTO_403_OG [0x193]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/02  11:59:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/02  12:00:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 00 00 00 00
2008/09/02  17:59:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/02  17:59:56.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2008/09/02  18:00:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 00 00 00 00
2008/09/02  18:00:16.0  XRT_QT_PROG_SET_409_OG [0x199]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2008/09/02  18:00:18.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2008/09/02  18:00:20.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/09/02  18:00:22.0  XRT_ARS_DIS_427_OG [0x1ab]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2008/09/02  18:02:00.0  XRT_CTRL_AUTO_444_OG [0x1bc]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/02  18:49:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/02  18:49:56.0  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2008/09/02  18:50:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 01 f9 cb f6 36
2008/09/02  18:50:16.0  XRT_QT_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2008/09/02  18:51:54.0  XRT_ARS_DIS_422_OG [0x1a6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2008/09/02  18:51:56.0  XRT_FLD_DIS_445_OG [0x1bd]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2008/09/02  18:51:58.0  XRT_FLRCTRL_DIS_416_OG [0x1a0]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/09/02  18:52:00.0  XRT_CTRL_AUTO_403_OG [0x193]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/02  20:02:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/02  20:26:00.0  XRT_Custom_430_OG [0x1ae]
2008/09/02  20:27:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/02  20:29:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/02  20:29:56.0  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2008/09/02  20:30:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 01 00 00 00 00
2008/09/02  20:30:16.0  XRT_QT_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2008/09/02  20:31:54.0  XRT_ARS_DIS_422_OG [0x1a6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2008/09/02  20:31:56.0  XRT_FLD_DIS_445_OG [0x1bd]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2008/09/02  20:31:58.0  XRT_FLRCTRL_DIS_416_OG [0x1a0]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/09/02  20:32:00.0  XRT_CTRL_AUTO_403_OG [0x193]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/02  21:40:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/02  22:03:00.0  XRT_Custom_430_OG [0x1ae]
2008/09/02  22:04:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/02  22:04:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/02  22:04:56.0  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2008/09/02  22:05:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 01 08 e5 08 e5
2008/09/02  22:05:16.0  XRT_QT_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2008/09/02  22:06:54.0  XRT_ARS_DIS_422_OG [0x1a6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2008/09/02  22:06:56.0  XRT_FLD_DIS_445_OG [0x1bd]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2008/09/02  22:06:58.0  XRT_FLRCTRL_DIS_416_OG [0x1a0]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/09/02  22:07:00.0  XRT_CTRL_AUTO_403_OG [0x193]
```

```
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/02  23:17:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/02  23:37:30.0  XRT_Custom_430_OG [0x1ae]
2008/09/02  23:38:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/02  23:59:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/02  23:59:56.0  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2008/09/03  00:00:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                        AOCU_NM              5 02-76 00 ac 00 00 00
2008/09/03  00:00:16.0  XRT_QT_PROG_SET_431_OG [0x1af]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 06
2008/09/03  00:01:54.0  XRT_ARS_DIS_422_OG [0x1a6]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2008/09/03  00:01:56.0  XRT_FLD_DIS_445_OG [0x1bd]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2008/09/03  00:01:58.0  XRT_FLRCTRL_DIS_416_OG [0x1a0]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/09/03  00:02:00.0  XRT_CTRL_AUTO_403_OG [0x193]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/03  00:54:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/03  01:01:30.0  XRT_Custom_430_OG [0x1ae]
2008/09/03  01:02:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/03  01:59:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/03  01:59:56.0  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2008/09/03  02:00:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                        AOCU_NM              5 02-76 00 00 00 54 00
2008/09/03  02:00:16.0  XRT_QT_PROG_SET_424_OG [0x1a8]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 12
2008/09/03  02:01:54.0  XRT_ARS_DIS_422_OG [0x1a6]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2008/09/03  02:01:56.0  XRT_FLD_DIS_445_OG [0x1bd]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2008/09/03  02:01:58.0  XRT_FLRCTRL_DIS_416_OG [0x1a0]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/09/03  02:02:00.0  XRT_CTRL_AUTO_403_OG [0x193]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/03  02:17:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/03  02:37:30.0  XRT_Custom_430_OG [0x1ae]
2008/09/03  02:38:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/03  03:51:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/03  04:00:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2008/09/03  06:01:24.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/03  06:01:26.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2008/09/03  06:01:30.0  AOCS_ORe-point_Start_1_OG [0x097]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2008/09/03  06:01:46.0  XRT_QT_PROG_SET_429_OG [0x1ad]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2008/09/03  06:01:48.0  XRT_FLD_DIS_419_OG [0x1a3]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2008/09/03  06:01:50.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/09/03  06:01:52.0  XRT_ARS_DIS_427_OG [0x1ab]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2008/09/03  06:03:30.0  XRT_CTRL_AUTO_444_OG [0x1bc]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/03  07:59:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/03  07:59:56.0  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2008/09/03  08:00:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                        AOCU_NM              5 02-76 03 00 00 00 00
2008/09/03  08:00:16.0  XRT_QT_PROG_SET_411_OG [0x19b]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0e
2008/09/03  08:01:54.0  XRT_ARS_DIS_422_OG [0x1a6]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2008/09/03  08:01:56.0  XRT_FLD_DIS_445_OG [0x1bd]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2008/09/03  08:01:58.0  XRT_FLRCTRL_DIS_416_OG [0x1a0]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/09/03  08:02:00.0  XRT_CTRL_AUTO_403_OG [0x193]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/03  08:45:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/03  09:06:00.0  XRT_Custom_430_OG [0x1ae]
2008/09/03  09:07:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/03  10:25:30.5  XRT_CTRL_MANU_435_OG [0x1b3]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/03  10:39:00.0  XRT_Custom_430_OG [0x1ae]
2008/09/03  10:40:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
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                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/03  11:59:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/03  12:00:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2008/09/03  18:04:24.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/03  18:04:26.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2008/09/03  18:04:30.0  AOCS_ORe-point_Start_1_OG [0x097]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2008/09/03  18:04:46.0  XRT_QT_PROG_SET_409_OG [0x199]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2008/09/03  18:04:48.0  XRT_FLD_DIS_419_OG [0x1a3]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2008/09/03  18:04:50.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/09/03  18:04:52.0  XRT_ARS_DIS_427_OG [0x1ab]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2008/09/03  18:06:30.0  XRT_CTRL_AUTO_444_OG [0x1bc]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/03  18:14:30.0  AOCS_ORe-point_Start_8_OG [0x09e]
                                    AOCU_NM              5 02-76 02 00 00 00 00
2008/09/03  22:14:30.0  AOCS_ORe-point_Start_1_OG [0x097]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2008/09/04  05:35:24.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/04  05:35:26.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2008/09/04  05:35:30.0  AOCS_ORe-point_Start_1_OG [0x097]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2008/09/04  05:35:46.0  XRT_QT_PROG_SET_429_OG [0x1ad]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2008/09/04  05:35:48.0  XRT_FLD_DIS_419_OG [0x1a3]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2008/09/04  05:35:50.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/09/04  05:35:52.0  XRT_ARS_DIS_427_OG [0x1ab]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2008/09/04  05:37:30.0  XRT_CTRL_AUTO_444_OG [0x1bc]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/04  07:59:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/04  07:59:56.0  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2008/09/04  08:00:00.0  AOCS_ORe-point_Start_9_OG [0x09f]
                                    AOCU_NM              5 02-76 04 00 00 00 00
2008/09/04  08:00:16.0  XRT_QT_PROG_SET_411_OG [0x19b]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0e
2008/09/04  08:01:54.0  XRT_ARS_DIS_422_OG [0x1a6]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2008/09/04  08:01:56.0  XRT_FLD_DIS_445_OG [0x1bd]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2008/09/04  08:01:58.0  XRT_FLRCTRL_DIS_416_OG [0x1a0]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/09/04  08:02:00.0  XRT_CTRL_AUTO_403_OG [0x193]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/04  09:23:00.5  XRT_CTRL_MANU_435_OG [0x1b3]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/04  09:42:30.0  XRT_Custom_430_OG [0x1ae]
2008/09/04  09:43:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/04  11:05:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/04  11:09:00.0  XRT_Custom_430_OG [0x1ae]
2008/09/04  11:10:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/04  11:15:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/04  11:36:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                    AOCU_NM              5 02-76 00 00 00 00 00
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