

XRT Timeline to be uploaded on 2008/09/09

Period: 2008/09/09 11:16:00 - 2008/09/13 10:30:00

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Normal mode

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XOB #15BF: Filament - 2x2 Al_poly, FOV 512x256 w/ context-lossless												
Term	Pointing (x, y)	Comment										
09/09 11:28:00 - 09/09 14:40:00	Fixed (-616.0, -716.0)	# OP start + 10min, HOP73 on SE limb prominence.										
09/10 08:02:00 - 09/10 09:51:00	Fixed (-616.0, -716.0)	HOP73 on SE limb prominence										
PROG= 20 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 20 12-time(s) 4.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 11.3s Obs 2x2 512x256 (1024, 1024) DPCM 0 0 300.0sec												
└─ Seqn= 83 1-time(s) 4.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 22.6s Obs 1x1 1024x512 (1024, 1024) DPCM 0 0 300.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 45.2s Obs 1x1 1024x512 (1024, 1024) DPCM 0 0 300.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #15A3: Synoptic Q95 2x2 - Al/poly(512/5795) + Dark cal(512 Q98) + Ti-poly(723/11571) + G-band(16)												
Term	Pointing (x, y)	Comment										
09/09 18:18:00 - 09/10 06:01:24	Fixed (0.0, 0.0)	synoptic, shifted 16.0 min										
09/10 18:02:00 - 09/10 18:09:54	Fixed (0.0, 0.0)	synoptic										
PROG= 11 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 18 1-time(s) 4.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 72 1-time(s) 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 76 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 92 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #15A4: Synoptic Q95 2x2 - Al/mesh(512/5795) + Dark cal(512 Q98) + Ti-poly(723/11571) + G-band(16)												
Term	Pointing (x, y)	Comment										
09/10 06:03:30 - 09/10 07:59:54	Fixed (0.0, 0.0)	synoptic, shifted 1.5 min										
09/11 06:07:30 - 09/11 08:14:54	Fixed (0.0, 0.0)	synoptic, shifted 5.5 min										
PROG= 12 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 87 1-time(s) 4.0sec												
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 72 1-time(s) 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 76 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 92 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #15B7: XBP Q95 Al/poly (AEC4) + Ti/poly (AEC4) + Thin-Be (AEC0)- med cadence -FOV512												
Term	Pointing (x, y)	Comment										
09/10 10:02:30 - 09/10 14:30:00	Track (560.4, -511.8) ^{Ⓜ 09/10 10:00:00}	VTT/QS (XBP)										
09/11 08:17:00 - 09/11 10:45:00	Track (298.0, -233.2) ^{Ⓜ 09/11 08:15:00}	VTT/QS (XBP)										
PROG= 08 Inf.-time(s)												
└─ Subr= 1 1-time(s) 120.0sec												
└─ Seqn= 77 10-time(s) 60.0sec												
└─ Al-poly/Open C-poly/Open close Safe Norm 8.00s Obs 1x1 512x512 (1024, 1024) Q=95 4 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 8.00s Obs 1x1 512x512 (1024, 1024) Q=95 4 0 2.0sec												
└─ Subr= 2 1-time(s) 4.0sec												
└─ Seqn= 56 1-time(s) 4.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 64.0s Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #15C1: CH - Al/Mesh (32s) + Ti/Poly (64s) - 512x512 - Q95 -less synoptic - with half-res. full frame												
Term	Pointing (x, y)	Comment										
09/10 18:12:00 - 09/11 00:10:00	Track (-8.6, -96.0) ^{Ⓜ 09/10 18:10:00}	DC coronal hole										
PROG= 04 Inf.-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 76 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 6-time(s) 1200.0sec												
Seqn= 62 1-time(s) 4.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	32.0s	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 64 10-time(s) 120.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	32.0s	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	1.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	64.0s	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 85 1-time(s) 1200.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	64.0s	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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NOT USED

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Active Region Search

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NOT USED

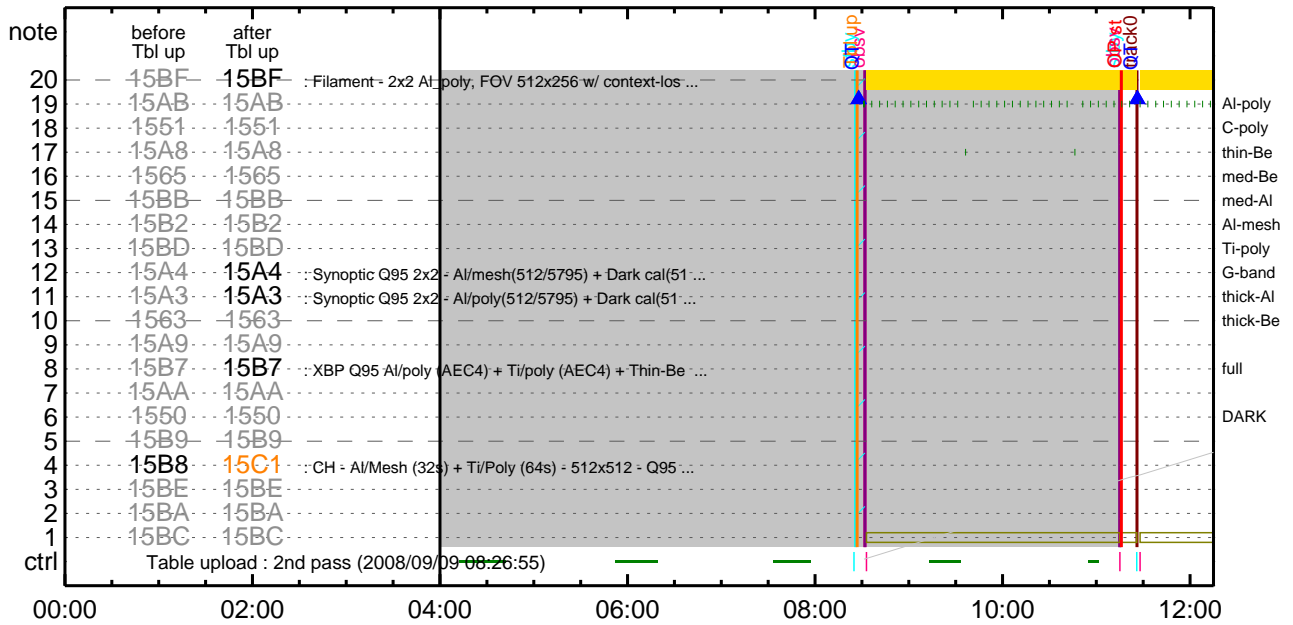
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Flare Detection

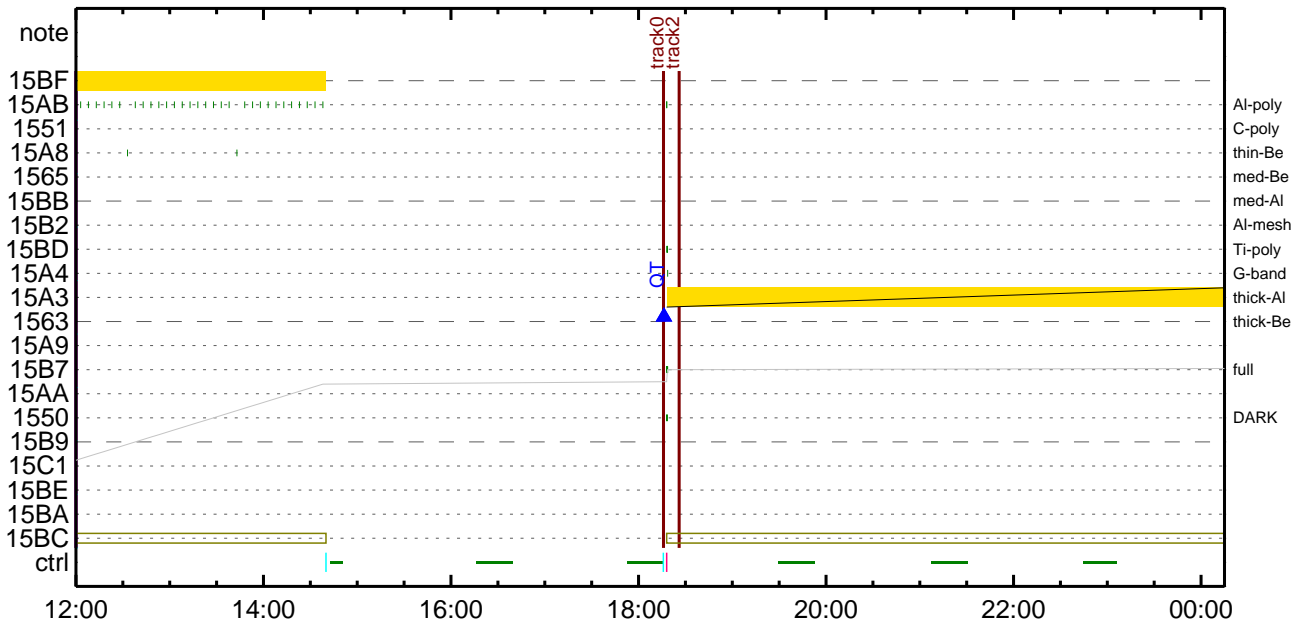
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NOT USED

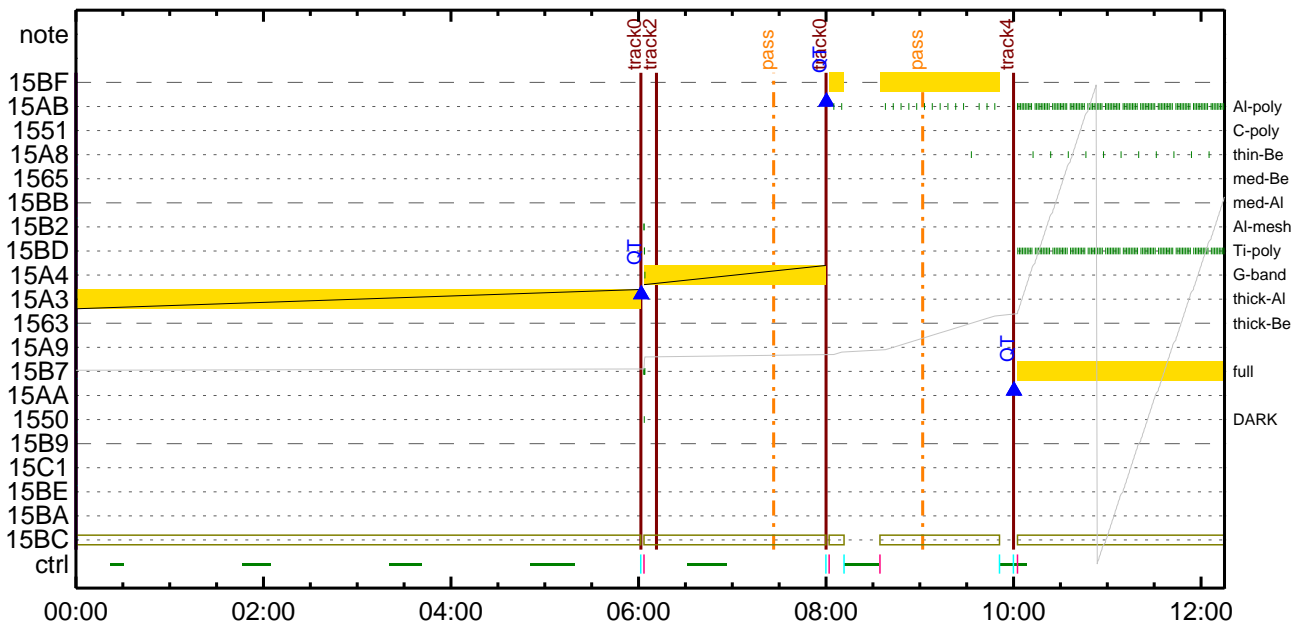
CMDI #0126 2008/09/09



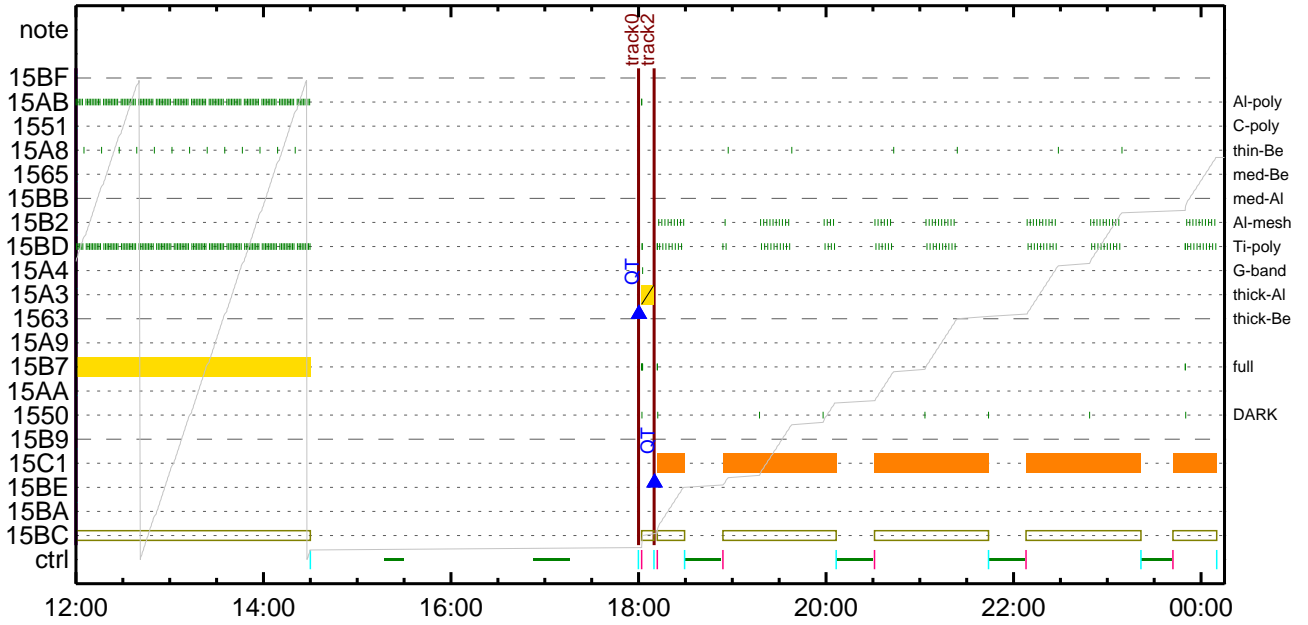
CMDI #0126 2008/09/09



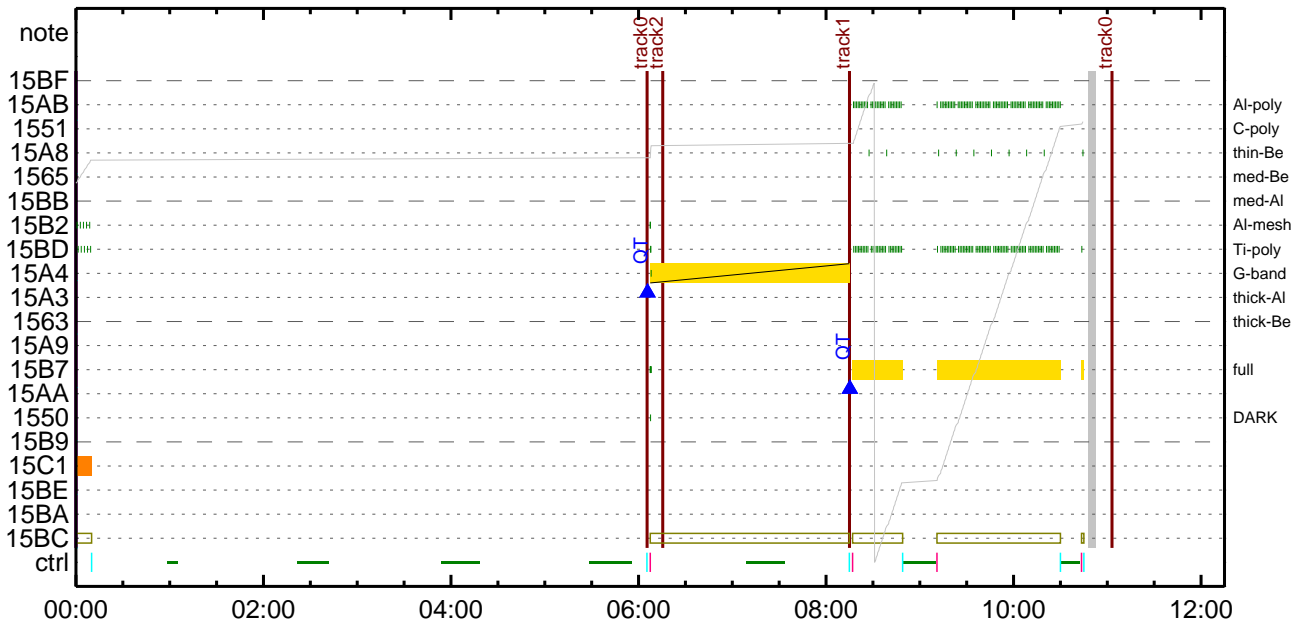
CMDI #0126 2008/09/10



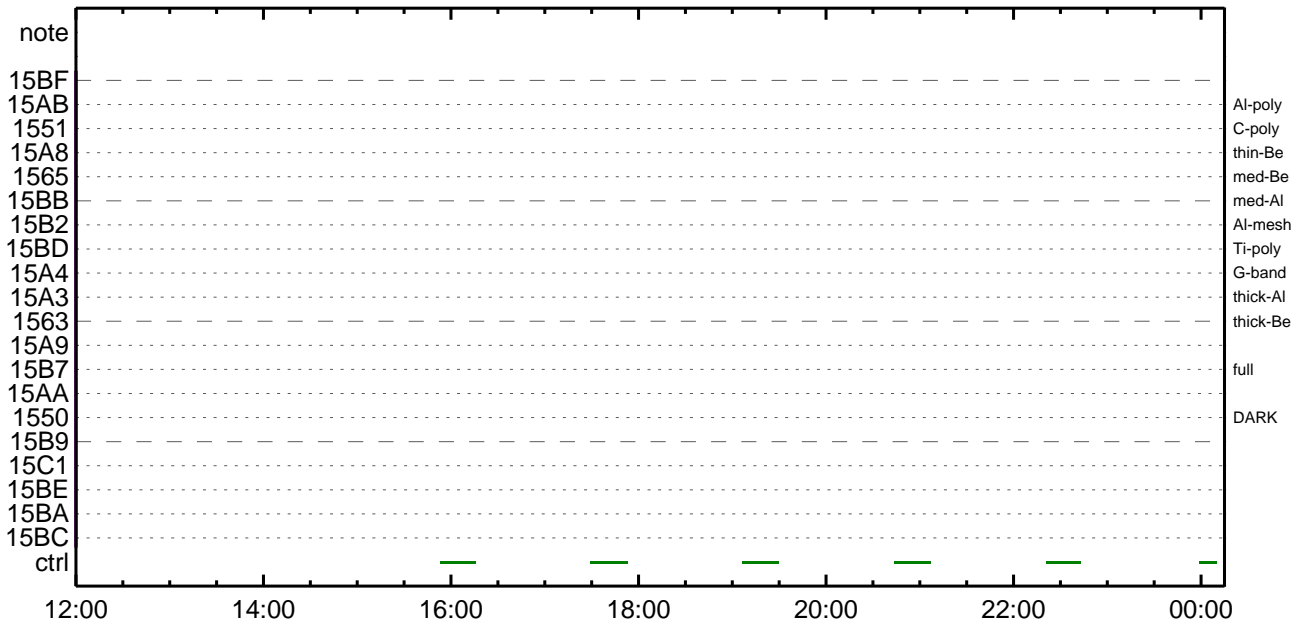
CMDI #0126 2008/09/10



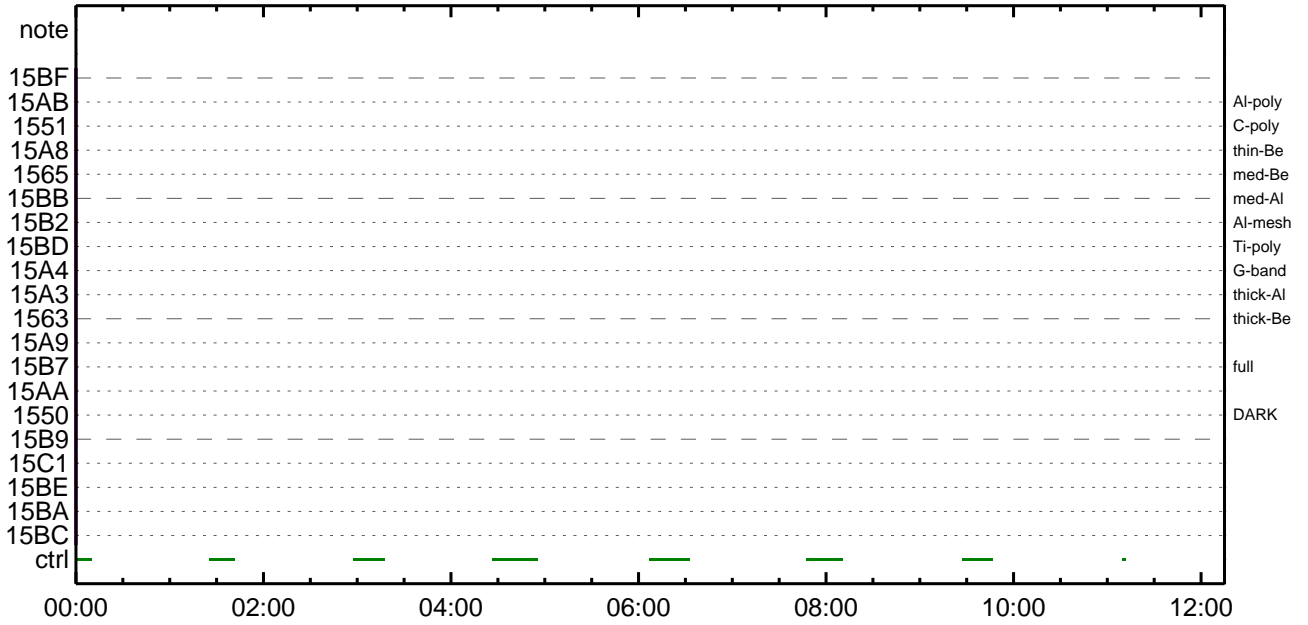
CMDI #0126 2008/09/11



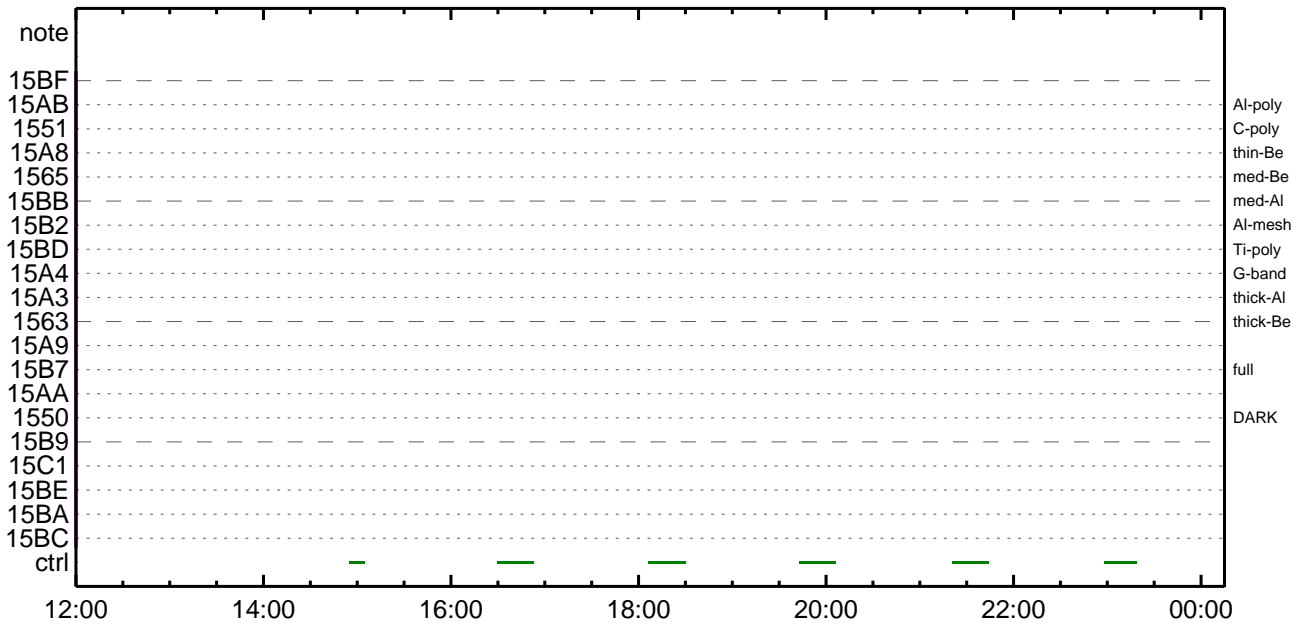
CMDI #0126 2008/09/11



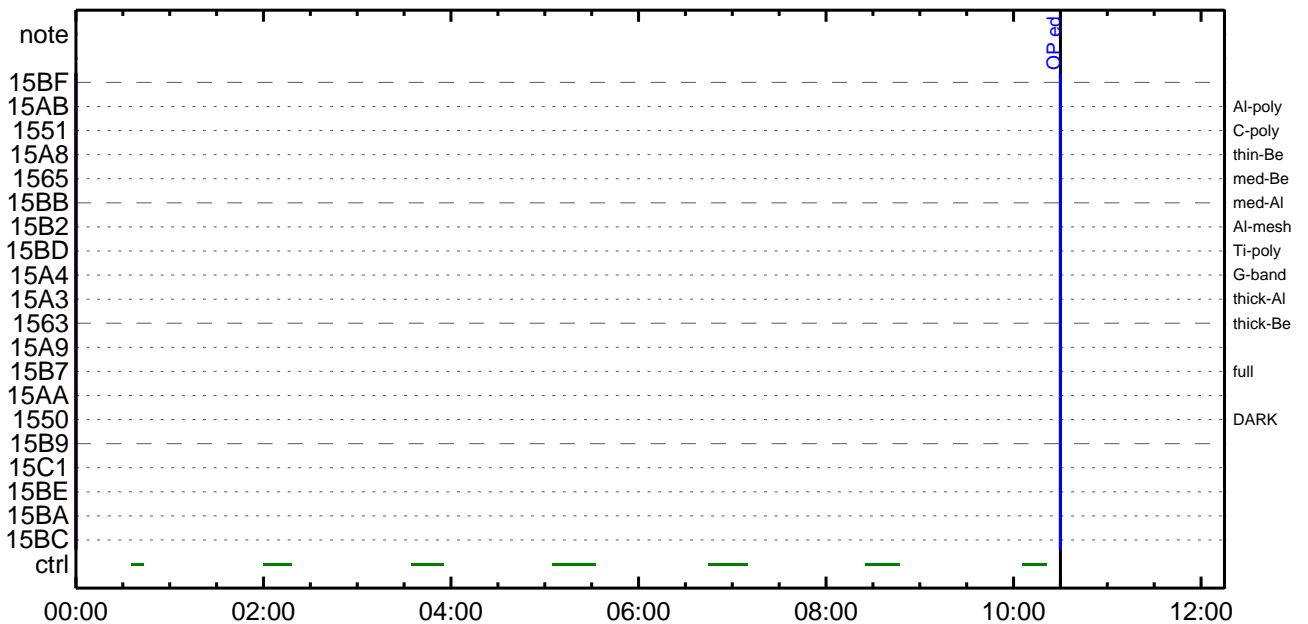
CMDI #0126 2008/09/12

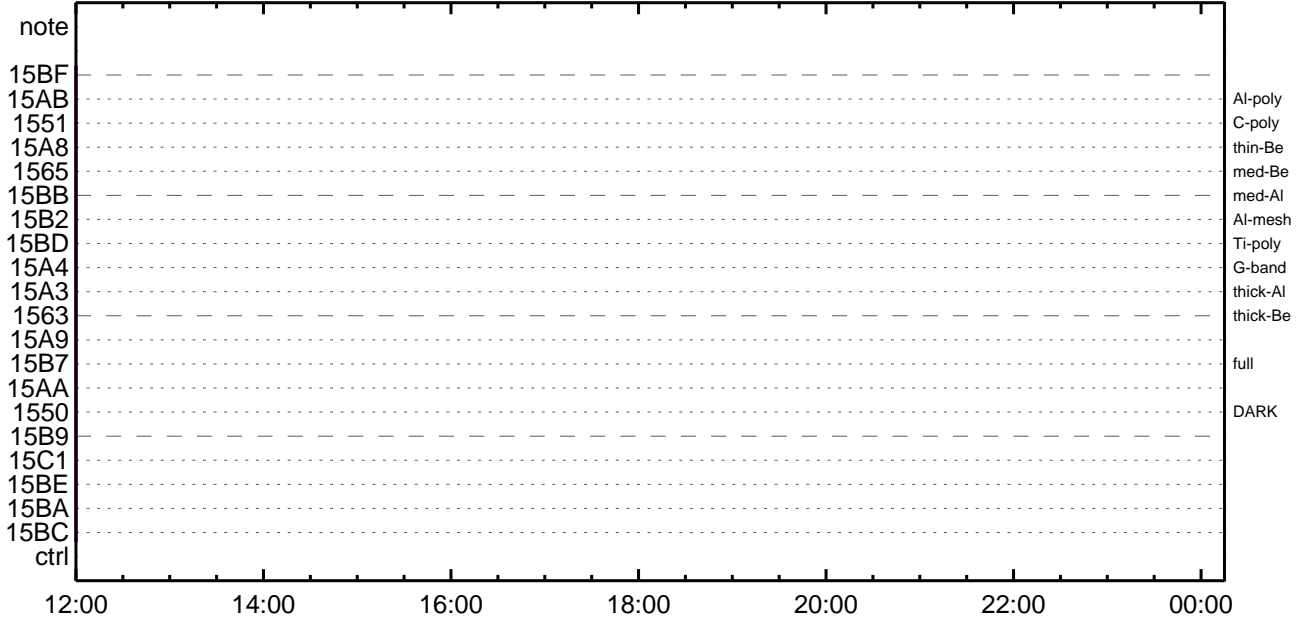


CMDI #0126 2008/09/12



CMDI #0126 2008/09/13






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0096 C.                0300;SET0EDUMPAI±0iYNY1aÇ1Ôa|a3aE;E
0097 C.
0098 . C. TIY3YFYOYÉ0dÀDİç (UT)
0099 +. TI 2008-09-09 11:11:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2008-09-09 11:11:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2008-09-09 11:11:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2008-09-09 11:15:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C.                °È²¼0İÄê%îİÑ0İYÁYŞYÄY-¹àİÜ
0116 C.                çç[HK1_TI_CMD_ENA/DIS]                EQ        ENA
0117 C.                çç[HK1_TI_CMD_NUM]                EQ        4
0118 C.                çç[HK1_NEXT_EXEC_PIM]                EQ        DHU
0119 C.                çç[HK1_NEXT_EXEC_DC]                EQ        0xB3
0120 C.
0121 . C. *****
0122 C. TIİİî°èYÄYÓY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                çç[HK1_DMP_TOP_ADRS_1]                EQ        07
0129 C.                çç[HK1_DMP_TOP_ADRS_0]                EQ        2B
0130 C.                çç[HK1_DMP_BLOCK_NUM]                EQ        3
0131 C.                çç[HK1_DMP_REPEAT_NUM]                EQ        0
0132 C.                çç[HK1_DMA_DMP_PIM]                EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                çç[HK1_PKT_FORM_NO]                EQ        7
0136 C.                çç[HK1_PKT_GEN_TIME]                EQ        0.25 s
0137 C.                çç[HK1_S_TLM_BIT_RATE]                EQ        32k
0138 C.                çç[HK1_X_TLM_BIT_RATE]                EQ        4M
0139 C.                çç[HK1_DMP_CHK_FLG]                EQ        EXEC
0140 C.
0141 . C. YÄYÓY×½³İ»0d³İÇŞ
0142 C.                çç[HK1_DMP_CHK_FLG]                EQ        NON
0143 C.
0144 . C. RAM ID=TI_TBL0İ%È¹Ç•è²İOK0d³İÇŞ
0145 C.
0146 . C. DHUYâ;¼YÉ;È¼Y¼, Yİ;¼YÈ;È0dİá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                çç[HK1_PKT_FORM_NO]                EQ        2
0150 C.                çç[HK1_PKT_GEN_TIME]                EQ        0.5S
0151 C.                çç[HK1_S_TLM_BIT_RATE]                EQ        32K
0152 C.                çç[HK1_X_TLM_BIT_RATE]                EQ        4M
0153 C.
0154 . C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2008-09-09 11:15:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC (21 02)
0163 +. TI 2008-09-09 11:15:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC (22)
0166 . C.                [ ] [HK1_TI_CMD_NUM]                EQ        2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C. *****
0171 C. SOT TI command set
0172 C. *****
0173 C. Execute, after the success of OP upload.
0174 +. TI 2008-09-09 11:15:16.0
0175 DC 07-F0 MDP_SOT_MODE_STBY
0176 BC (41)
0177 . C. -----
0178 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0179 C. -----
0180 C. ***** SOT END *****
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2008-09-09 11:15:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 . C.                [ ] [HK1_TI_CMD_NUM]                EQ        1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP ´üÃî0İ»ö¼Y0ÈÄD0¹0èDCBC•×²è *****
0192 C. (¼â°İYÖYÄYÈY½YÉY¼YÇYè0È¼0¼¼Ä»Ü0¹0è)
0193 . S. DC-BC dcbc-402:DCBC
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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCs Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 + DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 2898435.6 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 C.
0139 C. ***** XRT START *****
0140 C.
0141 +. DC 07-F0 MDP_XRT_CTRL_MANU
0142 BC (c1)
0143 + DC 07-F0 MDP_XRT_MODE_STBY
0144 BC (c3)
0145 . C. ----- Success Verify ? OK / NG_____
0146 C.
0147 C. XRT Obs. Table Upload
0148 . S. RAM ram-291:MDP_OBS_X
0149 ( )
0150 C.
0151 +. DC 07-F0 MDP_DUMP_XRTTBL
0152 BC (84 07 00 00 00 3a d4)
0153 . C. ----- Comparison Check ? OK / ERR _____
0154 C.
0155 C.
0156 +. DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 01 b1 b1 04 04)
0158 + DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 02 b1 b1 08 08)
0160 + DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 03 b1 b1 08 08)
0162 + DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 04 b1 b1 06 06)
0164 + DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 06 80 80 08 04)
0166 + DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 07 80 80 10 08)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 08 80 80 20 20)
0170 + DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 09 80 80 08 08)
0172 + DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 0f 80 80 06 06)
0174 + DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 10 80 80 04 04)
0176 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0177 BC (c4 14)
0178 + DC 07-F0 MDP_XRT_ARS_DIS
0179 BC (d5)
0180 + DC 07-F0 MDP_XRT_FLD_DIS
0181 BC (d9)
0182 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0183 BC (c9)
0184 . C. ----- Success Verify ? OK / NG _____
0185 C.
0186 C.
0187 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0188 C.
0189 +. DC 07-F0 MDP_XRT_MODE_OBSV
0190 BC (c2)
0191 +. DC 07-F0 MDP_XRT_CTRL_AUTO
0192 BC (c0)
0193 +. TI 2008-09-09 11:15:02.0

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0194 DC 07-F0 MDP_XRT_MODE_OBSV
0195 BC (c2)
0196 +. TI 2008-09-09 11:15:04.0
0197 DC 07-F0 MDP_XRT_CTRL_AUTO
0198 BC (c0)
0199 . C. ----- Success Verify ? OK / NG ____
0200 C.
0201 C. ***** XRT END *****
0202 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0203 +. DC 07-FC EIS_MODE_MANU
0204 BC (21 02)
0205 . C. Verify EIS in MANUAL mode
0206 . C. Estimated OBSTBL upload time is 9 s
0207 C. *****
0208 C. EIS START OBSTBL LOAD
0209 C. *****
0210 . S. RAM ram-821:EIS_OBSTBL
0211 ( )
0212 +. DC 07-FC EIS_DUMP_OBSTBL
0213 BC (07 07 07 00 00 70 00)
0214 C.
0215 C. Execute, after the success of OBSTBL upload.
0216 C. Set EIS TI-commands
0217 +. TI 2008-09-09 11:15:50.0
0218 DC 07-FC EIS_MODE_CHG_ENA
0219 BC (20)
0220 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0221 C. *****
0222 C. EIS END OBSTBL LOAD
0223 C. *****
0224 C.
0225 . C. ***** MDP 'uÃIaÎ»ô%ÝoEÄDq¹aèDCBC•x²è *****
0226 C. (%ã°İ¥Ó¥Ä¥E¥P¥E¥ã¥ç¥éaE%¼aa¼Ä»Ûa¹aè)
0227 . S. DC-BC dcbc-402:DCBC
0228 (MDP_known_event)
0229 C.
0230 C.
0231 . C. ***** ¥D¥¹•İ Daily±;İÑaE'Øa¹aèDCBC•x²è *****
0232 . S. DC-BC dcbc-153:DCBC
0233 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0234 C.
0235 C.
0236 . C. ;ãLOS¥Ä¥S¥Ä¥-¼Ä»Û;ä
0237 C.
0238 . C. ***** LOS *****
0239 C.

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0096 C.
0097 C.
0098 C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-262:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Stop SP table >
0120 +. DC 07-F0 MDP_SP_CTRL_MANU
0121 BC (61)
0122 C. -----
0123 C. MDP_SP_CTRL_MODE = MANU [ ]
0124 C. -----
0125 C.
0126 . C. <Upload SP Observation Table>
0127 . S. RAM ram-287:MDP_OBS_S
0128 ( )
0129 C.
0130 . C. < Dump RAMID=MDP_OBS_S >
0131 +. DC 07-F0 MDP_DUMP_SPTBL
0132 BC (83 07 00 00 00 38 b8)
0133 C. -----
0134 C. MDP_OBS_S verify = OK/NG [ ]
0135 C. -----
0136 C.
0137 . C. < Upload DPL table >
0138 C.
0139 C. ¶ç¥Ã¥×¥í;¼¥ÉñÊÁ°ñÊSTX_CHKñòOFFñÊñ¹ñě
0140 C.
0141 . S. RAM ram-271:MDP_DPL
0142 ( )
0143 C.
0144 . C. < Dump RAMID=MDP_DPL >
0145 +. DC 07-F0 MDP_DUMP_FGTBL
0146 BC (82 07 00 38 b8 00 40)
0147 C. -----
0148 C. MDP_DPL verify = OK [ ]
0149 C. -----
0150 C.
0151 C. STX_CHKñòONñÊñ¹ñě
0152 C.
0153 . C. < Update MDP DSC PAR1 >
0154 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0155 BC (4c)
0156 C. MDP_CMD_CODE = F04C0700 [ ]
0157 C. MDP_CMD_CNT (count-up 1) [ ]
0158 C. -----
0159 C.
0160 . C.
0161 . C. < Resume FG table (auto mode) >
0162 +. DC 07-F0 MDP_FG_CTRL_AUTO
0163 BC (50)
0164 . C. -----
0165 C. MDP_FG_CTRL_MODE = AUTO [ ]
0166 C. -----
0167 C.
0168 C. *****
0169 C. SOT TI command set
0170 C. *****
0171 C. Execute, after the success of TBL upload.
0172 +. TI 2008-09-09 11:15:18.0
0173 DC 07-F0 MDP_SOT_MODE_OBSV
0174 BC (40)
0175 C. -----
0176 C. HKI_TI_CMD_NUM = 1 CNTUP [ ]
0177 C. -----
0178 C.
0179 C. Only when FG_CTRL_AUTO is used in RT.
0180 +. TI 2008-09-09 11:15:20.0
0181 DC 07-F0 MDP_FG_CTRL_AUTO
0182 BC (50)
0183 C. -----
0184 C. HKI_TI_CMD_NUM = 1 CNTUP [ ]
0185 C. -----
0186 C. ***** SOT END *****
0187 C.
0188 C. ***** MDP `úÃîñî»ö¼¥ñÊÃñ¹ñěDCBC•x²ě *****
0189 C. (¼ñ°î¼Ö¥Ã¥Ê¥Ï¥É¥Á¥ç¥ěñÊ¼ñ¼Ã»Ûñ¹ñě)
0190 . S. DC-BC dcbc-402:DCBC
0191 (MDP_known_event)
0192 C.
0193 C.
```

```
0194 . C. ***** ¥D¥!•İ Daily±;İÑøĒ'Øσ¹σēDCBC•x²è *****
0195 . S. DC-BC dcbc-153:DCBC
0196 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0197 C.
0198 C.
0199 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0200 C.
0201 . C. ***** LOS *****
0202 C.
```

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*** OP Sequence for XRT ***

2008/09/09	11:25:54.0	XRT_CTRL_MANU_428_OG [0x1ac]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/09/09	11:25:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2008/09/09	11:26:00.0	AOCS_Ore-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	00 3f a5 36 c0			
2008/09/09	11:26:16.0	XRT_QT_PROG_SET_440_OG [0x1b8]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14			
2008/09/09	11:27:54.0	XRT_ARS_DIS_422_OG [0x1a6]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2008/09/09	11:27:56.0	XRT_FLD_DIS_445_OG [0x1bd]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2008/09/09	11:27:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2008/09/09	11:28:00.0	XRT_CTRL_AUTO_403_OG [0x193]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/09/09	14:40:00.0	XRT_CTRL_MANU_428_OG [0x1ac]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/09/09	18:15:54.0	XRT_CTRL_MANU_428_OG [0x1ac]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/09/09	18:15:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2008/09/09	18:16:00.0	AOCS_Ore-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2008/09/09	18:16:16.0	XRT_QT_PROG_SET_409_OG [0x199]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b			
2008/09/09	18:16:18.0	XRT_FLD_DIS_419_OG [0x1a3]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2008/09/09	18:16:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2008/09/09	18:16:22.0	XRT_ARS_DIS_427_OG [0x1ab]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2008/09/09	18:18:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/09/09	18:26:00.0	AOCS_Ore-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	02 00 00 00 00			
2008/09/10	06:01:24.0	XRT_CTRL_MANU_428_OG [0x1ac]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/09/10	06:01:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2008/09/10	06:01:30.0	AOCS_Ore-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2008/09/10	06:01:46.0	XRT_QT_PROG_SET_429_OG [0x1ad]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c			
2008/09/10	06:01:48.0	XRT_FLD_DIS_419_OG [0x1a3]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2008/09/10	06:01:50.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2008/09/10	06:01:52.0	XRT_ARS_DIS_427_OG [0x1ab]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2008/09/10	06:03:30.0	XRT_CTRL_AUTO_444_OG [0x1bc]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/09/10	06:11:30.0	AOCS_Ore-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	02 00 00 00 00			
2008/09/10	07:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/09/10	07:59:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2008/09/10	08:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	00 3f a5 36 c0			
2008/09/10	08:00:16.0	XRT_QT_PROG_SET_440_OG [0x1b8]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14			
2008/09/10	08:01:54.0	XRT_ARS_DIS_422_OG [0x1a6]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2008/09/10	08:01:56.0	XRT_FLD_DIS_445_OG [0x1bd]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2008/09/10	08:01:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2008/09/10	08:02:00.0	XRT_CTRL_AUTO_403_OG [0x193]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/09/10	08:11:30.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/09/10	08:33:30.0	XRT_Custom_430_OG [0x1ae]						
2008/09/10	08:34:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/09/10	09:51:00.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/09/10	09:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/09/10	09:59:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2008/09/10	10:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]						
		AOCU_NM	5	02-76	04 00 00 00 00			
2008/09/10	10:00:16.0	XRT_QT_PROG_SET_412_OG [0x19c]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08			
2008/09/10	10:01:54.0	XRT_ARS_DIS_422_OG [0x1a6]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2008/09/10	10:01:56.0	XRT_FLD_DIS_445_OG [0x1bd]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2008/09/10	10:01:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			

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2008/09/10	10:02:30.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/09/10	14:30:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/10	17:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/10	17:59:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2008/09/10	18:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2008/09/10	18:00:16.0	XRT_QT_PROG_SET_409_OG [0x199]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2008/09/10	18:00:18.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/09/10	18:00:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/09/10	18:00:22.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/09/10	18:02:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/09/10	18:09:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/10	18:09:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2008/09/10	18:10:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2008/09/10	18:10:16.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2008/09/10	18:11:54.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/09/10	18:11:56.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/09/10	18:11:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/09/10	18:12:00.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/09/10	18:29:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/10	18:53:00.0	XRT_Custom_430_OG [0x1ae]							
2008/09/10	18:54:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/09/10	20:06:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/10	20:30:00.0	XRT_Custom_430_OG [0x1ae]							
2008/09/10	20:31:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/09/10	21:44:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/10	22:07:00.0	XRT_Custom_430_OG [0x1ae]							
2008/09/10	22:08:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/09/10	23:21:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/10	23:41:00.0	XRT_Custom_430_OG [0x1ae]							
2008/09/10	23:42:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/09/11	00:10:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/11	06:05:24.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/11	06:05:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2008/09/11	06:05:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2008/09/11	06:05:46.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2008/09/11	06:05:48.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/09/11	06:05:50.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/09/11	06:05:52.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/09/11	06:07:30.0	XRT_CTRL_AUTO_444_OG [0x1bc]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/09/11	06:15:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2008/09/11	08:14:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/11	08:14:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2008/09/11	08:15:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2008/09/11	08:15:16.0	XRT_QT_PROG_SET_412_OG [0x19c]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2008/09/11	08:16:54.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/09/11	08:16:56.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/09/11	08:16:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/09/11	08:17:00.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/09/11	08:49:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							

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2008/09/11	09:10:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1														
2008/09/11	09:11:00.5	XRT_CTRL_AUTO_432_OG [0x1b0]																		
2008/09/11	10:30:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0														
2008/09/11	10:42:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1														
2008/09/11	10:43:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]																		
2008/09/11	10:45:00.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_AUTO	1	07-F0	c0														
2008/09/11	11:03:00.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1														
		AOCU_NM		5	02-76	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00