

XRT Timeline to be uploaded on 2008/09/11

Period: 2008/09/11 10:53:00 - 2008/09/16 10:44:00

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Normal mode

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XOB #15C3: Filament - 2x2 Al_poly, FOV 1024x512 w/ context-lossless												
Term	Pointing (x, y)		Comment									
09/11 11:05:00 - 09/11 12:59:00	Track (69.7, 682.9)	@ 09/11 11:03:00	# OP start + 10min, HOP 73, track filament with La Palma.									
09/12 08:11:30 - 09/12 09:59:54	Track (166.9, 685.6)	@ 09/12 08:00:00	* HOP 73, track filament with La Palma									
09/13 08:02:00 - 09/16 10:44:00	Track (272.0, 691.2)	@ 09/13 08:00:00	* HOP 73, track filament with La Palma.									
PROG= 02 Inf.-time(s)												
└ Subr= 1 1-time(s) 2.0sec												
└ Seqn= 22 12-time(s) 4.0sec												
└ Al-poly/Open Al-poly/Open close Safe Norm 11.3s Obs 2x2 1024x512 (1024, 1024) DPCM 0 0 300.0sec												
└ Seqn= 83 1-time(s) 4.0sec												
└ Al-poly/Open Al-poly/Open close Safe Norm 22.6s Obs 1x1 1024x512 (1024, 1024) DPCM 0 0 300.0sec												
└ thin-Be/Open thin-Be/Open close Safe Norm 45.2s Obs 1x1 1024x512 (1024, 1024) DPCM 0 0 300.0sec												
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #15A3: Synoptic Q95 2x2 - Al/poly(512/5795) + Dark cal(512 Q98) + Ti-poly(723/11571) + G-band(16)												
Term	Pointing (x, y)		Comment									
09/11 17:56:30 - 09/11 18:04:24	Fixed (0.0, 0.0)		synoptic, shifted -5.5 min									
09/12 17:44:30 - 09/12 17:52:24	Fixed (0.0, 0.0)		synoptic, shifted -17.5 min									
PROG= 11 1-time(s)												
└ Subr= 1 1-time(s) 12.0sec												
└ Seqn= 18 1-time(s) 4.0sec												
└ Al-poly/Open Al-poly/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└ Al-poly/Open Al-poly/thick-Al close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└ Seqn= 72 1-time(s) 2.0sec												
└ Open/Al-mesh Open/Al-mesh close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└ Seqn= 76 1-time(s) 4.0sec												
└ Open/Ti-poly Open/thick-Al close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└ Open/Ti-poly Open/thick-Al close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└ Seqn= 92 1-time(s) 2.0sec												
└ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #15C1: CH - Al/Mesh (32s) + Ti/Poly (64s) - 512x512 - Q95 -less synoptic - with half-res. full frame												
Term	Pointing (x, y)		Comment									
09/11 18:06:30 - 09/12 05:39:24	Track (183.2, 44.1)	@ 09/11 18:04:30	# On-disk CH (Farid).									
09/12 17:54:30 - 09/13 03:02:26	Track (389.3, 52.5)	@ 09/12 17:52:30	# Cont.									
PROG= 04 Inf.-time(s)												
└ Subr= 1 1-time(s) 12.0sec												
└ Seqn= 76 1-time(s) 4.0sec												
└ Open/Ti-poly Open/thick-Al close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└ Open/Ti-poly Open/thick-Al close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└ Subr= 2 6-time(s) 1200.0sec												
└ Seqn= 62 1-time(s) 4.0sec												
└ Open/Al-mesh Open/Al-mesh close Safe Dark 32.0s Obs 1x1 512x512 (1024, 1024) Q=98 0 0 2.0sec												
└ Seqn= 64 10-time(s) 120.0sec												
└ Open/Al-mesh Open/Al-mesh close Safe Norm 32.0s Obs 1x1 512x512 (1024, 1024) Q=95 0 0 1.0sec												
└ Open/Ti-poly Open/Ti-poly close Safe Norm 64.0s Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec												
└ Seqn= 85 1-time(s) 1200.0sec												
└ thin-Be/Open thin-Be/Open close Safe Norm 64.0s Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #15A4: Synoptic Q95 2x2 - Al/mesh(512/5795) + Dark cal(512 Q98) + Ti-poly(723/11571) + G-band(16)												
Term	Pointing (x, y)		Comment									
09/12 05:41:30 - 09/12 07:59:54	Fixed (0.0, 0.0)		synoptic, shifted -20.5 min									
09/13 06:02:00 - 09/13 07:59:54	Fixed (0.0, 0.0)		synoptic									
PROG= 12 1-time(s)												
└ Subr= 1 1-time(s) 12.0sec												
└ Seqn= 87 1-time(s) 4.0sec												
└ Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└ Open/Al-mesh Open/Ti-poly close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└ Seqn= 72 1-time(s) 2.0sec												
└ Open/Al-mesh Open/Al-mesh close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└ Seqn= 76 1-time(s) 4.0sec												
└ Open/Ti-poly Open/thick-Al close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└ Open/Ti-poly Open/thick-Al close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└ Seqn= 92 1-time(s) 2.0sec												
└ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #15B7: XBP Q95 Al/poly (AEC4) + Ti/poly (AEC4) + Thin-Be (AEC0)- med cadence -FOV512												
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Term	Pointing (x, y)	Comment
09/12 10:02:30 - 09/12 11:59:00	Track (-225.2, 425.1) © 09/12 10:00:00	* Track bright point in QS with VTT
PROG= 08 Inf.-time(s)		
└ Subr= 1	1-time(s) 120.0sec	
└└ Seqn= 77	10-time(s) 60.0sec	
└└└ Al-poly/Open	C-poly/Open close Safe Norm 8.00s	Obs 1x1 512x512 (1024, 1024) Q=95 4 0 2.0sec
└└└ Open/Ti-poly	Open/thick-Al close Safe Norm 8.00s	Obs 1x1 512x512 (1024, 1024) Q=95 4 0 2.0sec
└ Subr= 2	1-time(s) 4.0sec	
└└ Seqn= 56	1-time(s) 4.0sec	
└└└ thin-Be/Open	thin-Be/Open close Safe Norm 64.0s	Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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NOT USED

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Active Region Search

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NOT USED

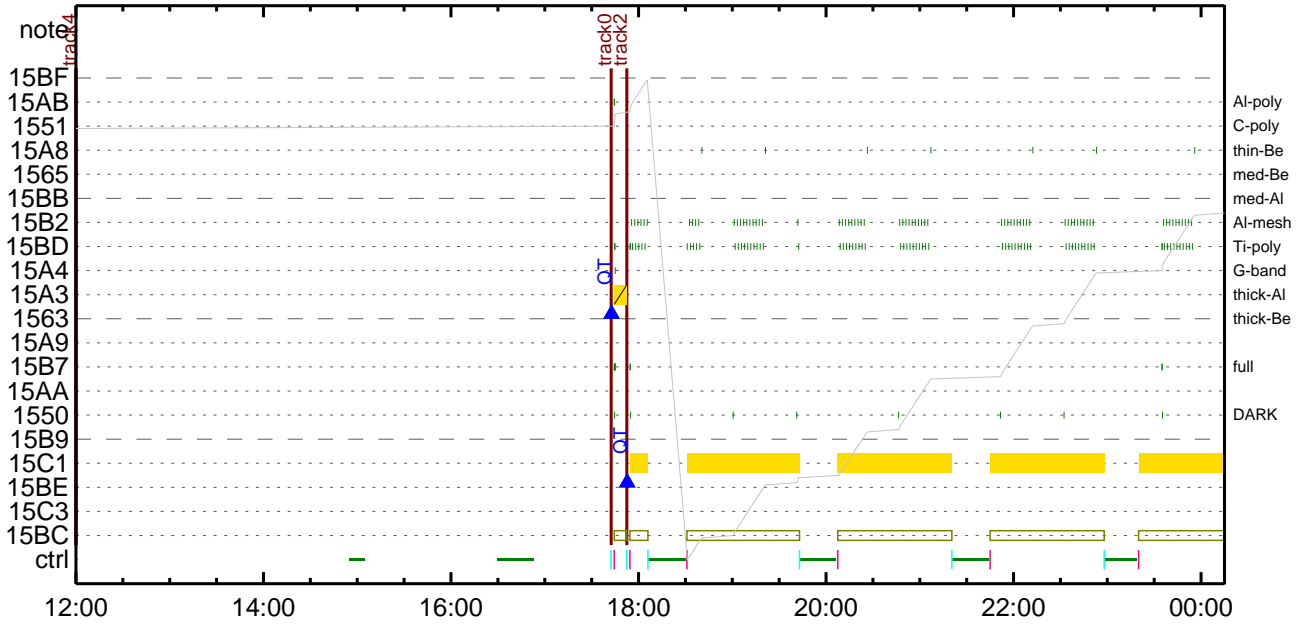
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Flare Detection

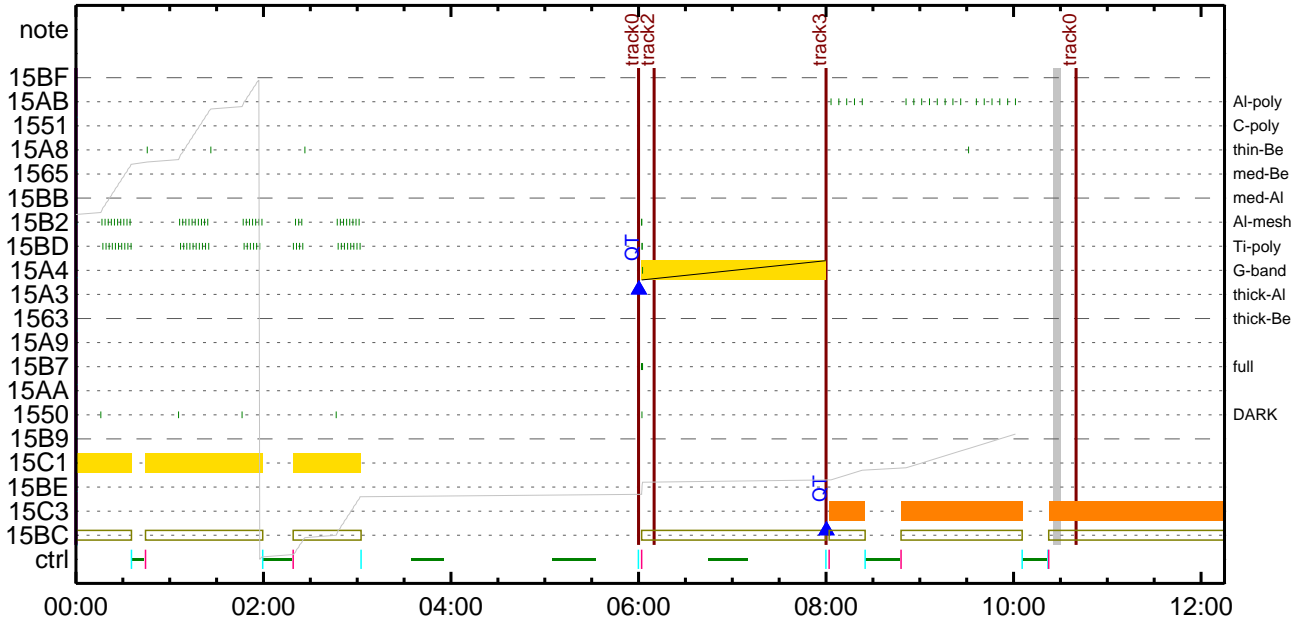
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NOT USED

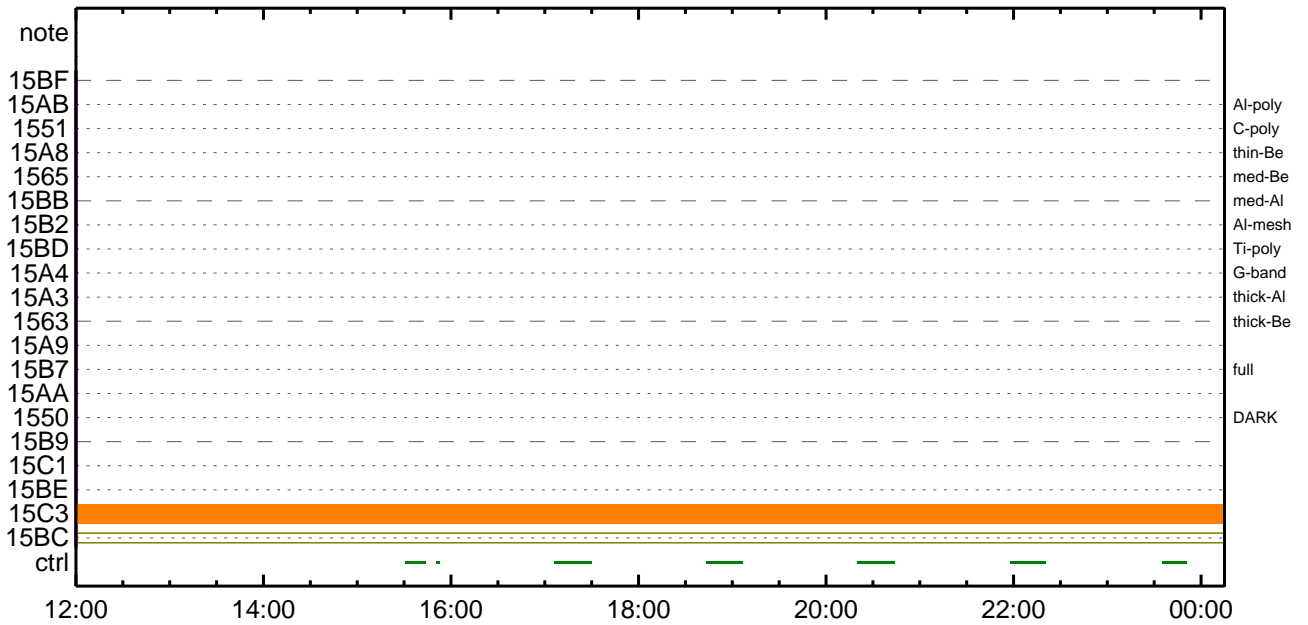
CMDI #0129 2008/09/12



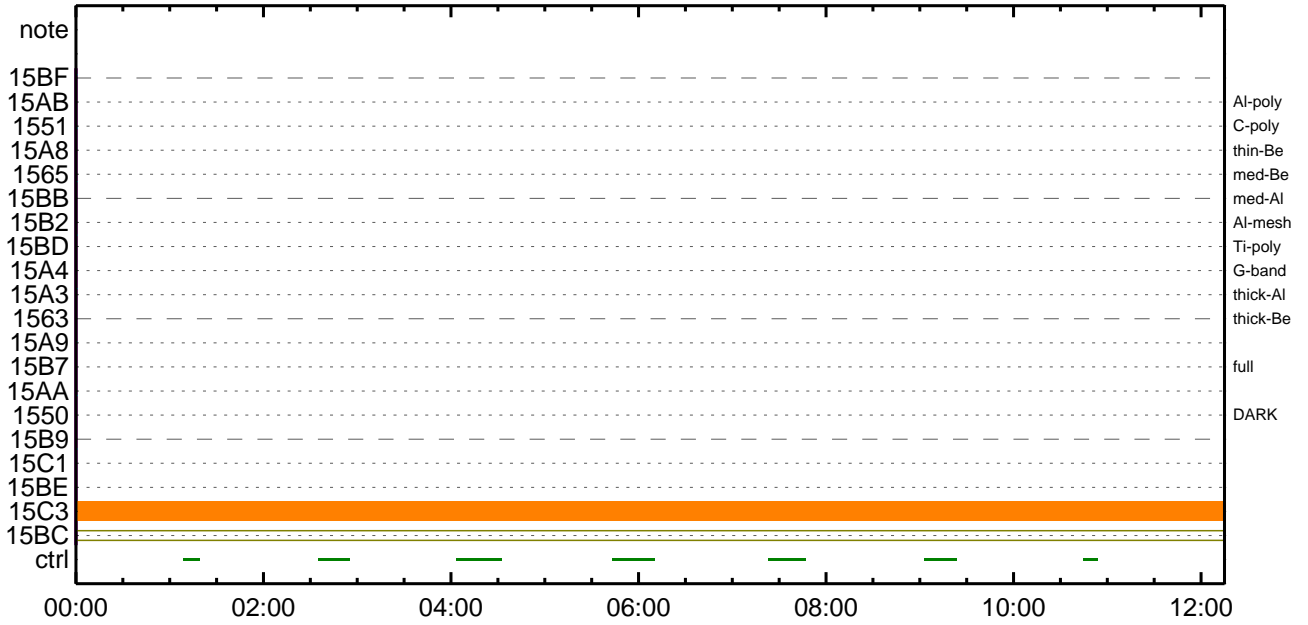
CMDI #0129 2008/09/13



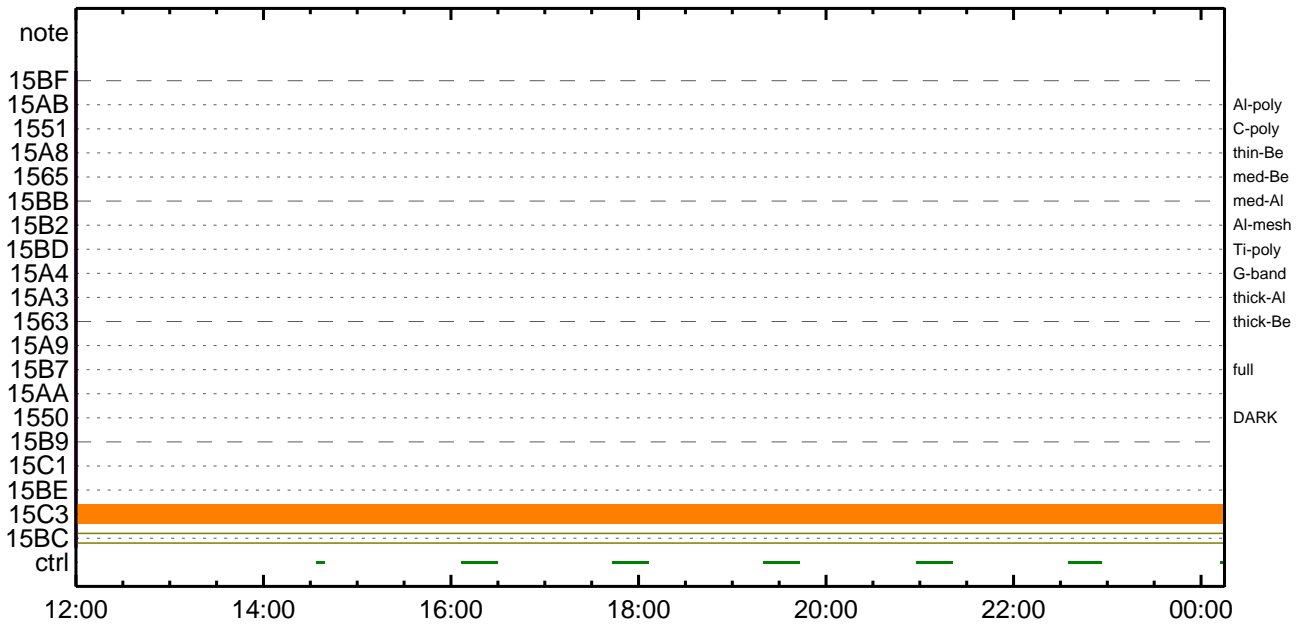
CMDI #0129 2008/09/13



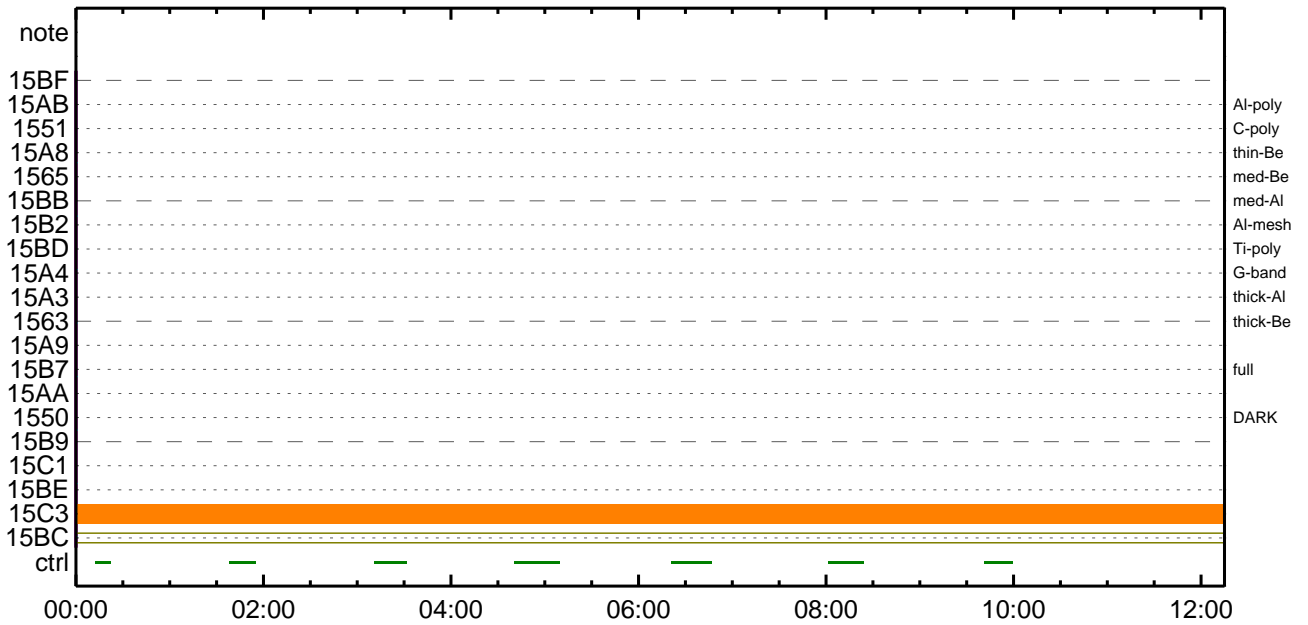
CMDI #0129 2008/09/14



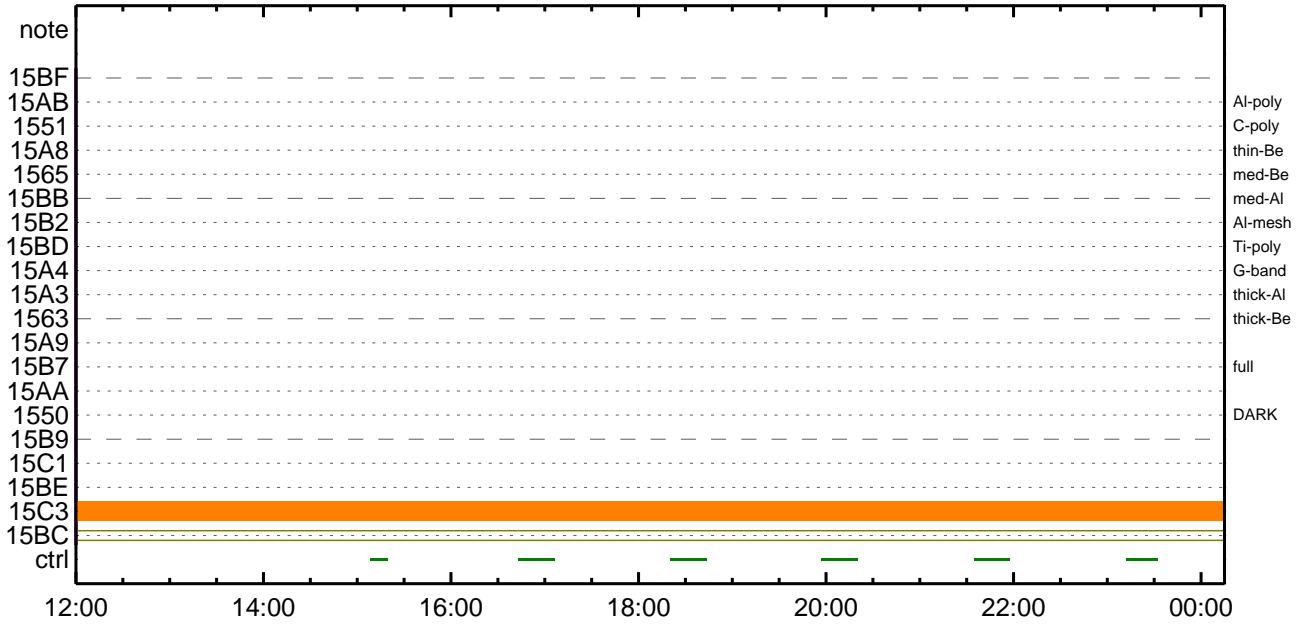
CMDI #0129 2008/09/14



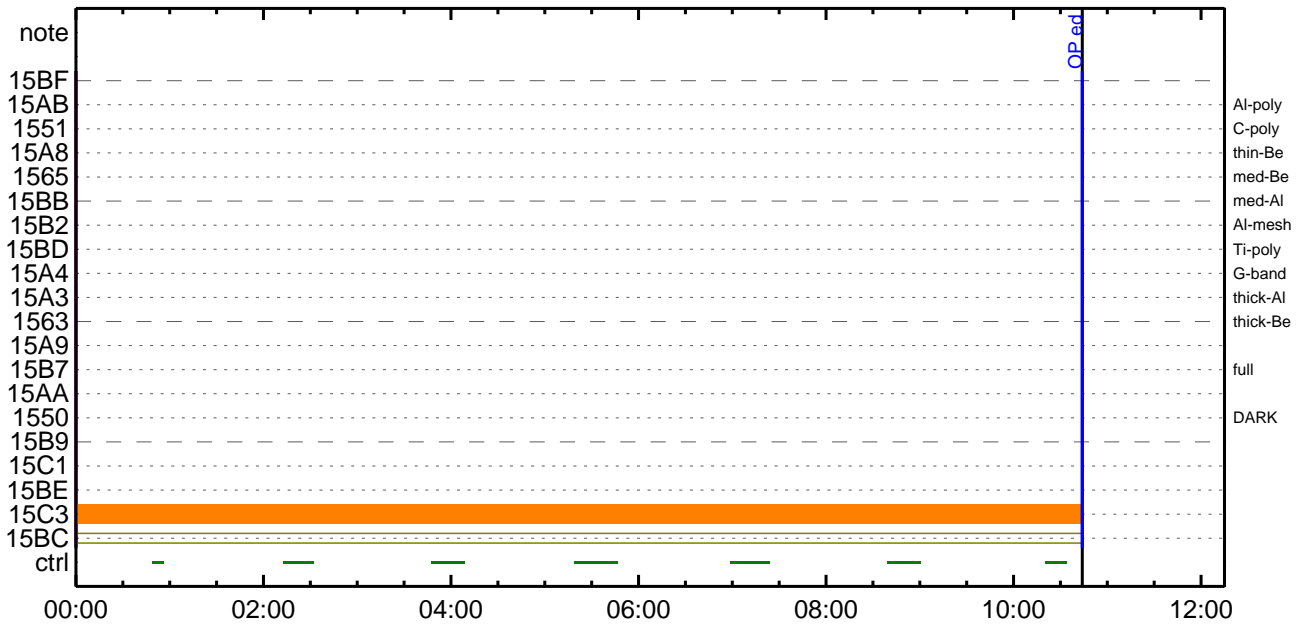
CMDI #0129 2008/09/15



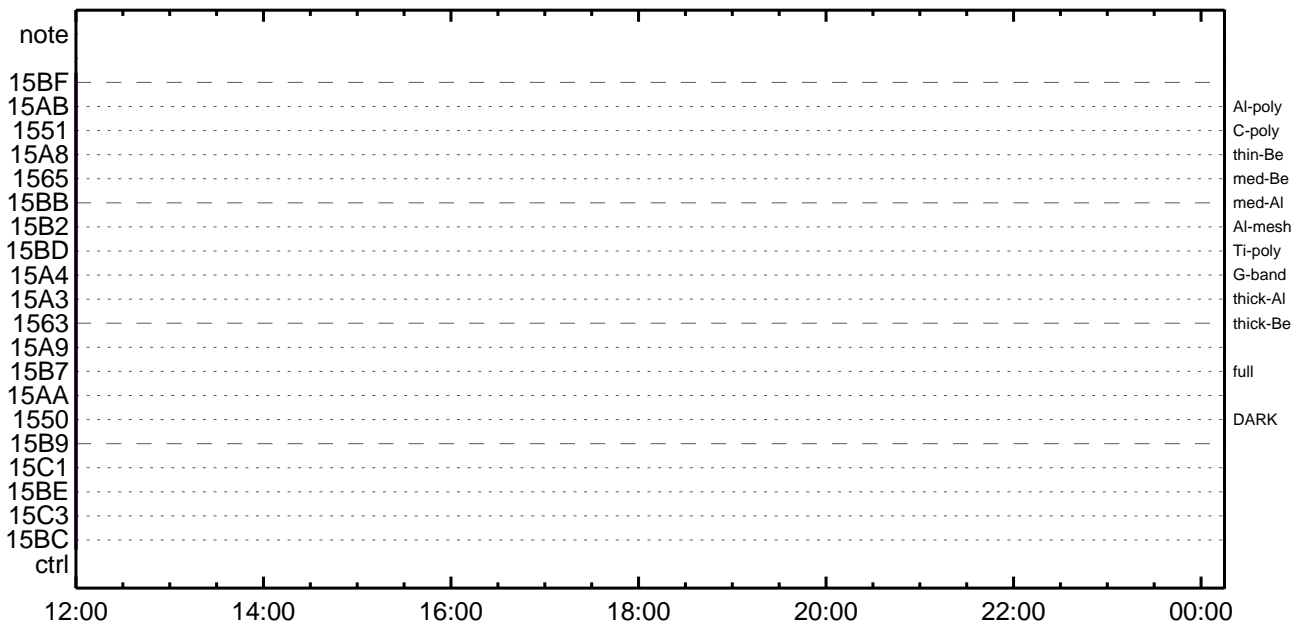
CMDI #0129 2008/09/15



CMDI #0129 2008/09/16



CMDI #0129 2008/09/16




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-128:OP
0104 ( )
0105 S. OG og-128:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½A´¶A°EÉ¬òA÷¿@ (¼âµ-YAYOYx½ê½çòðÃÓÆòÇ¼ª°¬òE¼î¹çòÇòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²¬Á÷¿@NG²î½î¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½A¹Ôª°¬E²ò³òE;f
0180 C. ²²ò¿;çSET²EEDUMP²îE±°îYNY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²Y½E²òðÃî¿(UT)
0183 +. TI 2008-09-11 10:48:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2008-09-11 10:48:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2008-09-11 10:48:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2008-09-11 10:52:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °È²¼αİÄè%İİñαİŷÄŷ§ŷÄŷ-¹àİŮ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 . C. *****
0206 C. TIİİ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]       EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]        EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]        EQ      EXEC
0224 C.
0225 . C. ŷÄŷÖŷ×½ªİ»αò³İÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]        EQ      NON
0227 C.
0228 . C. RAM ID=TI_TBLαİŷÈ¹ç•è²İOKαò³İÇ§
0229 C.
0230 . C. DHUŷâ;¼ŷÈ;Èŷ¼, ŷİ;¼ŷÈ;Èαòİāα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]        EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0237 C.
0238 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0239 C. Upload the Orbit Element and the Target Attitude
0240 C. RAM-ID:TARGET_ATT
0241 . S. RAM ram-150:TARGET_ATT
0242 ( )
0243 C.
0244 C.
0245 C. Set the dump memory area of TARGET_ATT
0246 +. DC 02-48 AOCU_DUMP_SET
0247 BC      (07 00 00 00 18 00)
0248 C.
0249 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0250 C.
0251 C.
0252 C. Change the TLMFormatNo for the AOCs Dump Format
0253 +. DC 01-22 DHU_MODE_CHNG
0254 BC      (04 0b f8)
0255 C.
0256 C. Wait for AOCSDUMP to end
0257 C.
0258 . C. Check the dump memory
0259 C.
0260 C.          Result = OK [ ]
0261 C.
0262 +. DC 01-22 DHU_MODE_CHNG
0263 BC      (02 0a f8)
0264 C.
0265 C.          <A_***>[TLM STS] FMT = 2 [ ]
0266 C.
0267 +. DC 02-8E AOCU_ORB_UPD
0268 . C.
0269 . C. Stop EIS observation and temporarily disable EIS mode changes
0270 C.
0271 C.
0272 C. ***** Start EIS operation (TI set) *****
0273 C. Execute, after the success of OP upload.
0274 C. Set EIS TI-commands
0275 +. TI 2008-09-11 10:52:30.0
0276 DC 07-FC EIS_MODE_MANU
0277 BC      (21 02)
0278 +. TI 2008-09-11 10:52:40.0
0279 DC 07-FC EIS_MODE_CHG_DIS
0280 BC      (22)
0281 . C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0282 C. ***** End EIS operation (TI set) *****
0283 C.
0284 C.
0285 C. *****
0286 C. SOT TI command set
0287 C. *****
0288 C. Execute, after the success of OP upload.
0289 +. TI 2008-09-11 10:52:16.0
0290 DC 07-F0 MDP_SOT_MODE_STBY
0291 BC      (41)

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 06 80 80 08 04)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 07 80 80 10 08)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 08 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 09 80 80 08 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 0f 80 80 06 06)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 10 80 80 04 04)
0136 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0137 BC (c4 14)
0138 + DC 07-F0 MDP_XRT_ARS_DIS
0139 BC (d5)
0140 + DC 07-F0 MDP_XRT_FLD_DIS
0141 BC (d9)
0142 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0143 BC (c9)
0144 . C. ----- Success Verify ? OK / NG ____
0145 C.
0146 C.
0147 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0148 C.
0149 +. DC 07-F0 MDP_XRT_MODE_OBSV
0150 BC (c2)
0151 +. DC 07-F0 MDP_XRT_CTRL_AUTO
0152 BC (c0)
0153 +. TI 2008-09-11 10:52:02.0
0154 DC 07-F0 MDP_XRT_MODE_OBSV
0155 BC (c2)
0156 +. TI 2008-09-11 10:52:04.0
0157 DC 07-F0 MDP_XRT_CTRL_AUTO
0158 BC (c0)
0159 . C. ----- Success Verify ? OK / NG ____
0160 C.
0161 C. ***** XRT END *****
0162 . C. *****
0163 C. SOT table upload
0164 C. *****
0165 . C. < Stop FG table >
0166 +. DC 07-F0 MDP_FG_CTRL_MANU
0167 BC (51)
0168 . C. -----
0169 C. MDP_FG_CTRL_MODE = MANU [ ]
0170 C. -----
0171 C.
0172 . C. <Upload FG Observation Table>
0173 . S. RAM ram-263:MDP_OBS_F
0174 ( )
0175 C.
0176 . C. < Dump RAMID=MDP_OBS_F >
0177 +. DC 07-F0 MDP_DUMP_FGTBL
0178 BC (82 07 00 00 00 38 b8)
0179 C. -----
0180 C. MDP_OBS_F verify = OK/NG [ ]
0181 C. -----
0182 C.
0183 . C. < Stop SP table >
0184 +. DC 07-F0 MDP_SP_CTRL_MANU
0185 BC (61)
0186 C. -----
0187 C. MDP_SP_CTRL_MODE = MANU [ ]
0188 C. -----
0189 C.
0190 . C. <Upload SP Observation Table>
0191 . S. RAM ram-288:MDP_OBS_S
0192 ( )
0193 C.

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*** OP Sequence for XRT ***

2008/09/11	11:02:54.0	XRT_CTRL_MANU_428_OG [0x1ac]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/09/11	11:02:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2008/09/11	11:03:00.0	AOCS_Ore-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	03 00 00 00 00			
2008/09/11	11:03:16.0	XRT_QT_PROG_SET_414_OG [0x19e]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02			
2008/09/11	11:04:54.0	XRT_ARS_DIS_422_OG [0x1a6]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2008/09/11	11:04:56.0	XRT_FLD_DIS_445_OG [0x1bd]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2008/09/11	11:04:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2008/09/11	11:05:00.0	XRT_CTRL_AUTO_403_OG [0x193]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/09/11	12:59:00.0	XRT_CTRL_MANU_428_OG [0x1ac]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/09/11	13:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	04 00 00 00 ec e6			
2008/09/11	17:54:24.0	XRT_CTRL_MANU_428_OG [0x1ac]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/09/11	17:54:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2008/09/11	17:54:30.0	AOCS_Ore-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	00 00 00 00 00 00			
2008/09/11	17:54:46.0	XRT_QT_PROG_SET_409_OG [0x199]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b			
2008/09/11	17:54:48.0	XRT_FLD_DIS_419_OG [0x1a3]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2008/09/11	17:54:50.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2008/09/11	17:54:52.0	XRT_ARS_DIS_427_OG [0x1ab]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2008/09/11	17:56:30.0	XRT_CTRL_AUTO_444_OG [0x1bc]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/09/11	18:04:24.0	XRT_CTRL_MANU_428_OG [0x1ac]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/09/11	18:04:26.0	XRT_FOCUS_POSITION_441_OG [0x1b9]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2008/09/11	18:04:30.0	AOCS_Ore-point_Start_4_OG [0x09a]						
		AOCU_NM	5	02-76	02 00 00 00 00 00			
2008/09/11	18:04:46.0	XRT_QT_PROG_SET_446_OG [0x1be]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04			
2008/09/11	18:06:24.0	XRT_ARS_DIS_422_OG [0x1a6]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2008/09/11	18:06:26.0	XRT_FLD_DIS_445_OG [0x1bd]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2008/09/11	18:06:28.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2008/09/11	18:06:30.0	XRT_CTRL_AUTO_403_OG [0x193]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/09/11	19:06:30.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/09/11	19:30:00.0	XRT_Custom_430_OG [0x1ae]						
2008/09/11	19:31:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/09/11	20:43:30.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/09/11	21:07:00.0	XRT_Custom_430_OG [0x1ae]						
2008/09/11	21:08:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/09/11	22:21:00.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/09/11	22:43:00.0	XRT_Custom_430_OG [0x1ae]						
2008/09/11	22:44:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/09/11	23:58:30.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/09/12	00:10:00.0	XRT_Custom_430_OG [0x1ae]						
2008/09/12	00:11:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/09/12	01:25:30.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/09/12	01:41:30.0	XRT_Custom_430_OG [0x1ae]						
2008/09/12	01:42:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/09/12	02:57:30.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/09/12	03:18:00.0	XRT_Custom_430_OG [0x1ae]						
2008/09/12	03:19:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/09/12	04:26:30.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/09/12	04:55:30.0	XRT_Custom_430_OG [0x1ae]						
2008/09/12	04:56:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/09/12	05:39:24.0	XRT_CTRL_MANU_428_OG [0x1ac]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/09/12	05:39:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			

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2008/09/12	05:39:30.0	AOCS_ORe-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 00 00 00
2008/09/12	05:39:46.0	XRT_QT_PROG_SET_429_OG [0x1ad] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2008/09/12	05:39:48.0	XRT_FLD_DIS_419_OG [0x1a3] MDP_XRT_FLD_DIS	1	07-F0	d9
2008/09/12	05:39:50.0	XRT_FLRCTRL_DIS_447_OG [0x1bf] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2008/09/12	05:39:52.0	XRT_ARS_DIS_427_OG [0x1ab] MDP_XRT_ARS_DIS	1	07-F0	d5
2008/09/12	05:41:30.0	XRT_CTRL_AUTO_444_OG [0x1bc] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/09/12	05:49:30.0	AOCS_ORe-point_Start_4_OG [0x09a] AOCU_NM	5	02-76	02 00 00 00
2008/09/12	07:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac] MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/09/12	07:59:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2008/09/12	08:00:00.0	AOCS_ORe-point_Start_1_OG [0x097] AOCU_NM	5	02-76	03 00 00 00
2008/09/12	08:00:16.0	XRT_QT_PROG_SET_414_OG [0x19e] MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2008/09/12	08:01:54.0	XRT_ARS_DIS_422_OG [0x1a6] MDP_XRT_ARS_DIS	1	07-F0	d5
2008/09/12	08:01:56.0	XRT_FLD_DIS_445_OG [0x1bd] MDP_XRT_FLD_DIS	1	07-F0	d9
2008/09/12	08:01:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2008/09/12	08:11:30.0	XRT_CTRL_AUTO_403_OG [0x193] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/09/12	09:27:00.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/09/12	09:46:30.0	XRT_Custom_430_OG [0x1ae]			
2008/09/12	09:47:30.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/09/12	09:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac] MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/09/12	09:59:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2008/09/12	10:00:00.0	AOCS_ORe-point_Start_5_OG [0x09b] AOCU_NM	5	02-76	01 00 00 00
2008/09/12	10:00:16.0	XRT_QT_PROG_SET_412_OG [0x19c] MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2008/09/12	10:01:54.0	XRT_ARS_DIS_422_OG [0x1a6] MDP_XRT_ARS_DIS	1	07-F0	d5
2008/09/12	10:01:56.0	XRT_FLD_DIS_445_OG [0x1bd] MDP_XRT_FLD_DIS	1	07-F0	d9
2008/09/12	10:01:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2008/09/12	10:02:30.0	XRT_CTRL_AUTO_403_OG [0x193] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/09/12	11:09:30.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/09/12	11:12:00.5	XRT_Custom_430_OG [0x1ae]			
2008/09/12	11:13:00.5	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/09/12	11:59:00.0	XRT_CTRL_MANU_428_OG [0x1ac] MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/09/12	12:00:00.0	AOCS_ORe-point_Start_6_OG [0x09c] AOCU_NM	5	02-76	04 00 00 00
2008/09/12	17:42:24.0	XRT_CTRL_MANU_428_OG [0x1ac] MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/09/12	17:42:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2008/09/12	17:42:30.0	AOCS_ORe-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 00 00 00
2008/09/12	17:42:46.0	XRT_QT_PROG_SET_409_OG [0x199] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2008/09/12	17:42:48.0	XRT_FLD_DIS_419_OG [0x1a3] MDP_XRT_FLD_DIS	1	07-F0	d9
2008/09/12	17:42:50.0	XRT_FLRCTRL_DIS_447_OG [0x1bf] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2008/09/12	17:42:52.0	XRT_ARS_DIS_427_OG [0x1ab] MDP_XRT_ARS_DIS	1	07-F0	d5
2008/09/12	17:44:30.0	XRT_CTRL_AUTO_444_OG [0x1bc] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/09/12	17:52:24.0	XRT_CTRL_MANU_428_OG [0x1ac] MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/09/12	17:52:26.0	XRT_FOCUS_POSITION_441_OG [0x1b9] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2008/09/12	17:52:30.0	AOCS_ORe-point_Start_4_OG [0x09a] AOCU_NM	5	02-76	02 00 00 00
2008/09/12	17:52:46.0	XRT_QT_PROG_SET_446_OG [0x1be] MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2008/09/12	17:54:24.0	XRT_ARS_DIS_422_OG [0x1a6] MDP_XRT_ARS_DIS	1	07-F0	d5
2008/09/12	17:54:26.0	XRT_FLD_DIS_445_OG [0x1bd] MDP_XRT_FLD_DIS	1	07-F0	d9
2008/09/12	17:54:28.0	XRT_FLRCTRL_DIS_416_OG [0x1a0] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2008/09/12	17:54:30.0	XRT_CTRL_AUTO_403_OG [0x193] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/09/12	18:06:00.0	XRT_CTRL_MANU_435_OG [0x1b3]			

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2008/09/12	18:30:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/12	18:31:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
2008/09/12	19:43:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/09/12	20:06:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/12	20:07:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
2008/09/12	21:20:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/09/12	21:44:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/12	21:45:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
2008/09/12	22:58:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/09/12	23:19:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/12	23:20:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
2008/09/13	00:35:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/09/13	00:43:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/13	00:44:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
2008/09/13	01:59:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/09/13	02:18:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/13	02:19:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
2008/09/13	03:02:26.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/09/13	05:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/13	05:59:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/13	06:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2008/09/13	06:00:16.0	XRT_QT_PROG_SET_429_OG [0x1ad]	AOCU_NM	5	02-76	00 00 00 00 00				
2008/09/13	06:00:18.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2008/09/13	06:00:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/09/13	06:00:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/09/13	06:02:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/09/13	06:10:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/09/13	07:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	AOCU_NM	5	02-76	02 00 00 00 00				
2008/09/13	07:59:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/13	08:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2008/09/13	08:00:16.0	XRT_QT_PROG_SET_414_OG [0x19e]	AOCU_NM	5	02-76	03 00 00 00 00				
2008/09/13	08:01:54.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2008/09/13	08:01:56.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/09/13	08:01:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/09/13	08:02:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/09/13	08:25:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/09/13	08:47:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/13	08:48:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
2008/09/13	10:05:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/09/13	10:21:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/13	10:22:00.0	XRT_CTRL_MANU_428_OG [0x1ac]								
2008/09/13	10:22:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/09/13	10:40:00.0	AOCS_Ore-point_Start_3_OG [0x099]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
		AOCU_NM		5	02-76	00 00 00 00 00				