XRT Timeline to be uploaded on 2008/09/23

Period: 2008/09/23 10:11:00 - 2008/09/27 11:01:00

* * * * * **Normal mode** * * * * *

XOB #1563: CCD Monitor During Bakeout - G-band + dark - wide FOV

Term	Pointing (x, y)	Comment
09/23 10:30:10 - 09/23 14:26:00	Track (-24.5, -0.1) @ 09/23 10:21:00	# OP start + 10min dics center tracking for EIS

PROG= 01 Inf.-time(s)
Subr= 1 1-time(s) 600.0sec
Seqn= 98 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec

XOB #1565: CCD Monitor During Bakeout - G-band + dark - wide FOV - lower cadence (30min)

Term	Pointing (x, y)	Comment
09/23 14:30:10 - 09/24 04:00:00	Track (-24.5, -0.1) @ 09/23 10:21:00	# OP start + 10min dics center tracking for EIS

PROG= 06 Inf.-time(s)
Subr= 1 1-time(s) 1800.0sec
Seqn= 98 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec

XOB #15C4: Polar Monitoring -Al/poly 16sec G-band 384x384

Term	Pointing (x, y)	Comment
09/24 10:42:00 - 09/24 13:00:00	Fixed (0.0, 900.0)	#HOP81

PROG= 05 Inf.-time(s)
Subr= 1 20-time(s) 2.0sec
Seqn= 35 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 94 30-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1048, 1064)	Q=90	0	0	60.0sec

XOB #15A3: Synoptic Q95 2x2 - Al/poly(512/5795) + Dark cal(512 Q98) + Ti-poly(723/11571) + G-band(16)

Term	Pointing (x, y)	Comment
09/24 18:32:00 - 09/24 18:39:54	Fixed (0.0, 0.0)	synoptic, shifted 3.0 min

PROG= 11 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 18 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 72 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 76 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 92 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #15A8: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant

Term	Pointing (x, y)	Comment
09/24 18:43:00 - 09/24 18:49:54	Fixed (-528.4, -528.4)	:quadrant pointing for XRT No1

PROG= 18 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 28 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec

XOB #15A9: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant

Term	Pointing (x, y)	Comment
09/24 18:53:00 - 09/24 18:59:54	Fixed (528.4, -528.4)	:quadrant pointing for XRT No2

PROG= 14 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 41 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec

Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #15AA: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant

Term	Pointing (x, y)	Comment
09/24 19:03:00 - 09/24 19:09:54	Fixed (528.4, 528.4)	:quadrant pointing for XRT No3

PROG= 13 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 8 1-time(s) 12.0sec

Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #15AB: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant

Term	Pointing (x, y)	Comment
09/24 19:13:00 - 09/25 05:33:54	Fixed (-528.4, 528.4)	:quadrant pointing for XRT No4

PROG= 15 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 40 1-time(s) 12.0sec

Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #15A4: Synoptic Q95 2x2 - Al/mesh(512/5795) + Dark cal(512 Q98) + Ti-poly(723/11571) + G-band(16)

Term	Pointing (x, y)	Comment
09/25 05:36:00 - 09/25 06:29:54	Fixed (0.0, 0.0)	synoptic, shifted -26.0 min

PROG= 12 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 87 1-time(s) 4.0sec

Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 72 1-time(s) 2.0sec

Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 76 1-time(s) 4.0sec

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 92 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #15C7: Dither_Pointing_Q3_5x-ray-512x512_NO_GBAND-long_exposure

Term	Pointing (x, y)	Comment
09/25 06:33:00 - 09/25 06:37:54	Fixed (528.4, 528.4)	:dether pointing for XRT No1 Q3
09/25 06:41:00 - 09/25 06:45:54	Fixed (529.4, 538.7)	:dether pointing for XRT No2 Q3
09/25 06:49:00 - 09/25 09:56:54	Fixed (530.5, 500.6)	:dether pointing for XRT No3 Q3

PROG= 03 1-time(s)
Subr= 1 1-time(s) 30.0sec
Seqn= 53 1-time(s) 30.0sec

thin-Be/Open	thin-Be/Open	close	Safe	Norm	64.0s	Obs	1x1	512x512 (768, 768)	DPCM	0	0	6.0sec
C-poly/Open	thin-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	512x512 (768, 768)	DPCM	0	0	6.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16.0s	Obs	1x1	512x512 (768, 768)	DPCM	0	0	6.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	11.3s	Obs	1x1	512x512 (768, 768)	DPCM	0	0	6.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	5.66s	Obs	1x1	512x512 (768, 768)	DPCM	0	0	6.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

* * * * * **Flare mode** * * * * *

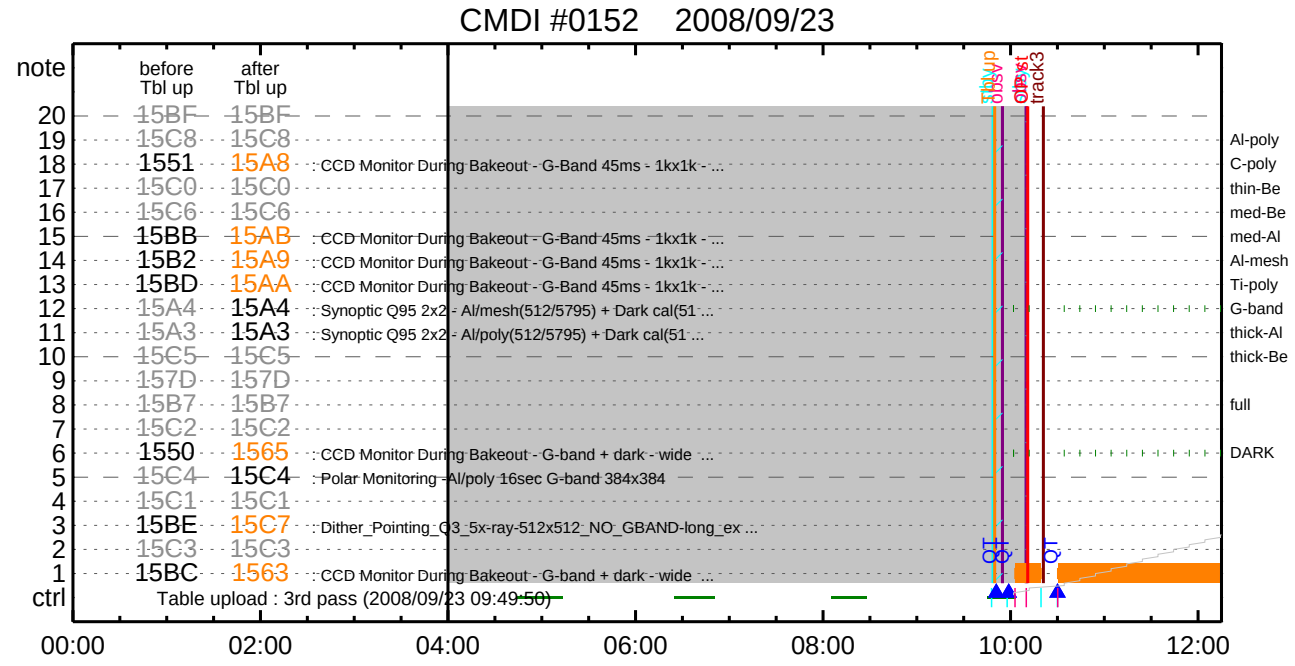
NOT USED

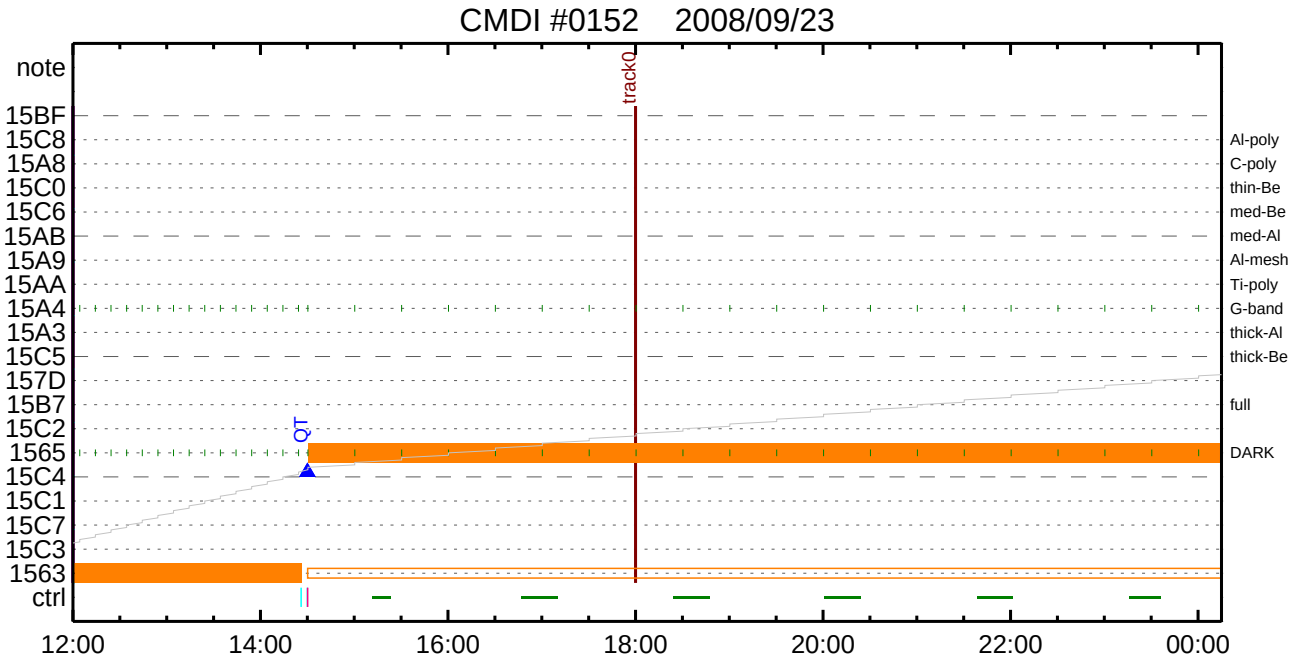
* * * * * **Active Region Search** * * * * *

NOT USED

* * * * * **Flare Detection** * * * * *

NOT USED


CMDI #0152 2008/09/23
note
before Tbl up
after Tbl up
20 15BF 15BF
19 15C8 15C8
18 1551 15A8 : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
17 15C0 15C0
16 15C6 15C6
15 15BB 15AB : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
14 15B2 15A9 : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
13 15BD 15AA : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
12 15A4 15A4 : Synoptic Q95 2x2 - Al/mesh(512/5795) + Dark cal(51 ...
11 15A3 15A3 : Synoptic Q95 2x2 - Al/poly(512/5795) + Dark cal(51 ...
10 15C5 15C5
9 157D 157D
8 15B7 15B7
7 15C2 15C2
6 1550 1565 : CCD Monitor During Bakeout - G-band + dark - wide ...
5 15C4 15C4 : Polar Monitoring - Al/poly 16sec G-band 384x384
4 15C1 15C1
3 15BE 15C7 : Dither_Pointing_G3_5x-ray-512x512_NO_GBAND-long_ex ...
2 15C3 15C3
1 15BC 1563 : CCD Monitor During Bakeout - G-band + dark - wide ...
ctrl Table upload : 3rd pass (2008/09/23 09:49:50)
track3
QT
00:00
02:00
04:00
06:00
08:00
10:00
12:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0152 2008/09/23
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15AB
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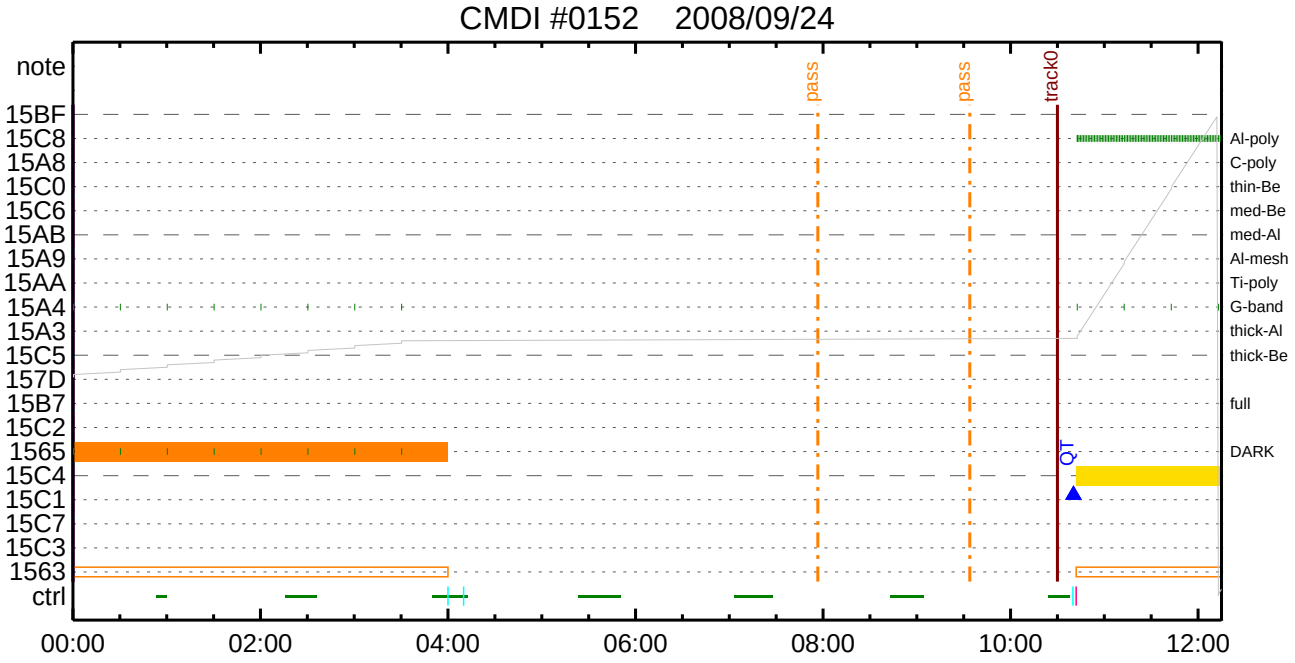
15AA
15A4
15A3
15C5
157D
15B7
15C2
1565
15C4
15C1
15C7
15C3
1563
ctrl
track0
QT
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0152 2008/09/24
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15A9
15AA
15A4
15A3
15C5
157D
15B7
15C2
1565
15C4
15C1
15C7
15C3
1563
ctrl
pass
pass
track0
QT
00:00
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08:00
10:00
12:00
Al-poly
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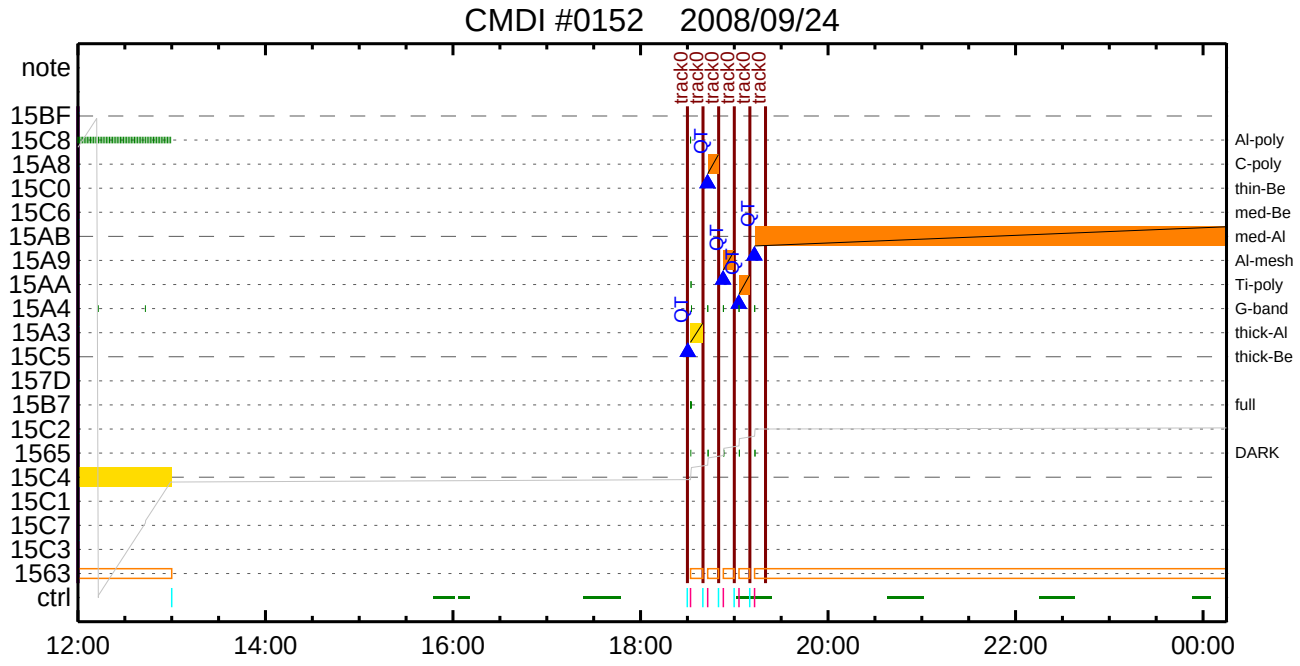
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0152 2008/09/24
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15C0
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157D
15B7
15C2
1565
15C4
15C1
15C7
15C3
1563
ctrl
track0
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
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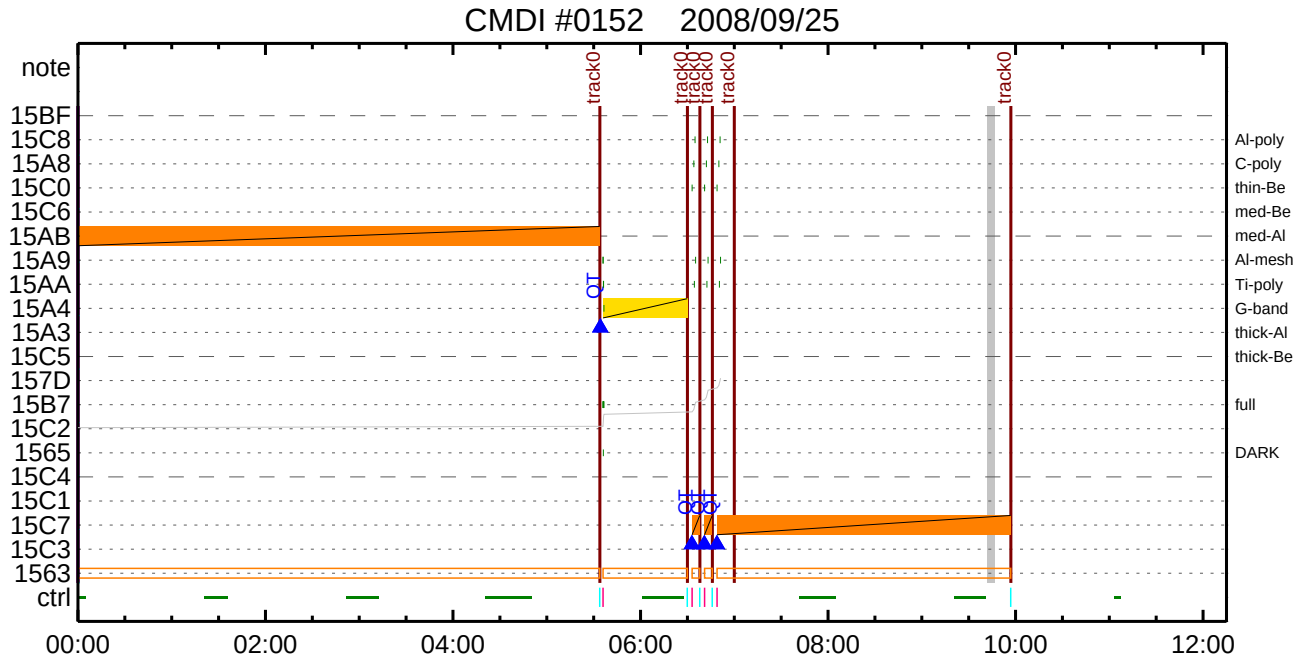
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15B7
15C2
1565
15C4
15C1
15C7
15C3
1563
ctrl
track0
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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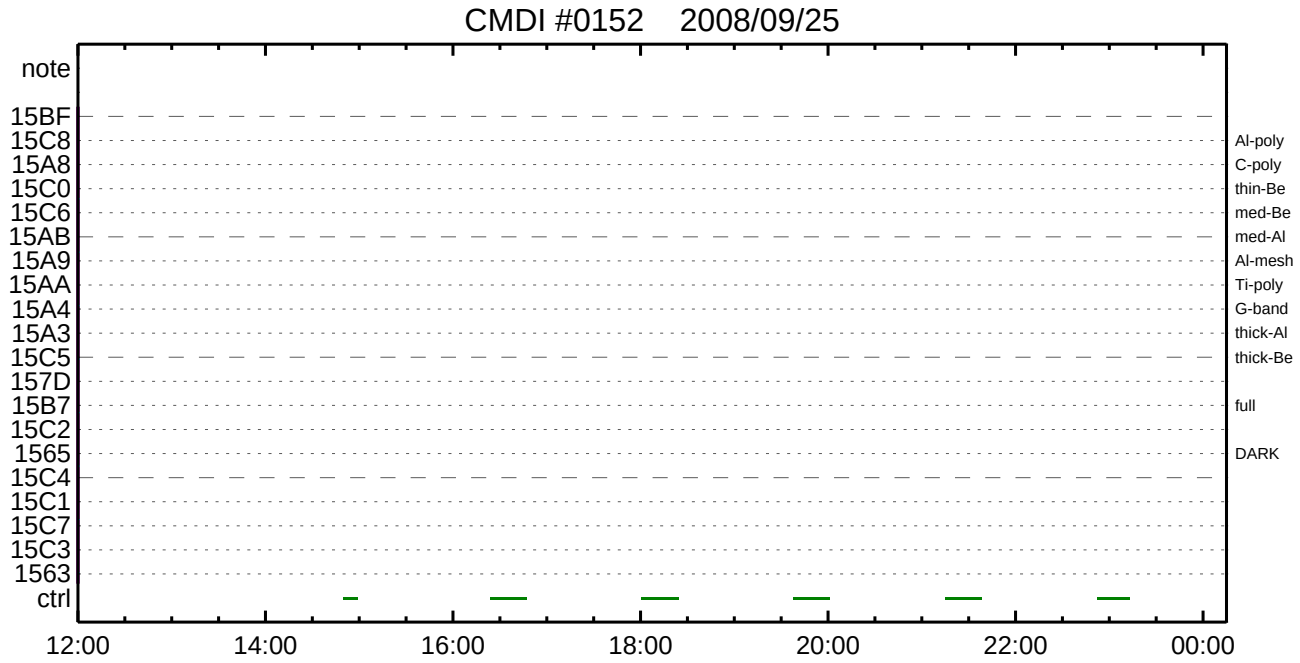
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15AA
15A4
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15C5
157D
15B7
15C2
1565
15C4
15C1
15C7
15C3
1563
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
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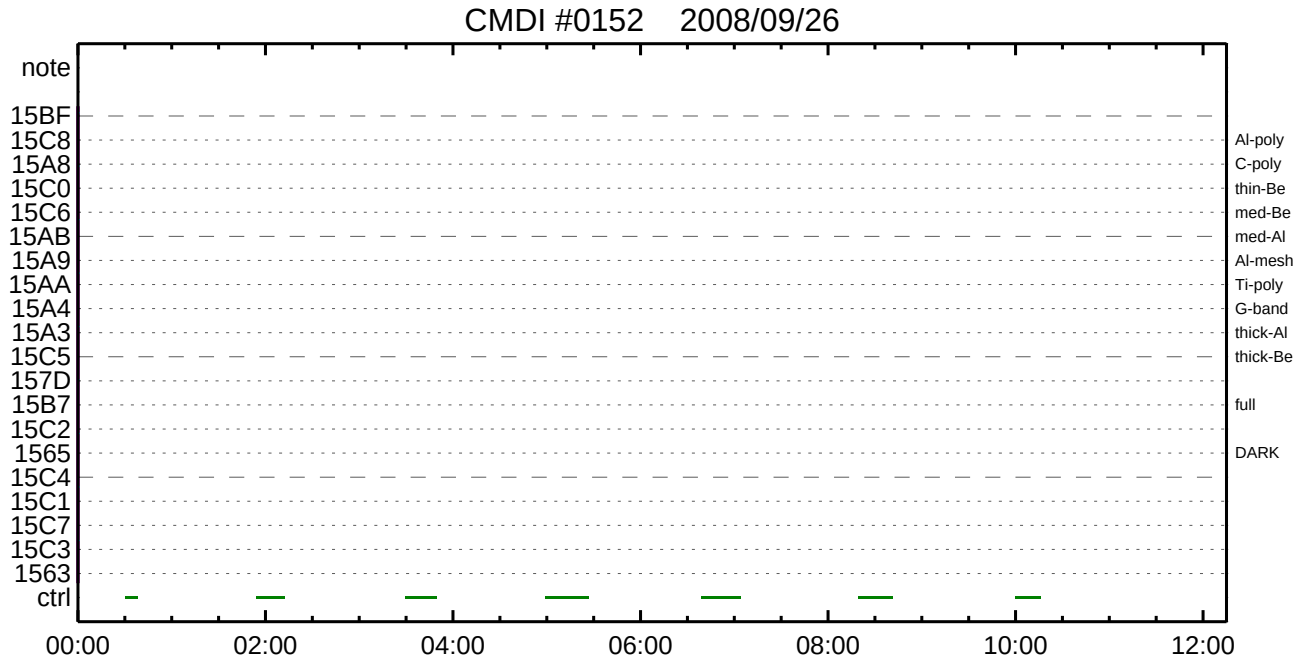
CMDI #0152 2008/09/26
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15C5
157D
15B7
15C2
1565
15C4
15C1
15C7
15C3
1563
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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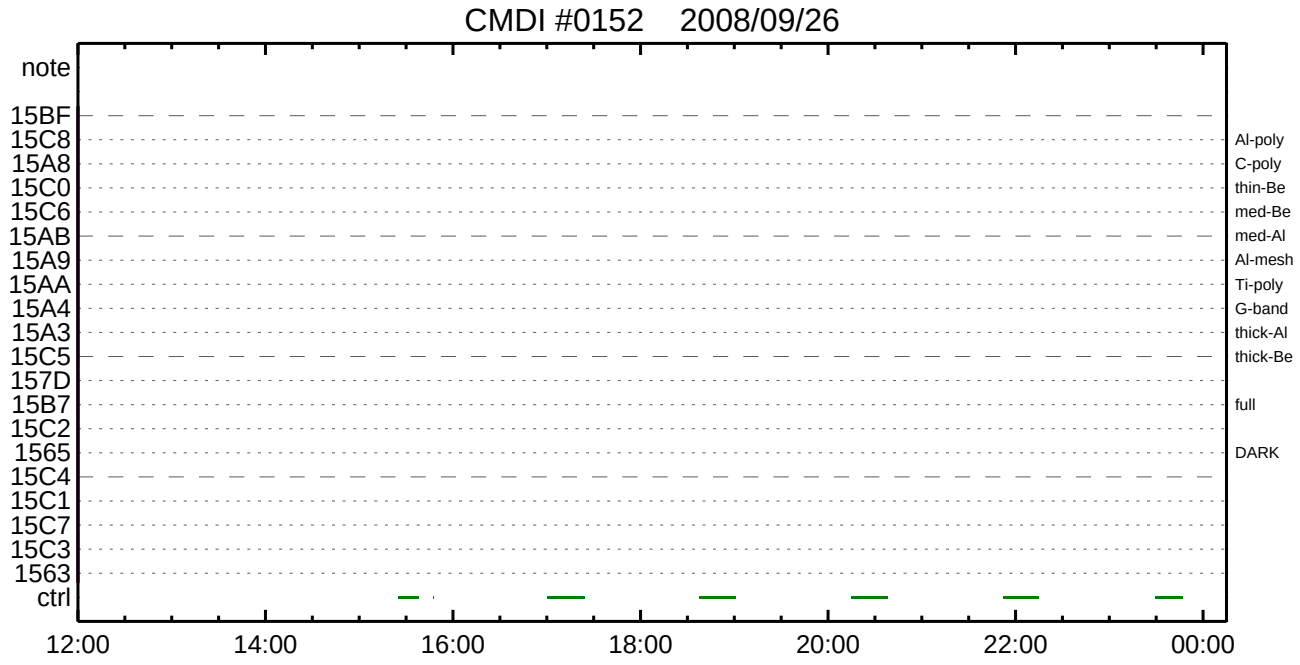
CMDI #0152 2008/09/26
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15C5
157D
15B7
15C2
1565
15C4
15C1
15C7
15C3
1563
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
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16:00
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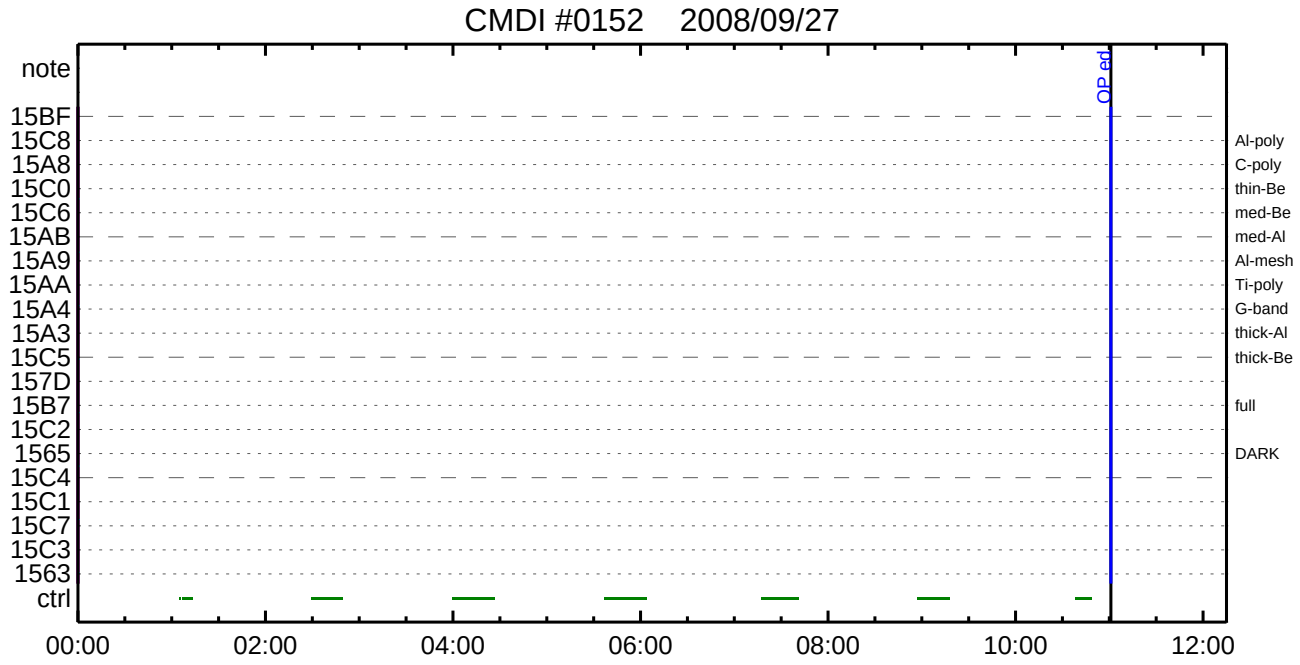
CMDI #0152 2008/09/27
note
OP ed
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15A9
15AA
15A4
15A3
15C5
157D
15B7
15C2
1565
15C4
15C1
15C7
15C3
1563
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
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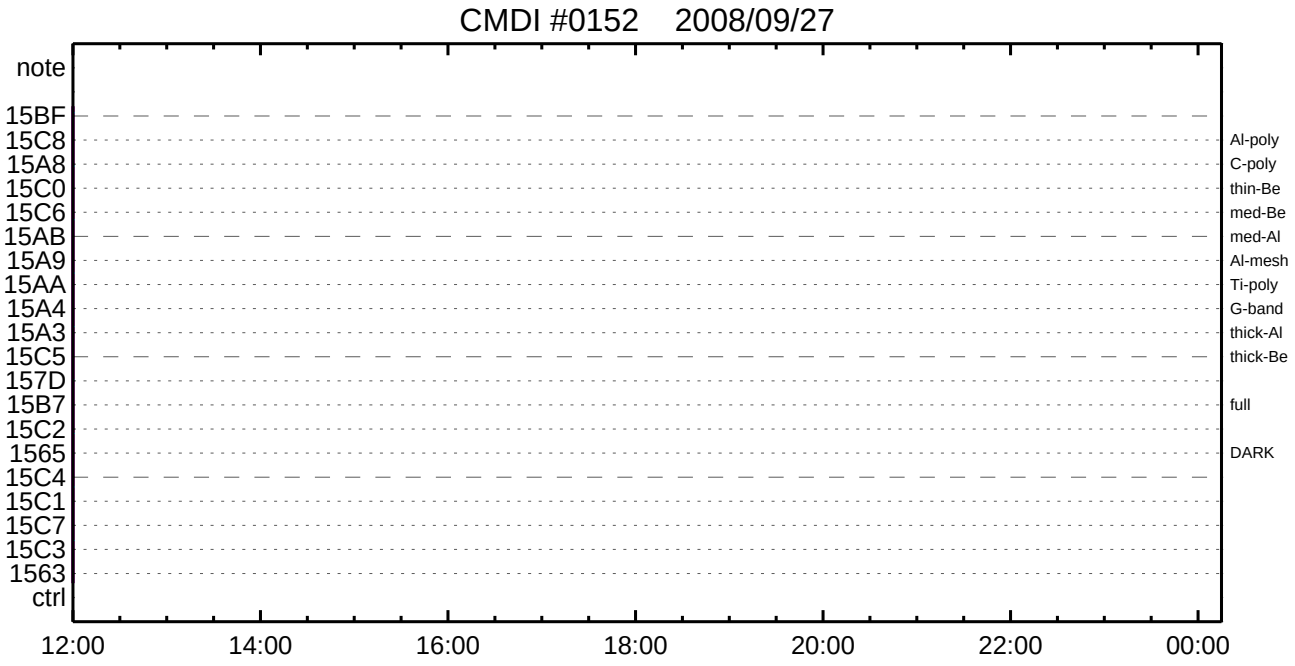
12:00


CMDI #0152 2008/09/27
note
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15A9
15AA
15A4
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15C5
157D
15B7
15C2
1565
15C4
15C1
15C7
15C3
1563
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
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```
(a) Spacecraft Operation Procedure (real-commands)

main-150 2008-09-23 12:15:03 194 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0016     C. ****************************
0017     C.
0018   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0019   . S. OP    op-150:OP
0020     ()
0021   . S. OG    og-150:OG
0022     ()
0023     C.
0024   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0025     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0026   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027     BC       (20 00 7f 01 02)
0028     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0029     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0030     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0031     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0032     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0033   +. DC 01-22 DHU_MODE_CHNG
0034     BC       (07 0b f8)
0035     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0036     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0037     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0038     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0039     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0040   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0041     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0042   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0043     C.
0044     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0045   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046     BC       (20 80 7f 01 02)
0047     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0048     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0049     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0050     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0051     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0052   +. DC 01-22 DHU_MODE_CHNG
0053     BC       (07 0b f8)
0054     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0055     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0056     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0057     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0058     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0059   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0060     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0061   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0062     C.
0063     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065     BC       (21 00 41 01 02)
0066     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0067     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0068     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0069     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0070     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0071   +. DC 01-22 DHU_MODE_CHNG
0072     BC       (07 0b f8)
0073     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0074     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0075     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0076     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0077     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0078   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0079     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0080   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0081     C.
0082   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0083     C. DHU¥â¡¼¥É¡Ê¼ý¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0084   +. DC 01-22 DHU_MODE_CHNG
0085     BC       (02 0a f8)
0086     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0087     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0088     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0089     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0090     C.
0091   . C. ********************************
0092     C. TI-CMD SET (OPOG STOP/COPY/START)
0093     C. ********************************
0094     C.
0095   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
```

```
0096     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Î¤¦¤³¤È¡£
0097     C.
0098   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0099  +. TI 2008-09-23 10:06:00.0
0100     DC 01-B3 DHU_OP_STOP
0101     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0102     C.
0103  +. TI 2008-09-23 10:06:01.0
0104     DC 01-B4 DHU_OP_COPY
0105     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0106     C.
0107  +. TI 2008-09-23 10:06:01.0
0108     DC 01-B5 DHU_OPOG_COPY
0109     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0110     C.
0111  +. TI 2008-09-23 10:10:59.5
0112     DC 01-B2 DHU_OP_START
0113     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0114     C.
0115     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0116     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0117     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0118     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0119     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0120     C.
0121   . C. ***************************
0122     C. TIÎÎ°è¥À¥ó¥×
0123     C. ***************************
0124     C.
0125     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0126  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127     BC       (03 ab 03 01 02)
0128     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0129     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0130     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0131     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0132     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0133  +. DC 01-22 DHU_MODE_CHNG
0134     BC       (07 0b f8)
0135     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0136     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0137     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0138     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0139     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0140     C.
0141   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0142     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0143     C.
0144   . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0145     C.
0146   . C. DHU¥â¡¼¥É¡Ê¼ý¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0147  +. DC 01-22 DHU_MODE_CHNG
0148     BC       (02 0a f8)
0149     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0150     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0151     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0152     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0153     C.
0154   . C. Stop EIS observation and temporarily disable EIS mode changes
0155     C.
0156     C.
0157     C. ********** Start EIS operation (TI set) **********
0158     C. Execute, after the success of OP upload.
0159     C. Set EIS TI-commands
0160  +. TI 2008-09-23 10:10:30.0
0161     DC 07-FC EIS_MODE_MANU
0162     BC       (21 02)
0163  +. TI 2008-09-23 10:10:40.0
0164     DC 07-FC EIS_MODE_CHG_DIS
0165     BC       (22)
0166   . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0167     C. *********** End EIS operation (TI set) ***********
0168     C.
0169     C.
0170     C.
0171     C. ************ XRT START ***********
0172     C. Execute, after the success of OP upload.
0173  +. TI 2008-09-23 10:10:00.0
0174     DC 07-F0 MDP_XRT_MODE_STBY
0175     BC       (c3)
0176   . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0177     C.
0178     C. ************ XRT END ***********
0179     C.
0180   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0181     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0182   . S. DC-BC dcbc-402:DCBC
0183     (MDP_known_event)
0184     C.
0185     C.
0186   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0187   . S. DC-BC dcbc-153:DCBC
0188     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0189     C.
0190     C.
0191   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0192     C.
0193   . C. ********** LOS **********
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0194 C.

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(a) Spacecraft Operation Procedure (real-commands)

main-151 2008-09-23 12:15:03 175 33 SOLAR-B MAIN //
0001     C.
0002    . C. *********** AOS ***********
0003     C.
0004    . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007    +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009    . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011    +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014    . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019    +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021    +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023    +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029    . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031    . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036    . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037    +. DC 01-29 DHU_S/X_VC4_OFF
0038    +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040    +  DC 06-B3 DR_REP_START
0041    +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046    . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047    +. DC 06-B3 DR_REP_START
0048    +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054    . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057    . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062    . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063    +. DC 01-29 DHU_S/X_VC4_OFF
0064    +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066    +  DC 06-B3 DR_REP_START
0067    +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072    . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073    +. DC 06-B3 DR_REP_START
0074    +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079    . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083    . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084    +. DC 06-B4 DR_REP_STOP
0085    +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090    . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091    +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093    +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106   +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113   +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                      Result = OK [ ]
0121     C.
0122   +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                         <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127   +. DC 02-8E AOCU_ORB_UPD
0128   . C.
0129   . C. ***** AOCS Commands (Orbital Element Update) *****
0130     C. Update the orbital element
0131   +. DC 02-50 AOCU_ORB_PRPGT_START
0132     BC       (16)
0133   +  DC 02-8E AOCU_ORB_UPD
0134     C.
0135     C.       <A_ORB>[ORBIT] EPC = 4111644.5 +- 1.0 (s) [ ]
0136     C.
0137   . C.
0138   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139   +. DC 07-FC EIS_MODE_MANU
0140     BC       (21 02)
0141   . C. Verify EIS in MANUAL mode
0142   . C. Estimated OBSTBL upload time is 10s
0143     C. ********************************
0144     C. EIS START OBSTBL LOAD
0145     C. ********************************
0146   . S. RAM   ram-820:EIS_OBSTBL
0147     ()
0148   +. DC 07-FC EIS_DUMP_OBSTBL
0149     BC       (07 07 07 00 00 70 00)
0150     C.
0151     C. Execute, after the success of OBSTBL upload.
0152     C. Set EIS TI-commands
0153   +. TI 2008-09-23 10:10:50.0
0154     DC 07-FC EIS_MODE_CHG_ENA
0155     BC       (20)
0156   . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0157     C. ********************************
0158     C. EIS END OBSTBL LOAD
0159     C. ********************************
0160     C.
0161   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0162     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0163   . S. DC-BC dcbc-402:DCBC
0164     (MDP_known_event)
0165     C.
0166     C.
0167   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0168   . S. DC-BC dcbc-153:DCBC
0169     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0170     C.
0171     C.
0172   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0173     C.
0174   . C. ********** LOS **********
0175     C.
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(a) Spacecraft Operation Procedure (real-commands)

main-152 2008-09-23 12:15:03 168 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ********************************
0015     C.  SOT table upload
0016     C. ********************************
0017   . C. < Upload DPL table >
0018   +. DC 07-F0 MDP_FG_CTRL_MANU
0019     BC       (51)
0020   . C. ------------------------------------
0021     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0022     C. ------------------------------------
0023     C.
0024     C. ¥¢¥Ã¥×¥í¡¼¥É¤ÎÁ°¤ËSTS_CHK¤òOFF¤Ë¤¹¤ë
0025     C.
0026   . S. RAM   ram-271:MDP_DPL
0027     ()
0028     C.
0029   . C. < Dump RAMID=MDP_DPL >
0030   +. DC 07-F0 MDP_DUMP_FGTBL
0031     BC       (82 07 00 38 b8 00 40)
0032     C. ------------------------------------
0033     C.  MDP_DPL verify          = OK    [ ]
0034     C. ------------------------------------
0035     C.
0036     C. STS_CHK¤òON¤Ë¤¹¤ë
0037     C.
0038   . C. < Update MDP DSC PAR1 >
0039   +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0040     BC       (4c)
0041     C.  MDP_CMD_CODE          = F04C0700[ ]
0042     C.  MDP_CMD_CNT        (count-up 1) [ ]
0043     C. ------------------------------------
0044     C.
0045   . C.
0046     C.
0047     C. ************ XRT START ************
0048     C.
0049   +. DC 07-F0 MDP_XRT_CTRL_MANU
0050     BC       (c1)
0051   +  DC 07-F0 MDP_XRT_MODE_STBY
0052     BC       (c3)
0053   . C. ----------- Success Verify ?             OK / NG____
0054     C.
0055     C. XRT Obs. Table Upload
0056   . S. RAM   ram-291:MDP_OBS_X
0057     ()
0058     C.
0059   +. DC 07-F0 MDP_DUMP_XRTTBL
0060     BC       (84 07 00 00 00 3a d4)
0061   . C. ----------- Comparison Check ?                      OK / ERR ____
0062     C.
0063     C.
0064   +. DC 07-F0 MDP_XRT_ROI_SET
0065     BC       (cd 01 b1 b1 04 04)
0066   +  DC 07-F0 MDP_XRT_ROI_SET
0067     BC       (cd 02 b1 b1 08 08)
0068   +  DC 07-F0 MDP_XRT_ROI_SET
0069     BC       (cd 03 b1 b1 08 08)
0070   +  DC 07-F0 MDP_XRT_ROI_SET
0071     BC       (cd 04 b1 b1 06 06)
0072   +  DC 07-F0 MDP_XRT_ROI_SET
0073     BC       (cd 06 80 80 20 04)
0074   +  DC 07-F0 MDP_XRT_ROI_SET
0075     BC       (cd 07 80 80 06 06)
0076   +  DC 07-F0 MDP_XRT_ROI_SET
0077     BC       (cd 08 83 85 06 06)
0078   +  DC 07-F0 MDP_XRT_ROI_SET
0079     BC       (cd 09 80 80 20 20)
0080   +  DC 07-F0 MDP_XRT_ROI_SET
0081     BC       (cd 0a c0 c0 10 10)
0082   +  DC 07-F0 MDP_XRT_ROI_SET
0083     BC       (cd 0b 40 c0 10 10)
0084   +  DC 07-F0 MDP_XRT_ROI_SET
0085     BC       (cd 0c 40 40 10 10)
0086   +  DC 07-F0 MDP_XRT_ROI_SET
0087     BC       (cd 0d c0 40 10 10)
0088   +  DC 07-F0 MDP_XRT_ROI_SET
0089     BC       (cd 0e 60 60 08 08)
0090   +  DC 07-F0 MDP_XRT_ROI_SET
0091     BC       (cd 0f 80 80 06 06)
0092   +  DC 07-F0 MDP_XRT_ROI_SET
0093     BC       (cd 10 80 80 04 04)
0094   +. DC 07-F0 MDP_XRT_QT_PROG_SET
0095     BC       (c4 01)
```

```
0096  +  DC 07-F0 MDP_XRT_ARS_DIS
0097     BC       (d5)
0098  +  DC 07-F0 MDP_XRT_FLD_DIS
0099     BC       (d9)
0100  +  DC 07-F0 MDP_XRT_FLRCTRL_DIS
0101     BC       (c9)
0102   . C. ---------- Success Verify ?            OK / NG ____
0103     C.
0104     C.
0105   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0106     C.
0107  +. DC 07-F0 MDP_XRT_MODE_OBSV
0108     BC       (c2)
0109  +. TI 2008-09-23 10:10:02.0
0110     DC 07-F0 MDP_XRT_MODE_OBSV
0111     BC       (c2)
0112  +. TI 2008-09-23 10:10:04.0
0113     DC 07-F0 MDP_XRT_CTRL_AUTO
0114     BC       (c0)
0115   . C. ---------- Success Verify ?            OK / NG ____
0116     C.
0117     C. ************ XRT END ***********
0118     C. ************************************************
0119     C.  START of XRT_CCD_HEATER_ON operation
0120     C. ************************************************
0121     C.
0122  +. DC 07-F0 MDP_XRT_CTRL_MANU
0123     BC       (c1)
0124     C. ---------- Success Verify ?           OK / NG;
0125     C.
0126  +. DC 04-BC TCIB_XRT_S_HTR_A_ENA
0127     C. ---------- Success Verify ?           OK / NG;
0128     C.
0129     C. ----------------------------------------
0130     C. If anomalous situation appeared, execute TCIB_XRT_S_HTR_A_DIS using DCBC-442 (line 24)
0131     C. ----------------------------------------
0132     C.
0133  +. DC 07-F0 MDP_XRT_QT_PROG_SET
0134     BC       (c4 01)
0135  +  DC 07-F0 MDP_XRT_FLD_DIS
0136     BC       (d9)
0137  +  DC 07-F0 MDP_XRT_FLRCTRL_DIS
0138     BC       (c9)
0139  +  DC 07-F0 MDP_XRT_ARS_DIS
0140     BC       (d5)
0141     C. ---------- Success Verify ?            OK / NG ____
0142     C.
0143     C.
0144     C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0145     C.
0146  +. DC 07-F0 MDP_XRT_CTRL_AUTO
0147     BC       (c0)
0148     C. ************************************************
0149     C.  END of XRT_CCD_HEATER_ON operation
0150     C. ************************************************
0151     C.
0152     C.
0153     C.
0154   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¤ëDCBC•×²è *****
0155     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¾Â»Ü¤¤ë)
0156   . S. DC-BC dcbc-402:DCBC
0157     (MDP_known_event)
0158     C.
0159     C.
0160   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¤ëDCBC•×²è *****
0161   . S. DC-BC dcbc-153:DCBC
0162     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0163     C.
0164     C.
0165   . C. ¡ãLOS¥Á¥§¥Ã¥¯¾Â»Ü¡ä
0166     C.
0167   . C. *********** LOS ***********
0168     C.
```

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*** OP Sequence for XRT ***

2008/09/23  10:19:30.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/23  10:21:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 03 00 00 00 00
2008/09/23  10:30:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/23  10:30:02.0  XRT_QT_PROG_SET_400_OG [0x190]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 01
2008/09/23  10:30:04.0  XRT_ARS_DIS_422_OG [0x1a6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2008/09/23  10:30:06.0  XRT_FLD_DIS_445_OG [0x1bd]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2008/09/23  10:30:08.0  XRT_FLRCTRL_DIS_416_OG [0x1a0]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/09/23  10:30:10.5  XRT_CTRL_AUTO_403_OG [0x193]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/23  14:26:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/23  14:30:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/23  14:30:02.0  XRT_QT_PROG_SET_431_OG [0x1af]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 06
2008/09/23  14:30:04.0  XRT_ARS_DIS_422_OG [0x1a6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2008/09/23  14:30:06.0  XRT_FLD_DIS_445_OG [0x1bd]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2008/09/23  14:30:08.0  XRT_FLRCTRL_DIS_416_OG [0x1a0]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/09/23  14:30:10.0  XRT_CTRL_AUTO_403_OG [0x193]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/23  18:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 54 00
2008/09/24  04:00:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/24  04:00:02.0  XRT_TCIB_XRT_S_HTR_A_DIS_433_OG [0x1b1]
                                TCIB_XRT_S_HTR_A_DIS 0 04-C0
2008/09/24  04:10:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/24  10:30:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 b0 00 00 00
2008/09/24  10:39:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/24  10:39:56.0  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2008/09/24  10:40:16.0  XRT_QT_PROG_SET_443_OG [0x1bb]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 05
2008/09/24  10:41:54.0  XRT_ARS_DIS_422_OG [0x1a6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2008/09/24  10:41:56.0  XRT_FLD_DIS_445_OG [0x1bd]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2008/09/24  10:41:58.0  XRT_FLRCTRL_DIS_416_OG [0x1a0]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/09/24  10:42:00.0  XRT_CTRL_AUTO_403_OG [0x193]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/24  13:00:00.5  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/24  18:29:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/24  18:29:56.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2008/09/24  18:30:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 00 00 00 00
2008/09/24  18:30:16.0  XRT_QT_PROG_SET_409_OG [0x199]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2008/09/24  18:30:18.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2008/09/24  18:30:20.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/09/24  18:30:22.0  XRT_ARS_DIS_427_OG [0x1ab]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2008/09/24  18:32:00.0  XRT_CTRL_AUTO_444_OG [0x1bc]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/24  18:39:54.0  XRT_CTRL_MANU_405_OG [0x195]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/24  18:40:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM              5 02-76 00 2e f9 2e f9
2008/09/24  18:42:32.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2008/09/24  18:42:52.0  XRT_QT_PROG_SET_424_OG [0x1a8]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 12
2008/09/24  18:42:54.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2008/09/24  18:42:56.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/09/24  18:42:58.0  XRT_ARS_DIS_418_OG [0x1a2]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2008/09/24  18:43:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/24  18:49:54.0  XRT_CTRL_MANU_405_OG [0x195]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/24  18:50:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
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                                    AOCU_NM              5 02-76 00 2e f9 d1 07
2008/09/24  18:52:32.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2008/09/24  18:52:52.0  XRT_QT_PROG_SET_411_OG [0x19b]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0e
2008/09/24  18:52:54.0  XRT_FLD_DIS_419_OG [0x1a3]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2008/09/24  18:52:56.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/09/24  18:52:58.0  XRT_ARS_DIS_418_OG [0x1a2]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2008/09/24  18:53:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/24  18:59:54.0  XRT_CTRL_MANU_405_OG [0x195]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/24  19:00:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                    AOCU_NM              5 02-76 00 d1 07 d1 07
2008/09/24  19:02:32.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2008/09/24  19:02:52.0  XRT_QT_PROG_SET_436_OG [0x1b4]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2008/09/24  19:02:54.0  XRT_FLD_DIS_419_OG [0x1a3]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2008/09/24  19:02:56.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/09/24  19:02:58.0  XRT_ARS_DIS_418_OG [0x1a2]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2008/09/24  19:03:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/24  19:09:54.0  XRT_CTRL_MANU_405_OG [0x195]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/24  19:10:00.0  AOCS_ORe-point_Start_8_OG [0x09e]
                                    AOCU_NM              5 02-76 00 d1 07 2e f9
2008/09/24  19:12:32.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2008/09/24  19:12:52.0  XRT_QT_PROG_SET_415_OG [0x19f]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2008/09/24  19:12:54.0  XRT_FLD_DIS_419_OG [0x1a3]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2008/09/24  19:12:56.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/09/24  19:12:58.0  XRT_ARS_DIS_418_OG [0x1a2]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2008/09/24  19:13:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/24  19:20:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                    AOCU_NM              5 02-76 00 b0 00 00 00
2008/09/25  05:33:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/25  05:33:56.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2008/09/25  05:34:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2008/09/25  05:34:16.0  XRT_QT_PROG_SET_429_OG [0x1ad]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2008/09/25  05:34:18.0  XRT_FLD_DIS_419_OG [0x1a3]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2008/09/25  05:34:20.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/09/25  05:34:22.0  XRT_ARS_DIS_427_OG [0x1ab]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2008/09/25  05:36:00.0  XRT_CTRL_AUTO_444_OG [0x1bc]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/25  06:29:54.0  XRT_CTRL_MANU_405_OG [0x195]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/25  06:30:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                    AOCU_NM              5 02-76 00 d1 07 d1 07
2008/09/25  06:32:32.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2008/09/25  06:32:52.0  XRT_QT_PROG_SET_425_OG [0x1a9]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2008/09/25  06:32:54.0  XRT_FLD_DIS_419_OG [0x1a3]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2008/09/25  06:32:56.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/09/25  06:32:58.0  XRT_ARS_DIS_418_OG [0x1a2]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2008/09/25  06:33:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/25  06:37:54.0  XRT_CTRL_MANU_405_OG [0x195]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/25  06:38:00.0  AOCS_ORe-point_Start_9_OG [0x09f]
                                    AOCU_NM              5 02-76 00 d0 21 d0 ee
2008/09/25  06:40:32.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2008/09/25  06:40:52.0  XRT_QT_PROG_SET_425_OG [0x1a9]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2008/09/25  06:40:54.0  XRT_FLD_DIS_419_OG [0x1a3]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2008/09/25  06:40:56.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/09/25  06:40:58.0  XRT_ARS_DIS_418_OG [0x1a2]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
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2008/09/25  06:41:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/25  06:45:54.0  XRT_CTRL_MANU_405_OG [0x195]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/25  06:46:00.0  AOCS_ORe-point_Start_10_OG [0x0a0]
                                AOCU_NM              5 02-76 00 d3 7d d0 d5
2008/09/25  06:48:32.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2008/09/25  06:48:52.0  XRT_QT_PROG_SET_425_OG [0x1a9]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2008/09/25  06:48:54.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2008/09/25  06:48:56.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2008/09/25  06:48:58.0  XRT_ARS_DIS_418_OG [0x1a2]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2008/09/25  06:49:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2008/09/25  07:00:00.5  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 b0 00 00 00
2008/09/25  09:56:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2008/09/25  09:57:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 00 00 00 00
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