

# XRT Timeline to be uploaded on 2008/10/14

Period: 2008/10/14 10:09:00 - 2008/10/18 11:00:00

\* \* \* \* \*

Normal mode

\* \* \* \* \*

XOB #15D5: Microflares - Be/thin 11sec AEC 256x256 Q98												
Term		Pointing (x, y)					Comment					
10/14 10:21:02 - 10/14 13:00:00		Track ( -128.0, 349.6) @ 10/14 10:19:00					# HOP88 at active region					
10/15 07:32:08 - 10/15 12:30:00		Track ( 30.9, 349.8) @ 10/15 06:10:00					HOP 88 active region					
<b>PROG= 14 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 42 1-time(s) 2.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 11.3s Obs 1x1 256x256 (1024, 1024) Q=98 1 0 55.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #15DB: Al/poly 8s cad=60s + dark FOV384x384												
Term		Pointing (x, y)					Comment					
10/14 15:02:02 - 10/14 17:58:54		Track ( 0.0, 0.0) @ 10/14 15:00:00					HOP 90 QS					
10/15 15:02:02 - 10/15 17:22:00		Track ( 0.0, -0.1) @ 10/15 15:00:00					HOP90 QS Ca jets					
<b>PROG= 19 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 75 30-time(s) 60.0sec												
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 8.00s Obs 1x1 384x384 (1024, 1024) DPCM 1 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 88 1-time(s) 2.0sec												
└─ Al-poly/Open C-poly/Open close Safe Dark 8.00s Obs 1x1 384x384 (1024, 1024) DPCM 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #15D6: Synoptic Q95 2x2 - Al/poly(362/5795) + Dark cal(512 Q98) + Ti-poly(512/11571) + G-band(16)												
Term		Pointing (x, y)					Comment					
10/14 18:01:00 - 10/14 18:08:54		Fixed ( 0.0, 0.0)					synoptic, shifted -1.0 min					
10/15 18:03:30 - 10/15 18:11:24		Fixed ( 0.0, 0.0)					synoptic, shifted 1.5 min					
<b>PROG= 06 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 74 1-time(s) 4.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 57 1-time(s) 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Dark 354ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 4 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 92 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #15DA: Nanoflares - Ti/poly, Al/poly 384x384 - Q95												
Term		Pointing (x, y)					Comment					
10/14 18:11:00 - 10/14 23:14:30		Track ( 29.3, 0.2) @ 10/14 18:09:00					HOP 91 + EIS sensitivity monitoring					
<b>PROG= 05 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 47 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 22.6s Obs 1x1 384x384 (1048, 1064) Q=98 0 0 25.0sec												
└─ Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 32.0s Obs 1x1 384x384 (1048, 1064) Q=98 0 0 25.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #15DC: Synoptic 2x2 Q98 Al/mesh (4096ms) Al/poly (4096ms) Ti/poly (4096ms) C/poly (4096ms) Al/poly+Ti/poly (8192ms) Thin-Be (65536ms) Med-Be												
Term		Pointing (x, y)					Comment					
10/15 00:02:06 - 10/15 05:59:54		Track ( 29.3, 0.2) @ 10/14 18:09:00					HOP 91 + EIS sensitivity monitoring					
<b>PROG= 16 1-time(s)</b>												
└─ Subr= 1 8-time(s) 1800.0sec												
└─ Seqn= 95 1-time(s) 4.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 59 1-time(s) 4.0sec												
└─ C-poly/Open C-poly/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 9 1-time(s) 4.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 46 1-time(s) 4.0sec												
└─ Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 15 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 50 1-time(s) 4.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 14 1-time(s) 4.0sec												
└─ med-Be/Open med-Al/Open close Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												

**XOB #15D7: Synoptic Q95 2x2 - Al/mesh(512/5795) + Dark cal(362 Q98) + Ti-poly(512/11571) + G-band(16)**

Term	Pointing (x, y)	Comment
10/15 06:02:00 - 10/15 07:01:30	Fixed ( 0.0, 0.0)	synoptic
10/15 07:27:30 - 10/15 07:30:00	Track ( 30.9, 349.8) @ 10/15 06:10:00	HOP 88 active region
10/16 05:34:30 - 10/16 05:59:30	Fixed ( 0.0, 0.0)	synoptic, shifted -27.5 min
10/16 06:27:00 - 10/18 11:00:00	Track ( 217.6, 353.7) @ 10/16 05:42:30	HOP88 active region

**PROG= 03 1-time(s)**

Subr= 1	1-time(s)	12.0sec											
<b>Seqn= 36</b>	<b>1-time(s)</b>	<b>4.0sec</b>											
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	5.66s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 57</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	354ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 4</b>	<b>1-time(s)</b>	<b>4.0sec</b>											
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 92</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec

**XOB #15DD: AR - Al/poly + Ti/poly - Q90 - 1x1 - 384FOV with G-Band + context**

Term	Pointing (x, y)	Comment
10/15 18:13:32 - 10/15 23:51:30	Track ( 127.0, 351.3) @ 10/15 18:11:30	Disk AR

**PROG= 09 Inf.-time(s)**

Subr= 1	6-time(s)	2.0sec											
<b>Seqn= 21</b>	<b>10-time(s)</b>	<b>120.0sec</b>											
Al-poly/Open	C-poly/Open	close	Safe	Norm	8.00s	Obs	1x1	384x384	(1024, 1024)	Q=90	1	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	1x1	384x384	(1024, 1024)	Q=90	1	0	2.0sec
<b>Seqn= 39</b>	<b>1-time(s)</b>	<b>60.0sec</b>											
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	512x512	(1024, 1024)	Q=90	0	0	2.0sec
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
<b>Seqn= 60</b>	<b>1-time(s)</b>	<b>1200.0sec</b>											
Al-poly/Open	C-poly/Open	close	Safe	Norm	1.00s	Obs	1x1	512x512	(1024, 1024)	Q=90	1	0	2.0sec

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**Flare mode**

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NOT USED

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**Active Region Search**

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NOT USED

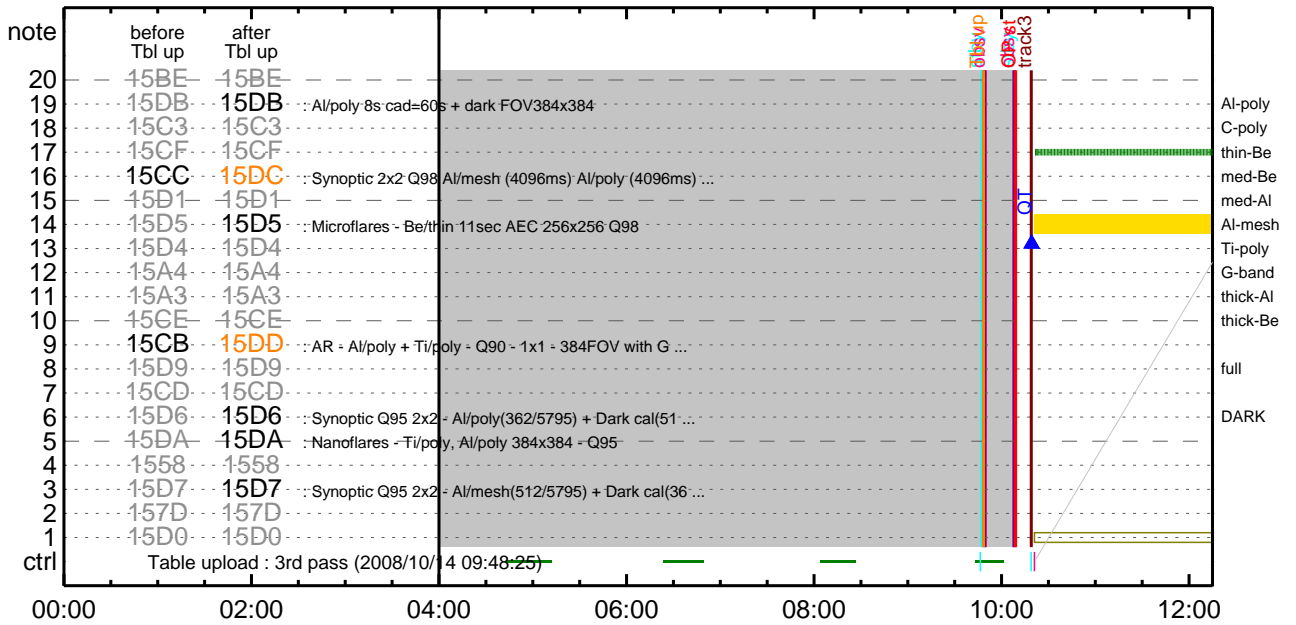
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**Flare Detection**

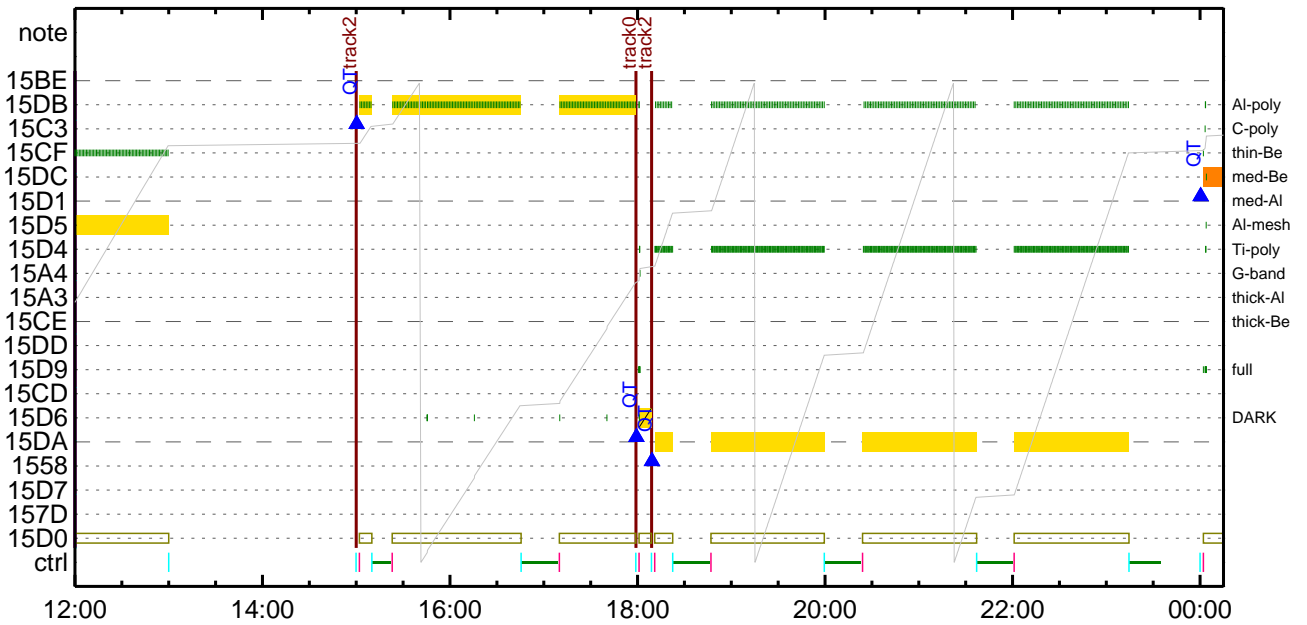
\* \* \* \* \*

NOT USED

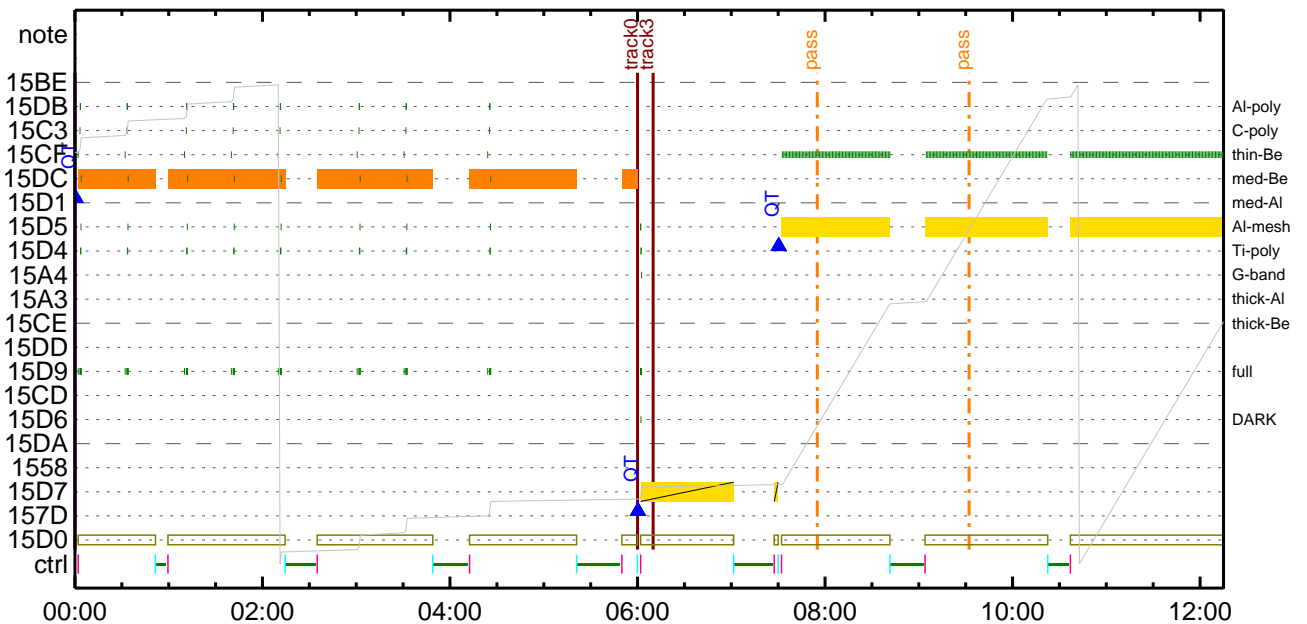
### CMDI #0189 2008/10/14



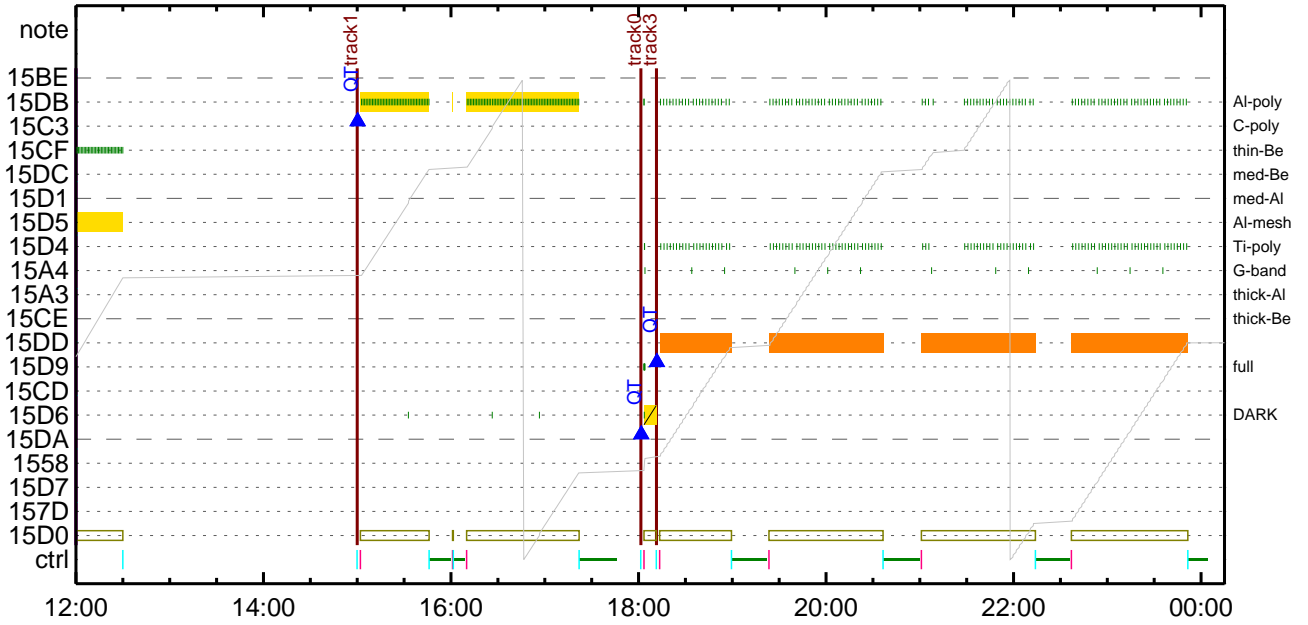
### CMDI #0189 2008/10/14



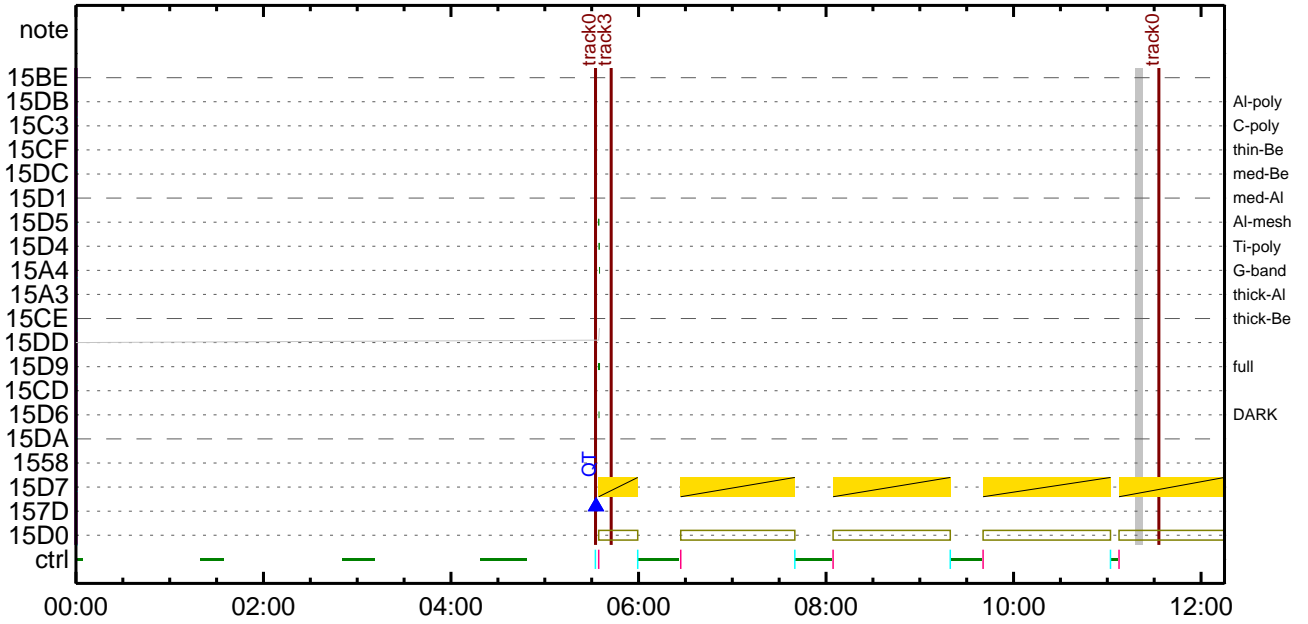
### CMDI #0189 2008/10/15



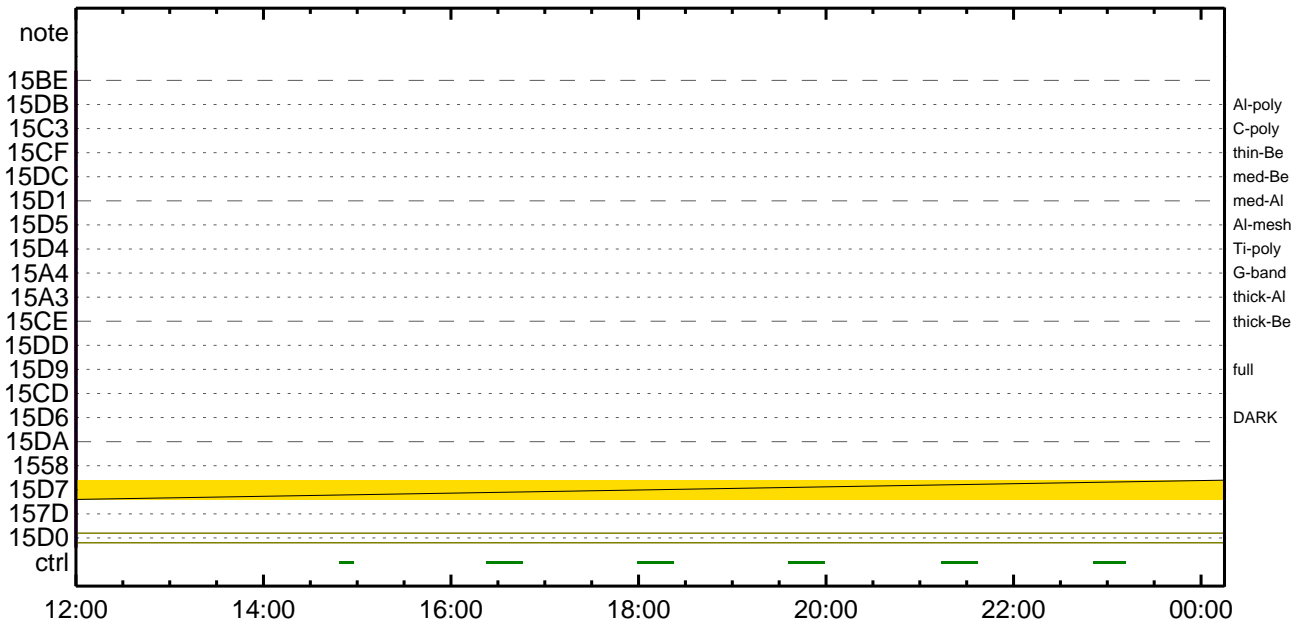
### CMDI #0189 2008/10/15



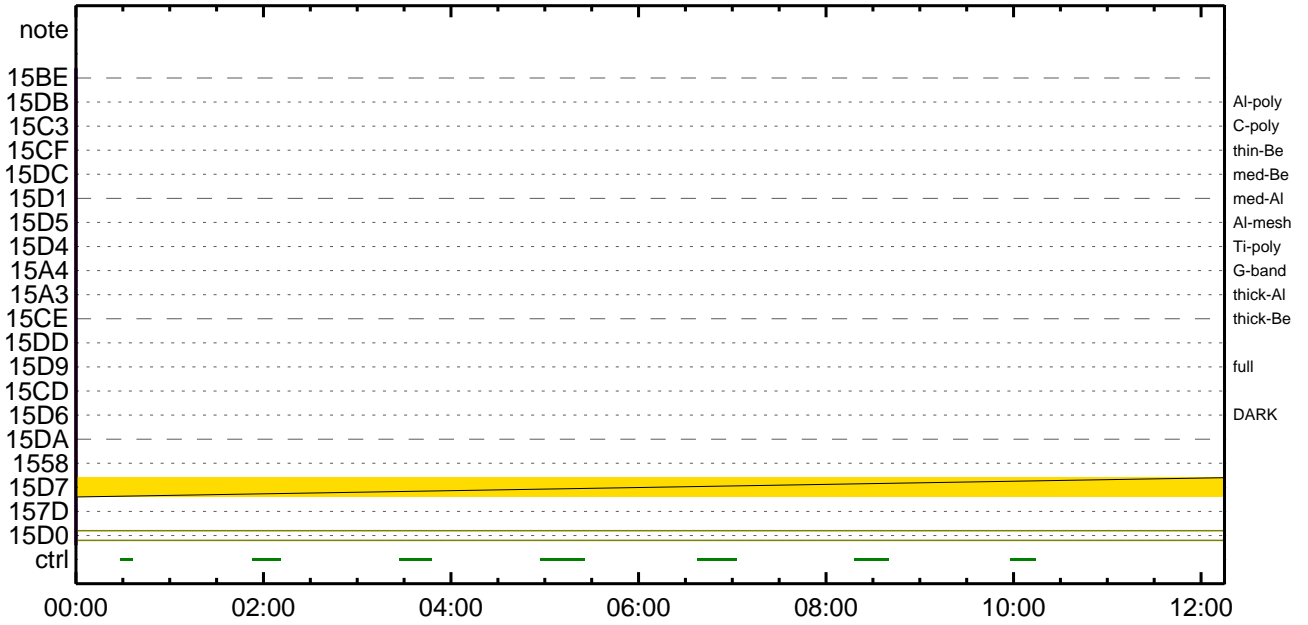
### CMDI #0189 2008/10/16



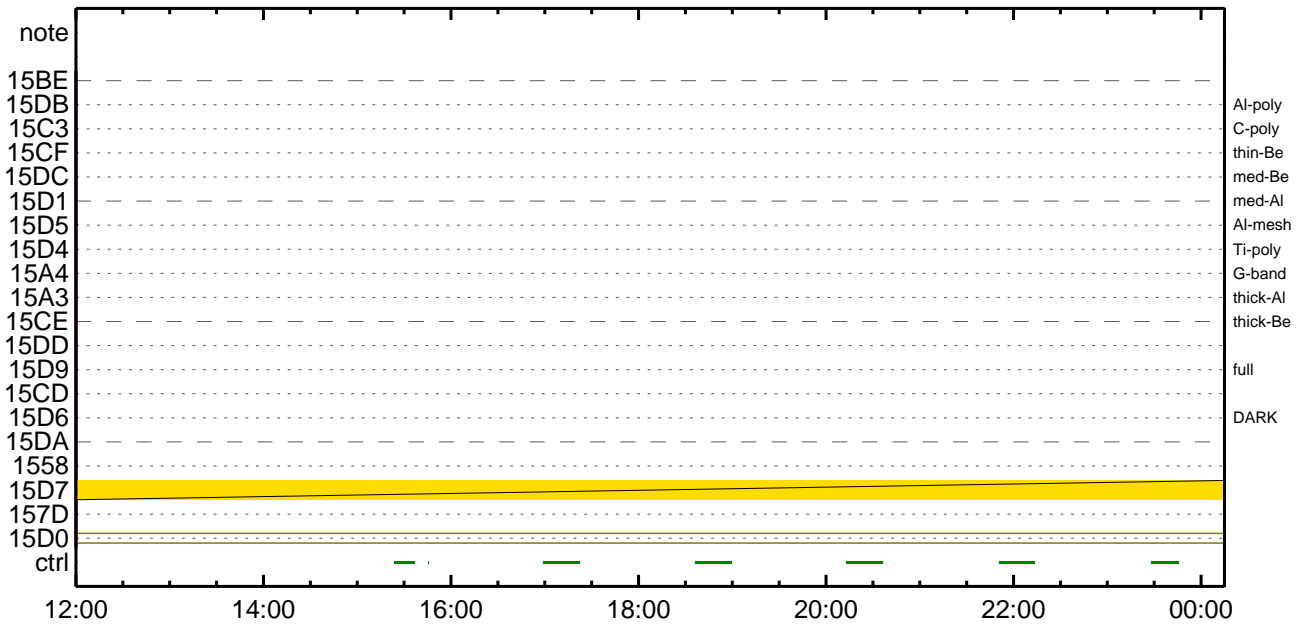
### CMDI #0189 2008/10/16



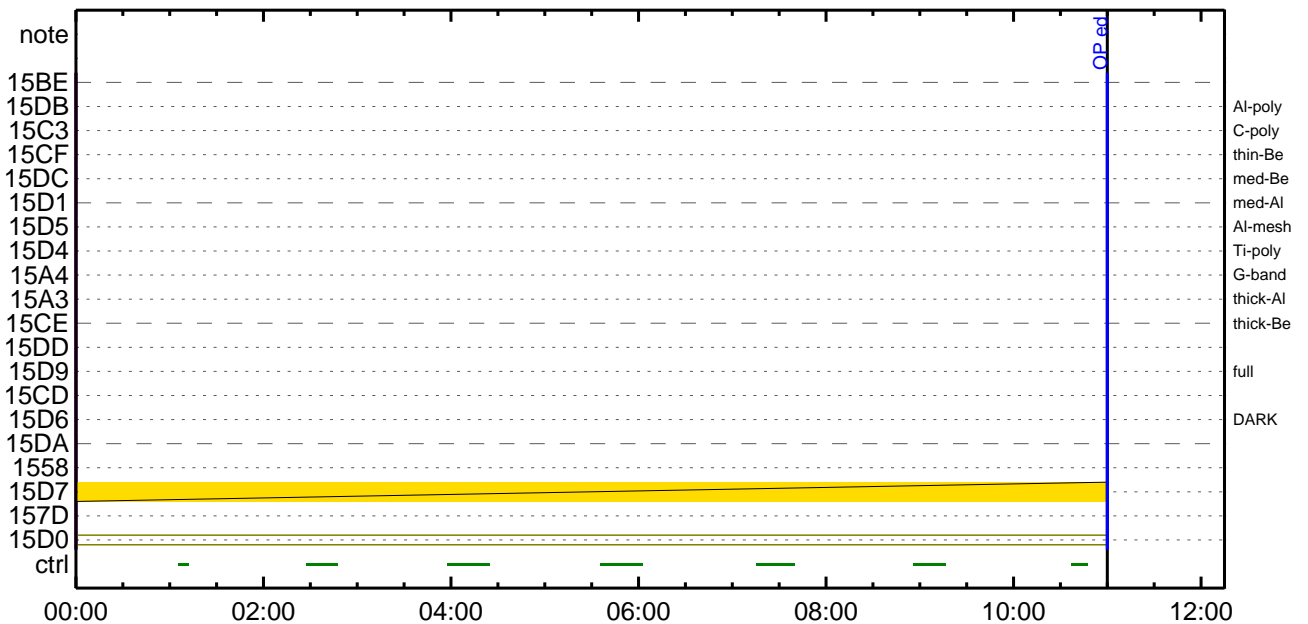
CMDI #0189 2008/10/17



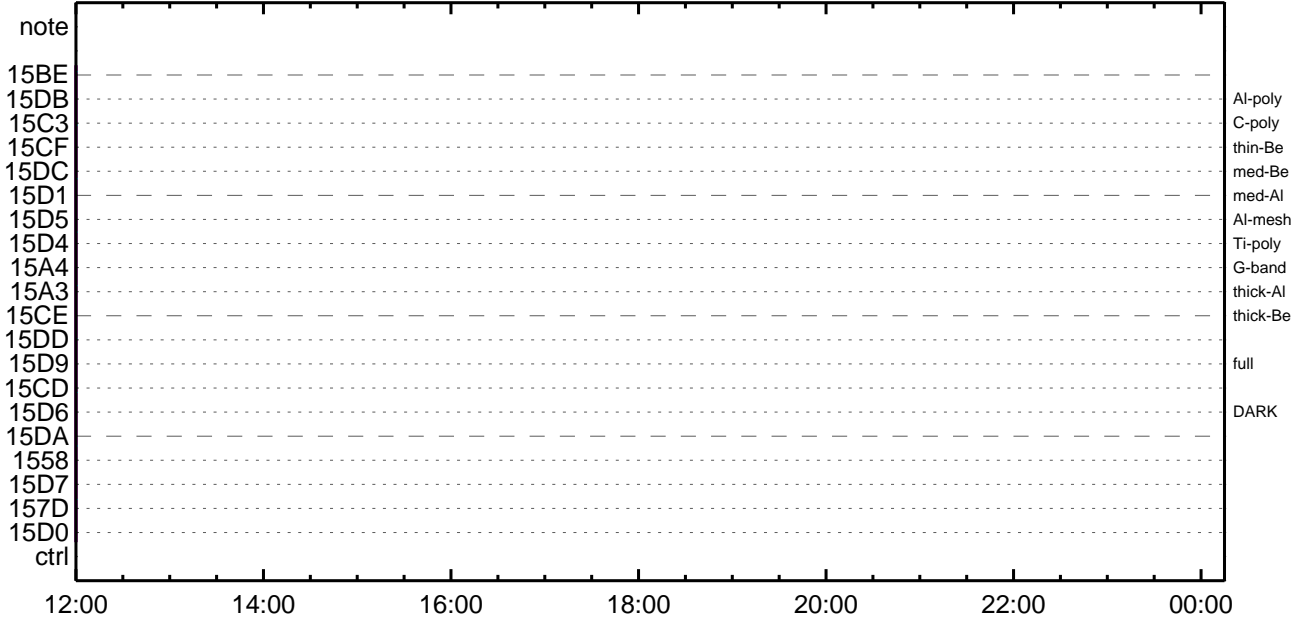
CMDI #0189 2008/10/17



CMDI #0189 2008/10/18



CMDI #0189 2008/10/18





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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-192:OP
0104 ( )
0105 S. OG og-192:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½A´¶A°EÉ¬òA÷¿@ (¼âµ-YAYOYx½ê½çòðÃÓÆòÇ¼ª°¬òE¼i¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²¬Á÷¿@NG²î½i¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½A¹Ôª°¬E²ò³òE;f
0180 C. ²²ò¿;çSET²EEDUMP²î½±°iYNY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²Y½YÉòðÃDî¿(UT)
0183 +. TI 2008-10-14 10:04:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2008-10-14 10:04:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2008-10-14 10:04:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2008-10-14 10:08:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]             EQ       1COUNTUP
0198 C.
0199 C. °È²¼ªíÄè%ííñªîî¥Á¥§¥Á¥-¹àîû
0200 C.          çç[HK1_TI_CMD_ENA/DIS]         EQ       ENA
0201 C.          çç[HK1_TI_CMD_NUM]             EQ       4
0202 C.          çç[HK1_NEXT_EXEC_PIM]         EQ       DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]         EQ       0xB3
0204 C.
0205 C. *****
0206 C. TIîî°è¥Á¥ô¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]       EQ       07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]       EQ       2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]        EQ       3
0215 C.          çç[HK1_DMP_REPEAT_NUM]       EQ       0
0216 C.          çç[HK1_DMA_DMP_PIM]         EQ       DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]          EQ       7
0220 C.          çç[HK1_PKT_GEN_TIME]         EQ       0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]       EQ       32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]       EQ       4M
0223 C.          çç[HK1_DMP_CHK_FLG]         EQ       EXEC
0224 C.
0225 C. ¥Á¥ô¥×½ªî»ª³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]         EQ       NON
0227 C.
0228 C. RAM ID=TI_TBLªîî°è²¼ªíÄè¹çªè²îOKª³îç§
0229 C.
0230 C. DHU¥ª;¼¥È;È%¼¥, ¥î;¼¥È;Èª³îªª¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]          EQ       2
0234 C.          çç[HK1_PKT_GEN_TIME]         EQ       0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]       EQ       32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]       EQ       4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2008-10-14 10:08:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2008-10-14 10:08:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [      ] [HK1_TI_CMD_NUM]     EQ       2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C. *****
0255 C. SOT TI command set
0256 C. *****
0257 C. Execute, after the success of OP upload.
0258 +. TI 2008-10-14 10:08:16.0
0259 DC 07-F0 MDP_SOT_MODE_STBY
0260 BC      (41)
0261 C. -----
0262 C.      HK1_TI_CMD_NUM         = 1 CNTUP [ ]
0263 C. -----
0264 C. ***** SOT END *****
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2008-10-14 10:08:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [      ] [HK1_TI_CMD_NUM]     EQ       1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP `ûÃîªí»ò¼¥ªEÄª¹ªèDCBCª²è *****
0276 C. (%ª°î¥Á¥È¥¹¥Ä¥ç¥èªÈ%¼ªªªª»Üª¹ªè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ¥Ä¥¹·î Daily±çîñªE'ªª¹ªèDCBCª²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ¦ªLOS¥Á¥§¥Á¥-¼ªª»Ü;ª
0287 C.
0288 C. ***** LOS *****
0289 C.

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(a) Spacecraft Operation Procedure (real-commands)

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main-194 2008-10-14 13:37:51 171 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èð¿ðÀð•µ°È»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ÈÈÈµ•íÍÈ;ÈðÈ¼°ÇÓð•ð¿¼í¹çðÍ;çÀ®, ùð¹ðÈðÈðçÁ+¿®ð•ðÈððð³ðÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG ____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 06 80 80 04 04)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 07 80 80 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 08 80 80 20 20)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 09 83 85 06 06)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 0a 80 80 08 08)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0f 80 80 06 06)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 10 80 80 04 04)
0054 . C. ----- Success Verify ? OK / NG ____
0055 C.
0056 C.
0057 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0058 C.
0059 +. DC 07-F0 MDP_XRT_MODE_OBSV
0060 BC (c2)
0061 +. TI 2008-10-14 10:08:02.0
0062 DC 07-F0 MDP_XRT_MODE_OBSV
0063 BC (c2)
0064 . C. ----- Success Verify ? OK / NG ____
0065 C.
0066 C. ***** XRT END *****
0067 . C. *****
0068 C. SOT table upload
0069 C. *****
0070 . C. < Stop FG table >
0071 +. DC 07-F0 MDP_FG_CTRL_MANU
0072 BC (51)
0073 . C. -----
0074 C. MDP_FG_CTRL_MODE = MANU [ ]
0075 C. -----
0076 C.
0077 . C. <Upload FG Observation Table>
0078 . S. RAM ram-266:MDP_OBS_F
0079 ( )
0080 C.
0081 . C. < Dump RAMID=MDP_OBS_F >
0082 +. DC 07-F0 MDP_DUMP_FGTBL
0083 BC (82 07 00 00 00 38 b8)
0084 C. -----
0085 C. MDP_OBS_F verify = OK/NG [ ]
0086 C. -----
0087 C.
0088 . C. < Stop SP table >
0089 +. DC 07-F0 MDP_SP_CTRL_MANU
0090 BC (61)
0091 C. -----
0092 C. MDP_SP_CTRL_MODE = MANU [ ]
0093 C. -----
0094 C.
0095 . C. <Upload SP Observation Table>
```

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0096 . S. RAM ram-283:MDP_OBS_S
0097 ( )
0098 C.
0099 . C. < Dump RAMID=MDP_OBS_S >
0100 +. DC 07-F0 MDP_DUMP_SPTBL
0101 BC (83 07 00 00 00 38 b8)
0102 C. -----
0103 C. MDP_OBS_S verify = OK/NG [ ]
0104 C. -----
0105 C.
0106 . C. < Upload DPL table >
0107 C.
0108 C. ¥ç¥Ã¥×¥í;¼¥É°îÁ°°ESTS_CHKαðOFFαÈα¹αè
0109 C.
0110 . S. RAM ram-271:MDP_DPL
0111 ( )
0112 C.
0113 . C. < Dump RAMID=MDP_DPL >
0114 +. DC 07-F0 MDP_DUMP_FGTBL
0115 BC (82 07 00 38 b8 00 40)
0116 C. -----
0117 C. MDP_DPL verify = OK [ ]
0118 C. -----
0119 C.
0120 C. STS_CHKαðONαÈα¹αè
0121 C.
0122 . C. < Update MDP DSC PAR1 >
0123 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0124 BC (4c)
0125 C. MDP_CMD_CODE = F04C0700 [ ]
0126 C. MDP_CMD_CNT (count-up 1) [ ]
0127 C. -----
0128 C.
0129 . C.
0130 . C. < Resume FG table (auto mode) >
0131 +. DC 07-F0 MDP_FG_CTRL_AUTO
0132 BC (50)
0133 . C. -----
0134 C. MDP_FG_CTRL_MODE = AUTO [ ]
0135 C. -----
0136 C.
0137 C. *****
0138 C. SOT TI command set
0139 C. *****
0140 C. Execute, after the success of TBL upload.
0141 +. TI 2008-10-14 10:08:18.0
0142 DC 07-F0 MDP_SOT_MODE_OBSV
0143 BC (40)
0144 . C. -----
0145 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0146 C. -----
0147 C.
0148 C. Only when FG_CTRL_AUTO is used in RT.
0149 +. TI 2008-10-14 10:08:20.0
0150 DC 07-F0 MDP_FG_CTRL_AUTO
0151 BC (50)
0152 . C. -----
0153 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0154 C. -----
0155 C. ***** SOT END *****
0156 C.
0157 . C. ***** MDP `ûÃîαî»ò¼¥αÈÁα¹αèDCBC•x²è *****
0158 C. (¼á°î¥Ó¥Ã¥È¥Þ¥È¥á¥ç¥èαÈ¼α¼Á»Ûα¹αè)
0159 . S. DC-BC dcbc-402:DCBC
0160 (MDP_known_event)
0161 C.
0162 C.
0163 . C. ***** ¥Ð¥¹•İ Daily±;îÑαÈ´Øα¹αèDCBC•x²è *****
0164 . S. DC-BC dcbc-153:DCBC
0165 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0166 C.
0167 C.
0168 . C. ;ãLOS¥Ã¥S¥Ã¥¼¼Á»Û;ã
0169 C.
0170 . C. ***** LOS *****
0171 C.
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\*\*\* OP Sequence for XRT \*\*\*

2008/10/14	10:18:54.0	XRT_CTRL_MANU_428_OG [0x1ac]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/10/14	10:18:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2008/10/14	10:19:00.0	AOCS_OrE-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	03 00 00 00 00			
2008/10/14	10:19:16.0	XRT_QT_PROG_SET_411_OG [0x19b]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e			
2008/10/14	10:20:54.0	XRT_AEC_RESET_413_OG [0x19d]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2008/10/14	10:20:56.0	XRT_ARS_DIS_422_OG [0x1a6]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2008/10/14	10:20:58.0	XRT_FLD_DIS_445_OG [0x1bd]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2008/10/14	10:21:00.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2008/10/14	10:21:02.0	XRT_CTRL_AUTO_403_OG [0x193]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/10/14	13:00:00.0	XRT_CTRL_MANU_428_OG [0x1ac]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/10/14	14:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/10/14	14:59:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2008/10/14	15:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	02 00 00 00 00			
2008/10/14	15:00:16.0	XRT_QT_PROG_SET_438_OG [0x1b6]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13			
2008/10/14	15:01:54.0	XRT_AEC_RESET_413_OG [0x19d]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2008/10/14	15:01:56.0	XRT_ARS_DIS_422_OG [0x1a6]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2008/10/14	15:01:58.0	XRT_FLD_DIS_445_OG [0x1bd]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2008/10/14	15:02:00.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2008/10/14	15:02:02.0	XRT_CTRL_AUTO_403_OG [0x193]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/10/14	15:10:00.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/10/14	15:22:00.0	XRT_Custom_430_OG [0x1ae]						
2008/10/14	15:23:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/10/14	16:45:30.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/10/14	17:09:00.0	XRT_Custom_430_OG [0x1ae]						
2008/10/14	17:10:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/10/14	17:58:54.0	XRT_CTRL_MANU_428_OG [0x1ac]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/10/14	17:58:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2008/10/14	17:59:00.0	AOCS_OrE-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2008/10/14	17:59:16.0	XRT_QT_PROG_SET_431_OG [0x1af]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06			
2008/10/14	17:59:18.0	XRT_FLD_DIS_419_OG [0x1a3]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2008/10/14	17:59:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2008/10/14	17:59:22.0	XRT_ARS_DIS_427_OG [0x1ab]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2008/10/14	18:01:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/10/14	18:08:54.0	XRT_CTRL_MANU_428_OG [0x1ac]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/10/14	18:08:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2008/10/14	18:09:00.0	AOCS_OrE-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	02 00 00 00 00			
2008/10/14	18:09:16.0	XRT_QT_PROG_SET_443_OG [0x1bb]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05			
2008/10/14	18:10:54.0	XRT_ARS_DIS_422_OG [0x1a6]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2008/10/14	18:10:56.0	XRT_FLD_DIS_445_OG [0x1bd]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2008/10/14	18:10:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2008/10/14	18:11:00.0	XRT_CTRL_AUTO_403_OG [0x193]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/10/14	18:22:30.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/10/14	18:46:00.0	XRT_Custom_430_OG [0x1ae]						
2008/10/14	18:47:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/10/14	19:59:30.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/10/14	20:23:00.0	XRT_Custom_430_OG [0x1ae]						
2008/10/14	20:24:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/10/14	21:37:00.0	XRT_CTRL_MANU_435_OG [0x1b3]						

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2008/10/14	22:00:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/10/14	22:01:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/14	23:14:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/10/15	00:00:00.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/10/15	00:00:02.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2008/10/15	00:00:22.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2008/10/15	00:02:00.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/10/15	00:02:02.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/10/15	00:02:04.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/10/15	00:02:06.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/15	00:51:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/10/15	00:58:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/15	00:59:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/15	02:14:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/10/15	02:34:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/15	02:35:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/15	03:49:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/10/15	04:11:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/15	04:12:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/15	05:21:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/10/15	05:49:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/15	05:50:00.5	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/15	05:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/10/15	05:59:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2008/10/15	06:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00	00
2008/10/15	06:00:16.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2008/10/15	06:00:18.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/10/15	06:00:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/10/15	06:00:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/10/15	06:02:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/15	06:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03	00	00	00	00
2008/10/15	07:01:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/10/15	07:26:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/15	07:27:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/15	07:30:00.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/10/15	07:30:02.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2008/10/15	07:30:22.0	XRT_QT_PROG_SET_411_OG [0x19b]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2008/10/15	07:32:00.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2008/10/15	07:32:02.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/10/15	07:32:04.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/10/15	07:32:06.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/10/15	07:32:08.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/15	08:41:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/10/15	09:03:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/15	09:04:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/15	10:22:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/10/15	10:36:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/15	10:37:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/15	12:30:00.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/10/15	14:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2008/10/15	14:59:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2008/10/15	15:00:00.0	AOCs_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	01	00	00	00	00
2008/10/15	15:00:16.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2008/10/15	15:01:54.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2008/10/15	15:01:56.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/10/15	15:01:58.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/10/15	15:02:00.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/10/15	15:02:02.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/15	15:46:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/10/15	16:00:00.0	XRT_Custom_430_OG [0x1ae]							
2008/10/15	16:01:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/15	16:01:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/10/15	16:09:00.0	XRT_Custom_430_OG [0x1ae]							
2008/10/15	16:10:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/15	17:22:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/10/15	18:01:24.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/10/15	18:01:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2008/10/15	18:01:30.0	AOCs_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2008/10/15	18:01:46.0	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2008/10/15	18:01:48.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/10/15	18:01:50.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/10/15	18:01:52.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/10/15	18:03:30.0	XRT_CTRL_AUTO_444_OG [0x1bc]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/15	18:11:24.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/10/15	18:11:26.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2008/10/15	18:11:30.0	AOCs_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03	00	00	00	00
2008/10/15	18:11:46.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	09			
2008/10/15	18:13:24.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2008/10/15	18:13:26.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/10/15	18:13:28.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/10/15	18:13:30.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/10/15	18:13:32.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/15	18:59:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/10/15	19:22:30.0	XRT_Custom_430_OG [0x1ae]							
2008/10/15	19:23:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/15	20:36:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/10/15	21:00:00.0	XRT_Custom_430_OG [0x1ae]							
2008/10/15	21:01:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/15	22:14:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/10/15	22:36:00.5	XRT_Custom_430_OG [0x1ae]							
2008/10/15	22:37:00.5	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/10/15	23:51:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/10/16	05:32:24.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/10/16	05:32:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2008/10/16	05:32:30.0	AOCs_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2008/10/16	05:32:46.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2008/10/16	05:32:48.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/10/16	05:32:50.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/10/16	05:32:52.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				



2008/10/16	05:34:30.0	XRT_CTRL_AUTO_444_OG [0x1bc]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/10/16	05:42:30.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2008/10/16	05:59:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/10/16	06:26:00.0	XRT_Custom_430_OG [0x1ae]			
2008/10/16	06:27:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/10/16	07:40:00.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/10/16	08:03:30.0	XRT_Custom_430_OG [0x1ae]			
2008/10/16	08:04:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/10/16	09:19:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/10/16	09:39:30.0	XRT_Custom_430_OG [0x1ae]			
2008/10/16	09:40:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/10/16	11:02:00.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/10/16	11:06:30.0	XRT_Custom_430_OG [0x1ae]			
2008/10/16	11:07:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/10/16	11:33:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00