

# XRT Timeline to be uploaded on 2008/12/12

Period: 2008/12/12 10:50:00 - 2008/12/17 10:41:00

\* \* \* \* \*

Normal mode

\* \* \* \* \*

<b>XOB #1625: QS dynamics - Al/poly (12s) 384x384 - Q95 - 25s-cad</b>													
Term	Pointing (x, y)	Comment											
12/12 11:02:12 - 12/12 18:05:54	Track ( 0.0, -0.0) @ 12/12 11:00:00	# OP start + 10min, QS dynamic study											
<b>PROG= 17 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 25.0sec													
└─ Seqn= 9 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	11.3s	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1606: Synoptic Q95 2x2 - Al/poly(256/5795) + Dark cal(512 Q98) + Ti-poly(362/11571) + G-band(16)</b>													
Term	Pointing (x, y)	Comment											
12/12 18:08:00 - 12/12 19:28:00	Fixed ( 0.0, 0.0)	synoptic, shifted 6.0 min											
<b>PROG= 19 1-time(s)</b>													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 70 1-time(s) 4.0sec													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 57 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 16 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 92 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1624: AR over West limb - Al/poly 384FOV Q95 - 25s-cad - Ti/poly + Thin-Be context - AEC4 - offset</b>													
Term	Pointing (x, y)	Comment											
12/12 19:28:34 - 12/13 00:50:00	Fixed ( 0.0, 0.0)	#											
<b>PROG= 08 Inf.-time(s)</b>													
└─ Subr= 1 30-time(s) 25.0sec													
└─ Seqn= 2 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1856, 592)	Q=95	4	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 53 2-time(s) 4.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1856, 592)	Q=95	4	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	11.3s	Obs	1x1	384x384 (1856, 592)	Q=95	4	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Dark	22.6s	Obs	1x1	384x384 (1856, 592)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #15D7: Synoptic Q95 2x2 - Al/mesh(362/5795) + Dark cal(362 Q98) + Ti-poly(512/11571) + G-band(16)</b>													
Term	Pointing (x, y)	Comment											
12/13 05:39:00 - 12/13 09:44:54	Fixed ( 0.0, 0.0)	synoptic, shifted -23.0 min											
12/14 06:00:30 - 12/14 10:02:30	Fixed ( 0.0, 0.0)	synoptic, shifted -1.5 min											
12/15 06:18:30 - 12/15 09:59:54	Fixed ( 0.0, 0.0)	synoptic, shifted 16.5 min											
12/16 05:52:30 - 12/17 10:41:00	Fixed ( 0.0, 0.0)	synoptic, shifted -9.5 min											
<b>PROG= 04 1-time(s)</b>													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 29 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 57 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 4 1-time(s) 4.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 92 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1619: Al/poly (AEC4) + Ti/poly (AEC4) + Thin-Be (AEC4) - 3-min cadence - FOV512 - Q95</b>													
Term	Pointing (x, y)	Comment											
12/13 09:47:02 - 12/13 17:39:54	Fixed ( 0.0, -830.0)	EIS: CH study											
<b>PROG= 03 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 180.0sec													
└─ Seqn= 63 3-time(s) 180.0sec													
	Al-poly/Open	C-poly/Open	close	Safe	Norm	8.00s	Obs	1x1	512x512 (1024, 1024)	Q=95	4	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	1x1	512x512 (1024, 1024)	Q=95	4	0	2.0sec
└─ Subr= 2 1-time(s) 8.0sec													
└─ Seqn= 93 2-time(s) 4.0sec													

┌	┌	thin-Be/Open	thin-Be/Open	close	Safe	Norm	22.6s	Obs	1x1	512x512 (1024, 1024)	Q=95	4	0	2.0sec	
		<b>Seqn= 10</b>		<b>1-time(s)</b>		<b>4.0sec</b>									
┌	┌	thin-Be/Open	thin-Be/Open	close	Safe	Dark	22.6s	Obs	1x1	512x512 (1024, 1024)	DPCM	0	0	2.0sec	
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

**XOB #15D6: Synoptic Q95 2x2 - Al/poly(362/5795) + Dark cal(512 Q98) + Ti-poly(512/11571) + G-band(16)**

Term	Pointing (x, y)	Comment
12/13 17:42:00 - 12/13 18:28:00	Fixed ( 0.0, 0.0)	synoptic, shifted -20.0 min
12/14 18:02:00 - 12/14 18:09:54	Fixed ( 0.0, 0.0)	synoptic
12/15 18:07:54 - 12/16 05:50:24	Fixed ( 0.0, 0.0)	synoptic, shifted 3.5 min

**PROG= 16** **1-time(s)**

┌ **Subr= 1** **1-time(s)** **12.0sec**

┌	┌	Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
		Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
		<b>Seqn= 57</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
┌	┌	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
		<b>Seqn= 4</b>		<b>1-time(s)</b>		<b>4.0sec</b>								
┌	┌	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
		Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
		<b>Seqn= 92</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
┌	┌	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1623: C/poly (AEC4) + Thin-Be (4s) + Med-Al (32s) - 4x4 - 1024x2048FOV - DPCM**

Term	Pointing (x, y)	Comment
12/13 18:28:32 - 12/14 05:58:24	Fixed (-496.4, 0.0)	XRT: XBP brightness study

**PROG= 20** **Inf.-time(s)**

┌ **Subr= 1** **2-time(s)** **540.0sec**

┌	┌	C-poly/Open	C-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	1024x2048 (1024, 1024)	DPCM	4	0	2.0sec
		thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	4x4	1024x2048 (1024, 1024)	DPCM	0	0	2.0sec
		<b>Seqn= 87</b>		<b>2-time(s)</b>		<b>4.0sec</b>								
┌	┌	med-Al/Open	Open/thick-Al	close	Safe	Norm	32.0s	Obs	4x4	1024x2048 (1024, 1024)	DPCM	0	0	2.0sec
		med-Al/Open	Open/thick-Al	close	Safe	Dark	32.0s	Obs	4x4	1024x2048 (1024, 1024)	DPCM	0	0	2.0sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1615: Al/poly (AEC4) + Ti/poly (AEC4) + Thin-Be (AEC4) - 2-min cadence - FOV384 - Q95**

Term	Pointing (x, y)	Comment
12/14 10:23:08 - 12/14 17:59:54	Fixed ( 955.0, 0.0)	EIS: Quiet corona at west limb

**PROG= 02** **Inf.-time(s)**

┌ **Subr= 1** **1-time(s)** **120.0sec**

┌	┌	Al-poly/Open	C-poly/Open	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	4	0	2.0sec
		Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	4	0	2.0sec
		<b>Subr= 2</b>		<b>1-time(s)</b>		<b>8.0sec</b>								
┌	┌	thin-Be/Open	thin-Be/Open	close	Safe	Norm	22.6s	Obs	1x1	384x384 (1024, 1024)	Q=95	4	0	2.0sec
		thin-Be/Open	thin-Be/Open	close	Safe	Dark	22.6s	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1622: C/poly (AEC4) + Thin-Be (4s) + Med-Al (32s) - 4x4 - 1024x2048FOV - DPCM - offset (512,1024)**

Term	Pointing (x, y)	Comment
12/14 18:10:26 - 12/15 06:16:24	Fixed ( 0.0, 0.0)	#

**PROG= 15** **Inf.-time(s)**

┌ **Subr= 1** **2-time(s)** **540.0sec**

┌	┌	C-poly/Open	C-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	1024x2048 (512, 1024)	DPCM	4	0	2.0sec
		thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	4x4	1024x2048 (512, 1024)	DPCM	0	0	2.0sec
		<b>Seqn= 12</b>		<b>2-time(s)</b>		<b>4.0sec</b>								
┌	┌	med-Al/Open	Open/thick-Al	close	Safe	Norm	32.0s	Obs	4x4	1024x2048 (512, 1024)	DPCM	0	0	2.0sec
		med-Al/Open	Open/thick-Al	close	Safe	Dark	32.0s	Obs	4x4	1024x2048 (512, 1024)	DPCM	0	0	2.0sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1626: HOP93 - Al/poly (12s) Q95 - G-band context 384x384FOV**

Term	Pointing (x, y)	Comment
12/15 10:02:00 - 12/15 17:40:00	Track ( -33.3, -0.3) @ 12/15 10:00:00	HOP93 looking at Quiet Sun at disk center

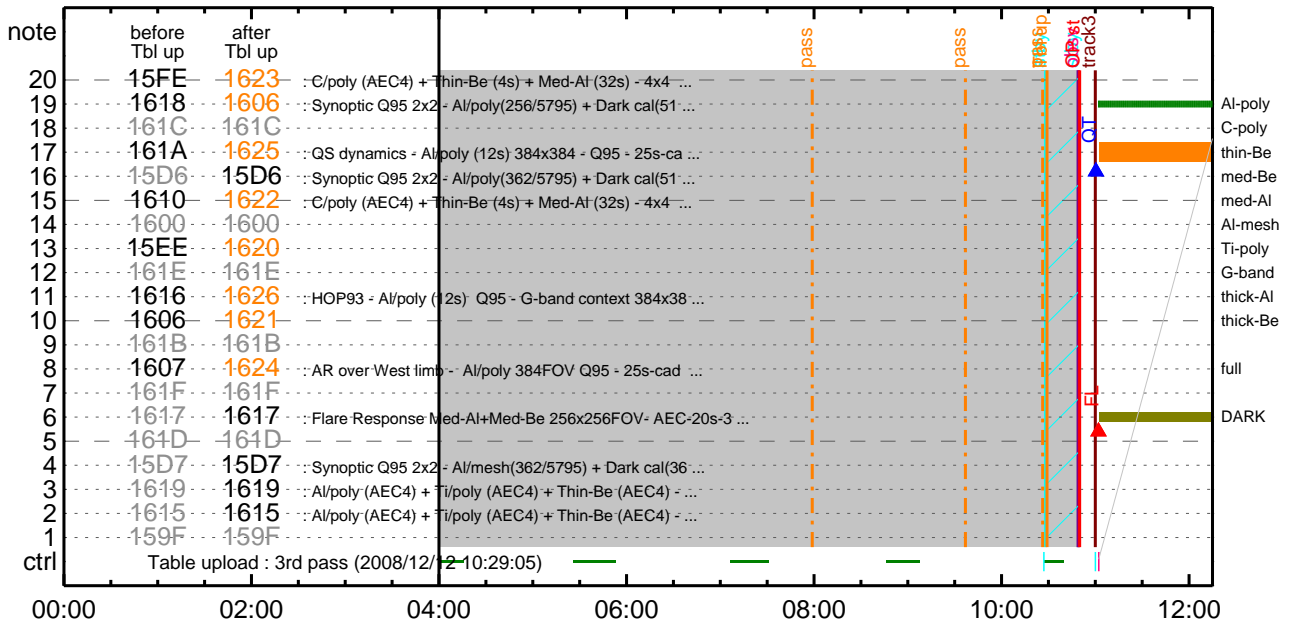
**PROG= 11** **Inf.-time(s)**

┌ **Subr= 1** **2-time(s)** **44.0sec**

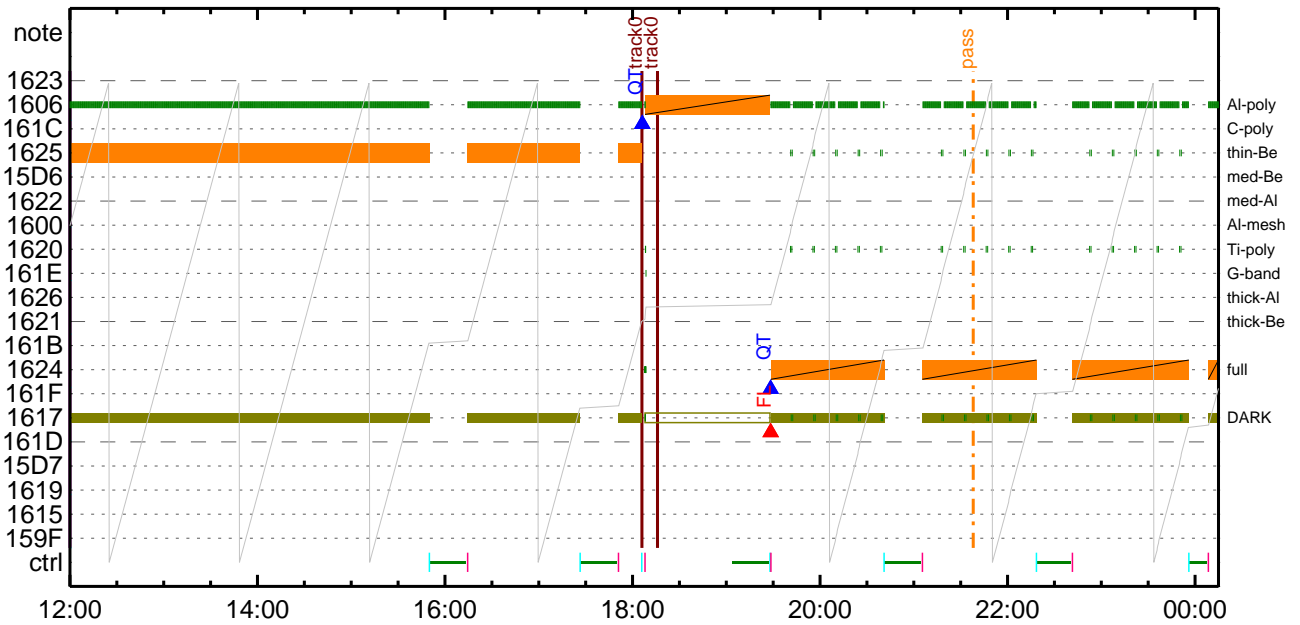
┌	┌	Al-poly/Open	Al-poly/Open	close	Safe	Dark	11.3s	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
		<b>Seqn= 9</b>		<b>45-time(s)</b>		<b>40.0sec</b>								
┌	┌	Al-poly/Open	Al-poly/Open	close	Safe	Norm	11.3s	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
		<b>Subr= 2</b>		<b>1-time(s)</b>		<b>2.0sec</b>								



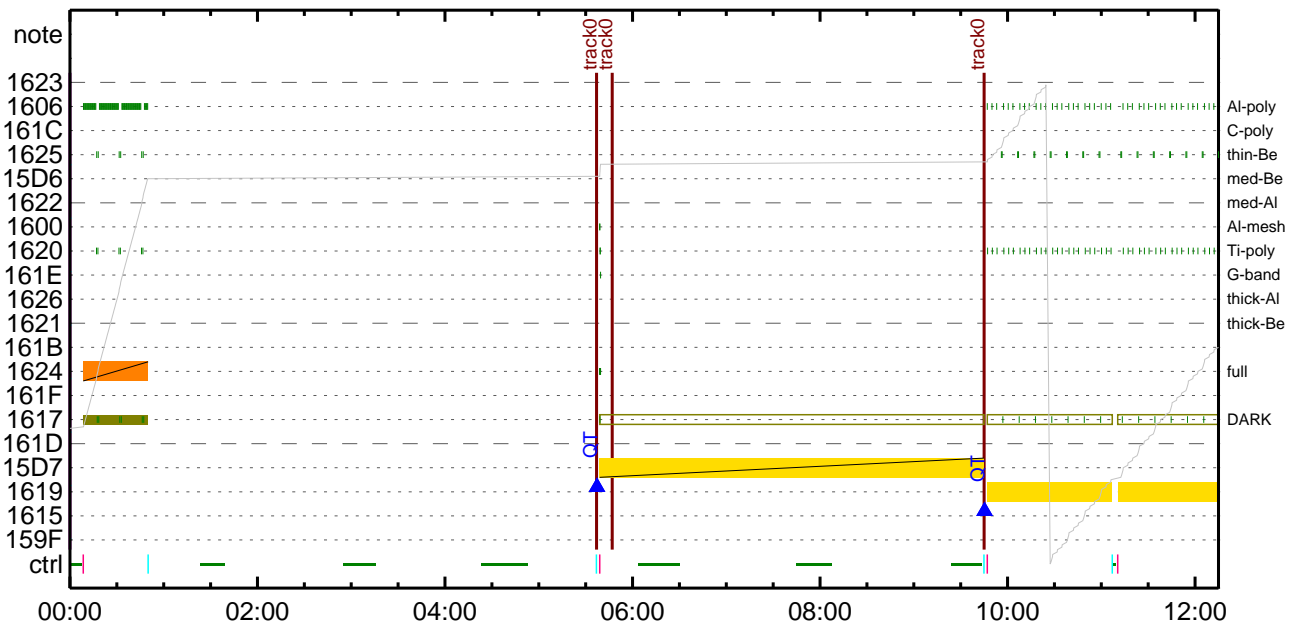
### CMDI #0297 2008/12/12



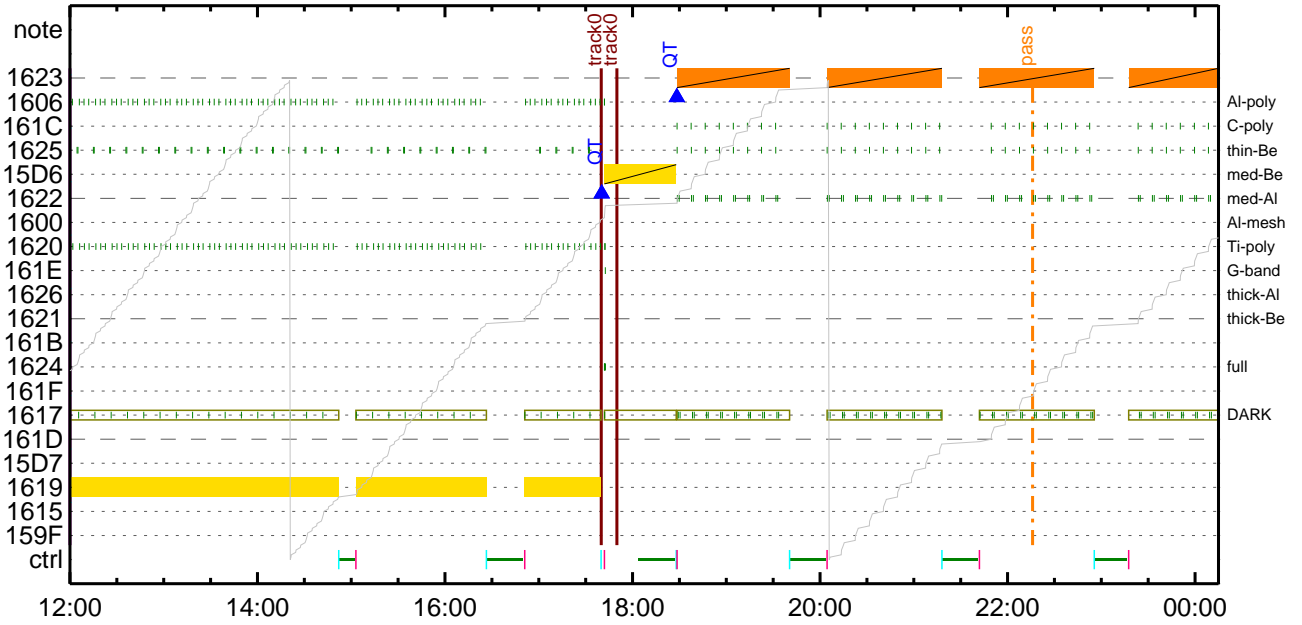
### CMDI #0297 2008/12/12



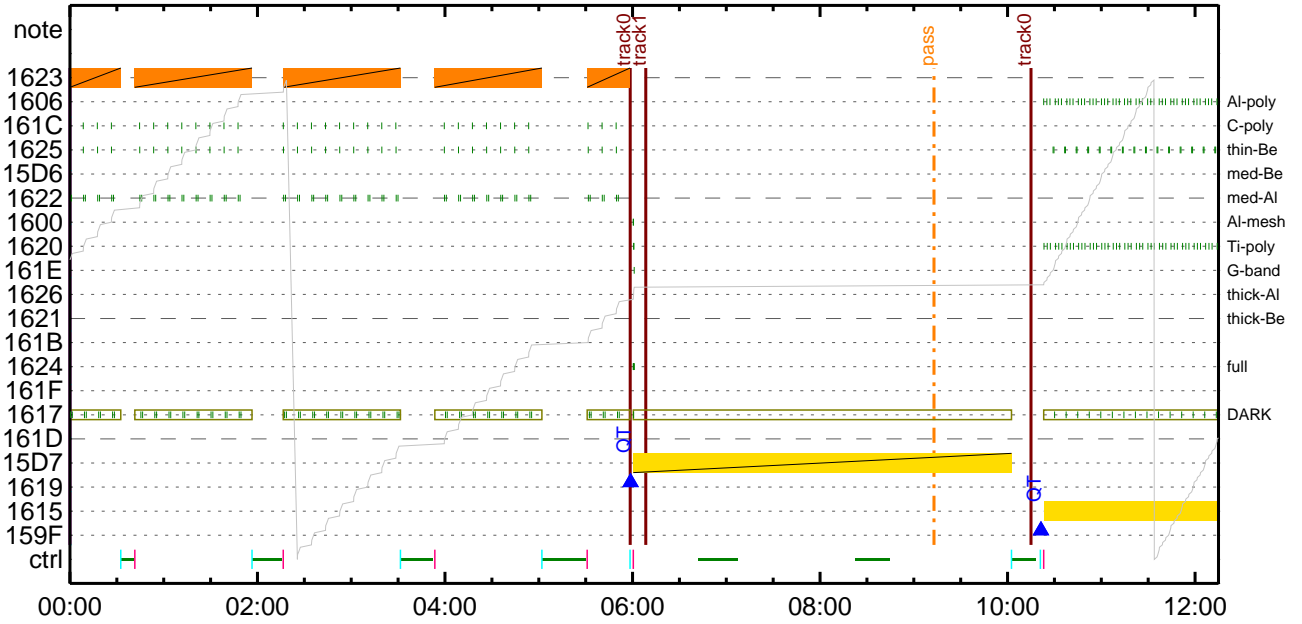
### CMDI #0297 2008/12/13



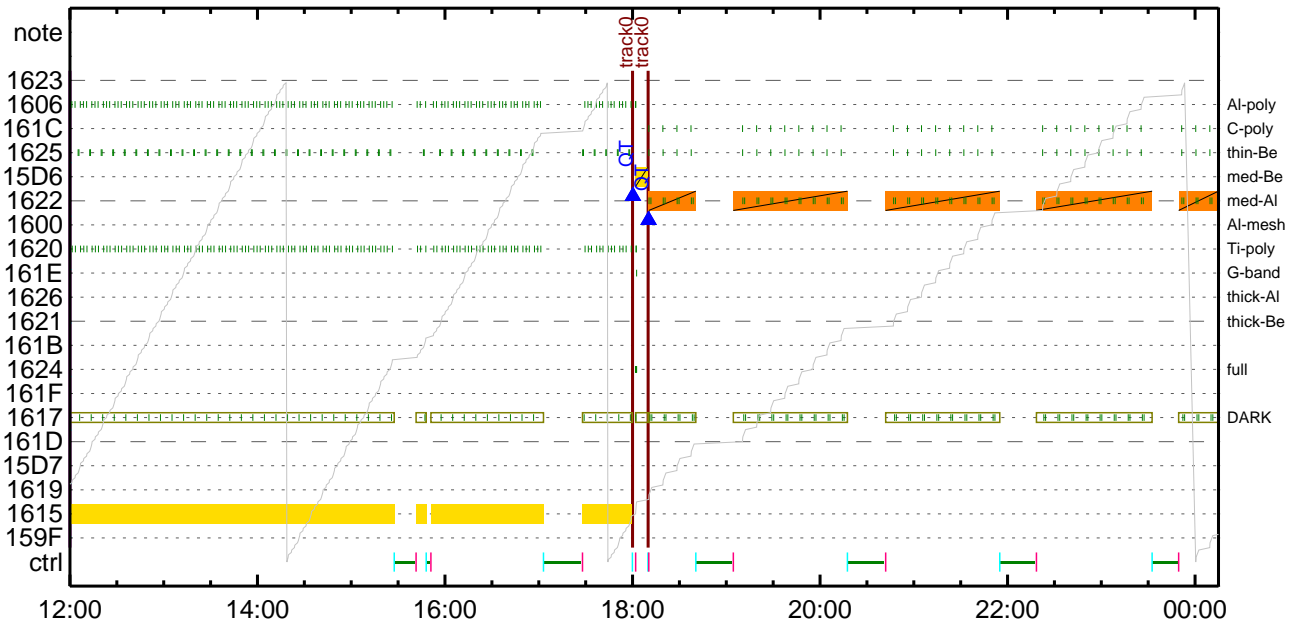
### CMDI #0297 2008/12/13



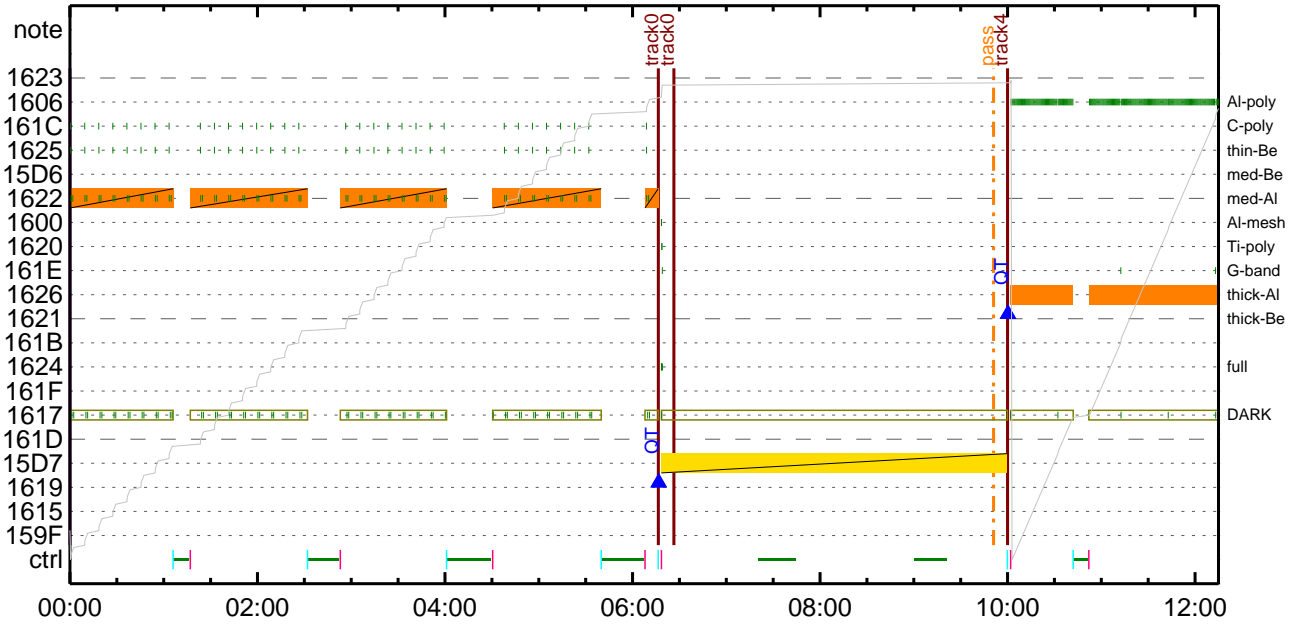
### CMDI #0297 2008/12/14



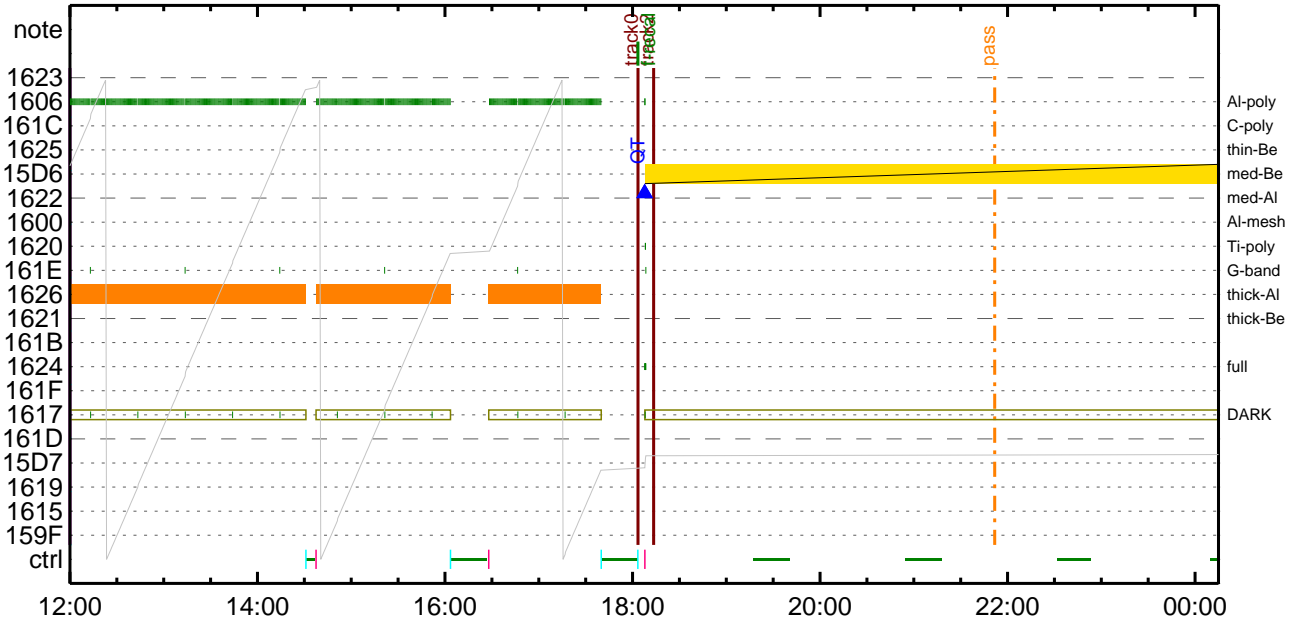
### CMDI #0297 2008/12/14



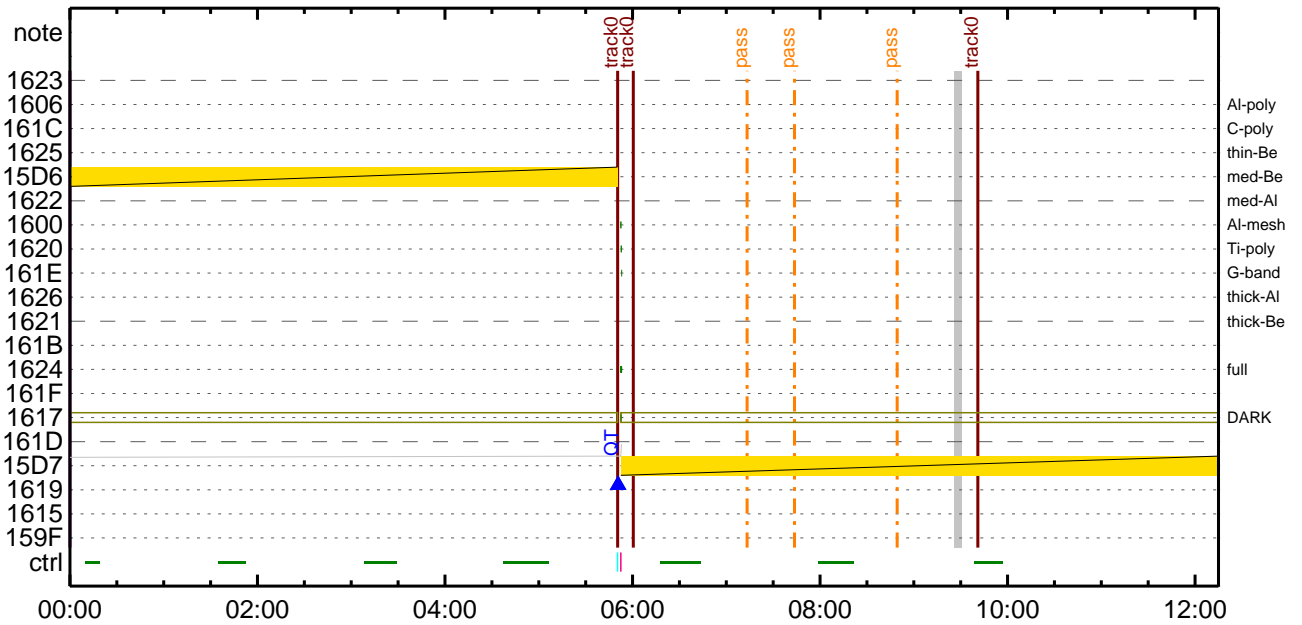
### CMDI #0297 2008/12/15



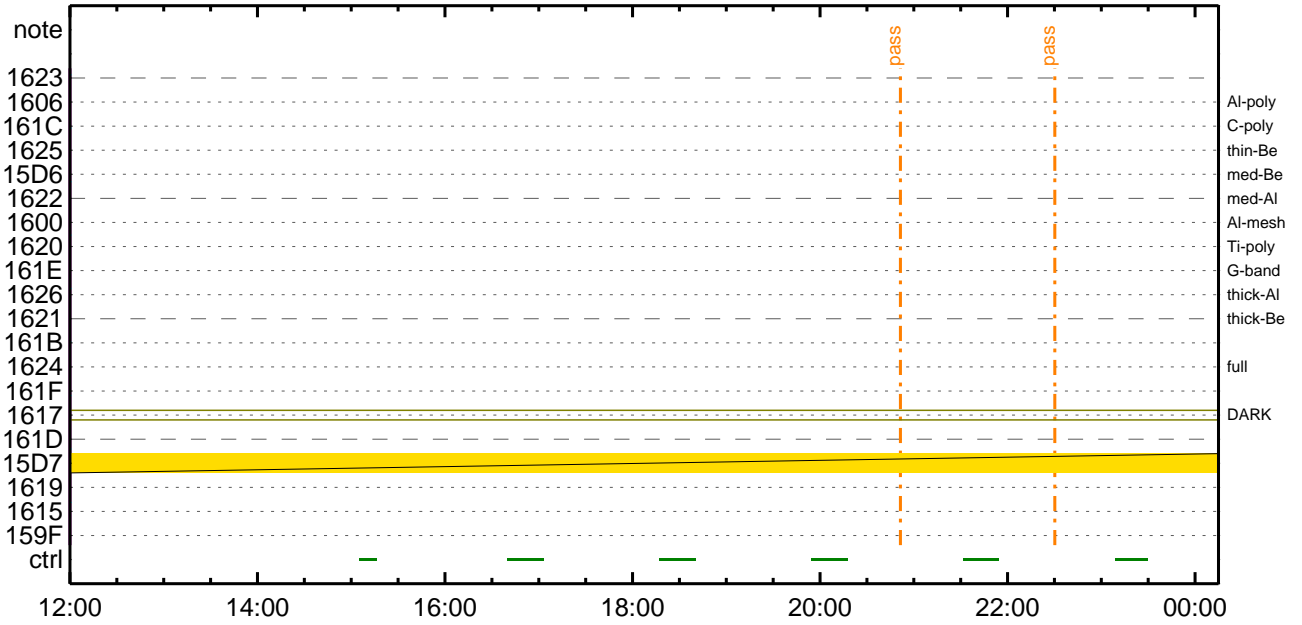
### CMDI #0297 2008/12/15



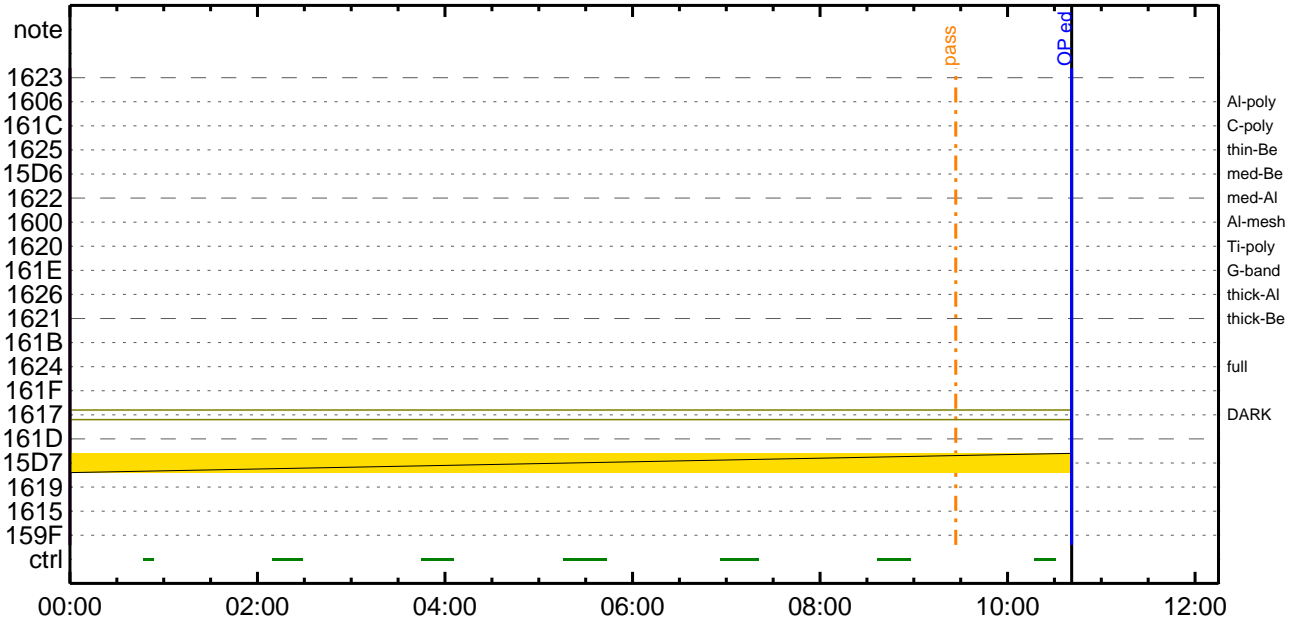
### CMDI #0297 2008/12/16



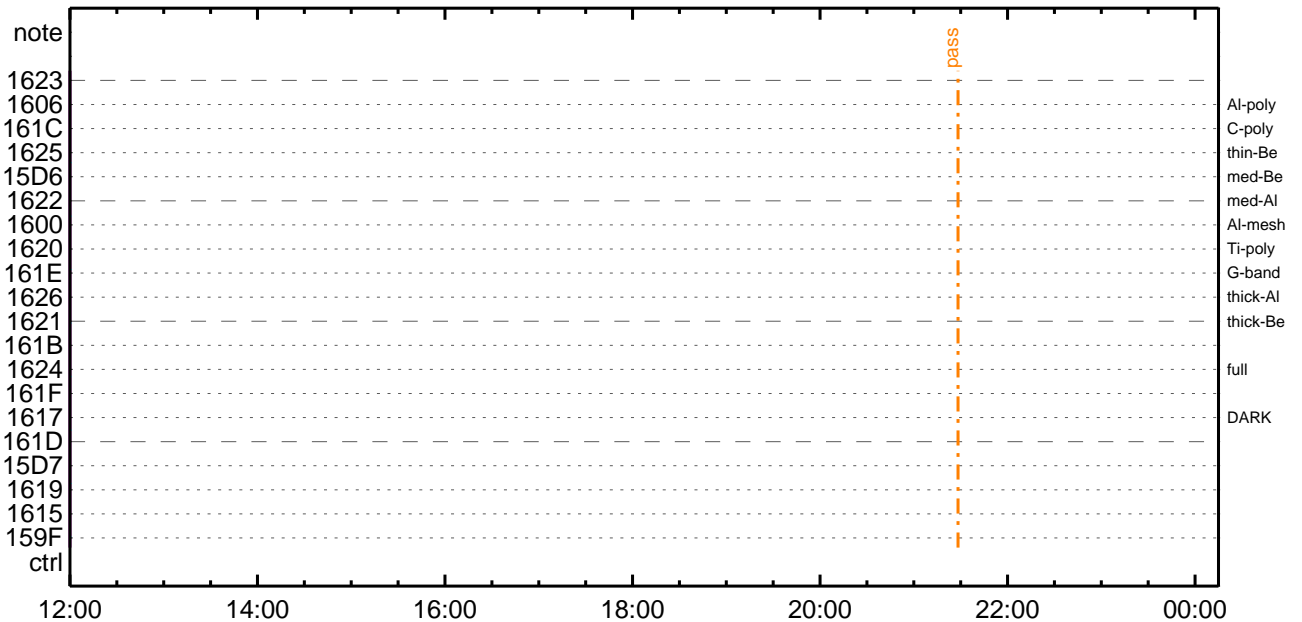
CMDI #0297 2008/12/16



CMDI #0297 2008/12/17



CMDI #0297 2008/12/17



(a) Spacecraft Operation Procedure (real-commands)

```
main-292 2008-12-12 12:20:51 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Ü;ä
0005 C.
0006 C. YÁYB;¼Y³YFÝÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ÒÁÒ•µ°Ë»Í×ÁÇ¿ÍYçYÁY×YÍ;¼YÉ;ÈÈèµ•ííÉ;ÈÒÈ¼°ÇÒÒ•Ò¿¼í¹ÇÒÍ;çÁ®, ùÒ¹ÒÈÒÒÒÇÁ+¿®Ò•ÒÈÒÒÒ³ÒÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYÍ;¼YÉ;|YÁYÖY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYÍ;¼YÉ;ä
0019 . S. OP op-292:OP
0020 ()
0021 . S. OG og-292:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPÍ°èYÁYÖY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÁYÖY×¼ªª Í»Òð³ÍÇ§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGÒÍ¼È¹ç•è²ÍOKÒð³ÍÇ§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÁYÖY×¼ªª Í»Òð³ÍÇ§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGÒÍ¼È¹ç•è²ÍOKÒð³ÍÇ§
0062 C.
0063 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÁYÖY×¼ªª Í»Òð³ÍÇ§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG,RAM ID=OPÒÍ¼È¹ç•è²ÍOKÒð³ÍÇ§
0081 C.
0082 . C. ***** òÈ²¼ÒÍ¼Á´¶Í°ÒÈÈ-Ò°Á+¿®(¼Áµ-YÁYÖY×¼¼È¼ÇÒðÁÓÁÇÇªªÒ°ÒÈ¼í¹ÇÒÇÒÁ) *****
0083 C. DHUYá;¼YÉ;È¼Y¼, YÍ;¼YÉ;ÈÒðÍáÒ¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE |§ OPOG UPLOADÒ-Á+¿®NGÒÍ¼í¹ç;ç°È²¼ÒÍTI-CMDÁ+¿®ÒÍ¼Á¹ÒÒ•ÒÈÒÒÒ³ÒÈ;f
```



```

0096 C.          SET EDUMP I±°iYÑY¹aÇ¹Ôa|a³aE;E
0097 C.
0098 C. TIY³YBYÖYÉaDÄI¿(UT)
0099 +. TI 2008-12-12 10:45:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0102 C.
0103 +. TI 2008-12-12 10:45:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0106 C.
0107 +. TI 2008-12-12 10:45:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0110 C.
0111 +. TI 2008-12-12 10:49:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0114 C.
0115 C. °E²¼aIÄê%ííÑaîYÁY§YÄY-¹àìÛ
0116 C.          ÇÇ[HK1_TI_CMD_ENA/DIS]        EQ          ENA
0117 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          4
0118 C.          ÇÇ[HK1_NEXT_EXEC_PIM]        EQ          DHU
0119 C.          ÇÇ[HK1_NEXT_EXEC_DC]        EQ          0xB3
0120 C.
0121 C. *****
0122 C. TIîî°èYÄYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC          (03 ab 03 01 02)
0128 C.          ÇÇ[HK1_DMP_TOP_ADRS_1]        EQ          07
0129 C.          ÇÇ[HK1_DMP_TOP_ADRS_0]        EQ          2B
0130 C.          ÇÇ[HK1_DMP_BLOCK_NUM]        EQ          3
0131 C.          ÇÇ[HK1_DMP_REPEAT_NUM]       EQ          0
0132 C.          ÇÇ[HK1_DMA_DMP_PIM]         EQ          DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC          (07 0b f8)
0135 C.          ÇÇ[HK1_PKT_FORM_NO]          EQ          7
0136 C.          ÇÇ[HK1_PKT_GEN_TIME]         EQ          0.25 s
0137 C.          ÇÇ[HK1_S_TLM_BIT_RATE]       EQ          32k
0138 C.          ÇÇ[HK1_X_TLM_BIT_RATE]      EQ          4M
0139 C.          ÇÇ[HK1_DMP_CHK_FLG]         EQ          EXEC
0140 C.
0141 C. YÄYÖY×½ªî»aD³îÇ§
0142 C.          ÇÇ[HK1_DMP_CHK_FLG]         EQ          NON
0143 C.
0144 C. RAM ID=TI_TBLaî%È¹Ç•è²IOKaaD³îÇ§
0145 C.
0146 C. DHUYâ;¼YÉ;È¼Y½,¥ì;¼YÈ;ËaaDîáa¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC          (02 0a f8)
0149 C.          ÇÇ[HK1_PKT_FORM_NO]          EQ          2
0150 C.          ÇÇ[HK1_PKT_GEN_TIME]         EQ          0.5S
0151 C.          ÇÇ[HK1_S_TLM_BIT_RATE]       EQ          32K
0152 C.          ÇÇ[HK1_X_TLM_BIT_RATE]      EQ          4M
0153 C.
0154 C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2008-12-12 10:49:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC          (21 02)
0163 +. TI 2008-12-12 10:49:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC          (22)
0166 C.          [ ] [HK1_TI_CMD_NUM]          EQ          2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C.
0171 C. ***** XRT START *****
0172 C. Execute, after the success of OP upload.
0173 +. TI 2008-12-12 10:49:00.0
0174 DC 07-F0 MDP_XRT_MODE_STBY
0175 BC          (c3)
0176 C.          [ ] [HK1_TI_CMD_NUM]          EQ          1COUNTUP
0177 C.
0178 C. ***** XRT END *****
0179 C. *****
0180 C. SOT TI command set
0181 C. *****
0182 C. Execute, after the success of OP upload.
0183 +. TI 2008-12-12 10:49:16.0
0184 DC 07-F0 MDP_SOT_MODE_STBY
0185 BC          (41)
0186 C. -----
0187 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0188 C. -----
0189 C. ***** SOT END *****
0190 C.
0191 C. ***** MDP `úÃîaî»ö¼YaaÈÄa¹aaDCBC•×²è *****
0192 C. (%â°îYÖYÄYÉYB¥EYáYÇYèaE¼aa¼Ä»Ûa¹aé)
0193 C. S. DC-BC dcbc-402:DCBC

```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```





(a) Spacecraft Operation Procedure (real-commands)

```
main-294 2008-12-12 12:20:51 132 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Ü;ä
0005 C.
0006 C. YÁY$;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È¿ÒÁß•µ°È»Í×ÁÇòÍ¥çYÁY×¥í;¼YÉ;ÈÈ%µ•íÉ;ÈßÈ¼°ÇÔß•ò¿¼í¹çòÍ;çÁ®, ùò¹òÈßòÇÁ+¿®ò•òÉòòò³òÈ;f
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-263:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Stop SP table >
0036 +. DC 07-F0 MDP_SP_CTRL_MANU
0037 BC (61)
0038 C. -----
0039 C. MDP_SP_CTRL_MODE = MANU [ ]
0040 C. -----
0041 C.
0042 . C. <Upload SP Observation Table>
0043 . S. RAM ram-287:MDP_OBS_S
0044 ( )
0045 C.
0046 . C. < Dump RAMID=MDP_OBS_S >
0047 +. DC 07-F0 MDP_DUMP_SPTBL
0048 BC (83 07 00 00 00 38 b8)
0049 C. -----
0050 C. MDP_OBS_S verify = OK/NG [ ]
0051 C. -----
0052 C.
0053 C. *****
0054 C. SOT TI command set
0055 C. *****
0056 C. Execute, after the success of TBL upload.
0057 +. TI 2008-12-12 10:49:18.0
0058 DC 07-F0 MDP_SOT_MODE_OBSV
0059 BC (40)
0060 . C. -----
0061 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0062 C. -----
0063 C.
0064 C.
0065 C. ***** XRT START *****
0066 C.
0067 +. DC 07-F0 MDP_XRT_CTRL_MANU
0068 BC (c1)
0069 + DC 07-F0 MDP_XRT_MODE_STBY
0070 BC (c3)
0071 . C. ----- Success Verify ? OK / NG_____
0072 C.
0073 C. XRT Obs. Table Upload
0074 . S. RAM ram-291:MDP_OBS_X
0075 ( )
0076 C.
0077 +. DC 07-F0 MDP_DUMP_XRTTBL
0078 BC (84 07 00 00 00 3a d4)
0079 . C. ----- Comparison Check ? OK / ERR _____
0080 C.
0081 C.
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 01 b1 b1 04 04)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 02 b1 b1 08 08)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 03 b1 b1 08 08)
0088 + DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 04 b1 b1 06 06)
0090 + DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 06 80 80 06 06)
0092 + DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 07 80 80 20 20)
0094 + DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 08 e8 4a 06 06)
```

```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097   BC      (cd 09 80 80 08 08)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099   BC      (cd 0a 80 80 10 20)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101   BC      (cd 0b 40 80 10 20)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103   BC      (cd 0f 80 80 04 04)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105   BC      (cd 10 80 80 10 10)
0106 . C. ----- Success Verify ?                OK / NG ____
0107   C.
0108   C.
0109 . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0110   C.
0111 +. TI 2008-12-12 10:49:02.0
0112   DC 07-F0 MDP_XRT_MODE_OBSV
0113   BC      (c2)
0114 . C. ----- Success Verify ?                OK / NG ____
0115   C.
0116   C. ***** XRT END *****
0117   C.
0118 . C. ***** MDP `ûÃîâî»ö¼ŸâÈâð¹âèDCBC•x²è *****
0119   C. (%â°ïŸÓŸŸĀŸĒŸŸŸĒŸŸŸĀŸĸŸèâĒ¼ââ¼Ā»Û¹âè)
0120 . S. DC-BC dcbc-402:DCBC
0121   (MDP_known_event)
0122   C.
0123   C.
0124 . C. ***** ŸDŸ¹.Ī Daily±;ĪŪâĒ`Ø¹âèDCBC•x²è *****
0125 . S. DC-BC dcbc-153:DCBC
0126   (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0127   C.
0128   C.
0129 . C. ;ăLOŸŸĀŸŸŸĀŸ¹¼Ā»Û;ă
0130   C.
0131 . C. ***** LOS *****
0132   C.
```

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\*\*\* OP Sequence for XRT \*\*\*

```

2008/12/12 11:00:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 03 00 00 00 00
2008/12/12 11:00:02.0 XRT_CTRL_MANU_428_OG [0x1ac]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2008/12/12 11:00:04.0 XRT_FOCUS_POSITION_441_OG [0x1b9]
                        XRT_FOCUS_POSITION          4 07-F8 22 fe 97 00
2008/12/12 11:00:24.0 XRT_QT_PROG_SET_423_OG [0x1a7]
                        MDP_XRT_QT_PROG_SET         2 07-F0 c4 11
2008/12/12 11:02:02.0 XRT_FL_PROG_SET_437_OG [0x1b5]
                        MDP_XRT_FL_PROG_SET         2 07-F0 c5 06
2008/12/12 11:02:04.0 XRT_FLD_RESET_439_OG [0x1b7]
                        MDP_XRT_FLD_RESET           1 07-F0 da
2008/12/12 11:02:06.0 XRT_ARS_DIS_422_OG [0x1a6]
                        MDP_XRT_ARS_DIS             1 07-F0 d5
2008/12/12 11:02:08.0 XRT_FLD_ENA_440_OG [0x1b8]
                        MDP_XRT_FLD_ENA            1 07-F0 d8
2008/12/12 11:02:10.0 XRT_FLRCTRL_ENA_446_OG [0x1be]
                        MDP_XRT_FLRCTRL_ENA        1 07-F0 c8
2008/12/12 11:02:12.0 XRT_CTRL_AUTO_403_OG [0x193]
                        MDP_XRT_CTRL_AUTO           1 07-F0 c0
2008/12/12 15:50:00.5 XRT_CTRL_MANU_435_OG [0x1b3]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2008/12/12 16:13:30.0 XRT_Custom_430_OG [0x1ae]
2008/12/12 16:14:30.0 XRT_CTRL_AUTO_432_OG [0x1b0]
                        MDP_XRT_CTRL_AUTO           1 07-F0 c0
2008/12/12 17:26:30.0 XRT_CTRL_MANU_435_OG [0x1b3]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2008/12/12 17:50:00.0 XRT_Custom_430_OG [0x1ae]
2008/12/12 17:51:00.0 XRT_CTRL_AUTO_432_OG [0x1b0]
                        MDP_XRT_CTRL_AUTO           1 07-F0 c0
2008/12/12 18:05:54.0 XRT_CTRL_MANU_428_OG [0x1ac]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2008/12/12 18:05:56.0 XRT_FOCUS_POSITION_442_OG [0x1ba]
                        XRT_FOCUS_POSITION          4 07-F8 22 ff aa 00
2008/12/12 18:06:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM                    5 02-76 00 00 00 00 00
2008/12/12 18:06:16.0 XRT_QT_PROG_SET_406_OG [0x196]
                        MDP_XRT_QT_PROG_SET         2 07-F0 c4 13
2008/12/12 18:06:18.0 XRT_FLD_DIS_419_OG [0x1a3]
                        MDP_XRT_FLD_DIS             1 07-F0 d9
2008/12/12 18:06:20.0 XRT_FLRCTRL_DIS_447_OG [0x1bf]
                        MDP_XRT_FLRCTRL_DIS         1 07-F0 c9
2008/12/12 18:06:22.0 XRT_ARS_DIS_427_OG [0x1ab]
                        MDP_XRT_ARS_DIS             1 07-F0 d5
2008/12/12 18:08:00.0 XRT_CTRL_AUTO_432_OG [0x1b0]
                        MDP_XRT_CTRL_AUTO           1 07-F0 c0
2008/12/12 18:16:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM                    5 02-76 00 00 00 00 00
2008/12/12 19:28:00.0 XRT_CTRL_MANU_428_OG [0x1ac]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2008/12/12 19:28:02.0 XRT_FOCUS_POSITION_436_OG [0x1b4]
                        XRT_FOCUS_POSITION          4 07-F8 22 ff aa 00
2008/12/12 19:28:22.0 XRT_QT_PROG_SET_412_OG [0x19c]
                        MDP_XRT_QT_PROG_SET         2 07-F0 c4 08
2008/12/12 19:28:24.0 XRT_FL_PROG_SET_437_OG [0x1b5]
                        MDP_XRT_FL_PROG_SET         2 07-F0 c5 06
2008/12/12 19:28:26.0 XRT_AEC_RESET_415_OG [0x19f]
                        MDP_XRT_AEC_RESET           1 07-F0 d0
2008/12/12 19:28:28.0 XRT_FLD_ENA_449_OG [0x1c1]
                        MDP_XRT_FLD_ENA            1 07-F0 d8
2008/12/12 19:28:30.0 XRT_FLRCTRL_ENA_401_OG [0x191]
                        MDP_XRT_FLRCTRL_ENA        1 07-F0 c8
2008/12/12 19:28:32.0 XRT_ARS_DIS_417_OG [0x1a1]
                        MDP_XRT_ARS_DIS             1 07-F0 d5
2008/12/12 19:28:34.0 XRT_CTRL_AUTO_432_OG [0x1b0]
                        MDP_XRT_CTRL_AUTO           1 07-F0 c0
2008/12/12 20:41:00.0 XRT_CTRL_MANU_435_OG [0x1b3]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2008/12/12 21:04:30.0 XRT_Custom_430_OG [0x1ae]
2008/12/12 21:05:30.0 XRT_CTRL_AUTO_432_OG [0x1b0]
                        MDP_XRT_CTRL_AUTO           1 07-F0 c0
2008/12/12 22:18:30.0 XRT_CTRL_MANU_435_OG [0x1b3]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2008/12/12 22:40:30.0 XRT_Custom_430_OG [0x1ae]
2008/12/12 22:41:30.0 XRT_CTRL_AUTO_432_OG [0x1b0]
                        MDP_XRT_CTRL_AUTO           1 07-F0 c0
2008/12/12 23:56:00.0 XRT_CTRL_MANU_435_OG [0x1b3]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2008/12/13 00:07:30.0 XRT_Custom_430_OG [0x1ae]
2008/12/13 00:08:30.0 XRT_CTRL_AUTO_432_OG [0x1b0]
                        MDP_XRT_CTRL_AUTO           1 07-F0 c0
2008/12/13 00:50:00.0 XRT_CTRL_MANU_428_OG [0x1ac]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2008/12/13 05:36:54.0 XRT_CTRL_MANU_428_OG [0x1ac]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2008/12/13 05:36:56.0 XRT_FOCUS_POSITION_442_OG [0x1ba]
                        XRT_FOCUS_POSITION          4 07-F8 22 ff aa 00
2008/12/13 05:37:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM                    5 02-76 00 00 00 00 00
2008/12/13 05:37:16.0 XRT_QT_PROG_SET_413_OG [0x19d]
                        MDP_XRT_QT_PROG_SET         2 07-F0 c4 04

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2008/12/13	05:37:18.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/12/13	05:37:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/12/13	05:37:22.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/12/13	05:39:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/13	05:47:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2008/12/13	09:44:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/13	09:44:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2008/12/13	09:45:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 49 ca 00 00				
2008/12/13	09:45:16.0	XRT_QT_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2008/12/13	09:46:54.0	XRT_AEC_RESET_415_OG [0x19f]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2008/12/13	09:46:56.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/12/13	09:46:58.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/12/13	09:47:00.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/12/13	09:47:02.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/13	11:07:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/13	11:09:30.0	XRT_Custom_430_OG [0x1ae]							
2008/12/13	11:10:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/13	14:52:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/13	15:02:00.0	XRT_Custom_430_OG [0x1ae]							
2008/12/13	15:03:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/13	16:26:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/13	16:50:00.0	XRT_Custom_430_OG [0x1ae]							
2008/12/13	16:51:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/13	17:39:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/13	17:39:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2008/12/13	17:40:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2008/12/13	17:40:16.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2008/12/13	17:40:18.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/12/13	17:40:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/12/13	17:40:22.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/12/13	17:42:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/13	17:50:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00 00 2c 20				
2008/12/13	18:28:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/13	18:28:02.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2008/12/13	18:28:22.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2008/12/13	18:28:24.0	XRT_AEC_RESET_415_OG [0x19f]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2008/12/13	18:28:26.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/12/13	18:28:28.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/12/13	18:28:30.0	XRT_ARS_DIS_417_OG [0x1a1]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/12/13	18:28:32.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/13	19:40:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/13	20:03:30.0	XRT_Custom_430_OG [0x1ae]							
2008/12/13	20:04:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/13	21:18:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/13	21:41:00.0	XRT_Custom_430_OG [0x1ae]							
2008/12/13	21:42:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/13	22:55:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/13	23:16:30.0	XRT_Custom_430_OG [0x1ae]							
2008/12/13	23:17:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/14	00:32:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							



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2008/12/14	00:40:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/14	00:41:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/14	01:56:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/14	02:15:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/14	02:16:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/14	03:31:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/14	03:52:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/14	03:53:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/14	05:02:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/14	05:30:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/14	05:31:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/14	05:58:24.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/14	05:58:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/14	05:58:30.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2008/12/14	05:58:46.0	XRT_QT_PROG_SET_413_OG [0x19d]	AOCU_NM	5	02-76	00	00	00	00	00
2008/12/14	05:58:48.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2008/12/14	05:58:50.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/12/14	05:58:52.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/12/14	06:00:30.0	XRT_CTRL_AUTO_444_OG [0x1bc]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/12/14	06:08:30.0	AOCS_Ore-point_Start_5_OG [0x09b]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/14	10:02:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	AOCU_NM	5	02-76	01	00	00	00	00
2008/12/14	10:15:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/14	10:21:00.0	XRT_CTRL_MANU_428_OG [0x1ac]	AOCU_NM	5	02-76	00	00	00	ab	1b
2008/12/14	10:21:02.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/14	10:21:22.0	XRT_QT_PROG_SET_414_OG [0x19e]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2008/12/14	10:23:00.0	XRT_AEC_RESET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2008/12/14	10:23:02.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2008/12/14	10:23:04.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/12/14	10:23:06.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/12/14	10:23:08.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/12/14	15:27:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/14	15:40:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/14	15:41:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/14	15:48:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/14	15:50:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/14	15:51:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/14	17:03:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/14	17:27:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/14	17:28:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/14	17:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/14	17:59:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2008/12/14	18:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00	00
2008/12/14	18:00:16.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2008/12/14	18:00:18.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/12/14	18:00:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/12/14	18:00:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/12/14	18:02:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/14	18:09:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/14	18:09:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2008/12/14	18:10:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00	00

2008/12/14	18:10:16.0	XRT_QT_PROG_SET_425_OG [0x1a9]	AOCU_NM	5	02-76	00	00	00	00	00
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2008/12/14	18:10:18.0	XRT_AEC_RESET_415_OG [0x19f]								
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2008/12/14	18:10:20.0	XRT_FLD_DIS_419_OG [0x1a3]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/12/14	18:10:22.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/12/14	18:10:24.0	XRT_ARS_DIS_417_OG [0x1a1]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/12/14	18:10:26.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/14	18:40:30.0	XRT_CTRL_MANU_435_OG [0x1b3]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/14	19:03:30.0	XRT_Custom_430_OG [0x1ae]								
2008/12/14	19:04:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/14	20:17:30.0	XRT_CTRL_MANU_435_OG [0x1b3]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/14	20:41:00.0	XRT_Custom_430_OG [0x1ae]								
2008/12/14	20:42:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/14	21:55:00.0	XRT_CTRL_MANU_435_OG [0x1b3]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/14	22:17:30.0	XRT_Custom_430_OG [0x1ae]								
2008/12/14	22:18:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/14	23:32:30.0	XRT_CTRL_MANU_435_OG [0x1b3]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/14	23:48:30.0	XRT_Custom_430_OG [0x1ae]								
2008/12/14	23:49:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/15	01:06:00.0	XRT_CTRL_MANU_435_OG [0x1b3]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/15	01:16:00.0	XRT_Custom_430_OG [0x1ae]								
2008/12/15	01:17:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/15	02:32:00.0	XRT_CTRL_MANU_435_OG [0x1b3]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/15	02:52:00.0	XRT_Custom_430_OG [0x1ae]								
2008/12/15	02:53:00.5	XRT_CTRL_AUTO_432_OG [0x1b0]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/15	04:01:00.0	XRT_CTRL_MANU_435_OG [0x1b3]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/15	04:29:30.0	XRT_Custom_430_OG [0x1ae]								
2008/12/15	04:30:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/15	05:40:00.0	XRT_CTRL_MANU_435_OG [0x1b3]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/15	06:07:00.0	XRT_Custom_430_OG [0x1ae]								
2008/12/15	06:08:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/15	06:16:24.0	XRT_CTRL_MANU_428_OG [0x1ac]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/15	06:16:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]								
			XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2008/12/15	06:16:30.0	AOCS_Ore-point_Start_2_OG [0x098]								
			AOCU_NM	5	02-76	00	00	00	00	00
2008/12/15	06:16:46.0	XRT_QT_PROG_SET_413_OG [0x19d]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2008/12/15	06:16:48.0	XRT_FLD_DIS_419_OG [0x1a3]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/12/15	06:16:50.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/12/15	06:16:52.0	XRT_ARS_DIS_427_OG [0x1ab]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/12/15	06:18:30.0	XRT_CTRL_AUTO_444_OG [0x1bc]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/15	06:26:30.0	AOCS_Ore-point_Start_2_OG [0x098]								
			AOCU_NM	5	02-76	00	00	00	00	00
2008/12/15	09:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/15	09:59:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]								
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2008/12/15	10:00:00.0	AOCS_Ore-point_Start_7_OG [0x09d]								
			AOCU_NM	5	02-76	04	00	00	00	00
2008/12/15	10:00:16.0	XRT_QT_PROG_SET_421_OG [0x1a5]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2008/12/15	10:01:54.0	XRT_ARS_DIS_422_OG [0x1a6]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/12/15	10:01:56.0	XRT_FLD_DIS_445_OG [0x1bd]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/12/15	10:01:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/12/15	10:02:00.0	XRT_CTRL_AUTO_403_OG [0x193]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/15	10:42:00.0	XRT_CTRL_MANU_435_OG [0x1b3]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/15	10:51:00.0	XRT_Custom_430_OG [0x1ae]								
2008/12/15	10:52:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/15	14:31:00.0	XRT_CTRL_MANU_435_OG [0x1b3]								

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2008/12/15	14:36:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/12/15	14:37:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
2008/12/15	16:03:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/12/15	16:27:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/12/15	16:28:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
2008/12/15	17:40:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/12/15	18:03:24.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/12/15	18:03:26.0	XRT_FOCUS_RECALIBRATE_448_OG [0x1c0]							
2008/12/15	18:03:30.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_RECAL	2	07-F8	78	00		
2008/12/15	18:07:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	AOCU_NM	5	02-76	00	00	00	00
2008/12/15	18:07:46.0	XRT_QT_PROG_SET_404_OG [0x194]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2008/12/15	18:07:48.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10		
2008/12/15	18:07:50.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2008/12/15	18:07:52.0	XRT_ARS_DIS_417_OG [0x1a1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2008/12/15	18:07:54.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2008/12/15	18:13:30.0	AOCS_ORe-point_Start_8_OG [0x09e]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/12/16	05:50:24.0	XRT_CTRL_MANU_428_OG [0x1ac]	AOCU_NM	5	02-76	02	00	00	00
2008/12/16	05:50:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2008/12/16	05:50:30.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2008/12/16	05:50:46.0	XRT_QT_PROG_SET_413_OG [0x19d]	AOCU_NM	5	02-76	00	00	00	00
2008/12/16	05:50:48.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04		
2008/12/16	05:50:50.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2008/12/16	05:50:52.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2008/12/16	05:52:30.0	XRT_CTRL_AUTO_444_OG [0x1bc]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2008/12/16	06:00:30.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2008/12/16	09:41:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00
			AOCU_NM	5	02-76	00	00	00	00