

XRT Timeline to be uploaded on 2008/12/18

Period: 2008/12/18 10:25:00 - 2008/12/23 11:08:00

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Normal mode

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XOB #1563: CCD Monitor During Bakeout - G-band + dark - wide FOV												
Term	Pointing (x, y)							Comment				
12/18 11:03:00 - 12/18 16:17:00	Track (-157.0, 169.0) ^{@ 12/18 10:35:00}							# OP start + 10min, equatorial CH diagnostics by EIS.				
12/18 16:41:30 - 12/18 17:53:30	Fixed (-917.1, -227.4)							* HOP 79, Irradiance scan, E-W, in S. (#1/11)				
PROG= 01 Inf.-time(s)												
└─ Subr= 1 1-time(s) 600.0sec												
└─ Seqn= 98 1-time(s) 4.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
└─ Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
XOB #1565: CCD Monitor During Bakeout - G-band + dark - wide FOV - lower cadence (30min)												
Term	Pointing (x, y)							Comment				
12/18 18:18:30 - 12/18 19:31:00	Fixed (180.5, -232.1)							#7/11				
12/18 19:55:00 - 12/18 20:39:11	Track (-78.8, 170.0) ^{@ 12/18 19:00:00}							# More equatorial CH by EIS.				
PROG= 11 Inf.-time(s)												
└─ Subr= 1 1-time(s) 1800.0sec												
└─ Seqn= 98 1-time(s) 4.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
└─ Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
XOB #15A8: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant												
Term	Pointing (x, y)							Comment				
12/19 04:03:00 - 12/19 04:09:54	Fixed (-528.4, -528.4)							* XRT four-quadrant pointing.				
PROG= 14 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 28 1-time(s) 12.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
└─ Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
└─ Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
XOB #15A9: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant												
Term	Pointing (x, y)							Comment				
12/19 04:13:00 - 12/19 04:19:54	Fixed (528.4, -528.4)											
PROG= 13 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 41 1-time(s) 12.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
└─ Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
└─ Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
XOB #15AA: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant												
Term	Pointing (x, y)							Comment				
12/19 04:23:00 - 12/19 04:29:54	Fixed (528.4, 528.4)											
PROG= 12 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 8 1-time(s) 12.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
└─ Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
└─ Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
XOB #15AB: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant												
Term	Pointing (x, y)							Comment				
12/19 04:33:00 - 12/19 04:40:00	Fixed (-528.4, 528.4)							* Four-quadrant pointing - end.				
PROG= 07 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 40 1-time(s) 12.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
└─ Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
└─ Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1615: Al/poly (AEC4) + Ti/poly (AEC4) + Thin-Be (AEC4) - 2-min cadence - FOV384 - Q95

Term	Pointing (x, y)	Comment
12/19 04:42:08 - 12/19 06:02:54	Track (-182.6, -2.1) @ 12/19 04:40:00	* Track weak CH near disc center.
12/19 06:15:08 - 12/19 09:52:30	Track (-168.2, -1.9) @ 12/19 06:13:00	# Cont.
PROG= 02 Inf.-time(s)		
Subr= 1 1-time(s) 120.0sec		
└─ Seqn= 42 3-time(s) 120.0sec		
Al-poly/Open	C-poly/Open close	Safe Norm 8.00s Obs 1x1 384x384 (1024, 1024) Q=95 4 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 8.00s Obs 1x1 384x384 (1024, 1024) Q=95 4 0 2.0sec
Subr= 2 1-time(s) 8.0sec		
└─ Seqn= 99 2-time(s) 4.0sec		
thin-Be/Open	thin-Be/Open close	Safe Norm 22.6s Obs 1x1 384x384 (1024, 1024) Q=95 4 0 2.0sec
└─ Seqn= 82 1-time(s) 4.0sec		
thin-Be/Open	thin-Be/Open close	Safe Dark 22.6s Obs 1x1 384x384 (1024, 1024) DPCM 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

XOB #15D7: Synoptic Q95 2x2 - Al/mesh(362/5795) + Dark cal(362 Q98) + Ti-poly(512/11571) + G-band(16)

Term	Pointing (x, y)	Comment
12/19 06:05:00 - 12/19 06:13:00	Fixed (0.0, 0.0)	synoptic, shifted 3.0 min
12/20 06:09:00 - 12/23 11:08:00	Fixed (0.0, 0.0)	synoptic, shifted 7.0 min
PROG= 04 1-time(s)		
Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 29 1-time(s) 4.0sec		
Open/Al-mesh	Open/Ti-poly close	Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Ti-poly close	Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 57 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Dark 354ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Seqn= 4 1-time(s) 4.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 92 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

XOB #15E7: HOP81 - Al/poly, G-band - 16s - 384x384

Term	Pointing (x, y)	Comment
12/19 10:10:30 - 12/19 17:59:54	Fixed (0.0, 940.0)	* HOP 81, polar monitoring campaign, during SAA-free period.
PROG= 18 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 1 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 94 30-time(s) 60.0sec		
Al-poly/Open	Al-poly/Open close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

XOB #1606: Synoptic Q95 2x2 - Al/poly(256/5795) + Dark cal(512 Q98) + Ti-poly(362/11571) + G-band(16)

Term	Pointing (x, y)	Comment
12/19 18:02:00 - 12/19 18:10:00	Fixed (0.0, 0.0)	synoptic
PROG= 19 1-time(s)		
Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 70 1-time(s) 4.0sec		
Al-poly/Open	Al-poly/Open close	Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Seqn= 57 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Dark 354ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Seqn= 16 1-time(s) 4.0sec		
Open/Ti-poly	Open/Ti-poly close	Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Seqn= 92 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

XOB #1619: Al/poly (AEC4) + Ti/poly (AEC4) + Thin-Be (AEC4) - 3-min cadence - FOV512 - Q95

Term	Pointing (x, y)	Comment
12/19 18:12:08 - 12/20 06:06:54	Track (-55.4, -0.5) @ 12/19 18:10:00	# Disc-center tracking for XRT QS study, with SOT support.
PROG= 03 Inf.-time(s)		
Subr= 1 1-time(s) 180.0sec		
└─ Seqn= 63 3-time(s) 180.0sec		
Al-poly/Open	C-poly/Open close	Safe Norm 8.00s Obs 1x1 512x512 (1024, 1024) Q=95 4 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 8.00s Obs 1x1 512x512 (1024, 1024) Q=95 4 0 2.0sec
Subr= 2 1-time(s) 8.0sec		
└─ Seqn= 93 2-time(s) 4.0sec		
thin-Be/Open	thin-Be/Open close	Safe Norm 22.6s Obs 1x1 512x512 (1024, 1024) Q=95 4 0 2.0sec

Seqn= 10 1-time(s) 4.0sec

thin-Be/Open	thin-Be/Open	close	Safe	Dark	22.6s	Obs	1x1	512x512 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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NOT USED

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Active Region Search

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NOT USED

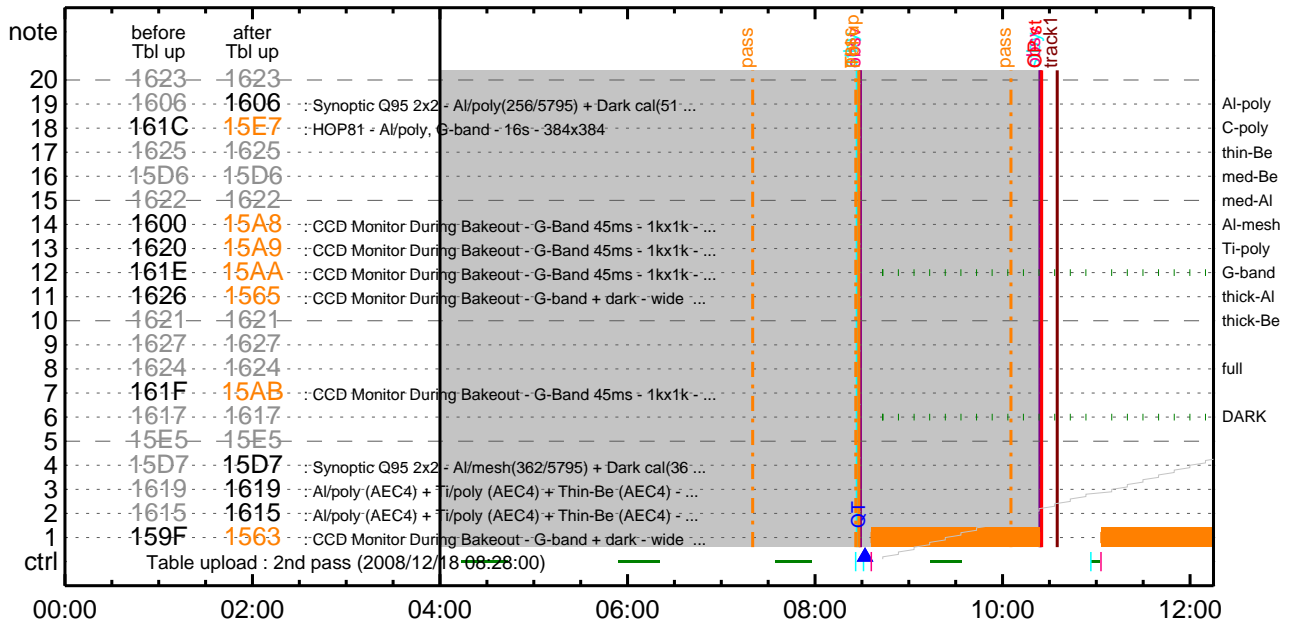
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Flare Detection

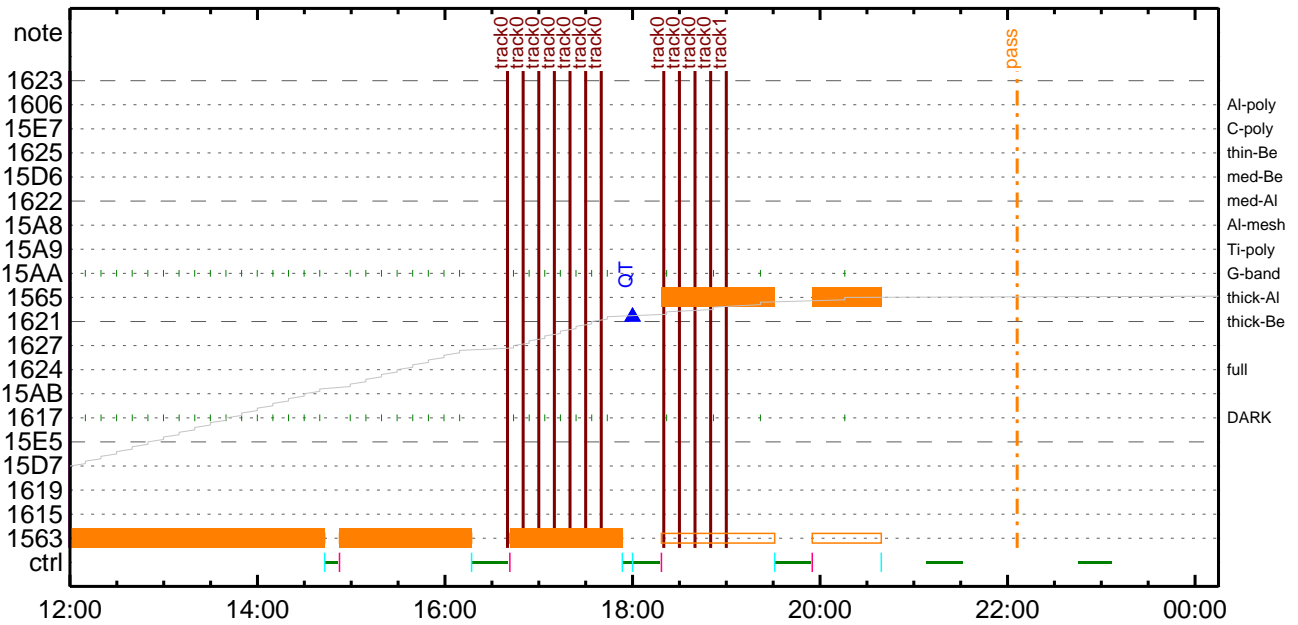
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NOT USED

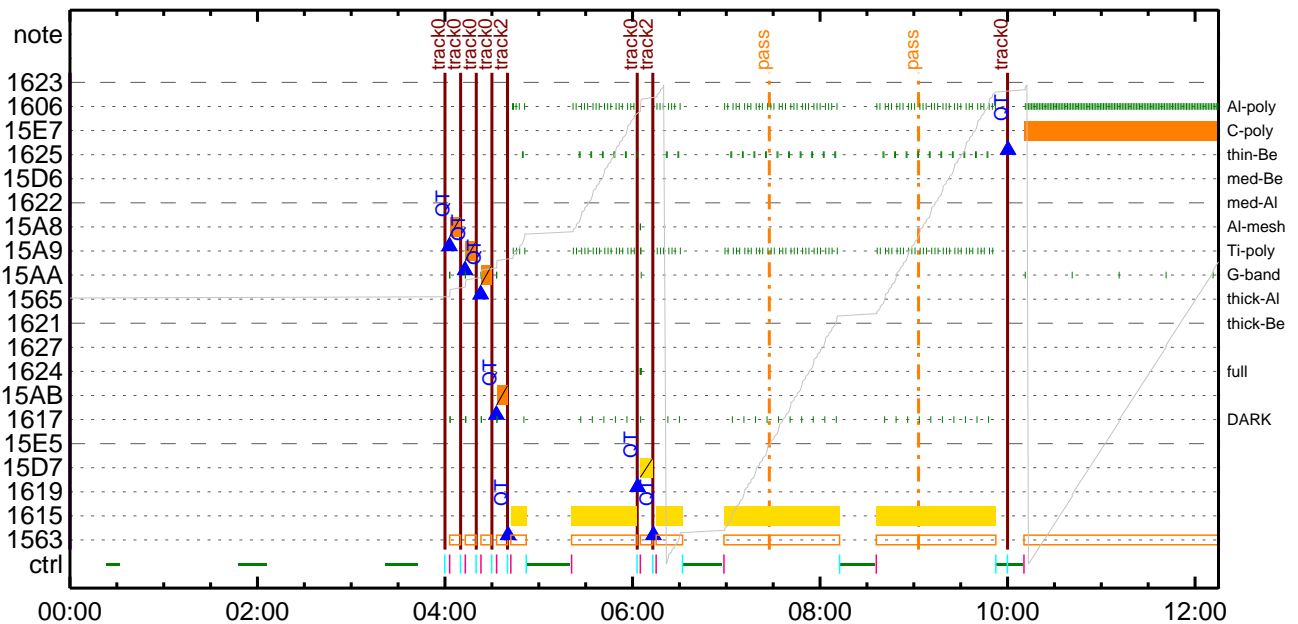
CMDI #0304 2008/12/18



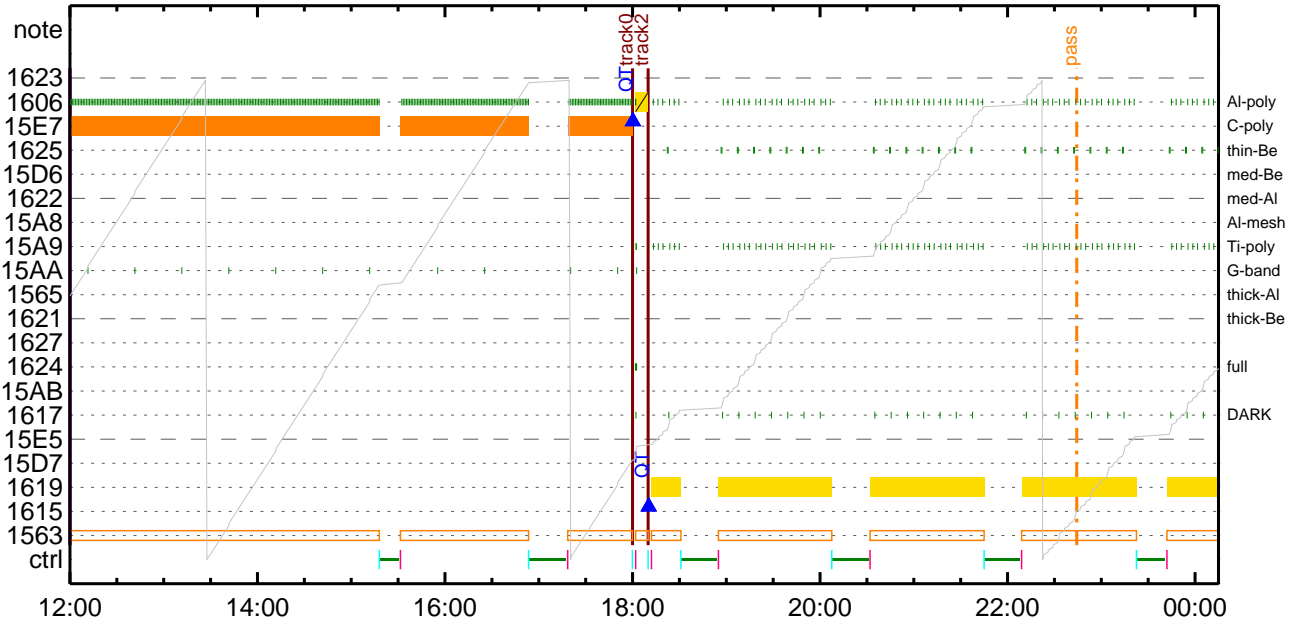
CMDI #0304 2008/12/18



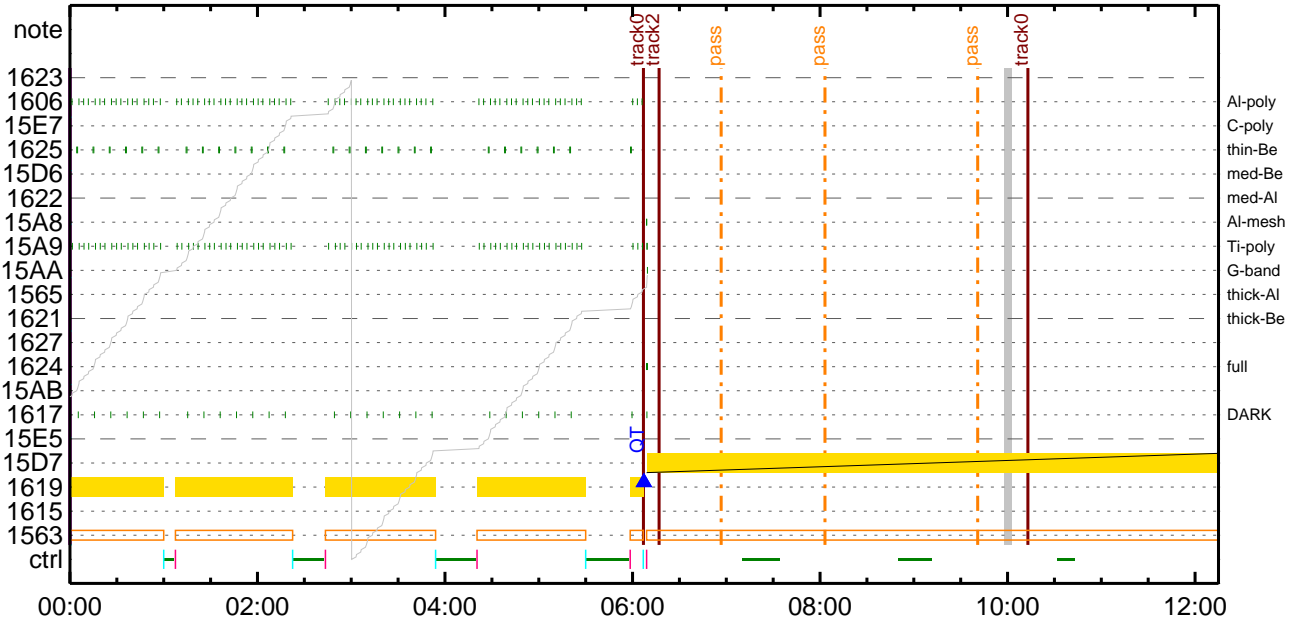
CMDI #0304 2008/12/19



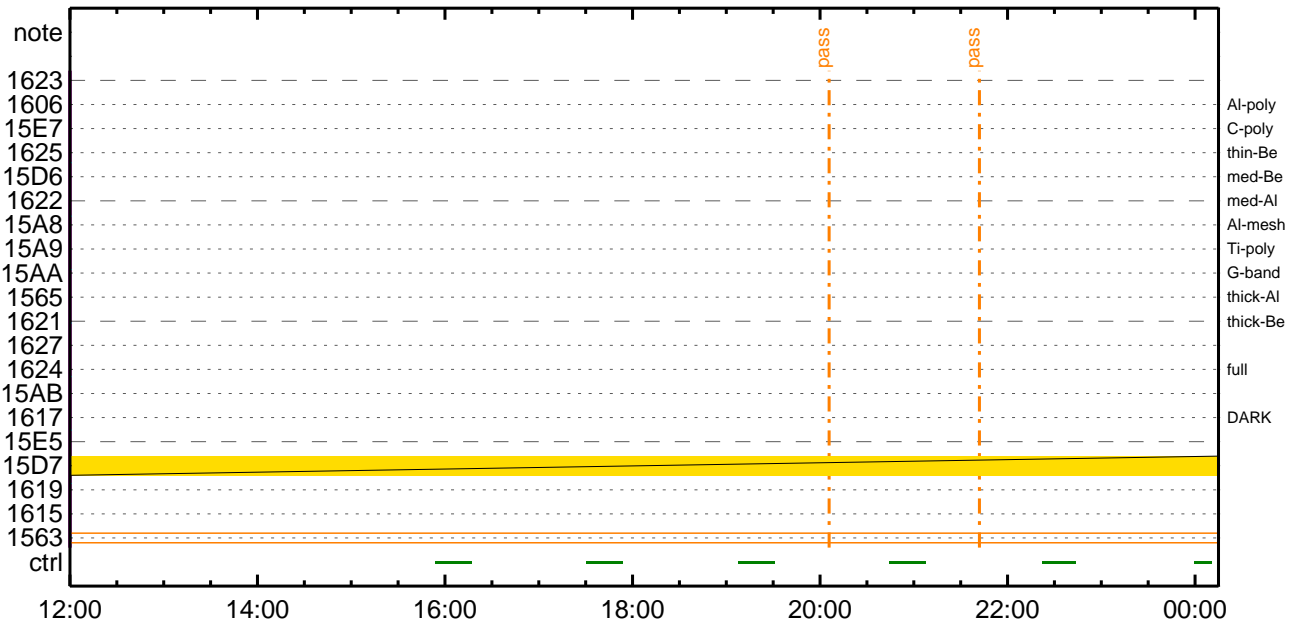
CMDI #0304 2008/12/19



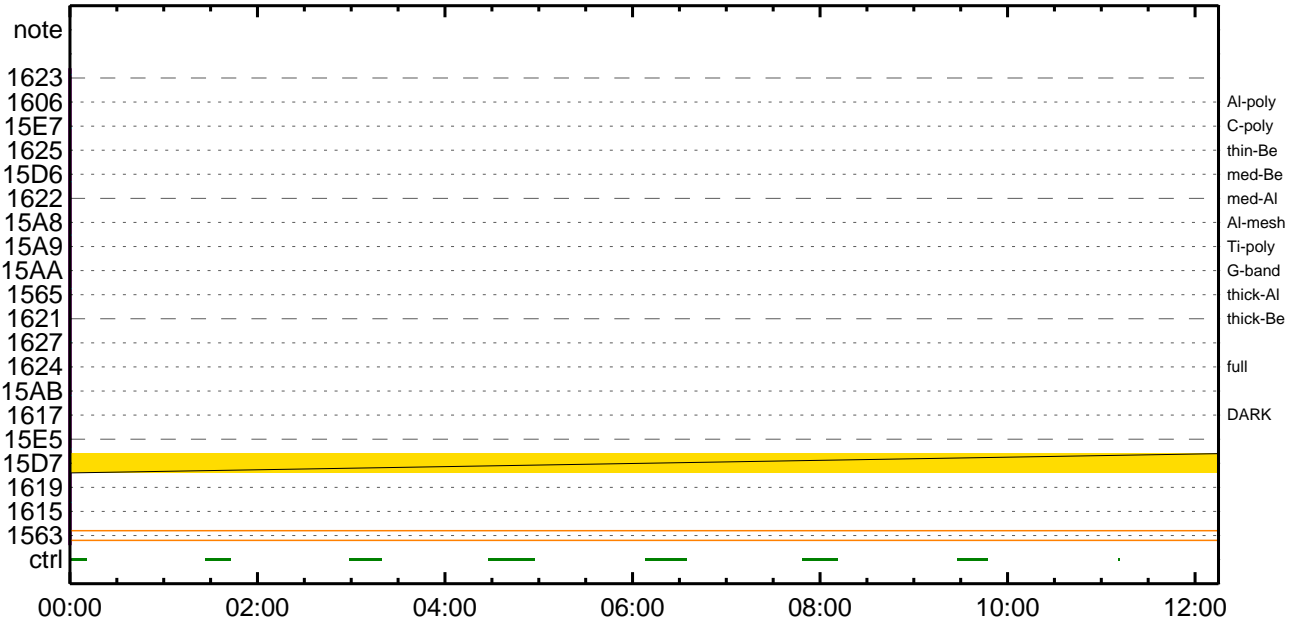
CMDI #0304 2008/12/20



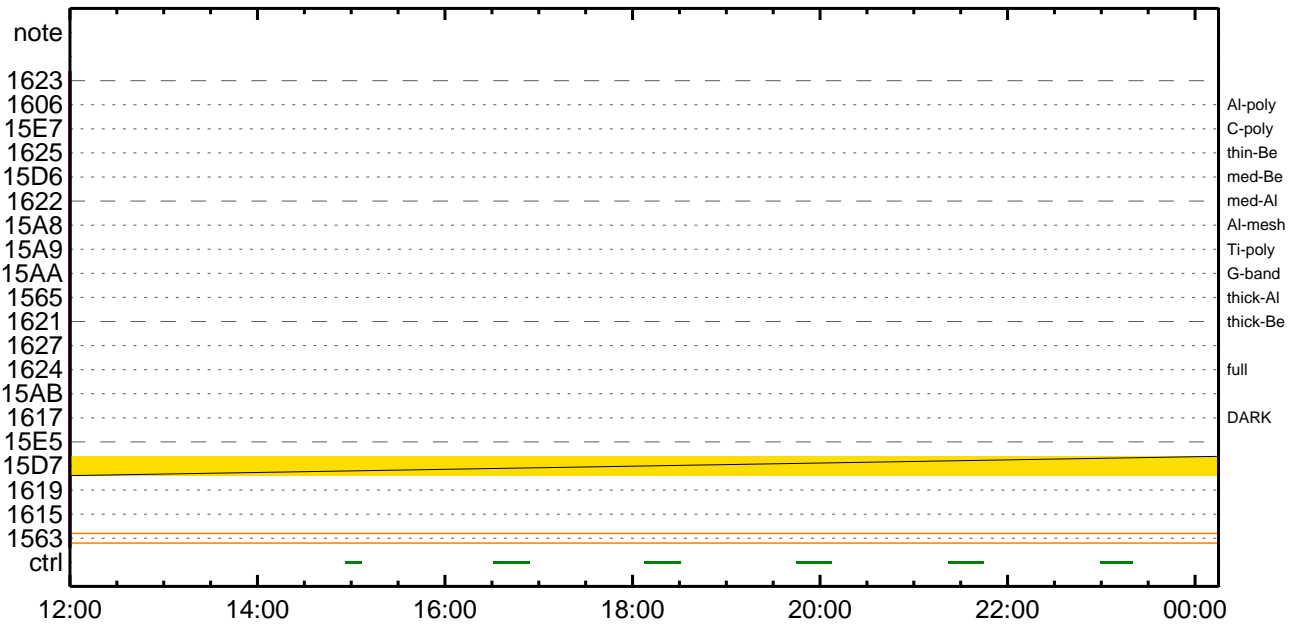
CMDI #0304 2008/12/20



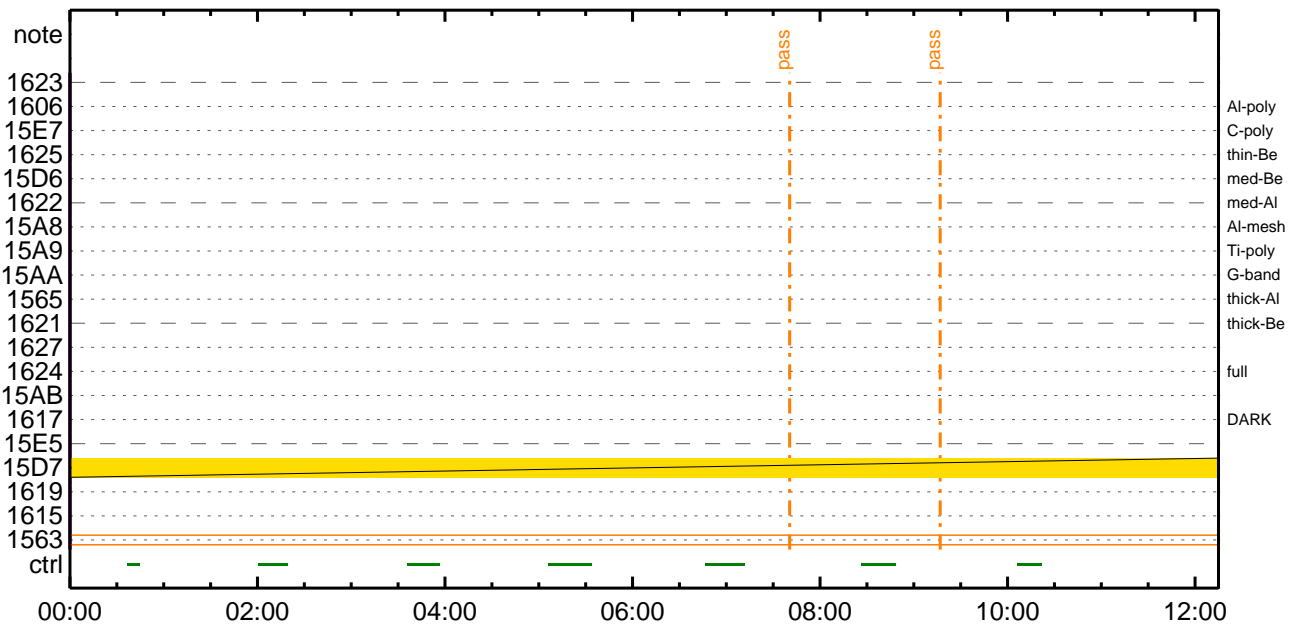
CMDI #0304 2008/12/21



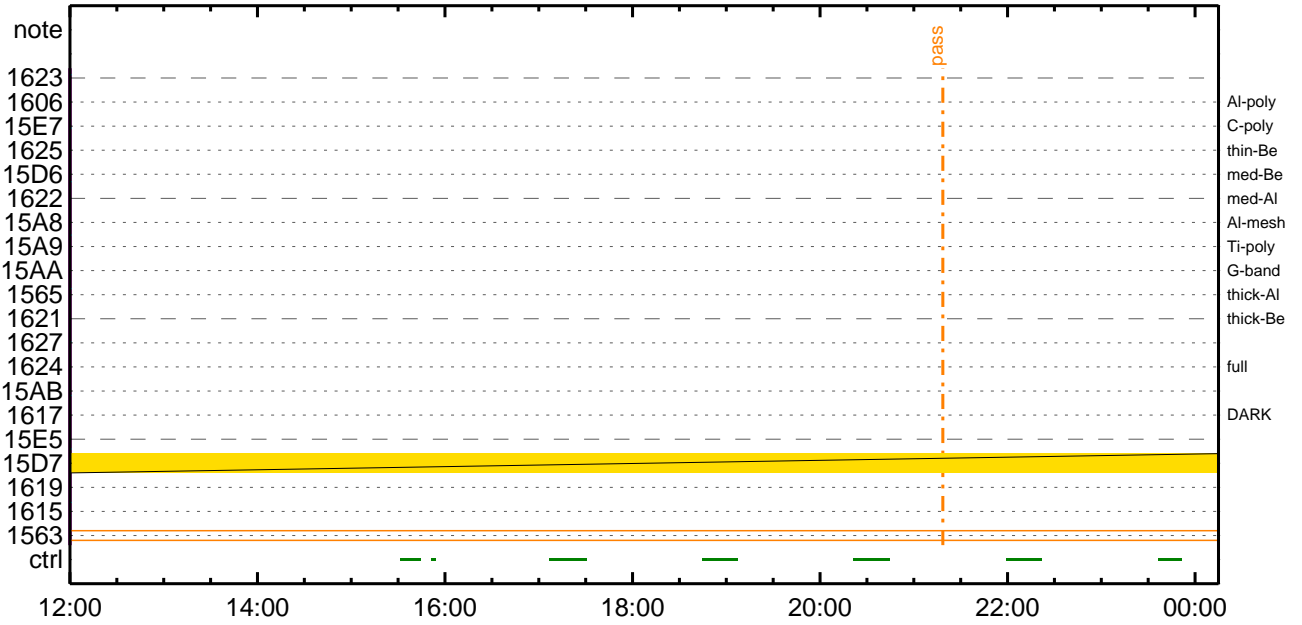
CMDI #0304 2008/12/21



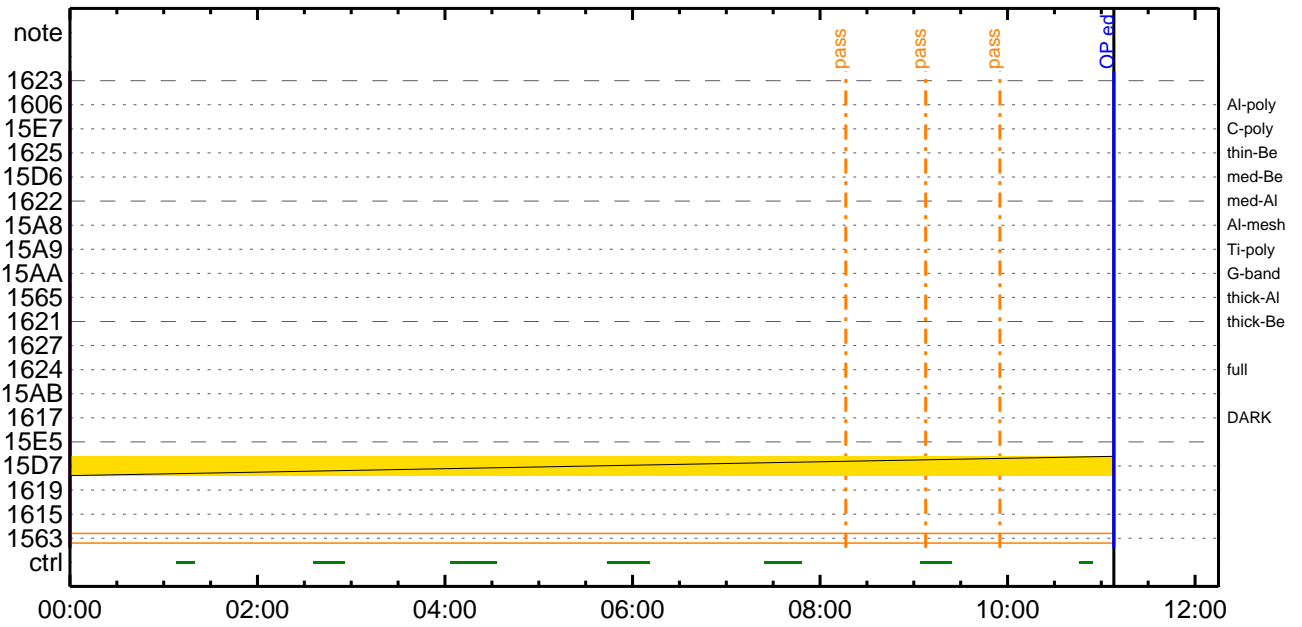
CMDI #0304 2008/12/22



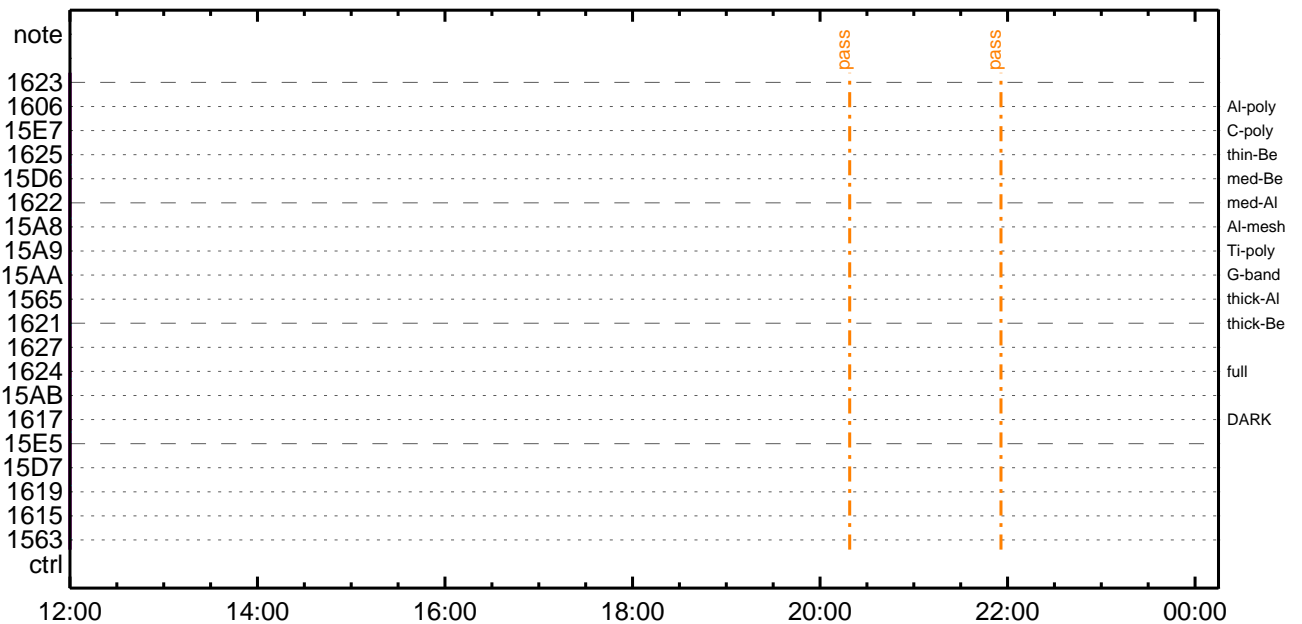
CMDI #0304 2008/12/22



CMDI #0304 2008/12/23



CMDI #0304 2008/12/23




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0096 C.                   0303; 03SET0EDUMP01E±0iYÑY¹0Ç¹Ô0|0³0E; E
0097 C.
0098 C. TIY³YFYYÖYÉ00dÁDİç (UT)
0099 +. TI 2008-12-18 10:20:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                   03[HK1_TI_CMD_NUM]           EQ           1COUNTUP
0102 C.
0103 +. TI 2008-12-18 10:20:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                   03[HK1_TI_CMD_NUM]           EQ           1COUNTUP
0106 C.
0107 +. TI 2008-12-18 10:20:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                   03[HK1_TI_CMD_NUM]           EQ           1COUNTUP
0110 C.
0111 +. TI 2008-12-18 10:24:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                   03[HK1_TI_CMD_NUM]           EQ           1COUNTUP
0114 C.
0115 C. 0E²%0İÄê%İİÑ0İYÁYŞYÁY-¹àİÜ
0116 C.                   03[HK1_TI_CMD_ENA/DIS]        EQ           ENA
0117 C.                   03[HK1_TI_CMD_NUM]           EQ           4
0118 C.                   03[HK1_NEXT_EXEC_PIM]        EQ           DHU
0119 C.                   03[HK1_NEXT_EXEC_DC]         EQ           0xB3
0120 C.
0121 C. *****
0122 C. TIİİ°èYÄYÖYx
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; § 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                   03[HK1_DMP_TOP_ADDR_1]        EQ           07
0129 C.                   03[HK1_DMP_TOP_ADDR_0]        EQ           2B
0130 C.                   03[HK1_DMP_BLOCK_NUM]         EQ           3
0131 C.                   03[HK1_DMP_REPEAT_NUM]        EQ           0
0132 C.                   03[HK1_DMA_DMP_PIM]          EQ           DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                   03[HK1_PKT_FORM_NO]           EQ           7
0136 C.                   03[HK1_PKT_GEN_TIME]          EQ           0.25 s
0137 C.                   03[HK1_S_TLM_BIT_RATE]        EQ           32k
0138 C.                   03[HK1_X_TLM_BIT_RATE]       EQ           4M
0139 C.                   03[HK1_DMP_CHK_FLG]          EQ           EXEC
0140 C.
0141 C. YÄYÖYx½¹İ»0d³İÇŞ
0142 C.                   03[HK1_DMP_CHK_FLG]          EQ           NON
0143 C.
0144 C. RAM ID=TI_TBL0İ%È¹Ç•è²İOK0d³İÇŞ
0145 C.
0146 C. DHUYâ;¼YÉ;È¼Y¼;Yİ;¼YÈ;È0dİá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                   03[HK1_PKT_FORM_NO]           EQ           2
0150 C.                   03[HK1_PKT_GEN_TIME]          EQ           0.5S
0151 C.                   03[HK1_S_TLM_BIT_RATE]        EQ           32K
0152 C.                   03[HK1_X_TLM_BIT_RATE]       EQ           4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2008-12-18 10:24:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 C. -----
0162 C. HK1_TI_CMD_NUM           = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2008-12-18 10:24:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2008-12-18 10:24:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 C.                   [ ] [HK1_TI_CMD_NUM]           EQ           2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2008-12-18 10:24:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 C.                   [ ] [HK1_TI_CMD_NUM]           EQ           1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 C. ***** MDP çÜÄİ0İ»ö¼Y0ÈÄ0¹0èDCBC•x²è *****
0192 C. (%Ä0İYÖYÄYÈYpYÈYáYçYè0E¼00¼Ä»Ü0¹0é)
0193 C. DC-BC dcbc-402:DCBC

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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 4s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-821:EIS_OBSTBL
0138 ( )
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2008-12-18 10:24:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C.
0152 C. ***** XRT START *****
0153 C.
0154 +. DC 07-F0 MDP_XRT_CTRL_MANU
0155 BC (c1)
0156 + DC 07-F0 MDP_XRT_MODE_STBY
0157 BC (c3)
0158 . C. ----- Success Verify ? OK / NG____
0159 C.
0160 C. XRT Obs. Table Upload
0161 . S. RAM ram-291:MDP_OBS_X
0162 ( )
0163 C.
0164 +. DC 07-F0 MDP_DUMP_XRTTBL
0165 BC (84 07 00 00 00 3a d4)
0166 . C. ----- Comparison Check ? OK / ERR ____
0167 C.
0168 C.
0169 +. DC 07-F0 MDP_XRT_ROI_SET
0170 BC (cd 01 b1 b1 04 04)
0171 + DC 07-F0 MDP_XRT_ROI_SET
0172 BC (cd 02 b1 b1 08 08)
0173 + DC 07-F0 MDP_XRT_ROI_SET
0174 BC (cd 03 b1 b1 08 08)
0175 + DC 07-F0 MDP_XRT_ROI_SET
0176 BC (cd 04 b1 b1 06 06)
0177 + DC 07-F0 MDP_XRT_ROI_SET
0178 BC (cd 06 80 80 20 04)
0179 + DC 07-F0 MDP_XRT_ROI_SET
0180 BC (cd 07 c0 c0 10 10)
0181 + DC 07-F0 MDP_XRT_ROI_SET
0182 BC (cd 08 40 c0 10 10)
0183 + DC 07-F0 MDP_XRT_ROI_SET
0184 BC (cd 09 40 40 10 10)
0185 + DC 07-F0 MDP_XRT_ROI_SET
0186 BC (cd 0a c0 40 10 10)
0187 + DC 07-F0 MDP_XRT_ROI_SET
0188 BC (cd 0b 80 80 06 06)
0189 + DC 07-F0 MDP_XRT_ROI_SET
0190 BC (cd 0c 80 80 20 20)
0191 + DC 07-F0 MDP_XRT_ROI_SET
0192 BC (cd 0d 85 83 06 06)
0193 + DC 07-F0 MDP_XRT_ROI_SET

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0194 BC (cd 0e 80 80 08 08)
0195 + DC 07-F0 MDP_XRT_ROI_SET
0196 BC (cd 0f 80 80 04 04)
0197 + DC 07-F0 MDP_XRT_ROI_SET
0198 BC (cd 10 80 80 10 10)
0199 . C. ----- Success Verify ? OK / NG ____
0200 C.
0201 C.
0202 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0203 C.
0204 +. DC 07-F0 MDP_XRT_MODE_OBSV
0205 BC (c2)
0206 +. TI 2008-12-18 10:24:02.0
0207 DC 07-F0 MDP_XRT_MODE_OBSV
0208 BC (c2)
0209 . C. ----- Success Verify ? OK / NG ____
0210 C.
0211 C. ***** XRT END *****
0212 . C. *****
0213 C. SOT table upload
0214 C. *****
0215 . C. < Stop FG table >
0216 +. DC 07-F0 MDP_FG_CTRL_MANU
0217 BC (51)
0218 . C. -----
0219 C. MDP_FG_CTRL_MODE = MANU [ ]
0220 C. -----
0221 C.
0222 . C. <Upload FG Observation Table>
0223 . S. RAM ram-265:MDP_OBS_F
0224 ( )
0225 C.
0226 . C. < Dump RAMID=MDP_OBS_F >
0227 +. DC 07-F0 MDP_DUMP_FGTBL
0228 BC (82 07 00 00 00 38 b8)
0229 C. -----
0230 C. MDP_OBS_F verify = OK/NG [ ]
0231 C. -----
0232 C.
0233 . C. < Stop SP table >
0234 +. DC 07-F0 MDP_SP_CTRL_MANU
0235 BC (61)
0236 . C. -----
0237 C. MDP_SP_CTRL_MODE = MANU [ ]
0238 C. -----
0239 C.
0240 . C. <Upload SP Observation Table>
0241 . S. RAM ram-288:MDP_OBS_S
0242 ( )
0243 C.
0244 . C. < Dump RAMID=MDP_OBS_S >
0245 +. DC 07-F0 MDP_DUMP_SPTBL
0246 BC (83 07 00 00 00 38 b8)
0247 C. -----
0248 C. MDP_OBS_S verify = OK/NG [ ]
0249 C. -----
0250 C.
0251 C. *****
0252 C. SOT TI command set
0253 C. *****
0254 C. Execute, after the success of TBL upload.
0255 +. TI 2008-12-18 10:24:18.0
0256 DC 07-F0 MDP_SOT_MODE_OBSV
0257 BC (40)
0258 . C. -----
0259 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0260 C. -----
0261 C.
0262 C. *****
0263 C. START of XRT_CCD_HEATER_ON operation
0264 C. *****
0265 C.
0266 +. DC 07-F0 MDP_XRT_CTRL_MANU
0267 BC (c1)
0268 C. ----- Success Verify ? OK / NG;
0269 C.
0270 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0271 BC (c4 01)
0272 + DC 07-F0 MDP_XRT_FLD_DIS
0273 BC (d9)
0274 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0275 BC (c9)
0276 + DC 07-F0 MDP_XRT_ARS_DIS
0277 BC (d5)
0278 C. ----- Success Verify ? OK / NG ____
0279 C.
0280 C.
0281 C. All OK? Yes--> Please Proceed. / No --> Stop here.
0282 C.
0283 +. DC 07-F0 MDP_XRT_CTRL_AUTO
0284 BC (c0)
0285 C. ----- Success Verify ? OK / NG;
0286 C.
0287 +. DC 04-BC TCIB_XRT_S_HTR_A_ENA
0288 C. ----- Success Verify ? OK / NG;
0289 C.
0290 C. -----
0291 C. If anomalous situation appeared, execute TCIB_XRT_S_HTR_A_DIS using DCBC-442 (line 24)

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0096 C.
0097 C.
0098 C.
0099 . C. ***** MDP 'ûÃîñî»ò¼ÿñĒĀĐñ¹ñĒDCBC•x²Ē *****
0100 C. (¼á°îÿÓÿĀÿĒÿÿÿĒÿáÿçÿĒĒ¼ññ¼Ā»Ûñ¹ñĒ)
0101 . S. DC-BC dcbc-402:DCBC
0102 (MDP_known_event)
0103 C.
0104 C.
0105 . C. ***** ÿĐÿ¹•Ī Daily±;ĪÑñĒ'Øñ¹ñĒDCBC•x²Ē *****
0106 . S. DC-BC dcbc-153:DCBC
0107 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0108 C.
0109 C.
0110 . C. ;ãLOSÿÁÿŒÿĀÿ¼Ā»Û;ã
0111 C.
0112 . C. ***** LOS *****
0113 C.

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Dec 18, 08 14:22

XRT_OGLIST_0304.chk

Page 1/4

*** OP Sequence for XRT ***

2008/12/18	10:35:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01	00	00	00	00
2008/12/18	10:56:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/18	11:02:00.0	XRT_Custom_430_OG [0x1ae]							
2008/12/18	11:03:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/18	14:43:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/18	14:51:30.0	XRT_Custom_430_OG [0x1ae]							
2008/12/18	14:52:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/18	16:17:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/18	16:40:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	14	39	51	81
2008/12/18	16:40:30.0	XRT_Custom_430_OG [0x1ae]							
2008/12/18	16:41:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/18	16:50:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	15	47	41	91
2008/12/18	17:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	14	f5	31	78
2008/12/18	17:10:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	14	bc	20	f5
2008/12/18	17:20:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	14	a3	10	08
2008/12/18	17:30:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	14	9b	fe	b9
2008/12/18	17:40:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	14	a3	ef	f8
2008/12/18	17:53:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/18	18:00:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/18	18:00:02.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2008/12/18	18:00:04.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/12/18	18:00:06.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/12/18	18:00:08.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/12/18	18:17:30.0	XRT_Custom_430_OG [0x1ae]							
2008/12/18	18:18:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/18	18:20:00.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	14	bc	df	0b
2008/12/18	18:30:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	14	f5	ce	88
2008/12/18	18:40:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00	15	47	be	6f
2008/12/18	18:50:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00	14	39	ae	7f
2008/12/18	19:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01	00	00	00	00
2008/12/18	19:31:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/18	19:54:00.0	XRT_Custom_430_OG [0x1ae]							
2008/12/18	19:55:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/18	20:39:11.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/18	20:39:13.0	XRT_TCIB_XRT_S_HTR_A_DIS_409_OG [0x199]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2008/12/19	03:59:54.0	XRT_CTRL_MANU_410_OG [0x19a]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/19	04:00:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00	2e	f9	2e	f9
2008/12/19	04:02:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2008/12/19	04:02:52.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2008/12/19	04:02:54.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2008/12/19	04:02:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2008/12/19	04:02:58.0	XRT_ARS_DIS_417_OG [0x1a1]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2008/12/19	04:03:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2008/12/19	04:09:54.0	XRT_CTRL_MANU_410_OG [0x19a]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2008/12/19	04:10:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00	2e	f9	d1	07
2008/12/19	04:12:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2008/12/19	04:12:52.0	XRT_QT_PROG_SET_405_OG [0x195]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2008/12/19	04:12:54.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				

Friday December 19, 2008

1/4

2008/12/19	04:12:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1 07-F0 c9
2008/12/19	04:12:58.0	XRT_ARS_DIS_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1 07-F0 d5
2008/12/19	04:13:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1 07-F0 c0
2008/12/19	04:19:54.0	XRT_CTRL_MANU_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1 07-F0 c1
2008/12/19	04:20:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]	AOCU_NM	5 02-76 00 d1 07 d1 07
2008/12/19	04:22:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4 07-F8 22 ff aa 00
2008/12/19	04:22:52.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2 07-F0 c4 0c
2008/12/19	04:22:54.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1 07-F0 d9
2008/12/19	04:22:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1 07-F0 c9
2008/12/19	04:22:58.0	XRT_ARS_DIS_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1 07-F0 d5
2008/12/19	04:23:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1 07-F0 c0
2008/12/19	04:29:54.0	XRT_CTRL_MANU_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1 07-F0 c1
2008/12/19	04:30:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]	AOCU_NM	5 02-76 00 d1 07 2e f9
2008/12/19	04:32:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4 07-F8 22 ff aa 00
2008/12/19	04:32:52.0	XRT_QT_PROG_SET_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2 07-F0 c4 07
2008/12/19	04:32:54.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1 07-F0 d9
2008/12/19	04:32:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1 07-F0 c9
2008/12/19	04:32:58.0	XRT_ARS_DIS_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1 07-F0 d5
2008/12/19	04:33:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1 07-F0 c0
2008/12/19	04:40:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]	AOCU_NM	5 02-76 02 00 00 00 00
2008/12/19	04:40:00.5	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1 07-F0 c1
2008/12/19	04:40:02.5	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4 07-F8 22 fe 97 00
2008/12/19	04:40:22.5	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2 07-F0 c4 02
2008/12/19	04:42:00.5	XRT_AEC_RESET_415_OG [0x19f]	MDP_XRT_AEC_RESET	1 07-F0 d0
2008/12/19	04:42:02.5	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1 07-F0 d5
2008/12/19	04:42:04.5	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1 07-F0 d9
2008/12/19	04:42:06.5	XRT_FLRCTRL_DIS_416_OG [0x1a0]	MDP_XRT_FLRCTRL_DIS	1 07-F0 c9
2008/12/19	04:42:08.5	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1 07-F0 c0
2008/12/19	04:52:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1 07-F0 c1
2008/12/19	05:20:00.0	XRT_Custom_430_OG [0x1ae]		
2008/12/19	05:21:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1 07-F0 c0
2008/12/19	06:02:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1 07-F0 c1
2008/12/19	06:02:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4 07-F8 22 ff aa 00
2008/12/19	06:03:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]	AOCU_NM	5 02-76 00 00 00 00 00
2008/12/19	06:03:16.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_QT_PROG_SET	2 07-F0 c4 04
2008/12/19	06:03:18.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1 07-F0 d9
2008/12/19	06:03:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1 07-F0 c9
2008/12/19	06:03:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1 07-F0 d5
2008/12/19	06:05:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]	MDP_XRT_CTRL_AUTO	1 07-F0 c0
2008/12/19	06:13:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]	AOCU_NM	5 02-76 02 00 00 00 00
2008/12/19	06:13:00.5	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1 07-F0 c1
2008/12/19	06:13:02.5	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4 07-F8 22 fe 97 00
2008/12/19	06:13:22.5	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2 07-F0 c4 02
2008/12/19	06:15:00.5	XRT_AEC_RESET_415_OG [0x19f]	MDP_XRT_AEC_RESET	1 07-F0 d0
2008/12/19	06:15:02.5	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1 07-F0 d5
2008/12/19	06:15:04.5	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1 07-F0 d9
2008/12/19	06:15:06.5	XRT_FLRCTRL_DIS_416_OG [0x1a0]	MDP_XRT_FLRCTRL_DIS	1 07-F0 c9

Dec 18, 08 14:22

XRT_OGLIST_0304.chk

Page 3/4

2008/12/19	06:15:08.5	XRT_CTRL_AUTO_403_OG [0x193] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/12/19	06:32:00.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/12/19	06:57:30.0	XRT_Custom_430_OG [0x1ae]			
2008/12/19	06:58:30.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/12/19	08:12:30.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/12/19	08:35:00.0	XRT_Custom_430_OG [0x1ae]			
2008/12/19	08:36:00.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/12/19	09:52:30.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/12/19	09:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac] MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/12/19	09:59:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2008/12/19	10:00:00.0	AOCS_ORe-point_Start_19_OG [0x0a9] AOCU_NM	5	02-76	00 ac 73 00 00
2008/12/19	10:00:16.0	XRT_QT_PROG_SET_424_OG [0x1a8] MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2008/12/19	10:01:54.0	XRT_ARS_DIS_422_OG [0x1a6] MDP_XRT_ARS_DIS	1	07-F0	d5
2008/12/19	10:01:56.0	XRT_FLD_DIS_445_OG [0x1bd] MDP_XRT_FLD_DIS	1	07-F0	d9
2008/12/19	10:01:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2008/12/19	10:09:30.0	XRT_Custom_430_OG [0x1ae]			
2008/12/19	10:10:30.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/12/19	15:18:00.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/12/19	15:30:30.0	XRT_Custom_430_OG [0x1ae]			
2008/12/19	15:31:30.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/12/19	16:53:30.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/12/19	17:17:30.0	XRT_Custom_430_OG [0x1ae]			
2008/12/19	17:18:30.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/12/19	17:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac] MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/12/19	17:59:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2008/12/19	18:00:00.0	AOCS_ORe-point_Start_18_OG [0x0a8] AOCU_NM	5	02-76	00 00 00 00 00
2008/12/19	18:00:16.0	XRT_QT_PROG_SET_406_OG [0x196] MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2008/12/19	18:00:18.0	XRT_FLD_DIS_419_OG [0x1a3] MDP_XRT_FLD_DIS	1	07-F0	d9
2008/12/19	18:00:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2008/12/19	18:00:22.0	XRT_ARS_DIS_427_OG [0x1ab] MDP_XRT_ARS_DIS	1	07-F0	d5
2008/12/19	18:02:00.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/12/19	18:10:00.0	AOCS_ORe-point_Start_17_OG [0x0a7] AOCU_NM	5	02-76	02 00 00 00 00
2008/12/19	18:10:00.5	XRT_CTRL_MANU_428_OG [0x1ac] MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/12/19	18:10:02.5	XRT_FOCUS_POSITION_441_OG [0x1b9] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2008/12/19	18:10:22.5	XRT_QT_PROG_SET_443_OG [0x1bb] MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2008/12/19	18:12:00.5	XRT_AEC_RESET_415_OG [0x19f] MDP_XRT_AEC_RESET	1	07-F0	d0
2008/12/19	18:12:02.5	XRT_ARS_DIS_422_OG [0x1a6] MDP_XRT_ARS_DIS	1	07-F0	d5
2008/12/19	18:12:04.5	XRT_FLD_DIS_445_OG [0x1bd] MDP_XRT_FLD_DIS	1	07-F0	d9
2008/12/19	18:12:06.5	XRT_FLRCTRL_DIS_416_OG [0x1a0] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2008/12/19	18:12:08.5	XRT_CTRL_AUTO_403_OG [0x193] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/12/19	18:31:00.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/12/19	18:54:00.0	XRT_Custom_430_OG [0x1ae]			
2008/12/19	18:55:00.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/12/19	20:07:30.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/12/19	20:31:00.0	XRT_Custom_430_OG [0x1ae]			
2008/12/19	20:32:00.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/12/19	21:45:00.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/12/19	22:08:00.0	XRT_Custom_430_OG [0x1ae]			
2008/12/19	22:09:00.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2008/12/19	23:22:30.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1
2008/12/19	23:41:00.0	XRT_Custom_430_OG [0x1ae]			

Dec 18, 08 14:22

XRT_OGLIST_0304.chk

Page 4/4

2008/12/19	23:42:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2008/12/20	01:00:00.0	XRT_CTRL_MANU_435_OG [0x1b3]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2008/12/20	01:06:30.0	XRT_Custom_430_OG [0x1ae]								
2008/12/20	01:07:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2008/12/20	02:22:30.0	XRT_CTRL_MANU_435_OG [0x1b3]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2008/12/20	02:42:30.0	XRT_Custom_430_OG [0x1ae]								
2008/12/20	02:43:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2008/12/20	03:54:00.0	XRT_CTRL_MANU_435_OG [0x1b3]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2008/12/20	04:19:30.0	XRT_Custom_430_OG [0x1ae]								
2008/12/20	04:20:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2008/12/20	05:30:00.0	XRT_CTRL_MANU_435_OG [0x1b3]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2008/12/20	05:57:30.0	XRT_Custom_430_OG [0x1ae]								
2008/12/20	05:58:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2008/12/20	06:06:54.0	XRT_CTRL_MANU_428_OG [0x1ac]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2008/12/20	06:06:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]								
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00					
2008/12/20	06:07:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]								
		AOCU_NM	5	02-76	00 00 00 00 00					
2008/12/20	06:07:16.0	XRT_QT_PROG_SET_413_OG [0x19d]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04					
2008/12/20	06:07:18.0	XRT_FLD_DIS_419_OG [0x1a3]								
		MDP_XRT_FLD_DIS	1	07-F0	d9					
2008/12/20	06:07:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]								
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2008/12/20	06:07:22.0	XRT_ARS_DIS_427_OG [0x1ab]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2008/12/20	06:09:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2008/12/20	06:17:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]								
		AOCU_NM	5	02-76	02 00 00 00 00					
2008/12/20	10:13:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]								
		AOCU_NM	5	02-76	00 00 00 00 00					