

XRT Timeline to be uploaded on 2009/01/04

Period: 2009/01/04 09:52:00 - 2009/01/08 11:15:00

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Normal mode

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XOB #162D: Full Sun - CME Watch - C/Poly + Thin-Be + Med-Be - 4x4 - AEC2													
Term		Pointing (x, y)					Comment						
01/04 10:21:36 - 01/04 17:59:54		Track (537.9, -4.7) @ 01/04 10:02:00					QS tracking, cont.						
01/04 18:13:00 - 01/05 06:14:24		Track (600.5, -6.9) @ 01/04 18:10:00					#						
PROG= 09 Inf.-time(s)													
Subr= 1		4-time(s)		480.0sec									
Seqn= 22		1-time(s)		60.0sec									
C-poly/Open		med-Be/Open close		Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Seqn= 91		1-time(s)		60.0sec									
thin-Be/Open		med-Be/Open close		Safe	Norm	4.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Seqn= 65		1-time(s)		60.0sec									
med-Be/Open		med-Be/Open close		Safe	Norm	22.6s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Subr= 2		1-time(s)		60.0sec									
Seqn= 60		1-time(s)		30.0sec									
C-poly/Open		C-poly/Open close		Safe	Dark	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #162E: Synoptic Q95 2x2 - Al/poly(181/4096) + Dark cal(256 Q98) + Ti-poly(256/8192) + G-band(16)													
Term		Pointing (x, y)					Comment						
01/04 18:03:00 - 01/04 18:09:54		Fixed (0.0, 0.0)					synoptic						
01/05 18:05:00 - 01/05 18:11:54		Fixed (0.0, 0.0)					synoptic, shifted 2.0 min						
PROG= 05 1-time(s)													
Subr= 1		1-time(s)		12.0sec									
Seqn= 59		1-time(s)		4.0sec									
Al-poly/Open		Al-poly/Open close		Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
		Al-poly/Open close		Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 49		1-time(s)		2.0sec									
Al-poly/Open		Al-poly/Open close		Safe	Dark	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 44		1-time(s)		4.0sec									
Open/Ti-poly		Open/Ti-poly close		Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
		Open/Ti-poly close		Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 92		1-time(s)		2.0sec									
Open/G-band		Open/G-band open		Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #162F: Synoptic Q95 2x2 - Al/mesh(128/4096) + Dark cal(256 Q98) + Ti-poly(256/8192) + G-band(16)													
Term		Pointing (x, y)					Comment						
01/05 06:17:30 - 01/05 06:24:24		Fixed (0.0, 0.0)					synoptic, shifted 14.5 min						
01/06 05:51:30 - 01/06 05:58:24		Fixed (0.0, 0.0)					synoptic, shifted -11.5 min						
PROG= 10 1-time(s)													
Subr= 1		1-time(s)		12.0sec									
Seqn= 25		1-time(s)		4.0sec									
Open/Al-mesh		Open/Al-mesh close		Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
		Open/Al-mesh close		Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 50		1-time(s)		2.0sec									
Open/Al-mesh		Open/Al-mesh close		Safe	Dark	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 44		1-time(s)		4.0sec									
Open/Ti-poly		Open/Ti-poly close		Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
		Open/Ti-poly close		Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 92		1-time(s)		2.0sec									
Open/G-band		Open/G-band open		Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1630: Al/poly 384FOV Q95 - 60s-cad - Ti/poly + Thin-Be context - AEC1													
Term		Pointing (x, y)					Comment						
01/05 06:30:32 - 01/05 17:38:00		Track (710.0, -386.9) @ 01/05 06:24:30					AR tracking						
01/05 18:14:00 - 01/06 05:48:24		Track (763.4, -391.3) @ 01/05 18:12:00					AR tracking, cont.						
01/06 06:00:30 - 01/06 09:36:30		Track (807.4, -396.2) @ 01/06 05:58:30					AR tracking, cont.						
PROG= 17 Inf.-time(s)													
Subr= 1		30-time(s)		60.0sec									
Seqn= 26		1-time(s)		30.0sec									
Al-poly/Open		Al-poly/thick-Al close		Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Subr= 2		1-time(s)		4.0sec									
Seqn= 21		2-time(s)		4.0sec									
Open/Ti-poly		Open/thick-Al close		Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
		thin-Be/Open close		Safe	Norm	11.3s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
		thin-Be/Open close		Safe	Dark	22.6s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

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Flare mode

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NOT USED

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Active Region Search

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NOT USED

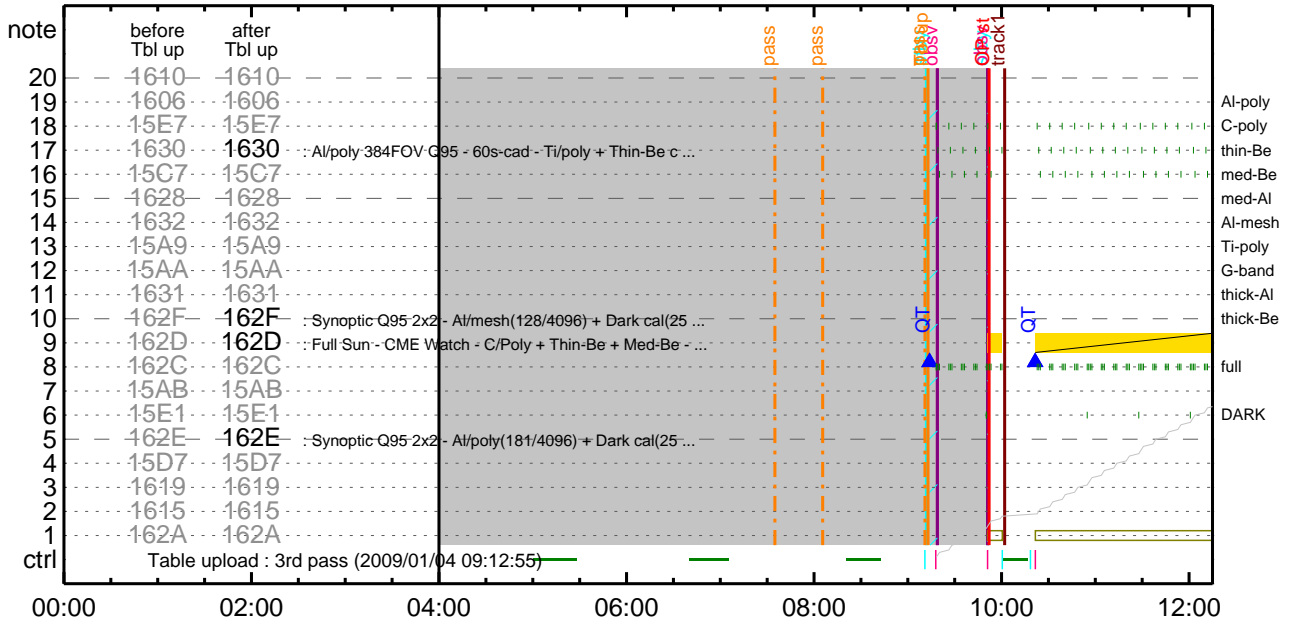
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Flare Detection

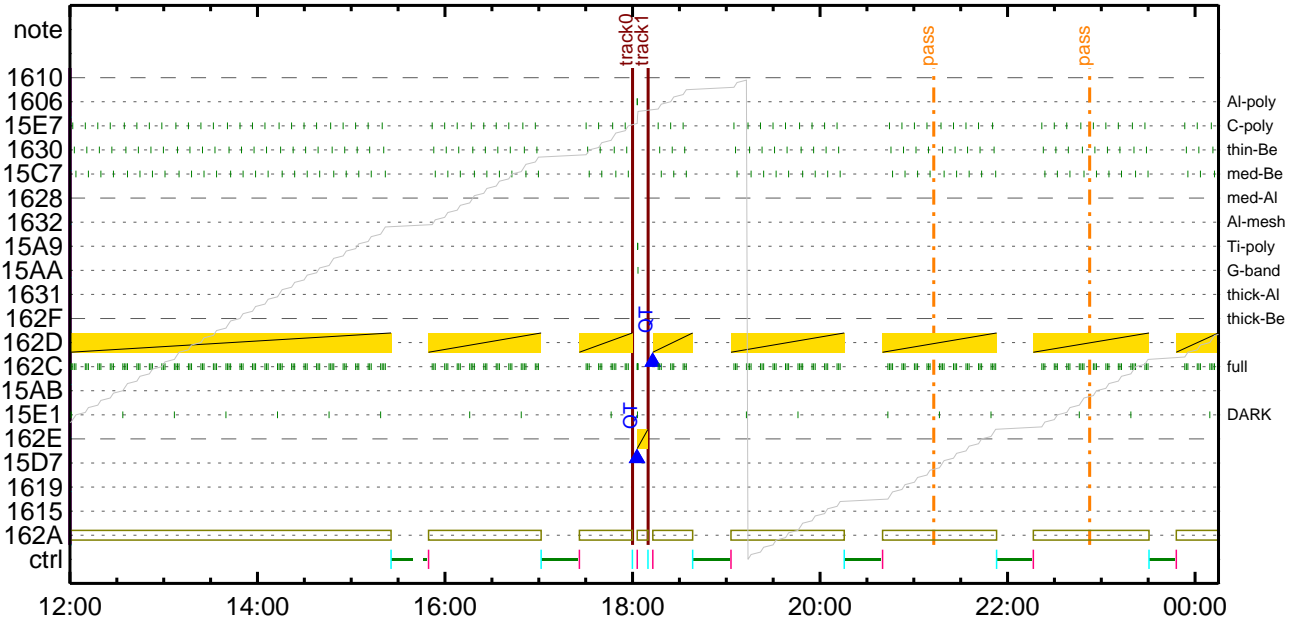
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NOT USED

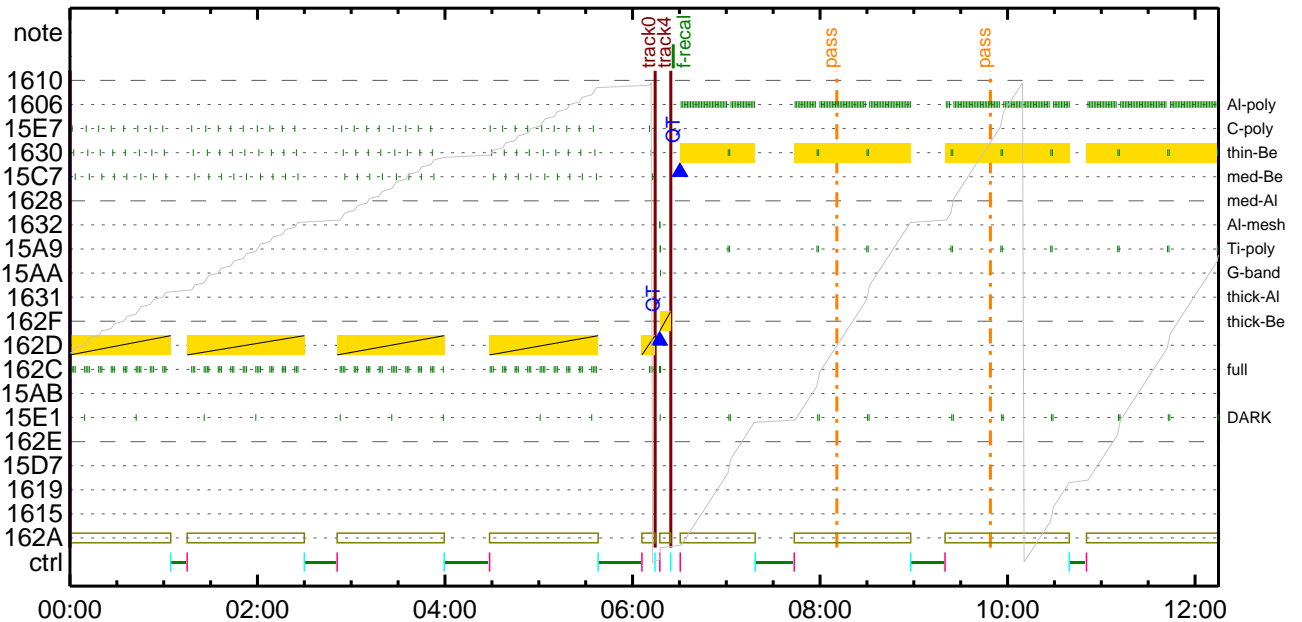
CMDI #0324 2009/01/04



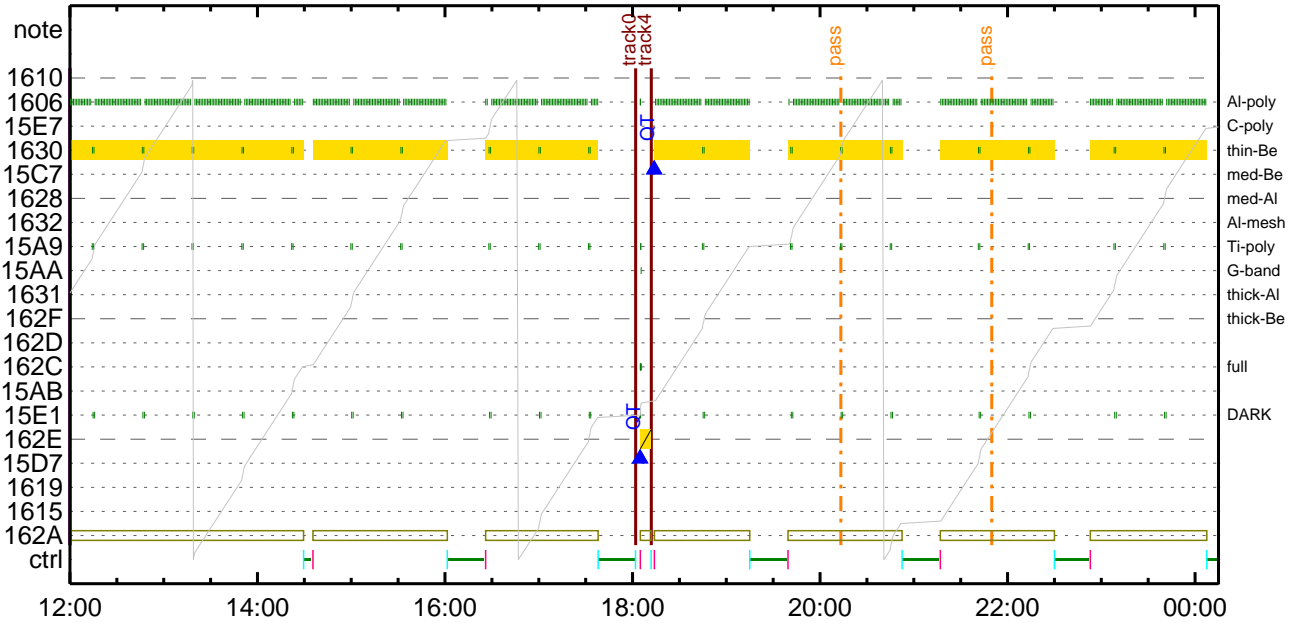
CMDI #0324 2009/01/04



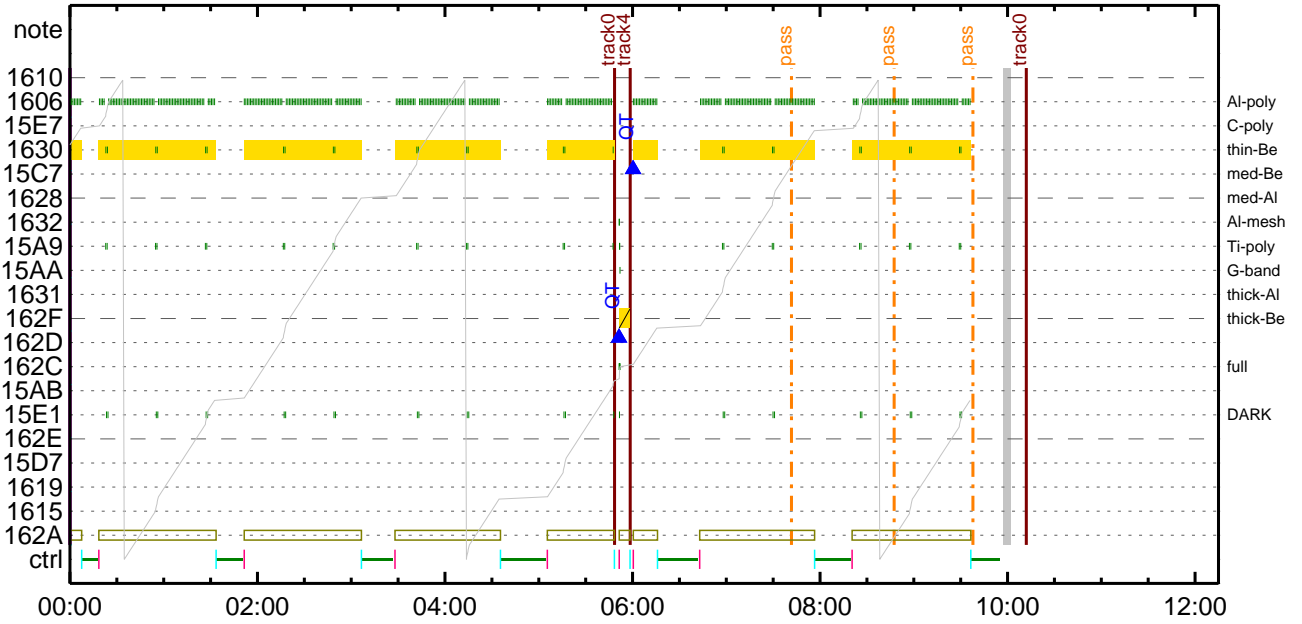
CMDI #0324 2009/01/05



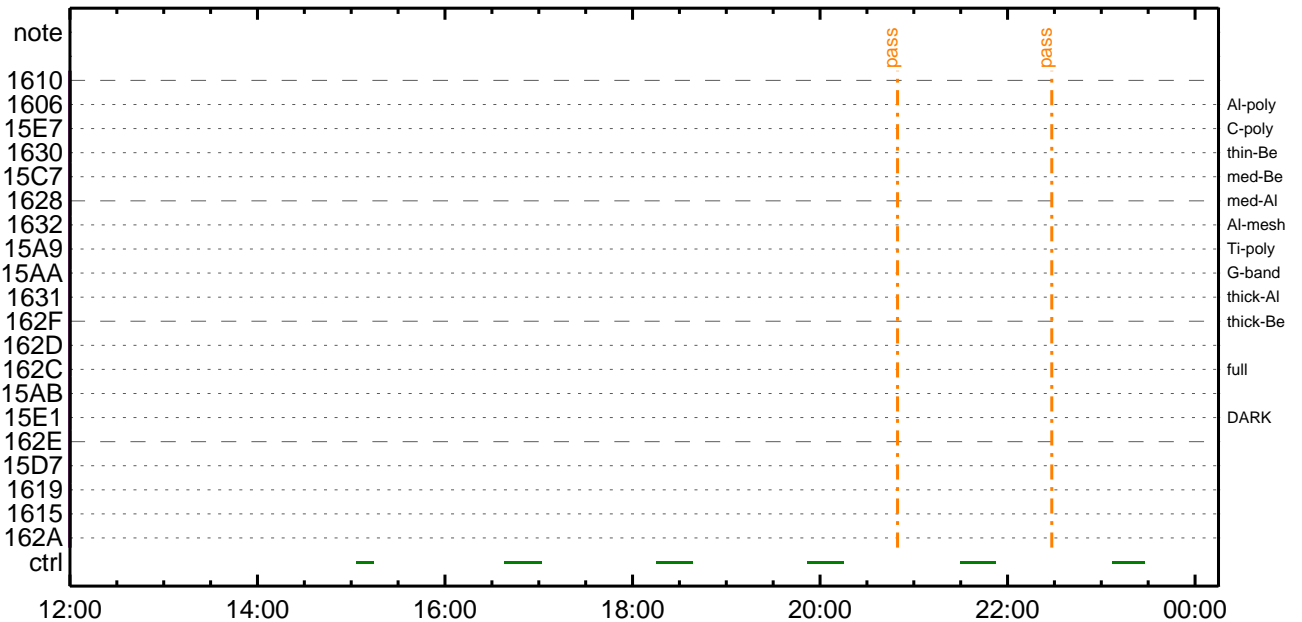
CMDI #0324 2009/01/05



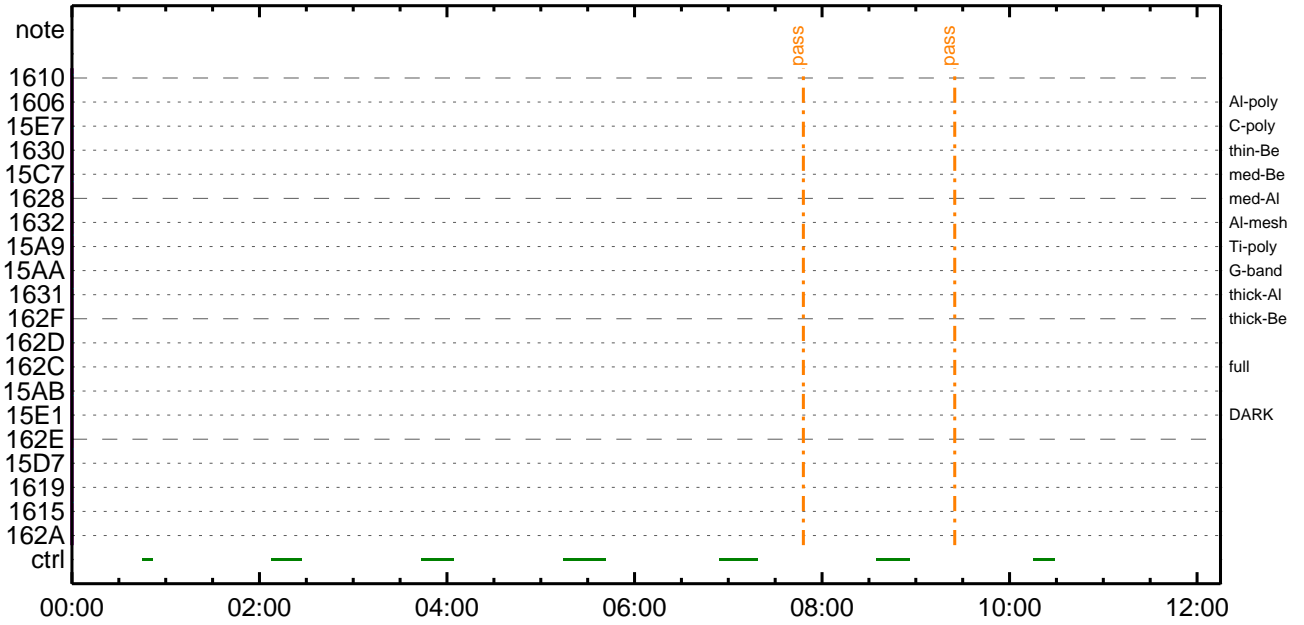
CMDI #0324 2009/01/06



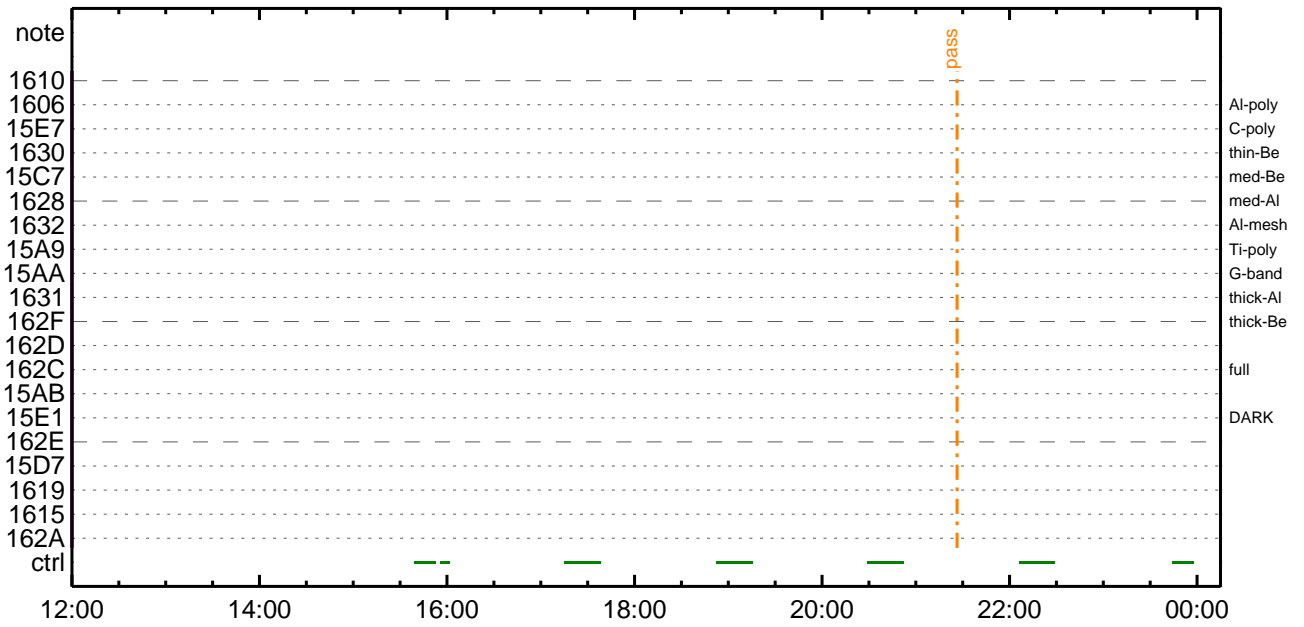
CMDI #0324 2009/01/06



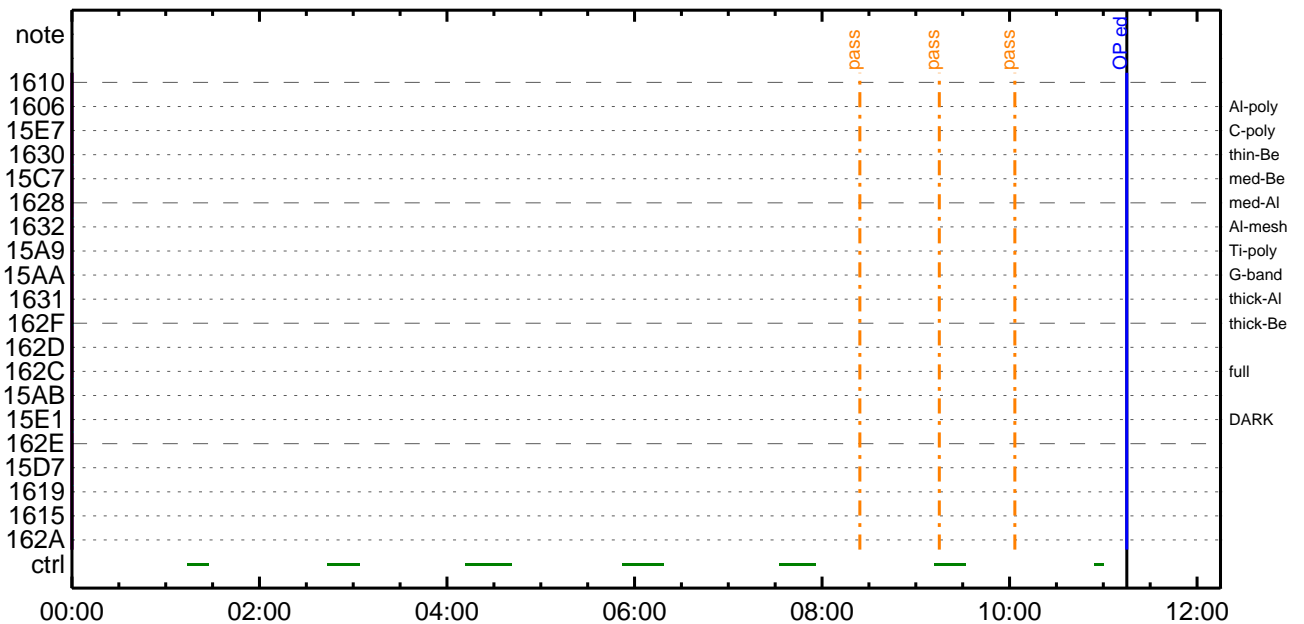
CMDI #0324 2009/01/07



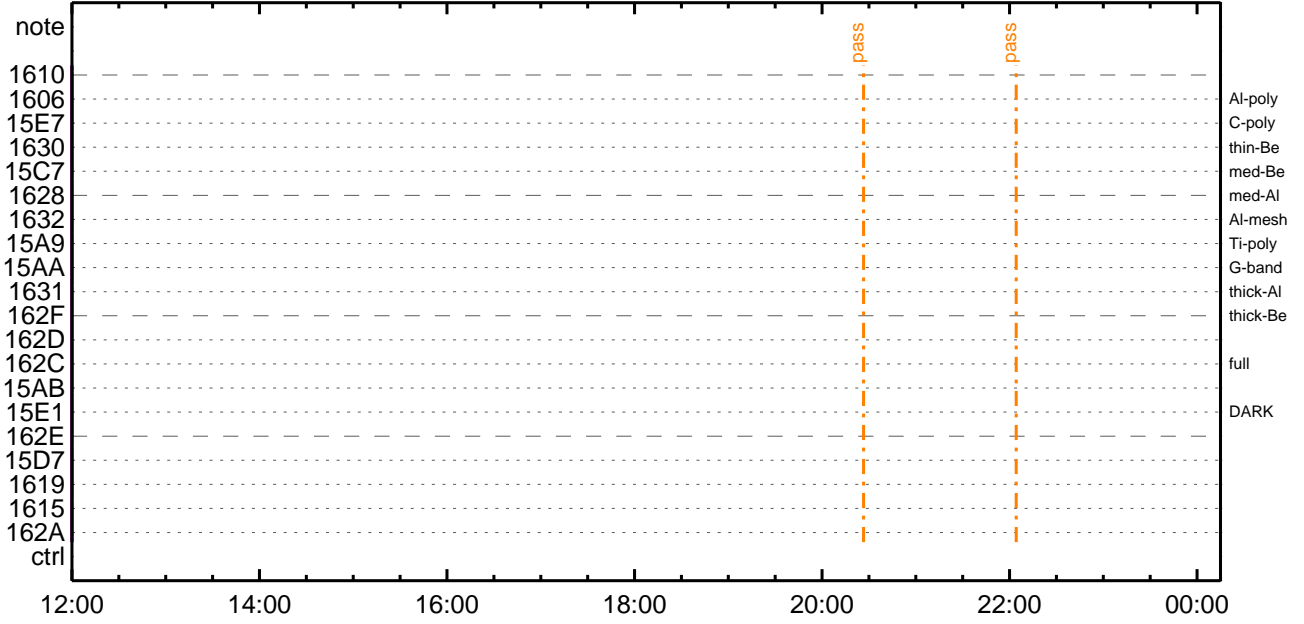
CMDI #0324 2009/01/07



CMDI #0324 2009/01/08



CMDI #0324 2009/01/08




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-327:OP
0104 ( )
0105 S. OG og-327:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYôYx½ª î»ò³ îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGªî½ª¹ç•è² îOKªò³ îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYôYx½ª î»ò³ îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGªî½ª¹ç•è² îOKªò³ îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYôYx½ª î»ò³ îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPªî½ª¹ç•è² îOKªò³ îÇ§
0165 C.
0166 C. ***** òÈ²¼ª î½ª¹ç•è² îOKªò³ îÇ§ *****
0167 C. DHUYã;4YE;ã;½ª¹ç•è² îOKªò³ îÇ§
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADª-Ã÷ç@NGªî½ª¹ç•è² îOKªò³ îÇ§
0180 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0181 C.
0182 C. TIY³YB½ª¹ç•è² îOKªò³ îÇ§ (UT)
0183 +. TI 2009-01-04 09:47:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2009-01-04 09:47:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2009-01-04 09:47:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2009-01-04 09:51:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          ÷÷[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0198 C.
0199 C. °Ê²¼αīÄē%īñαîŷÄŷ$ŷÄŷ-¹àîŭ
0200 C.          ÷÷[HK1_TI_CMD_ENA/DIS]      EQ          ENA
0201 C.          ÷÷[HK1_TI_CMD_NUM]          EQ          4
0202 C.          ÷÷[HK1_NEXT_EXEC_PIM]       EQ          DHU
0203 C.          ÷÷[HK1_NEXT_EXEC_DC]        EQ          0xB3
0204 C.
0205 . C. *****
0206 C. TIîî°êŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          ÷÷[HK1_DMP_TOP_ADRS_1]      EQ          07
0213 C.          ÷÷[HK1_DMP_TOP_ADRS_0]      EQ          2B
0214 C.          ÷÷[HK1_DMP_BLOCK_NUM]        EQ          3
0215 C.          ÷÷[HK1_DMP_REPEAT_NUM]       EQ          0
0216 C.          ÷÷[HK1_DMA_DMP_PIM]         EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          ÷÷[HK1_PKT_FORM_NO]          EQ          7
0220 C.          ÷÷[HK1_PKT_GEN_TIME]         EQ          0.25 s
0221 C.          ÷÷[HK1_S_TLM_BIT_RATE]      EQ          32k
0222 C.          ÷÷[HK1_X_TLM_BIT_RATE]      EQ          4M
0223 C.          ÷÷[HK1_DMP_CHK_FLG]         EQ          EXEC
0224 C.
0225 . C. ŷÄŷÖŷ×½ªî»ò³îÇ§
0226 C.          ÷÷[HK1_DMP_CHK_FLG]         EQ          NON
0227 C.
0228 . C. RAM ID=TI_TBLαîî¾Ê¹ç•ê²îOKαò³îÇ§
0229 C.
0230 . C. DHUŷâ;¼ŷÉ;Ê¼ŷ¼. ŷî;¼ŷË;Ëαòîäα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          ÷÷[HK1_PKT_FORM_NO]          EQ          2
0234 C.          ÷÷[HK1_PKT_GEN_TIME]         EQ          0.5S
0235 C.          ÷÷[HK1_S_TLM_BIT_RATE]      EQ          32K
0236 C.          ÷÷[HK1_X_TLM_BIT_RATE]      EQ          4M
0237 C.
0238 . C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2009-01-04 09:51:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2009-01-04 09:51:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 . C.          [ ] [HK1_TI_CMD_NUM]      EQ          2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C. *****
0255 C. SOT TI command set
0256 C. *****
0257 C. Execute, after the success of OP upload.
0258 +. TI 2009-01-04 09:51:16.0
0259 DC 07-F0 MDP_SOT_MODE_STBY
0260 BC      (41)
0261 . C. -----
0262 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0263 C. -----
0264 C. ***** SOT END *****
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2009-01-04 09:51:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 . C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 . C. ***** MDP `úÃîαī»ö¼ŷαËÄα¹αêDCBC•x²è *****
0276 C. (¼á°îŷÖŷÄŷËŷŷŷŷŷÄŷŷçŷèÈ¼αα¼Ä»Ūα¹αè)
0277 . S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 . C. ***** ŷĐŷ¹•İ Daily±çîñαË´Øα¹αêDCBC•x²è *****
0282 . S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 . C. ;ãLOSŷÄŷ$ŷÄŷ-¼Ä»Ū;ã
0287 C.
0288 . C. ***** LOS *****
0289 C.
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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-261:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Resume FG table (auto mode) >
0120 +. DC 07-F0 MDP_FG_CTRL_AUTO
0121 BC (50)
0122 . C. -----
0123 C. MDP_FG_CTRL_MODE = AUTO [ ]
0124 C. -----
0125 C.
0126 C. *****
0127 C. SOT TI command set
0128 C. *****
0129 C. Execute, after the success of TBL upload.
0130 +. TI 2009-01-04 09:51:18.0
0131 DC 07-F0 MDP_SOT_MODE_OBSV
0132 BC (40)
0133 . C. -----
0134 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0135 C. -----
0136 C.
0137 C. Only when FG_CTRL_AUTO is used in RT.
0138 +. TI 2009-01-04 09:51:20.0
0139 DC 07-F0 MDP_FG_CTRL_AUTO
0140 BC (50)
0141 . C. -----
0142 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0143 C. -----
0144 C. ***** SOT END *****
0145 C.
0146 C. ***** XRT START *****
0147 C.
0148 +. DC 07-F0 MDP_XRT_CTRL_MANU
0149 BC (c1)
0150 + DC 07-F0 MDP_XRT_MODE_STBY
0151 BC (c3)
0152 . C. ----- Success Verify ? OK / NG____
0153 C.
0154 C. XRT Obs. Table Upload
0155 . S. RAM ram-291:MDP_OBS_X
0156 ( )
0157 C.
0158 +. DC 07-F0 MDP_DUMP_XRTTBL
0159 BC (84 07 00 00 00 3a d4)
0160 . C. ----- Comparison Check ? OK / ERR ____
0161 C.
0162 C.
0163 +. DC 07-F0 MDP_XRT_ROI_SET
0164 BC (cd 01 b1 b1 04 04)
0165 + DC 07-F0 MDP_XRT_ROI_SET
0166 BC (cd 02 b1 b1 08 08)
0167 + DC 07-F0 MDP_XRT_ROI_SET
0168 BC (cd 03 b1 b1 08 08)
0169 + DC 07-F0 MDP_XRT_ROI_SET
0170 BC (cd 04 b1 b1 06 06)
0171 + DC 07-F0 MDP_XRT_ROI_SET
0172 BC (cd 06 80 80 20 20)
0173 + DC 07-F0 MDP_XRT_ROI_SET
0174 BC (cd 07 80 80 06 06)
0175 + DC 07-F0 MDP_XRT_ROI_SET
0176 BC (cd 0f 80 80 04 04)
0177 + DC 07-F0 MDP_XRT_ROI_SET
0178 BC (cd 10 80 80 10 10)
0179 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0180 BC (c4 09)
0181 + DC 07-F0 MDP_XRT_ARS_DIS
0182 BC (d5)
0183 + DC 07-F0 MDP_XRT_FLD_DIS
0184 BC (d9)
0185 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0186 BC (c9)
0187 +. DC 07-F0 MDP_XRT_CTRL_AUTO
0188 BC (c0)
0189 . C. ----- Success Verify ? OK / NG ____
0190 C.
0191 C.
0192 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0193 C.

```

0194 +. DC 07-F0 MDP_XRT_MODE_OBSV
0195 BC (c2)
0196 +. TI 2009-01-04 09:51:02.0
0197 DC 07-F0 MDP_XRT_MODE_OBSV
0198 BC (c2)
0199 +. TI 2009-01-04 09:51:04.0
0200 DC 07-F0 MDP_XRT_CTRL_AUTO
0201 BC (c0)
0202 . C. ----- Success Verify ? OK / NG _____
0203 . C.
0204 . C. ***** XRT END *****
0205 . C.
0206 . C. ***** MDP `úÃîñî»ò¼ÝñËÂÐñ¹ñèDCBC•x²è *****
0207 . C. (¼á°îÝÓÝÃÝËÝßÝËÝáÝçÝèñË¼ññ¼Á»Ûñ¹ñè)
0208 . S. DC-BC dcbc-402:DCBC
0209 (MDP_known_event)
0210 . C.
0211 . C.
0212 . C. ***** ¥Ð¥¹•Ĭ Daily±¿İÑñË´Øñ¹ñèDCBC•x²è *****
0213 . S. DC-BC dcbc-153:DCBC
0214 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0215 . C.
0216 . C.
0217 . C. ¡ãLOS¥Á¥\$¥Ã¥¬¼Á»Û¿ä
0218 . C.
0219 . C. ***** LOS *****
0220 . C.

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*** OP Sequence for XRT ***

2009/01/04	10:00:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/01/04	10:02:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2009/01/04	10:18:30.0	XRT_CTRL_MANU_429_OG [0x1ad]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/01/04	10:21:08.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/01/04	10:21:28.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2009/01/04	10:21:30.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/01/04	10:21:32.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/01/04	10:21:34.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/01/04	10:21:36.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/01/04	15:25:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/01/04	15:48:30.0	XRT_Custom_430_OG [0x1ae]							
2009/01/04	15:49:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/01/04	17:01:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/01/04	17:25:00.0	XRT_Custom_430_OG [0x1ae]							
2009/01/04	17:26:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/01/04	17:59:54.0	XRT_CTRL_MANU_429_OG [0x1ad]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/01/04	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2009/01/04	18:02:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/01/04	18:02:52.0	XRT_QT_PROG_SET_402_OG [0x192]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2009/01/04	18:02:54.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/01/04	18:02:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/01/04	18:02:58.0	XRT_ARS_DIS_417_OG [0x1a1]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/01/04	18:03:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/01/04	18:09:54.0	XRT_CTRL_MANU_429_OG [0x1ad]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/01/04	18:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2009/01/04	18:12:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/01/04	18:12:52.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2009/01/04	18:12:54.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/01/04	18:12:56.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/01/04	18:12:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/01/04	18:13:00.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/01/04	18:38:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/01/04	19:02:00.0	XRT_Custom_430_OG [0x1ae]							
2009/01/04	19:03:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/01/04	20:15:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/01/04	20:39:00.0	XRT_Custom_430_OG [0x1ae]							
2009/01/04	20:40:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/01/04	21:53:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/01/04	22:15:30.0	XRT_Custom_430_OG [0x1ae]							
2009/01/04	22:16:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/01/04	23:30:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/01/04	23:47:00.0	XRT_Custom_430_OG [0x1ae]							
2009/01/04	23:48:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/01/05	01:04:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/01/05	01:14:00.0	XRT_Custom_430_OG [0x1ae]							
2009/01/05	01:15:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/01/05	02:30:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/01/05	02:50:00.0	XRT_Custom_430_OG [0x1ae]							
2009/01/05	02:51:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/01/05	03:59:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							

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2009/01/05	04:27:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1						
2009/01/05	04:28:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]										
			MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2009/01/05	05:38:00.0	XRT_CTRL_MANU_435_OG [0x1b3]										
			MDP_XRT_CTRL_MANU	1	07-F0	c1						
2009/01/05	06:05:00.0	XRT_Custom_430_OG [0x1ae]										
2009/01/05	06:06:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]										
			MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2009/01/05	06:14:24.0	XRT_CTRL_MANU_429_OG [0x1ad]										
			MDP_XRT_CTRL_MANU	1	07-F0	c1						
2009/01/05	06:14:30.0	AOCS_Ore-point_Start_2_OG [0x098]										
			AOCU_NM	5	02-76	00	00	00	00	00	00	00
2009/01/05	06:17:02.0	XRT_FOCUS_POSITION_442_OG [0x1ba]										
			XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00			
2009/01/05	06:17:22.0	XRT_QT_PROG_SET_411_OG [0x19b]										
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a					
2009/01/05	06:17:24.0	XRT_FLD_DIS_419_OG [0x1a3]										
			MDP_XRT_FLD_DIS	1	07-F0	d9						
2009/01/05	06:17:26.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]										
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9						
2009/01/05	06:17:28.0	XRT_ARS_DIS_417_OG [0x1a1]										
			MDP_XRT_ARS_DIS	1	07-F0	d5						
2009/01/05	06:17:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]										
			MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2009/01/05	06:24:24.0	XRT_CTRL_MANU_433_OG [0x1b1]										
			MDP_XRT_CTRL_MANU	1	07-F0	c1						
2009/01/05	06:24:30.0	AOCS_Ore-point_Start_3_OG [0x099]										
			AOCU_NM	5	02-76	04	00	00	00	00	00	00
2009/01/05	06:26:02.0	XRT_FOCUS_RECALIBRATE_418_OG [0x1a2]										
			XRT_FOCUS_RECAL	2	07-F8	78	00					
2009/01/05	06:30:02.0	XRT_FOCUS_POSITION_441_OG [0x1b9]										
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00			
2009/01/05	06:30:22.0	XRT_QT_PROG_SET_423_OG [0x1a7]										
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	11					
2009/01/05	06:30:24.0	XRT_AEC_RESET_415_OG [0x19f]										
			MDP_XRT_AEC_RESET	1	07-F0	d0						
2009/01/05	06:30:26.0	XRT_ARS_DIS_422_OG [0x1a6]										
			MDP_XRT_ARS_DIS	1	07-F0	d5						
2009/01/05	06:30:28.0	XRT_FLD_DIS_445_OG [0x1bd]										
			MDP_XRT_FLD_DIS	1	07-F0	d9						
2009/01/05	06:30:30.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]										
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9						
2009/01/05	06:30:32.0	XRT_CTRL_AUTO_403_OG [0x193]										
			MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2009/01/05	07:18:30.5	XRT_CTRL_MANU_435_OG [0x1b3]										
			MDP_XRT_CTRL_MANU	1	07-F0	c1						
2009/01/05	07:42:30.0	XRT_Custom_430_OG [0x1ae]										
2009/01/05	07:43:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]										
			MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2009/01/05	08:58:00.0	XRT_CTRL_MANU_435_OG [0x1b3]										
			MDP_XRT_CTRL_MANU	1	07-F0	c1						
2009/01/05	09:19:00.0	XRT_Custom_430_OG [0x1ae]										
2009/01/05	09:20:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]										
			MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2009/01/05	10:39:30.0	XRT_CTRL_MANU_435_OG [0x1b3]										
			MDP_XRT_CTRL_MANU	1	07-F0	c1						
2009/01/05	10:49:30.0	XRT_Custom_430_OG [0x1ae]										
2009/01/05	10:50:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]										
			MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2009/01/05	14:29:30.0	XRT_CTRL_MANU_435_OG [0x1b3]										
			MDP_XRT_CTRL_MANU	1	07-F0	c1						
2009/01/05	14:34:30.0	XRT_Custom_430_OG [0x1ae]										
2009/01/05	14:35:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]										
			MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2009/01/05	16:01:30.0	XRT_CTRL_MANU_435_OG [0x1b3]										
			MDP_XRT_CTRL_MANU	1	07-F0	c1						
2009/01/05	16:25:00.0	XRT_Custom_430_OG [0x1ae]										
2009/01/05	16:26:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]										
			MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2009/01/05	17:38:00.0	XRT_CTRL_MANU_435_OG [0x1b3]										
			MDP_XRT_CTRL_MANU	1	07-F0	c1						
2009/01/05	18:01:54.0	XRT_CTRL_MANU_429_OG [0x1ad]										
			MDP_XRT_CTRL_MANU	1	07-F0	c1						
2009/01/05	18:02:00.5	AOCS_Ore-point_Start_2_OG [0x098]										
			AOCU_NM	5	02-76	00	00	00	00	00	00	00
2009/01/05	18:04:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]										
			XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00			
2009/01/05	18:04:52.0	XRT_QT_PROG_SET_402_OG [0x192]										
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	05					
2009/01/05	18:04:54.0	XRT_FLD_DIS_419_OG [0x1a3]										
			MDP_XRT_FLD_DIS	1	07-F0	d9						
2009/01/05	18:04:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]										
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9						
2009/01/05	18:04:58.0	XRT_ARS_DIS_417_OG [0x1a1]										
			MDP_XRT_ARS_DIS	1	07-F0	d5						
2009/01/05	18:05:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]										
			MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2009/01/05	18:11:54.0	XRT_CTRL_MANU_433_OG [0x1b1]										
			MDP_XRT_CTRL_MANU	1	07-F0	c1						
2009/01/05	18:12:00.0	AOCS_Ore-point_Start_3_OG [0x099]										
			AOCU_NM	5	02-76	04	00	00	00	00	00	00
2009/01/05	18:13:32.0	XRT_FOCUS_POSITION_441_OG [0x1b9]										

2009/01/05	18:13:52.0	XRT_QT_PROG_SET_423_OG [0x1a7]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	11		
2009/01/05	18:13:54.0	XRT_ARS_DIS_422_OG [0x1a6]		1	07-F0	d5			
2009/01/05	18:13:56.0	XRT_FLD_DIS_445_OG [0x1bd]		1	07-F0	d9			
2009/01/05	18:13:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]		1	07-F0	c9			
2009/01/05	18:14:00.0	XRT_CTRL_AUTO_403_OG [0x193]		1	07-F0	c0			
2009/01/05	19:15:00.0	XRT_CTRL_MANU_435_OG [0x1b3]		1	07-F0	c1			
2009/01/05	19:38:30.0	XRT_Custom_430_OG [0x1ae]							
2009/01/05	19:39:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]		1	07-F0	c0			
2009/01/05	20:52:30.0	XRT_CTRL_MANU_435_OG [0x1b3]		1	07-F0	c1			
2009/01/05	21:16:00.0	XRT_Custom_430_OG [0x1ae]							
2009/01/05	21:17:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]		1	07-F0	c0			
2009/01/05	22:30:00.0	XRT_CTRL_MANU_435_OG [0x1b3]		1	07-F0	c1			
2009/01/05	22:52:00.0	XRT_Custom_430_OG [0x1ae]							
2009/01/05	22:53:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]		1	07-F0	c0			
2009/01/06	00:07:30.0	XRT_CTRL_MANU_435_OG [0x1b3]		1	07-F0	c1			
2009/01/06	00:17:30.0	XRT_Custom_430_OG [0x1ae]							
2009/01/06	00:18:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]		1	07-F0	c0			
2009/01/06	01:33:30.0	XRT_CTRL_MANU_435_OG [0x1b3]		1	07-F0	c1			
2009/01/06	01:50:30.0	XRT_Custom_430_OG [0x1ae]							
2009/01/06	01:51:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]		1	07-F0	c0			
2009/01/06	03:06:30.0	XRT_CTRL_MANU_435_OG [0x1b3]		1	07-F0	c1			
2009/01/06	03:27:00.0	XRT_Custom_430_OG [0x1ae]							
2009/01/06	03:28:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]		1	07-F0	c0			
2009/01/06	04:35:30.0	XRT_CTRL_MANU_435_OG [0x1b3]		1	07-F0	c1			
2009/01/06	05:04:30.0	XRT_Custom_430_OG [0x1ae]							
2009/01/06	05:05:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]		1	07-F0	c0			
2009/01/06	05:48:24.0	XRT_CTRL_MANU_429_OG [0x1ad]		1	07-F0	c1			
2009/01/06	05:48:30.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00
2009/01/06	05:51:02.0	XRT_FOCUS_POSITION_442_OG [0x1ba]		4	07-F8	22	ff	aa	00
2009/01/06	05:51:22.0	XRT_QT_PROG_SET_411_OG [0x19b]	XRT_FOCUS_POSITION	2	07-F0	c4	0a		
2009/01/06	05:51:24.0	XRT_FLD_DIS_419_OG [0x1a3]		1	07-F0	d9			
2009/01/06	05:51:26.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]		1	07-F0	c9			
2009/01/06	05:51:28.0	XRT_ARS_DIS_417_OG [0x1a1]		1	07-F0	d5			
2009/01/06	05:51:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]		1	07-F0	c0			
2009/01/06	05:58:24.0	XRT_CTRL_MANU_433_OG [0x1b1]		1	07-F0	c1			
2009/01/06	05:58:30.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	04	00	00	00
2009/01/06	06:00:02.0	XRT_FOCUS_POSITION_441_OG [0x1b9]		4	07-F8	22	fe	97	00
2009/01/06	06:00:22.0	XRT_QT_PROG_SET_423_OG [0x1a7]	XRT_FOCUS_POSITION	2	07-F0	c4	11		
2009/01/06	06:00:24.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	1	07-F0	d5			
2009/01/06	06:00:26.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_ARS_DIS	1	07-F0	d9			
2009/01/06	06:00:28.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]	MDP_XRT_FLD_DIS	1	07-F0	c9			
2009/01/06	06:00:30.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c0			
2009/01/06	06:16:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c1			
2009/01/06	06:42:00.0	XRT_Custom_430_OG [0x1ae]							
2009/01/06	06:43:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]		1	07-F0	c0			
2009/01/06	07:56:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c1			
2009/01/06	08:19:30.0	XRT_Custom_430_OG [0x1ae]							
2009/01/06	08:20:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]		1	07-F0	c0			
2009/01/06	09:36:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c1			
2009/01/06	10:12:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00