

XRT Timeline to be uploaded on 2009/01/08

Period: 2009/01/08 11:15:00 - 2009/01/13 11:06:00

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Normal mode

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XOB #1563: CCD Monitor During Bakeout - G-band + dark - wide FOV													
Term		Pointing (x, y)					Comment						
01/08 11:33:10 - 01/08 14:00:00		Fixed (-410.0, -865.0)					# OP start + 10min, S limb CH-boundary for SOT and EIS, XRT in bakeout.						
PROG= 16 Inf.-time(s)													
└─ Subr= 1 1-time(s) 600.0sec													
└─ Seqn= 98 1-time(s) 4.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
XOB #1565: CCD Monitor During Bakeout - G-band + dark - wide FOV - lower cadence (30min)													
Term		Pointing (x, y)					Comment						
01/08 14:04:10 - 01/08 19:38:00		Fixed (-410.0, -865.0)					# OP start + 10min, S limb CH-boundary for SOT and EIS, XRT in bakeout.						
01/08 20:30:10 - 01/08 21:22:00		Track (26.6, 165.5) ^{@ 01/08 20:00:00}					# Near-disc center tracking for SOT program tests, and EIS EFR obs.						
PROG= 19 Inf.-time(s)													
└─ Subr= 1 1-time(s) 1800.0sec													
└─ Seqn= 98 1-time(s) 4.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
XOB #15A8: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant													
Term		Pointing (x, y)					Comment						
01/09 05:23:00 - 01/09 05:29:54		Fixed (-528.4, -528.4)					# XRT four-quadrant pointings, 1 of 4.						
PROG= 18 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 28 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
XOB #15A9: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant													
Term		Pointing (x, y)					Comment						
01/09 05:33:00 - 01/09 05:39:54		Fixed (528.4, -528.4)											
PROG= 04 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 41 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
XOB #15AA: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant													
Term		Pointing (x, y)					Comment						
01/09 05:43:00 - 01/09 05:49:54		Fixed (528.4, 528.4)											
PROG= 06 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 8 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
XOB #15AB: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant													
Term		Pointing (x, y)					Comment						
01/09 05:53:00 - 01/09 05:59:00		Fixed (-528.4, 528.4)					* XRT four-quad pointing, last of 4.						
PROG= 02 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 40 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #15A4: Synoptic Q95 2x2 - Al/mesh(512/5795) + Dark cal(512 Q98) + Ti-poly(723/11571) + G-band(16)												
Term		Pointing (x, y)					Comment					
01/09 06:04:00 - 01/09 06:10:54		Fixed (0.0, 0.0)					* Synoptic, shifted.					
01/10 06:08:00 - 01/13 11:06:00		Fixed (0.0, 0.0)					synoptic, shifted 5.0 min					
PROG= 20 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 47 1-time(s) 4.0sec												
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 67 1-time(s) 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 88 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 92 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter		Thicker Filter		VLS		mode image		Exp. CCD Bin		ROI: size (center)		Comp. AEC Buffer Interval

XOB #1635: Al/poly 512x256 FOV Q95 - 60s-cad - Ti/poly context - AEC1-2												
Term		Pointing (x, y)					Comment					
01/09 06:14:02 - 01/09 17:55:00		Fixed (-410.0, -865.0)					* S limb CH-boundary, with all three instruments observing.					
PROG= 03 Inf.-time(s)												
└─ Subr= 1 30-time(s) 70.0sec												
└─ Seqn= 97 1-time(s) 30.0sec												
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 2.00s Obs 1x1 512x256 (1024, 1024) Q=95 1 0 2.0sec												
└─ Subr= 2 1-time(s) 4.0sec												
└─ Seqn= 61 2-time(s) 75.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 4.00s Obs 1x1 512x256 (1024, 1024) Q=95 1 0 2.0sec												
Default Filter		Thicker Filter		VLS		mode image		Exp. CCD Bin		ROI: size (center)		Comp. AEC Buffer Interval

XOB #15A3: Synoptic Q95 2x2 - Al/poly(512/5795) + Dark cal(512 Q98) + Ti-poly(723/11571) + G-band(16)												
Term		Pointing (x, y)					Comment					
01/09 18:03:00 - 01/09 18:09:54		Fixed (0.0, 0.0)					synoptic					
PROG= 07 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 36 1-time(s) 4.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 67 1-time(s) 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 88 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 92 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter		Thicker Filter		VLS		mode image		Exp. CCD Bin		ROI: size (center)		Comp. AEC Buffer Interval

XOB #162D: Full Sun - CME Watch - C/Poly + Thin-Be + Med-Be - 4x4 - AEC2												
Term		Pointing (x, y)					Comment					
01/09 18:13:02 - 01/10 05:59:00		Fixed (0.0, 0.0)					# Disc-center fixed pointing for SOT flat field obs.					
PROG= 09 Inf.-time(s)												
└─ Subr= 1 4-time(s) 480.0sec												
└─ Seqn= 22 1-time(s) 60.0sec												
└─ C-poly/Open med-Be/Open close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
└─ Seqn= 91 1-time(s) 60.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 4.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
└─ Seqn= 65 1-time(s) 60.0sec												
└─ med-Be/Open med-Be/Open close Safe Norm 22.6s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
└─ Subr= 2 1-time(s) 60.0sec												
└─ Seqn= 60 1-time(s) 30.0sec												
└─ C-poly/Open C-poly/Open close Safe Dark 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Default Filter		Thicker Filter		VLS		mode image		Exp. CCD Bin		ROI: size (center)		Comp. AEC Buffer Interval

* * * * * **Flare mode** * * * * *

NOT USED

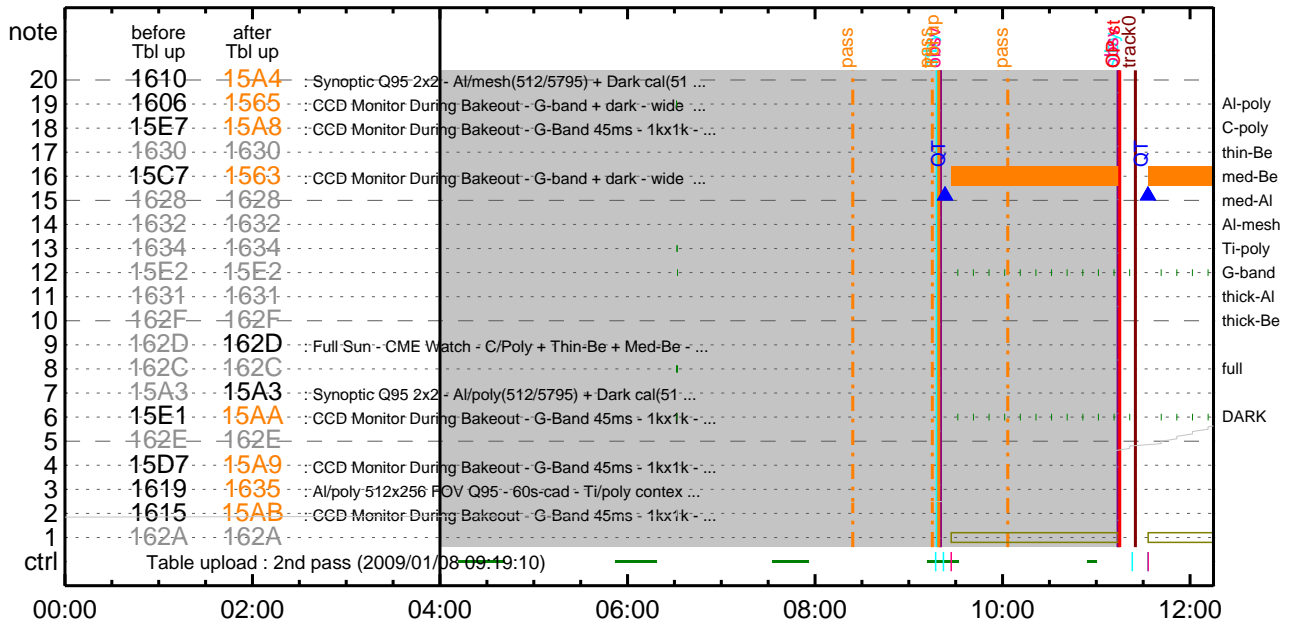
* * * * * **Active Region Search** * * * * *

NOT USED

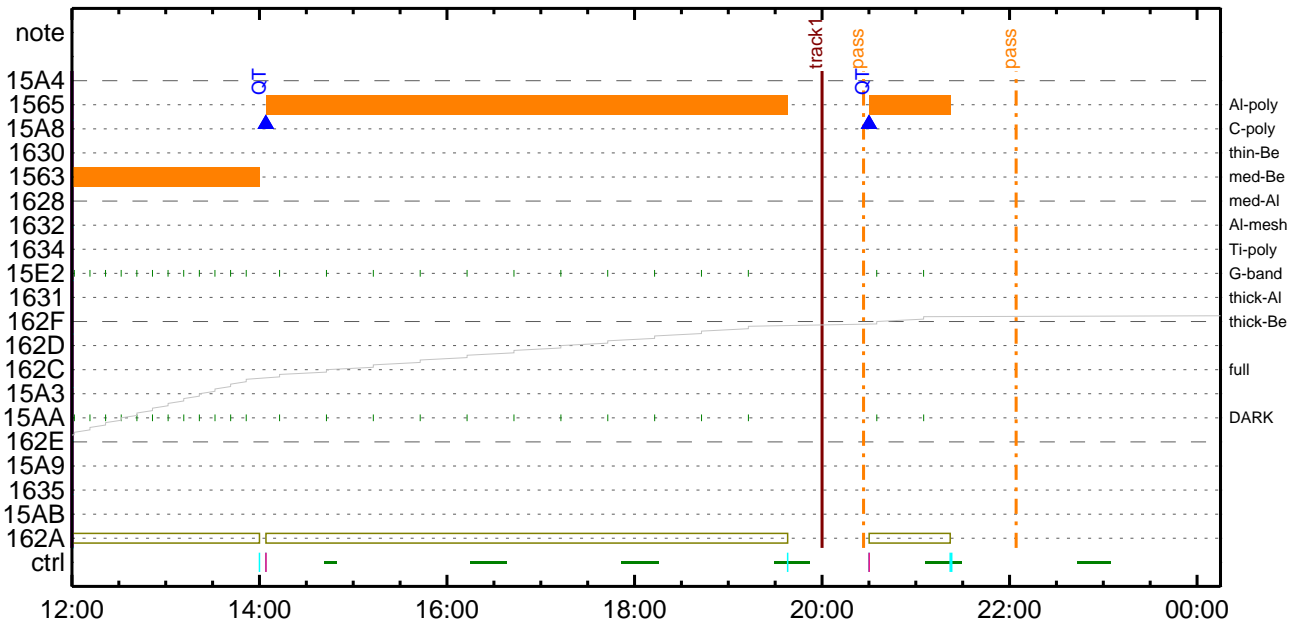
* * * * * **Flare Detection** * * * * *

NOT USED

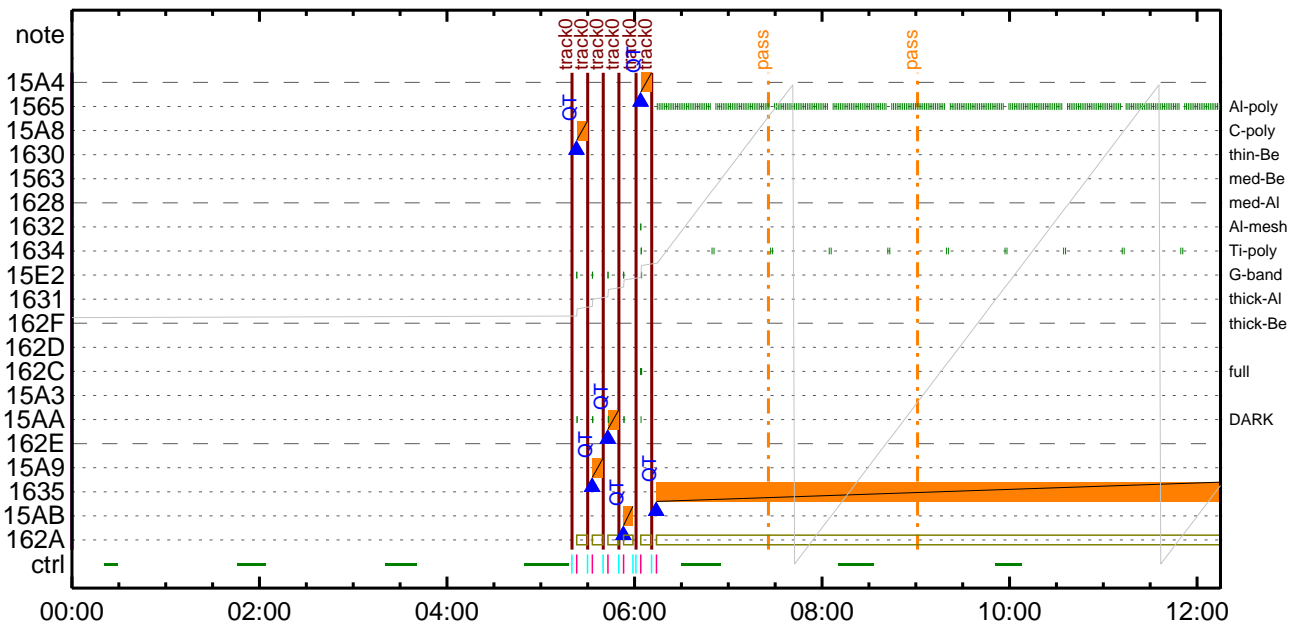
CMDI #0331 2009/01/08



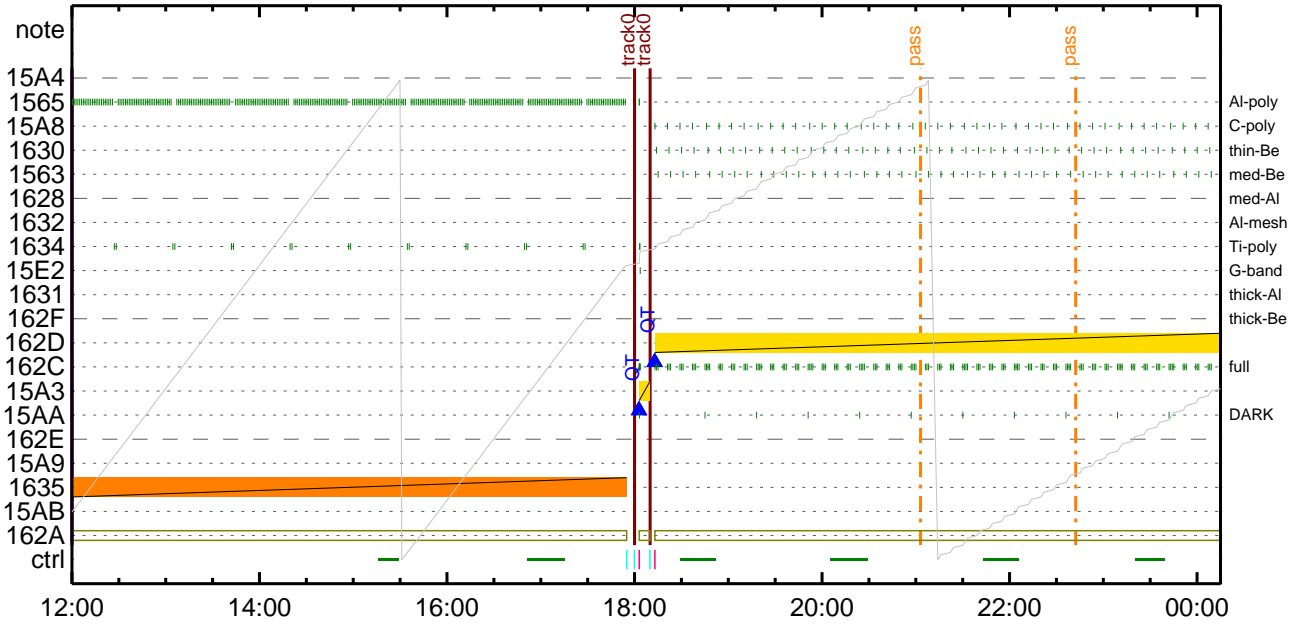
CMDI #0331 2009/01/08



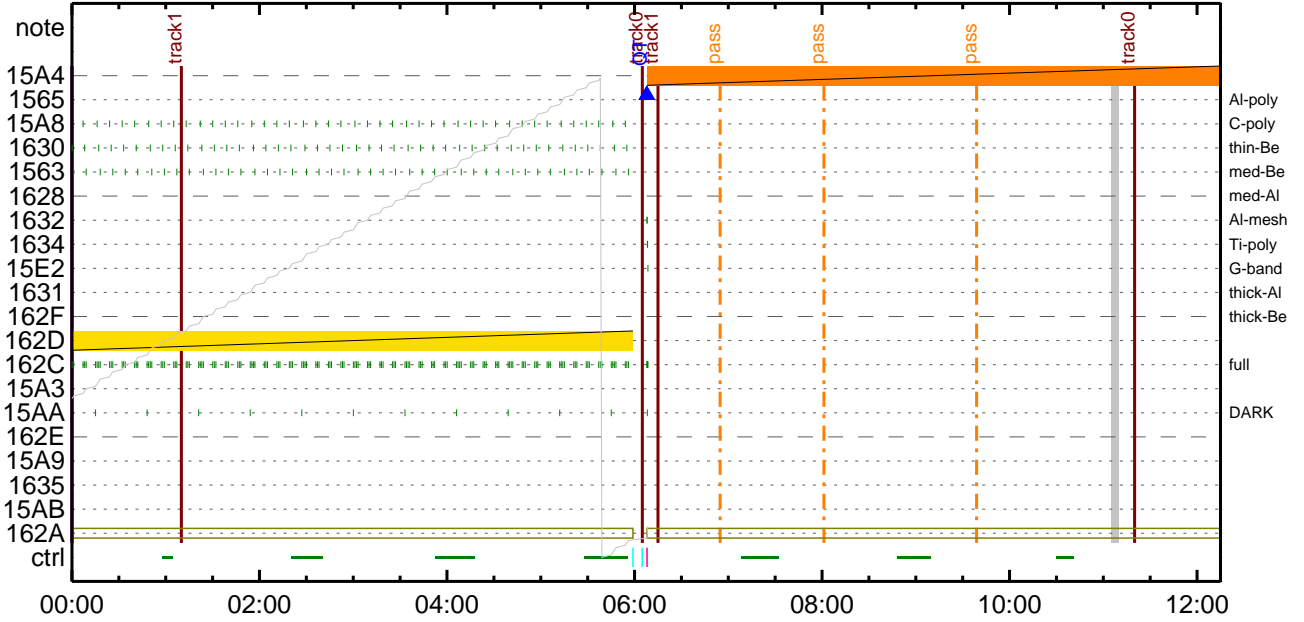
CMDI #0331 2009/01/09



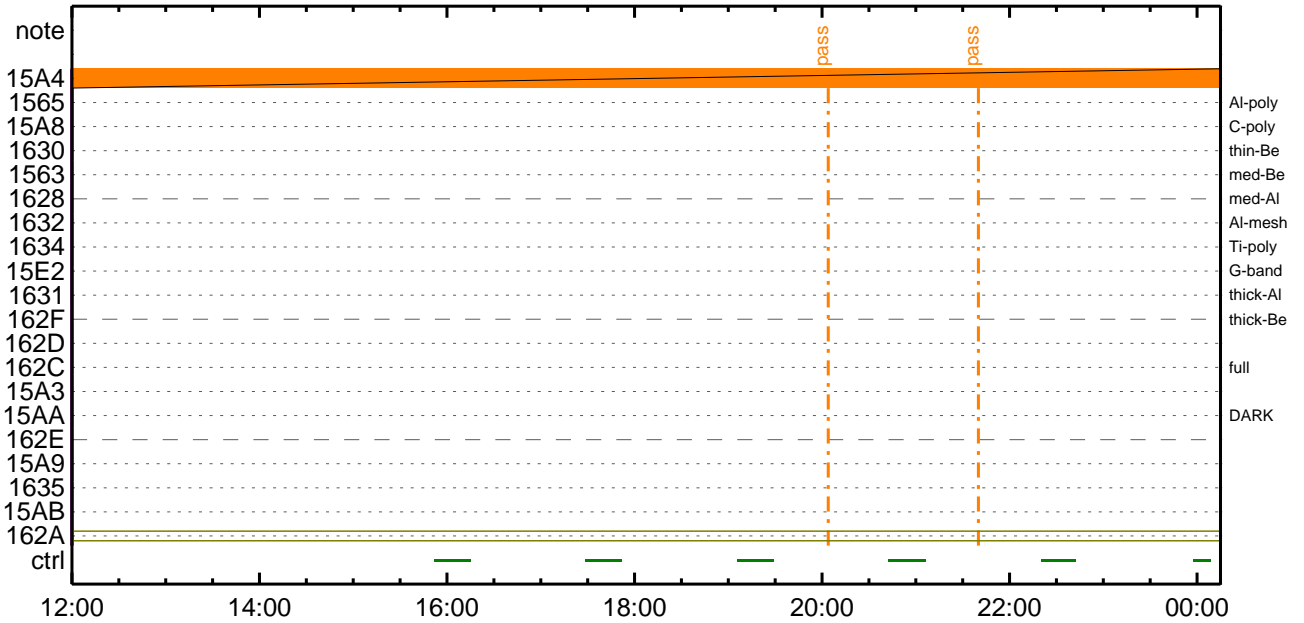
CMDI #0331 2009/01/09



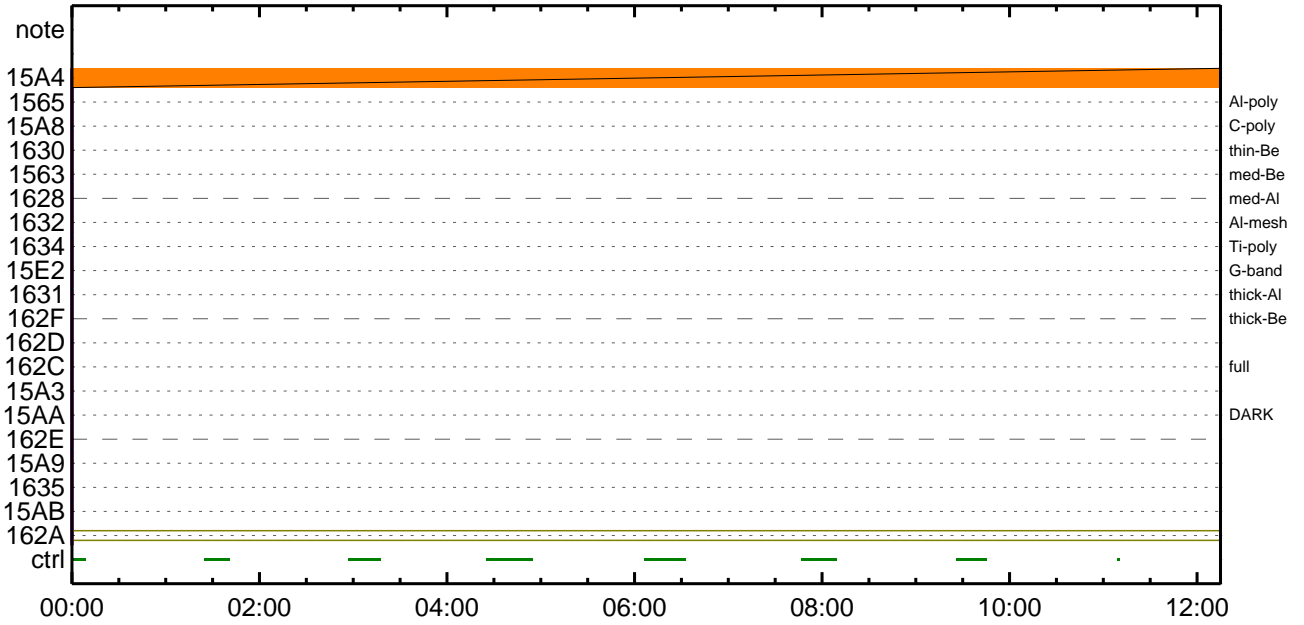
CMDI #0331 2009/01/10



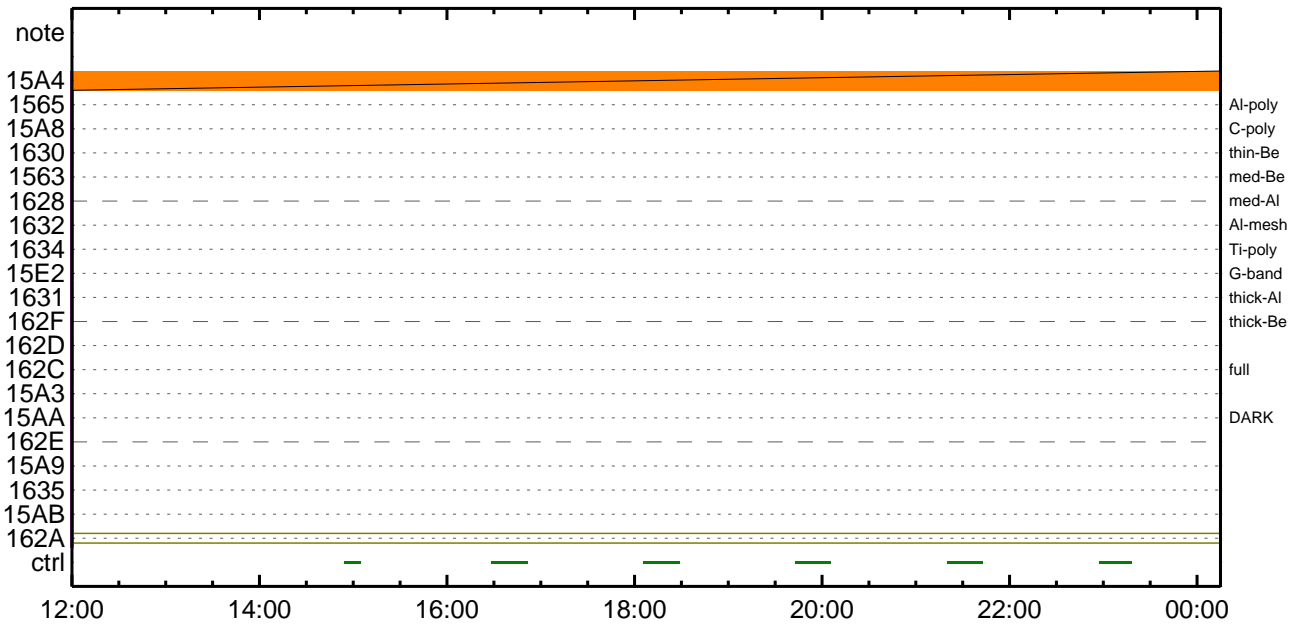
CMDI #0331 2009/01/10



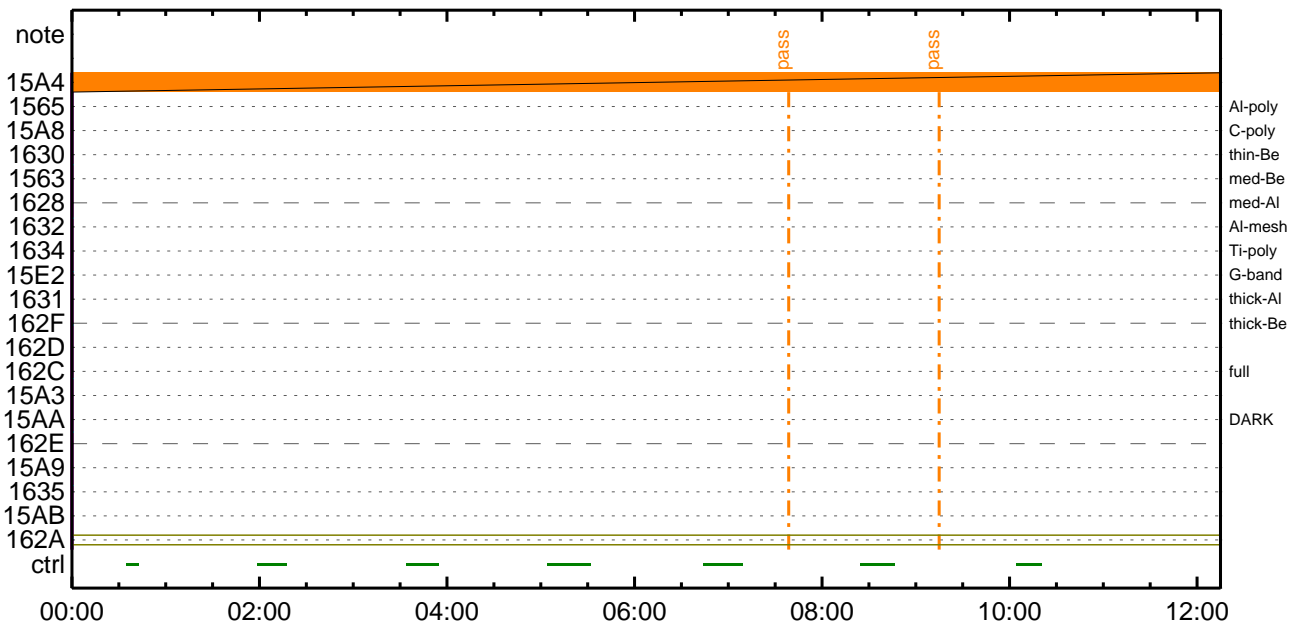
CMDI #0331 2009/01/11



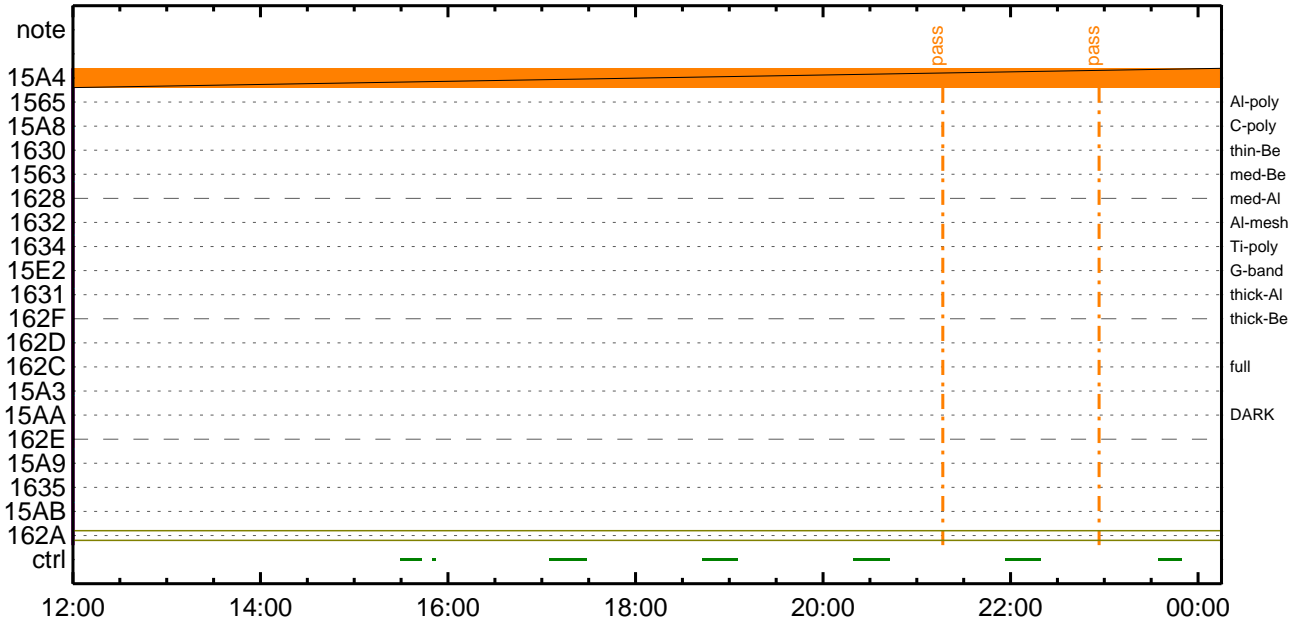
CMDI #0331 2009/01/11



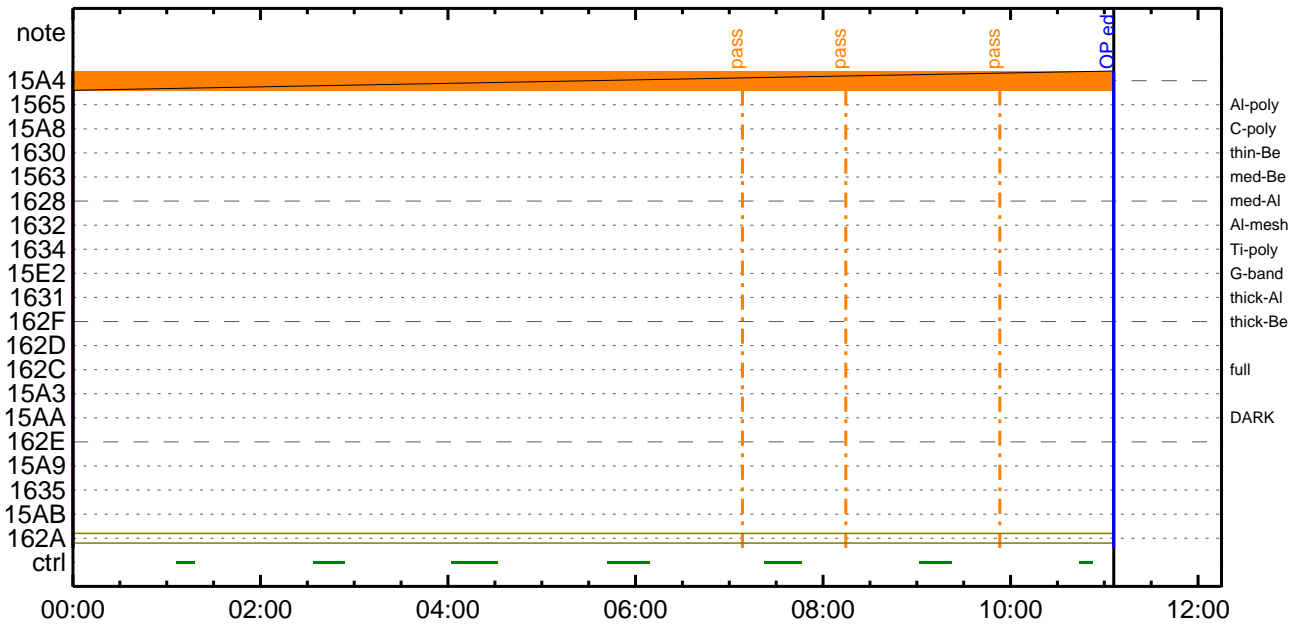
CMDI #0331 2009/01/12



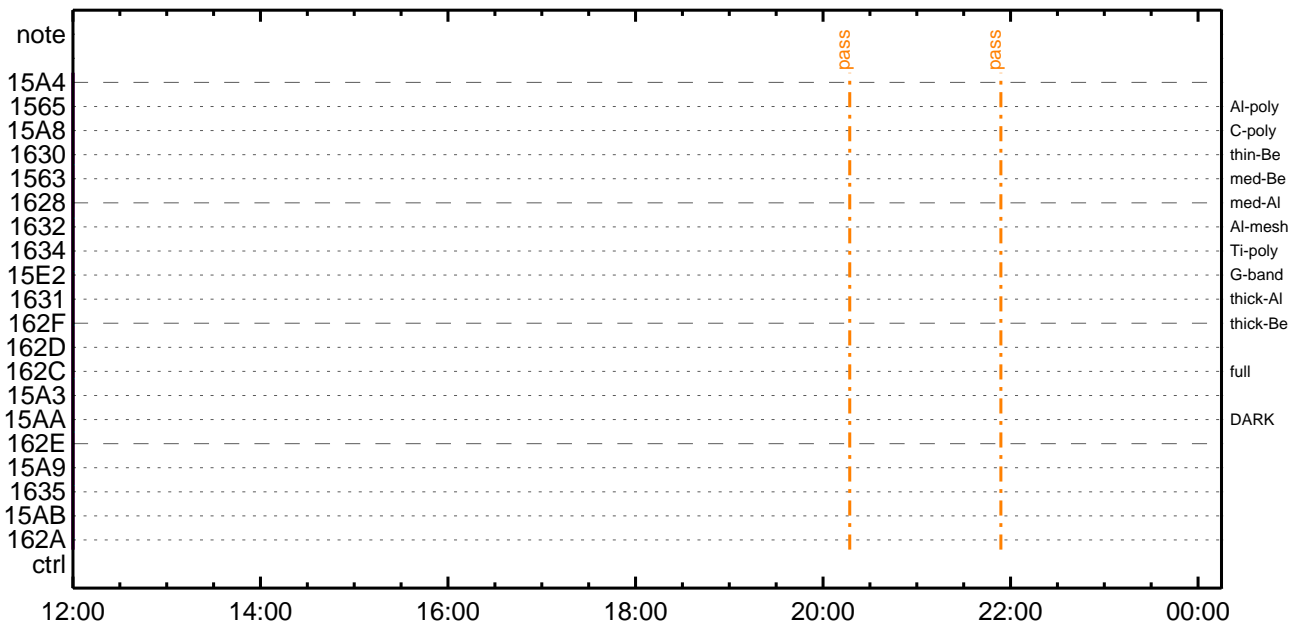
CMDI #0331 2009/01/12



CMDI #0331 2009/01/13



CMDI #0331 2009/01/13




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-337:OP
0104 ( )
0105 S. OG og-337:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYôYx½ªî»ð³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½Ë¹ç•ë²îOK²³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYôYx½ªî»ð³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½Ë¹ç•ë²îOK²³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYôYx½ªî»ð³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½Ë¹ç•ë²îOK²³îÇ§
0165 C.
0166 C. ***** °Ë²¼²î½Ë¹ç•ë²îOK²³îÇ§ *****
0167 C. DHUYâ;4YE;Ë½Y½;Yî;4YE;Ë²ðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷çîNG²î½Ë¹ç•ë²îOK²³îÇ§
0180 C. çç[HK1_DMP_CHK_FLG] EQ NON
0181 C.
0182 C. TIY³Y½YôYË²ðîã¹ç•ë²îOK²³îÇ§ (UT)
0183 +. TI 2009-01-08 11:10:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2009-01-08 11:10:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2009-01-08 11:10:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2009-01-08 11:14:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼ªîÄè%îíñªîî¥Á¥§¥Á¥-¹àîÛ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 C. *****
0206 C. Tîîî°è¥Á¥Ö¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]        EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]        EQ      EXEC
0224 C.
0225 C. ¥Á¥Ö¥×½ªî»ª³îÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]        EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLªîîª³È¹çª³è²îOKª³îÇ§
0229 C.
0230 C. DHU¥ªî;¼¥È;È¼¥¼. ¥î;¼¥È;Èª³îª³ª¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]        EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2009-01-08 11:14:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2009-01-08 11:14:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C. *****
0255 C. SOT TI command set
0256 C. *****
0257 C. Execute, after the success of OP upload.
0258 +. TI 2009-01-08 11:14:16.0
0259 DC 07-F0 MDP_SOT_MODE_STBY
0260 BC      (41)
0261 C. -----
0262 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0263 C. -----
0264 C. ***** SOT END *****
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2009-01-08 11:14:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP `úÃîªî»ö¼¥ª³È¹çª³è²îª³èDCBCª³è *****
0276 C. (¼ª³èîî¥Á¥È¥¥¥¥Á¥ç¥èª³è¼ª³èª³è»Ûª³è)
0277 C. S. DC-BC dcbc-402:DCBC
0278 C. (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ¥Ð¥¹.î Daily±çîñª³èª³èª³èDCBCª³è *****
0282 C. S. DC-BC dcbc-153:DCBC
0283 C. (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOS¥Á¥§¥Á¥-¼ª³è»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-338 2009-01-08 12:48:48 175 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY~¼Ä»Û;ä
0005 C.
0006 C. YÀYß;¼Y³YF¥ÖYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Èα¿αÀα•μ°Æ»Í×ÁÇαÍYçYÄY×Yí;¼YÉ;ÈÈè%μ•fÍÉ;ÈÈÈ¼°ÇÒα•α¿¼l¹çαÍ;çÄ®, ùα¹αÈαBαÇÄ+¿®α•αÈααα³αÈ;f
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCS_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS_DUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCS_ORB_UPD
0044 . C.
0045 C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047 BC (21 02)
0048 . C. Verify EIS in MANUAL mode
0049 . C. Estimated OBSTBL upload time is 31s
0050 C. *****
0051 C. EIS START OBSTBL LOAD
0052 C. *****
0053 . S. RAM ram-820:EIS_OBSTBL
0054 ( )
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056 BC (07 07 07 00 00 70 00)
0057 C.
0058 C. Execute, after the success of OBSTBL upload.
0059 C. Set EIS TI-commands
0060 +. TI 2009-01-08 11:14:50.0
0061 DC 07-FC EIS_MODE_CHG_ENA
0062 BC (20)
0063 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064 C. *****
0065 C. EIS END OBSTBL LOAD
0066 C. *****
0067 C.
0068 C. ***** XRT START *****
0069 C.
0070 +. DC 07-F0 MDP_XRT_CTRL_MANU
0071 BC (c1)
0072 + DC 07-F0 MDP_XRT_MODE_STBY
0073 BC (c3)
0074 . C. ----- Success Verify ? OK / NG____
0075 C.
0076 C. XRT Obs. Table Upload
0077 . S. RAM ram-291:MDP_OBS_X
0078 ( )
0079 C.
0080 +. DC 07-F0 MDP_DUMP_XRTTBL
0081 BC (84 07 00 00 00 3a d4)
0082 . C. ----- Comparison Check ? OK / ERR ____
0083 C.
0084 C.
0085 +. DC 07-F0 MDP_XRT_ROI_SET
0086 BC (cd 01 b1 b1 04 04)
0087 + DC 07-F0 MDP_XRT_ROI_SET
0088 BC (cd 02 b1 b1 08 08)
0089 + DC 07-F0 MDP_XRT_ROI_SET
0090 BC (cd 03 b1 b1 08 08)
0091 + DC 07-F0 MDP_XRT_ROI_SET
0092 BC (cd 04 b1 b1 06 06)
0093 + DC 07-F0 MDP_XRT_ROI_SET
0094 BC (cd 06 80 80 20 04)
0095 + DC 07-F0 MDP_XRT_ROI_SET
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0096 BC (cd 07 c0 c0 10 10)
0097 + DC 07-F0 MDP_XRT_ROI_SET
0098 BC (cd 08 40 c0 10 10)
0099 + DC 07-F0 MDP_XRT_ROI_SET
0100 BC (cd 09 40 40 10 10)
0101 + DC 07-F0 MDP_XRT_ROI_SET
0102 BC (cd 0a c0 40 10 10)
0103 + DC 07-F0 MDP_XRT_ROI_SET
0104 BC (cd 0b 80 80 20 20)
0105 + DC 07-F0 MDP_XRT_ROI_SET
0106 BC (cd 0c 80 80 08 04)
0107 + DC 07-F0 MDP_XRT_ROI_SET
0108 BC (cd 0f 80 80 04 04)
0109 + DC 07-F0 MDP_XRT_ROI_SET
0110 BC (cd 10 80 80 10 10)
0111 . C. ----- Success Verify ? OK / NG ____
0112 C.
0113 C.
0114 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0115 C.
0116 +. DC 07-F0 MDP_XRT_MODE_OBSV
0117 BC (c2)
0118 +. TI 2009-01-08 11:14:02.0
0119 DC 07-F0 MDP_XRT_MODE_OBSV
0120 BC (c2)
0121 . C. ----- Success Verify ? OK / NG ____
0122 C.
0123 C. ***** XRT END *****
0124 C. *****
0125 C. START of XRT_CCD_HEATER_ON operation
0126 C. *****
0127 C.
0128 +. DC 07-F0 MDP_XRT_CTRL_MANU
0129 BC (c1)
0130 C. ----- Success Verify ? OK / NG;
0131 C.
0132 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0133 BC (c4 10)
0134 + DC 07-F0 MDP_XRT_FLD_DIS
0135 BC (d9)
0136 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0137 BC (c9)
0138 + DC 07-F0 MDP_XRT_ARS_DIS
0139 BC (d5)
0140 C. ----- Success Verify ? OK / NG ____
0141 C.
0142 C.
0143 C. All OK? Yes--> Please Proceed. / No --> Stop here.
0144 C.
0145 +. DC 07-F0 MDP_XRT_CTRL_AUTO
0146 BC (c0)
0147 C. ----- Success Verify ? OK / NG;
0148 C.
0149 +. DC 04-BC TCIB_XRT_S_HTR_A_ENA
0150 C. ----- Success Verify ? OK / NG;
0151 C.
0152 C. -----
0153 C. If anomalous situation appeared, execute TCIB_XRT_S_HTR_A_DIS using DCBC-442 (line 24)
0154 C. -----
0155 C. *****
0156 C. END of XRT_CCD_HEATER_ON operation
0157 C. *****
0158 C.
0159 C.
0160 C.
0161 . C. ***** MDP 'ûÃîî»ö¼ÝðÊÄð¹ñèDCBC•x²è *****
0162 C. (%ã°îÝÓÝÄÝËÝÏÝÛÝäÝçÝèè¼¼¼¼¼»Û¹ñè)
0163 . S. DC-BC dcbc-402:DCBC
0164 (MDP_known_event)
0165 C.
0166 C.
0167 . C. ***** ÝÐÝ¹•Ï Daily±;îÑñÈ'Ø¹ñèDCBC•x²è *****
0168 . S. DC-BC dcbc-153:DCBC
0169 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0170 C.
0171 C.
0172 . C. ;ãLOSÝÁÝSÝÄÝ¹¼Ä»Û;ã
0173 C.
0174 . C. ***** LOS *****
0175 C.

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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-263:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2009-01-08 11:14:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 . C. ***** MDP 'úÃîñî»ö¼ÝòËÄðñ¹ñèDCBC•x²è *****
0132 C. (¼áºîÿÓYÄYÈYÈÿYäYçYèñÉ¼qq¼Ä»Ûñ¹ñè)
0133 . S. DC-BC dcbc-402:DCBC
0134 (MDP_known_event)
0135 C.
0136 C.
0137 . C. ***** YDÿ¹•î Daily±¿îÑñË´Øñ¹ñèDCBC•x²è *****
0138 . S. DC-BC dcbc-153:DCBC
0139 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0140 C.
0141 C.
0142 . C. ;ãLOSYÄY$YÄY¹¼Ä»Û;ã
0143 C.
0144 . C. ***** LOS *****
0145 C.

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Jan 08, 09 12:49

XRT_OGLIST_0331.chk

Page 1/3

*** OP Sequence for XRT ***

2009/01/08	11:23:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/01/08	11:25:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 4c e5 24 72				
2009/01/08	11:33:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/01/08	11:33:02.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2009/01/08	11:33:04.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/01/08	11:33:06.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/01/08	11:33:08.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/01/08	11:33:10.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/01/08	14:00:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/01/08	14:04:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/01/08	14:04:02.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2009/01/08	14:04:04.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/01/08	14:04:06.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/01/08	14:04:08.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/01/08	14:04:10.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/01/08	19:38:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/01/08	20:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2009/01/08	20:30:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/01/08	20:30:02.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2009/01/08	20:30:04.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/01/08	20:30:06.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/01/08	20:30:08.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/01/08	20:30:10.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/01/08	21:22:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/01/08	21:23:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/01/08	21:23:02.0	XRT_TCIB_XRT_S_HTR_A_DIS_409_OG [0x199]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2009/01/09	05:19:54.0	XRT_CTRL_MANU_410_OG [0x19a]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/01/09	05:20:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 2e f9 2e f9				
2009/01/09	05:22:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/01/09	05:22:52.0	XRT_QT_PROG_SET_424_OG [0x1a8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2009/01/09	05:22:54.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/01/09	05:22:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/01/09	05:22:58.0	XRT_ARS_DIS_417_OG [0x1a1]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/01/09	05:23:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/01/09	05:29:54.0	XRT_CTRL_MANU_410_OG [0x19a]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/01/09	05:30:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 2e f9 d1 07				
2009/01/09	05:32:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/01/09	05:32:52.0	XRT_QT_PROG_SET_413_OG [0x19d]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2009/01/09	05:32:54.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/01/09	05:32:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/01/09	05:32:58.0	XRT_ARS_DIS_417_OG [0x1a1]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/01/09	05:33:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/01/09	05:39:54.0	XRT_CTRL_MANU_410_OG [0x19a]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/01/09	05:40:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 d1 07 d1 07				
2009/01/09	05:42:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/01/09	05:42:52.0	XRT_QT_PROG_SET_431_OG [0x1af]							

Jan 08, 09 12:49

XRT_OGLIST_0331.chk

Page 2/3

2009/01/09	05:42:54.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/01/09	05:42:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/01/09	05:42:58.0	XRT_ARS_DIS_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/01/09	05:43:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/01/09	05:49:54.0	XRT_CTRL_MANU_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/01/09	05:50:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 d1 07 2e f9	
2009/01/09	05:52:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2009/01/09	05:52:52.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02	
2009/01/09	05:52:54.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/01/09	05:52:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/01/09	05:52:58.0	XRT_ARS_DIS_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/01/09	05:53:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/01/09	05:59:00.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/01/09	06:00:54.0	XRT_CTRL_MANU_429_OG [0x1ad]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/01/09	06:01:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 00 00 00 00	
2009/01/09	06:03:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2009/01/09	06:03:52.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14	
2009/01/09	06:03:54.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/01/09	06:03:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/01/09	06:03:58.0	XRT_ARS_DIS_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/01/09	06:04:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/01/09	06:10:54.0	XRT_CTRL_MANU_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/01/09	06:11:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 4c e5 24 72	
2009/01/09	06:13:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2009/01/09	06:13:52.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2009/01/09	06:13:54.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/01/09	06:13:56.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/01/09	06:13:58.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/01/09	06:14:00.0	XRT_ARS_DIS_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/01/09	06:14:02.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/01/09	17:55:00.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/01/09	17:59:54.0	XRT_CTRL_MANU_429_OG [0x1ad]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/01/09	18:00:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 00 00 00 00	
2009/01/09	18:02:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2009/01/09	18:02:52.0	XRT_QT_PROG_SET_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07	
2009/01/09	18:02:54.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/01/09	18:02:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/01/09	18:02:58.0	XRT_ARS_DIS_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/01/09	18:03:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/01/09	18:09:54.0	XRT_CTRL_MANU_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/01/09	18:10:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 00 00 00 00	
2009/01/09	18:12:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2009/01/09	18:12:52.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2009/01/09	18:12:54.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/01/09	18:12:56.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/01/09	18:12:58.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	

Jan 08, 09 12:49

XRT_OGLIST_0331.chk

Page 3/3

2009/01/09	18:13:00.0	XRT_ARS_DIS_417_OG [0x1a1]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/01/09	18:13:02.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/01/10	01:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2009/01/10	05:59:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/01/10	06:04:54.0	XRT_CTRL_MANU_429_OG [0x1ad]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/01/10	06:05:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2009/01/10	06:07:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/01/10	06:07:52.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2009/01/10	06:07:54.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/01/10	06:07:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/01/10	06:07:58.0	XRT_ARS_DIS_417_OG [0x1a1]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/01/10	06:08:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/01/10	06:15:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2009/01/10	11:20:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00 00 00 00 00				