XRT Timeline to be uploaded on 2009/02/05

Period: 2009/02/05 10:41:00 - 2009/02/10 10:31:00

* * * * * **Normal mode** * * * * *

XOB #1648: Waves-Al/poly 384FOV - 20s cadence with Ti-poly context+G-band context

Term	Pointing (x, y)	Comment
02/05 10:53:00 - 02/05 15:00:00	Fixed (-945.0, 0.0)	# OP start + 10min, EIS off-limb wave search.

PROG= 04 Inf.-time(s)

Subr= 1 3-time(s) 2.0sec

Seqn= 51 60-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	20.0sec

Seqn= 75 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
C-poly/Open	thin-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1024, 1024)	DPCM	1	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 39 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	1536x1536 (1024, 1024)	Q=90	0	0	2.0sec

XOB #15A3: Synoptic Q95 2x2 - Al/poly(512/5795) + Dark cal(512 Q98) + Ti-poly(723/11571) + G-band(16)

Term	Pointing (x, y)	Comment
02/05 17:59:30 - 02/05 18:07:24	Fixed (0.0, 0.0)	synoptic, shifted -2.5 min
02/06 18:19:00 - 02/06 18:26:54	Fixed (0.0, 0.0)	synoptic, shifted 17.0 min

PROG= 07 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 36 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 6 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 88 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 92 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #15A4: Synoptic Q95 2x2 - Al/mesh(512/5795) + Dark cal(512 Q98) + Ti-poly(723/11571) + G-band(16)

Term	Pointing (x, y)	Comment
02/06 06:32:30 - 02/06 06:40:24	Fixed (0.0, 0.0)	synoptic, shifted 30.5 min
02/07 06:05:00 - 02/07 06:12:54	Fixed (0.0, 0.0)	synoptic, shifted 3.0 min

PROG= 11 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 47 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 6 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 88 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 92 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1454: CH Jets - Al/poly - 32s exp - FOV512

Term	Pointing (x, y)	Comment
02/06 18:29:00 - 02/06 22:45:30	Fixed (0.0, -965.0)	# XRT polar jet studies, S pole.

PROG= 16 Inf.-time(s)

Subr= 1 1-time(s) 32.0sec

Seqn= 19 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	22.6s	Obs	1x1	512x512 (1024, 1024)	DPCM	0	0	2.0sec

XOB #1649: CH Jets - Al/poly - 32s exp - FOV384

Term	Pointing (x, y)	Comment
02/06 23:12:06 - 02/07 01:47:00	Fixed (0.0, -965.0)	# XRT polar jet studies, S pole.

PROG= 08 Inf.-time(s)

Subr= 1 1-time(s) 32.0sec

Seqn= 17 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	22.6s	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec

* * * * * **Flare mode** * * * * *

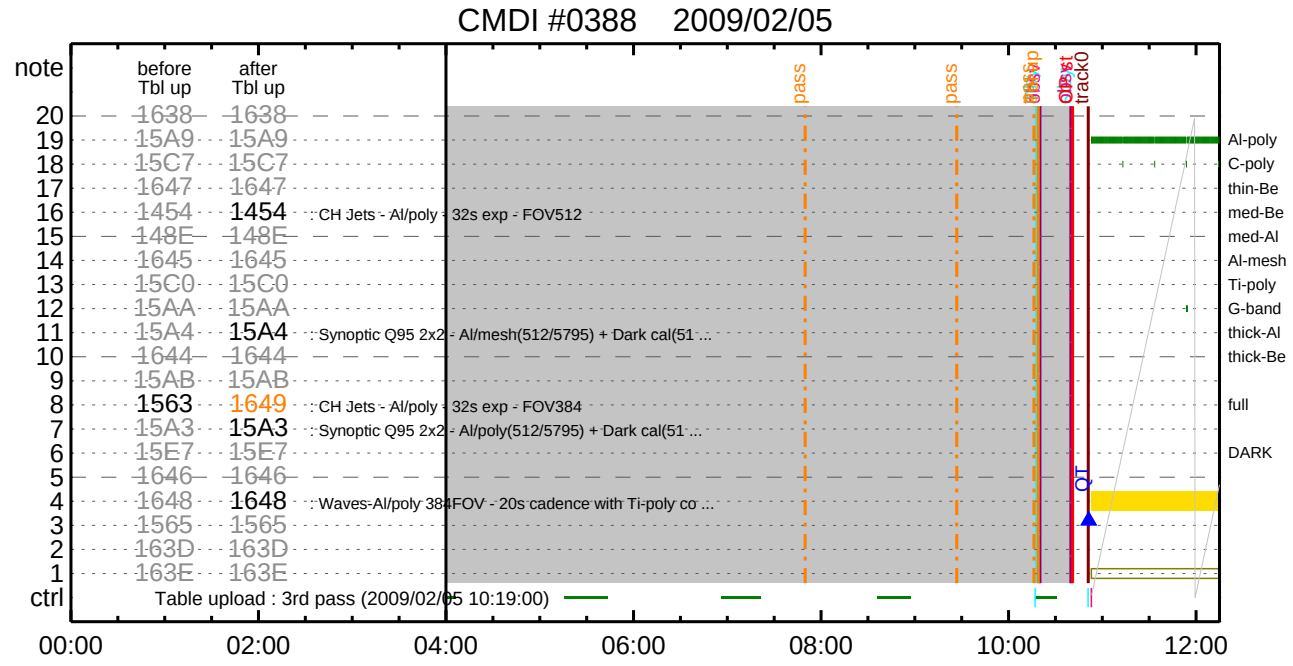
NOT USED

* * * * * **Active Region Search** * * * * *

NOT USED

* * * * * **Flare Detection** * * * * *

NOT USED


CMDI #0388 2009/02/05
note
before Tbl up
after Tbl up
20 1638 1638
19 15A9 15A9
18 15C7 15C7
17 1647 1647
16 1454 1454 : CH Jets - Al/poly - 32s exp - FOV512
15 148E 148E
14 1645 1645
13 15C0 15C0
12 15AA 15AA
11 15A4 15A4 : Synoptic Q95 2x2 - Al/mesh(512/5795) + Dark cal(51 ...
10 1644 1644
9 15AB 15AB
8 1563 1649 : CH Jets - Al/poly - 32s exp - FOV384
7 15A3 15A3 : Synoptic Q95 2x2 - Al/poly(512/5795) + Dark cal(51 ...
6 15E7 15E7
5 1646 1646
4 1648 1648 : Waves-Al/poly 384FOV - 20s cadence with Ti-poly co ...
3 1565 1565
2 163D 163D
1 163E 163E
ctrl Table upload : 3rd pass (2009/02/05 10:19:00)
pass
pass
track0
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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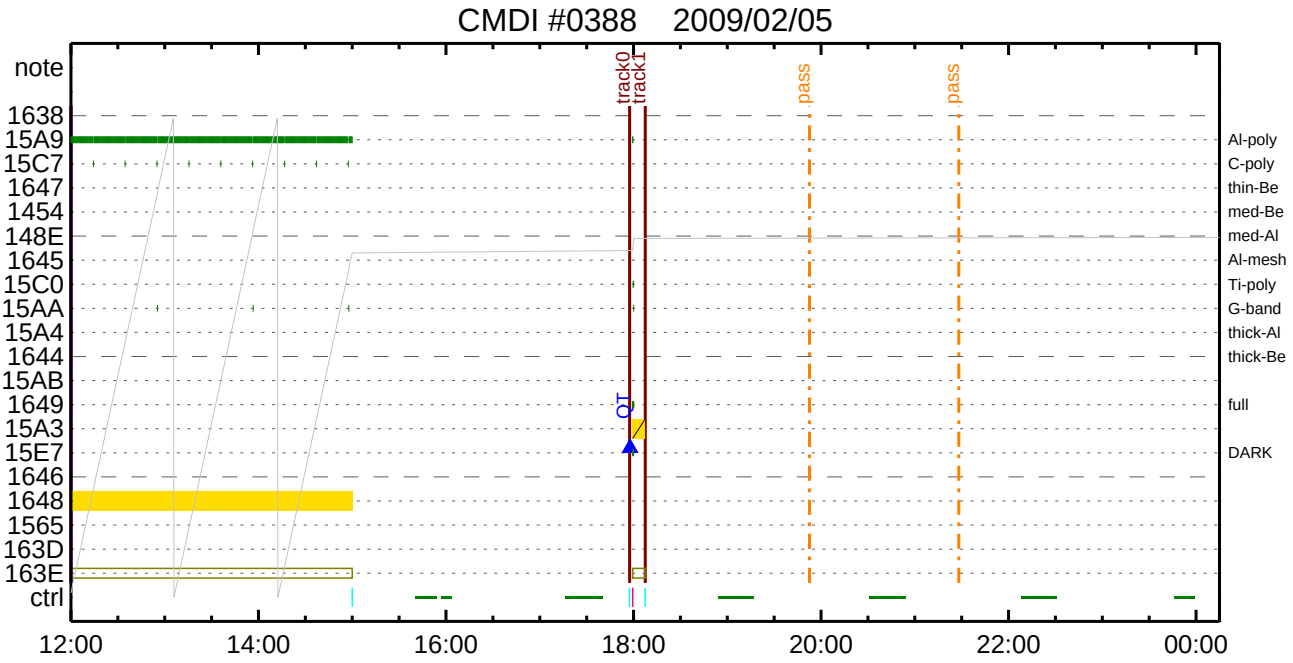
CMDI #0388 2009/02/05
note
1638
15A9
15C7
1647
1454
148E
1645
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15AA
15A4
1644
15AB
1649
15A3
15E7
1646
1648
1565
163D
163E
ctrl
track0
track1
pass
pass
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
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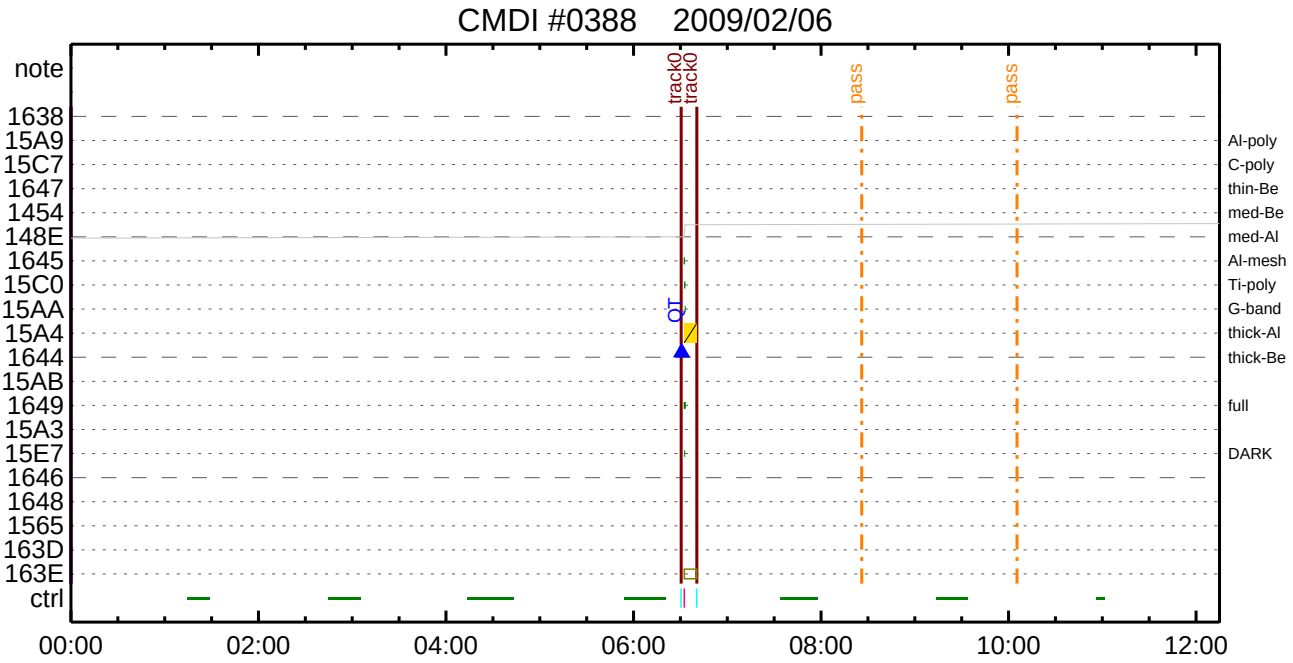
CMDI #0388 2009/02/06
note
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163D
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ctrl
track0
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pass
pass
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
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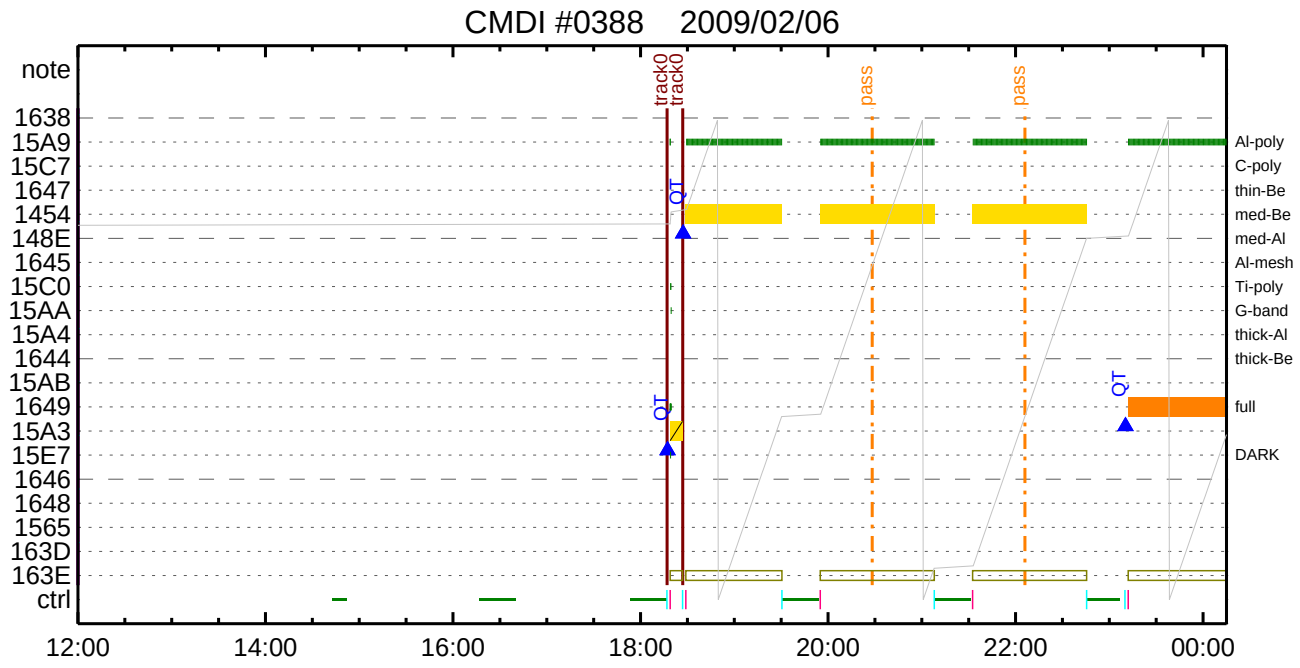
CMDI #0388 2009/02/06
note
1638
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C-poly
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med-Be
med-Al
Al-mesh
Ti-poly
G-band
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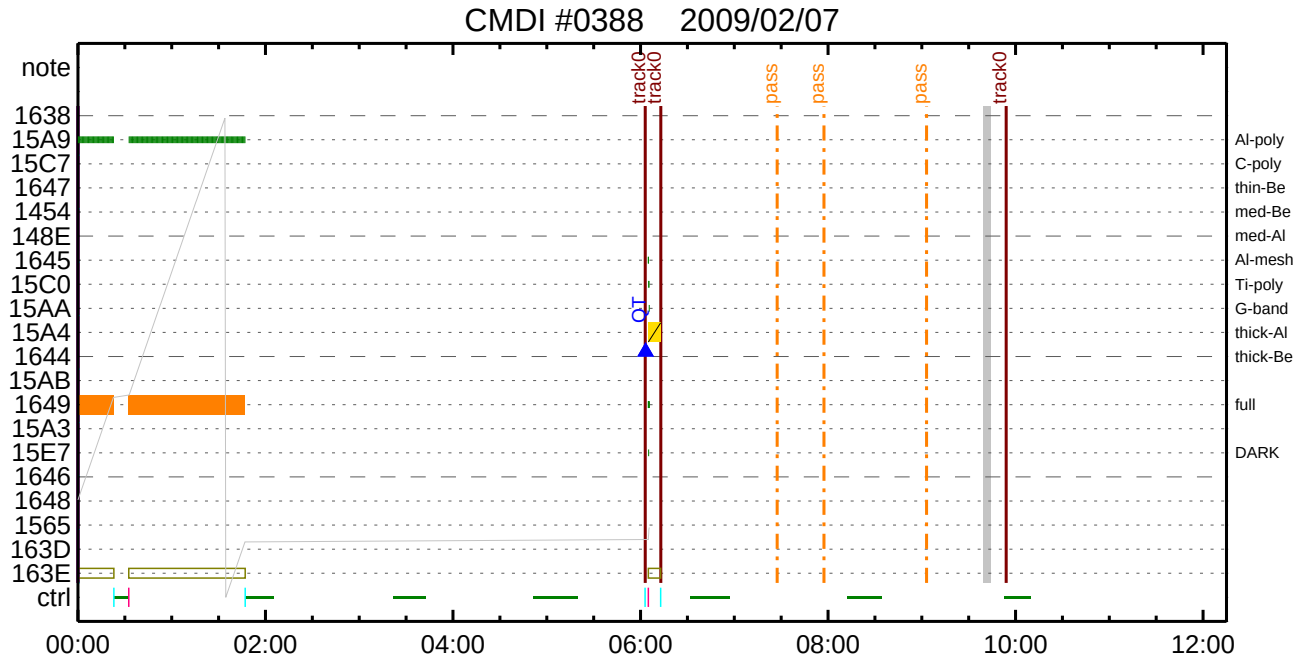
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148E
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Al-mesh
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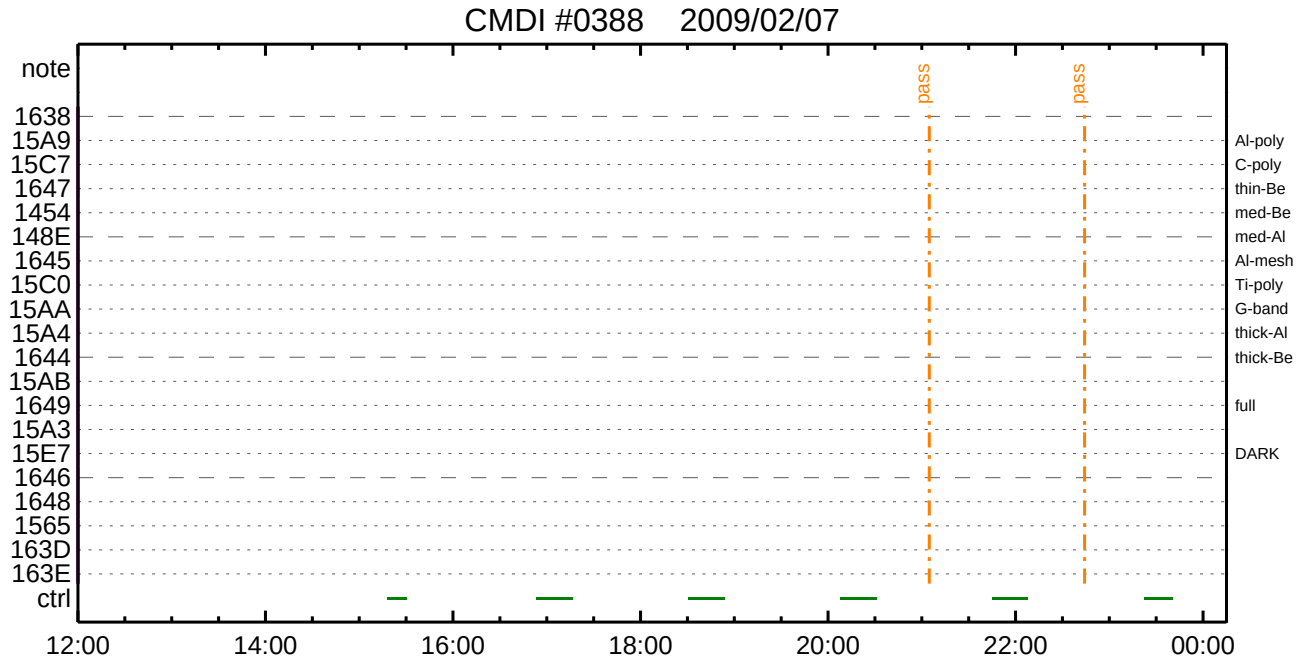
CMDI #0388 2009/02/07
note
1638
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pass
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med-Be
med-Al
Al-mesh
Ti-poly
G-band
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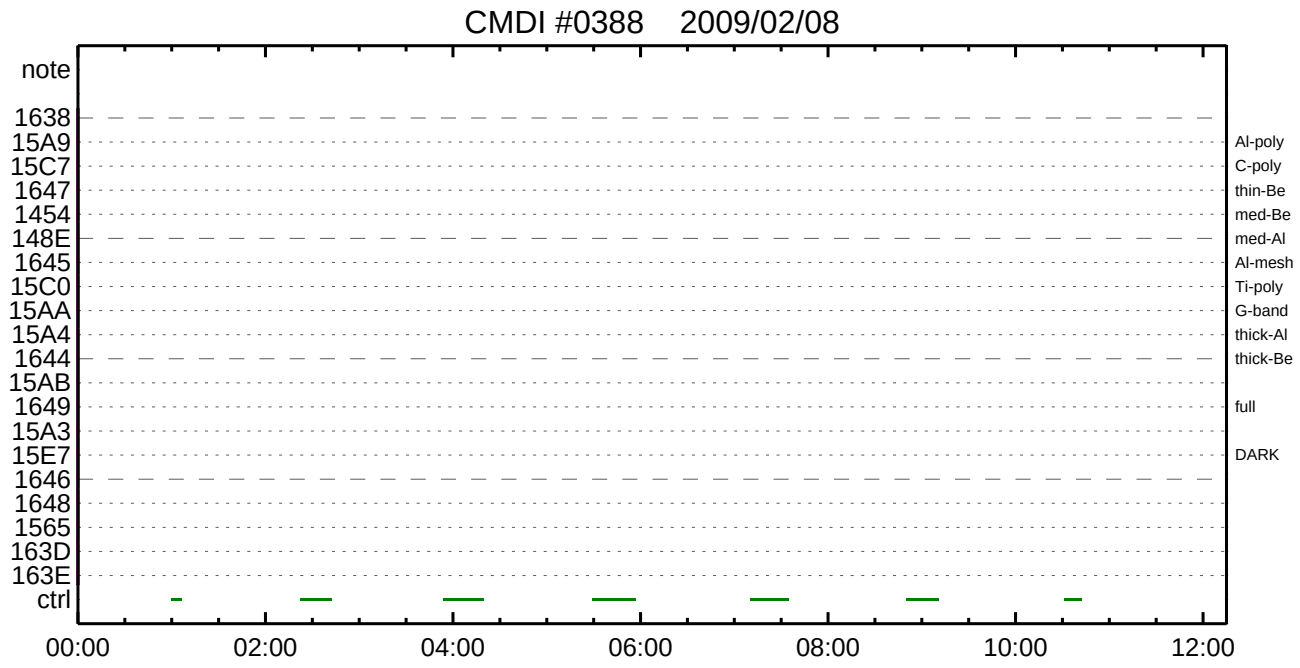
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148E
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15A4
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163E
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
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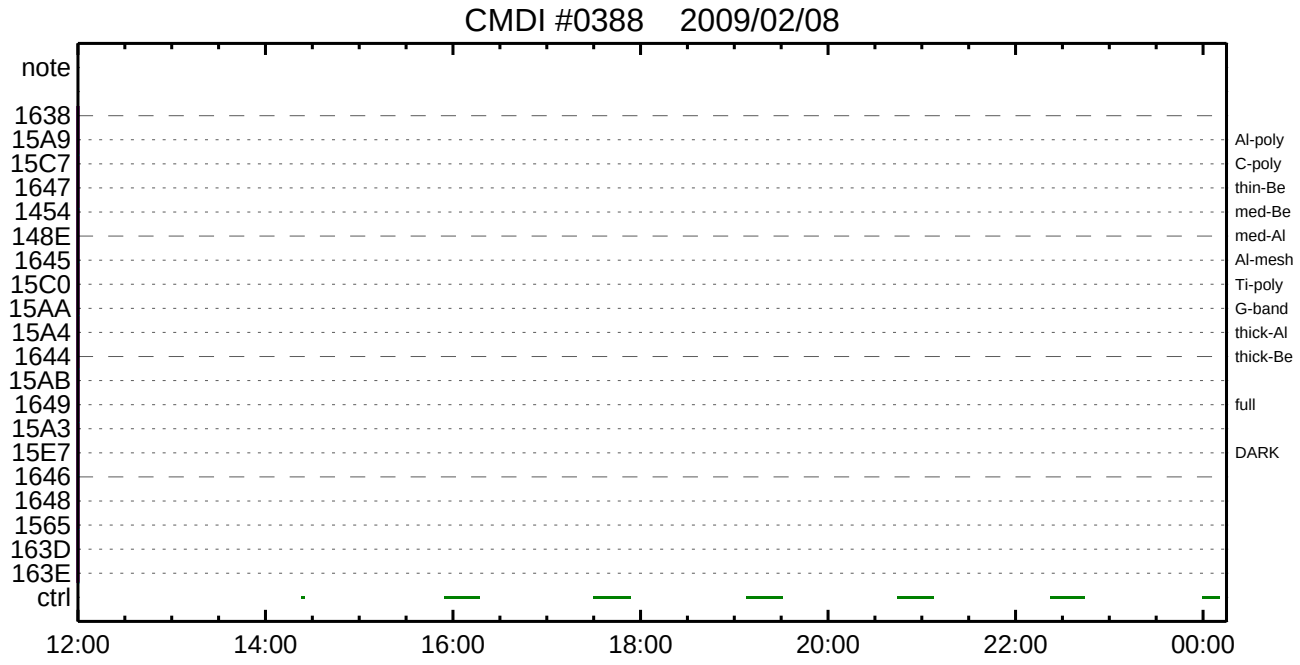
CMDI #0388 2009/02/08
note
1638
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C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
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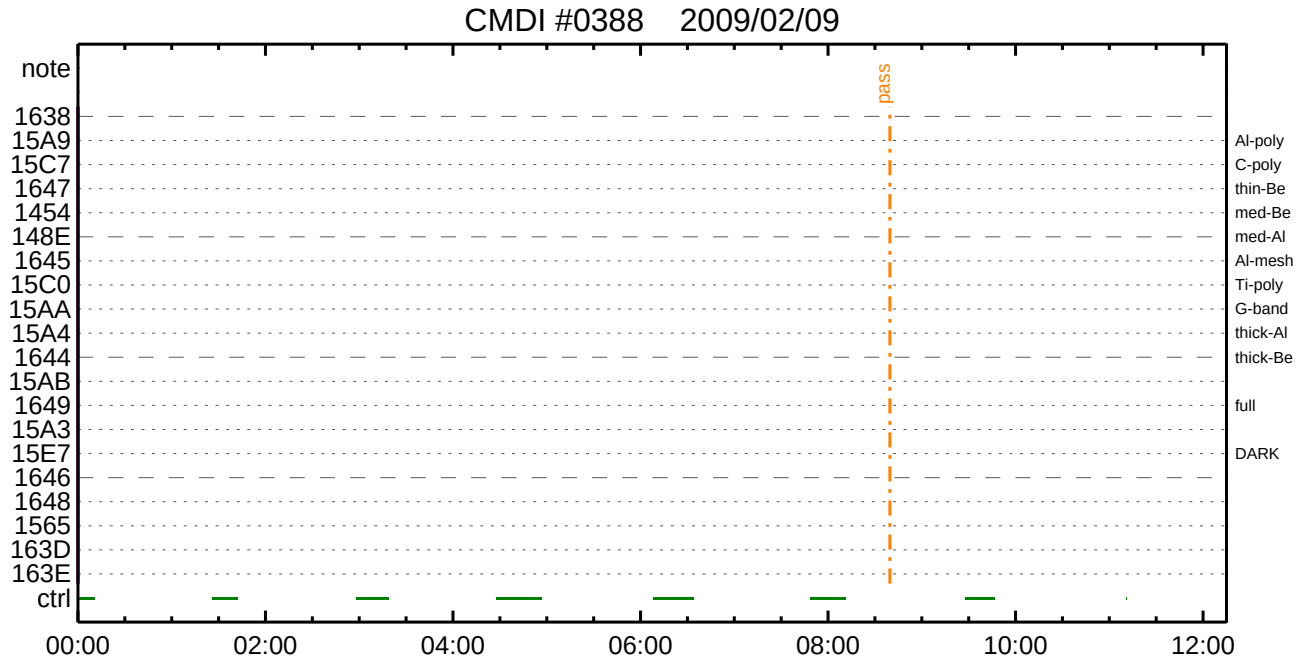
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note
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163E
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Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
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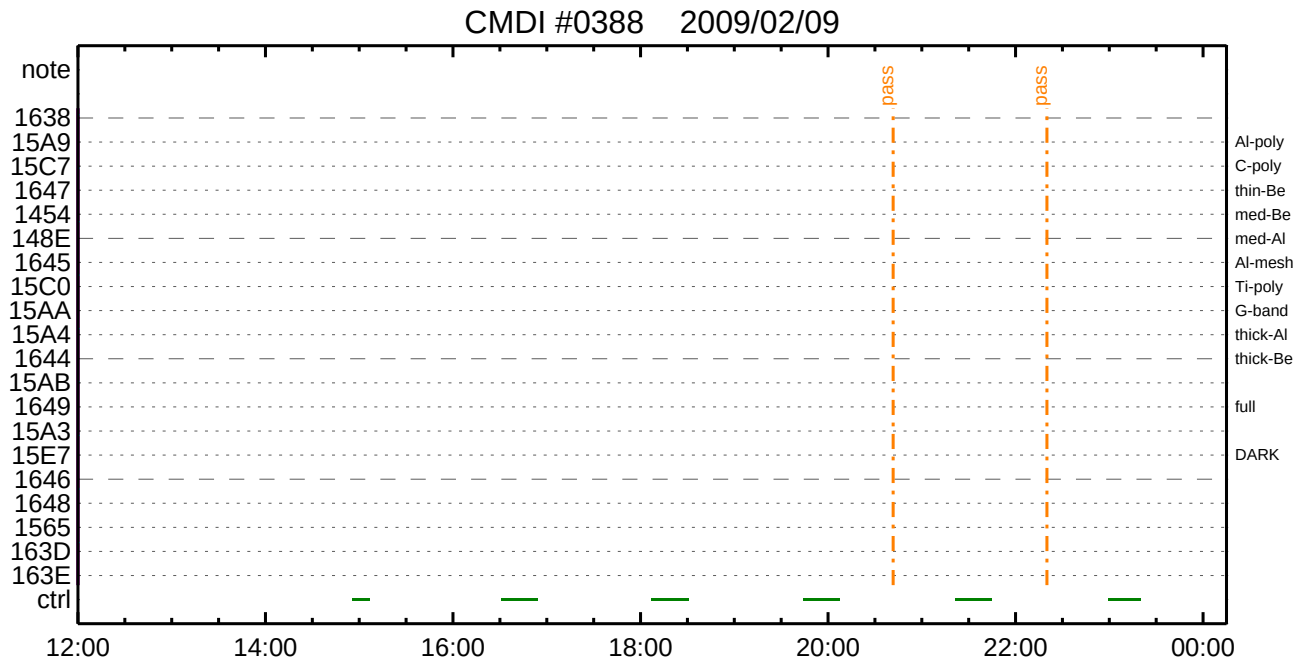
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note
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163E
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
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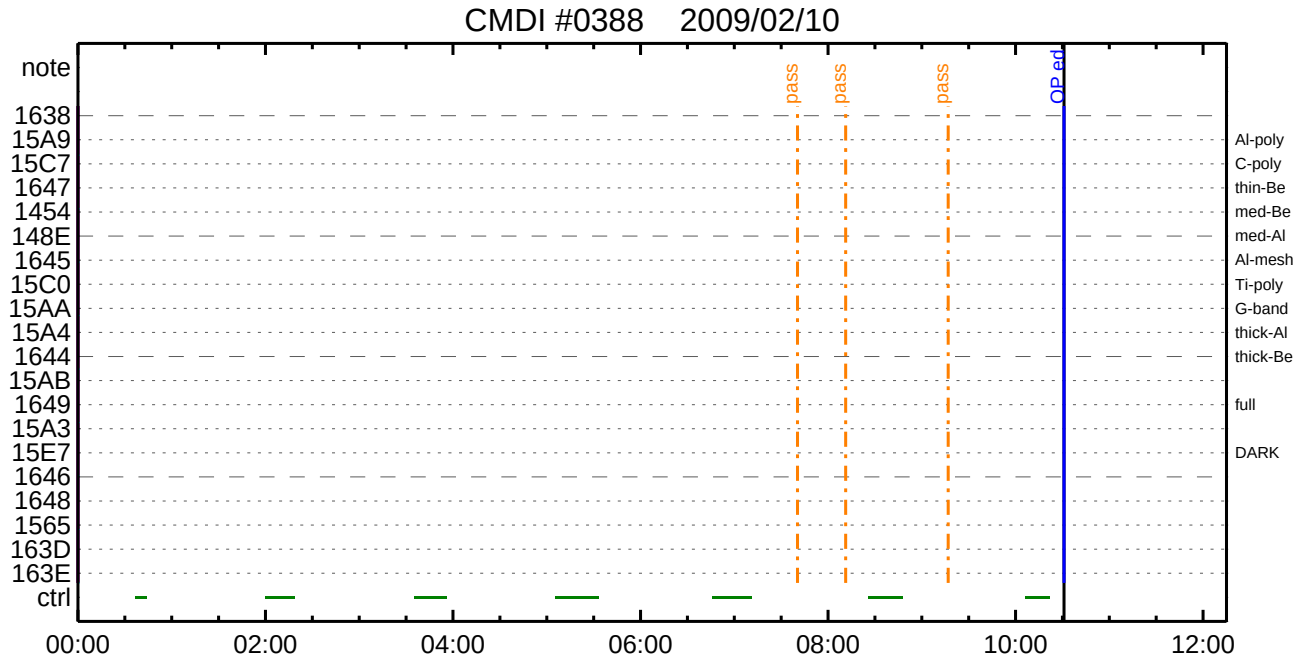
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note
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ctrl
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pass
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Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
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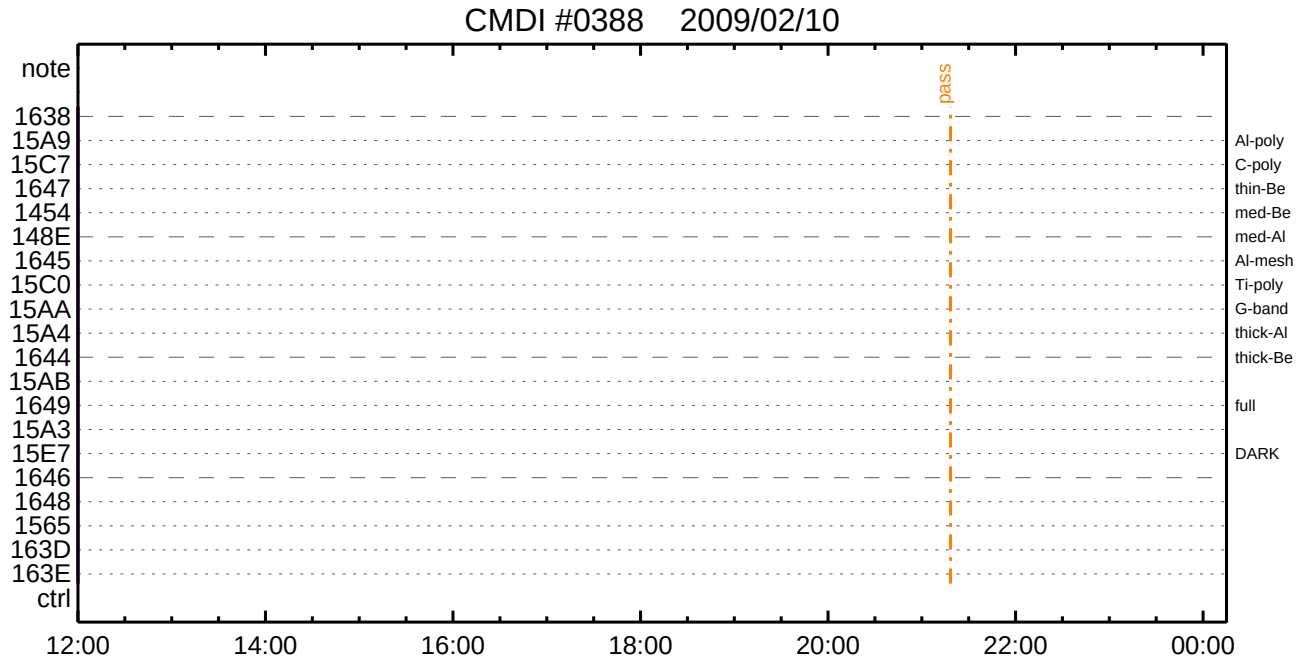
12:00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note
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163D
163E
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
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```
(a) Spacecraft Operation Procedure (real-commands)

main-395 2009-02-05 12:52:45 289 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ****************************
0099     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0100     C. ****************************
0101     C.
0102   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0103   . S. OP    op-395:OP
0104     ()
0105   . S. OG    og-395:OG
0106     ()
0107     C.
0108   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0109     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0110  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111     BC       (20 00 7f 01 02)
0112     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0113     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0114     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0115     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0116     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0117  +. DC 01-22 DHU_MODE_CHNG
0118     BC       (07 0b f8)
0119     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0120     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0122     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0123     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0124   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0125     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0126   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0127     C.
0128     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0129  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130     BC       (20 80 7f 01 02)
0131     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0132     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0133     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0134     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0135     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0136  +. DC 01-22 DHU_MODE_CHNG
0137     BC       (07 0b f8)
0138     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0139     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0141     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0142     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0143   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0144     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0145   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0146     C.
0147     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149     BC       (21 00 41 01 02)
0150     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0151     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0152     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0153     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0154     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0155  +. DC 01-22 DHU_MODE_CHNG
0156     BC       (07 0b f8)
0157     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0158     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0160     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0161     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0162   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0163     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0164   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0165     C.
0166   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0167     C. DHU¥â¡¼¥É¡Ê¼ý¸¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0168  +. DC 01-22 DHU_MODE_CHNG
0169     BC       (02 0a f8)
0170     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0171     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0172     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0173     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0174     C.
0175   . C. ********************************
0176     C. TI-CMD SET (OPOG STOP/COPY/START)
0177     C. ********************************
0178     C.
0179   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
0180     C.           ¤Þ¤¿¡¢¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¿¼Ç¹Ô¤¦¤³¤È¡£
0181     C.
0182   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0183  +. TI 2009-02-05 10:36:00.0
0184     DC 01-B3 DHU_OP_STOP
0185     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186     C.
0187  +. TI 2009-02-05 10:36:01.0
0188     DC 01-B4 DHU_OP_COPY
0189     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190     C.
0191  +. TI 2009-02-05 10:36:01.0
0192     DC 01-B5 DHU_OPOG_COPY
0193     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

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0194     C.
0195   +. TI 2009-02-05 10:40:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238    . C. Stop EIS observation and temporarily disable EIS mode changes
0239     C.
0240     C.
0241     C. ********** Start EIS operation (TI set) **********
0242     C. Execute, after the success of OP upload.
0243     C. Set EIS TI-commands
0244   +. TI 2009-02-05 10:40:30.0
0245     DC 07-FC EIS_MODE_MANU
0246     BC       (21 02)
0247   +. TI 2009-02-05 10:40:40.0
0248     DC 07-FC EIS_MODE_CHG_DIS
0249     BC       (22)
0250    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0251     C. ************ End EIS operation (TI set) ***********
0252     C.
0253     C.
0254     C. ********************************
0255     C.  SOT TI command set
0256     C. ********************************
0257     C. Execute, after the success of OP upload.
0258   +. TI 2009-02-05 10:40:16.0
0259     DC 07-F0 MDP_SOT_MODE_STBY
0260     BC       (41)
0261    . C. ------------------------------------
0262     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0263     C. ------------------------------------
0264     C. ************ SOT END ***********
0265     C.
0266     C. ************ XRT START ***********
0267     C. Execute, after the success of OP upload.
0268   +. TI 2009-02-05 10:40:00.0
0269     DC 07-F0 MDP_XRT_MODE_STBY
0270     BC       (c3)
0271    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0272     C.
0273     C. ************ XRT END ***********
0274     C.
0275    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0276     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0277    . S. DC-BC dcbc-402:DCBC
0278     (MDP_known_event)
0279     C.
0280     C.
0281    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0282    . S. DC-BC dcbc-153:DCBC
0283     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284     C.
0285     C.
0286    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0287     C.
0288    . C. ********** LOS **********
0289     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-396 2009-02-05 12:52:45 166 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇÇ¤Î»þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106   +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113   +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                      Result = OK [ ]
0121     C.
0122   +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                         <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127   +. DC 02-8E AOCU_ORB_UPD
0128    . C.
0129    . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130   +. DC 07-FC EIS_MODE_MANU
0131     BC       (21 02)
0132    . C. Verify EIS in MANUAL mode
0133    . C. Estimated OBSTBL upload time is 26s
0134     C. ********************************
0135     C. EIS START OBSTBL LOAD
0136     C. ********************************
0137    . S. RAM   ram-820:EIS_OBSTBL
0138     ()
0139   +. DC 07-FC EIS_DUMP_OBSTBL
0140     BC       (07 07 07 00 00 70 00)
0141     C.
0142     C. Execute, after the success of OBSTBL upload.
0143     C. Set EIS TI-commands
0144   +. TI 2009-02-05 10:40:50.0
0145     DC 07-FC EIS_MODE_CHG_ENA
0146     BC       (20)
0147    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0148     C. ********************************
0149     C. EIS END OBSTBL LOAD
0150     C. ********************************
0151     C.
0152    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0153     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0154    . S. DC-BC dcbc-402:DCBC
0155     (MDP_known_event)
0156     C.
0157     C.
0158    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0159    . S. DC-BC dcbc-153:DCBC
0160     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161     C.
0162     C.
0163    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0164     C.
0165    . C. *********** LOS ***********
0166     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-397 2009-02-05 12:52:45 130 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014     C.
0015     C. ************ XRT START ************
0016     C.
0017   +. DC 07-F0 MDP_XRT_CTRL_MANU
0018     BC       (c1)
0019   +  DC 07-F0 MDP_XRT_MODE_STBY
0020     BC       (c3)
0021   . C. ----------- Success Verify ?              OK / NG____
0022     C.
0023     C. XRT Obs. Table Upload
0024   . S. RAM   ram-291:MDP_OBS_X
0025     ()
0026     C.
0027   +. DC 07-F0 MDP_DUMP_XRTTBL
0028     BC       (84 07 00 00 00 3a d4)
0029   . C. ----------- Comparison Check ?                        OK / ERR ____
0030     C.
0031     C.
0032   +. DC 07-F0 MDP_XRT_ROI_SET
0033     BC       (cd 01 b1 b1 04 04)
0034   +  DC 07-F0 MDP_XRT_ROI_SET
0035     BC       (cd 02 b1 b1 08 08)
0036   +  DC 07-F0 MDP_XRT_ROI_SET
0037     BC       (cd 03 b1 b1 08 08)
0038   +  DC 07-F0 MDP_XRT_ROI_SET
0039     BC       (cd 04 b1 b1 06 06)
0040   +  DC 07-F0 MDP_XRT_ROI_SET
0041     BC       (cd 06 80 80 06 06)
0042   +  DC 07-F0 MDP_XRT_ROI_SET
0043     BC       (cd 07 80 80 08 08)
0044   +  DC 07-F0 MDP_XRT_ROI_SET
0045     BC       (cd 08 80 80 18 18)
0046   +  DC 07-F0 MDP_XRT_ROI_SET
0047     BC       (cd 09 80 80 20 20)
0048   +  DC 07-F0 MDP_XRT_ROI_SET
0049     BC       (cd 0f 80 80 04 04)
0050   +  DC 07-F0 MDP_XRT_ROI_SET
0051     BC       (cd 10 80 80 10 10)
0052   . C. ---------- Success Verify ?           OK / NG ____
0053     C.
0054     C.
0055   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0056     C.
0057   +. DC 07-F0 MDP_XRT_MODE_OBSV
0058     BC       (c2)
0059   +. TI 2009-02-05 10:40:02.0
0060     DC 07-F0 MDP_XRT_MODE_OBSV
0061     BC       (c2)
0062   . C. ---------- Success Verify ?           OK / NG ____
0063     C.
0064     C. ************ XRT END ***********
0065   . C. ********************************
0066     C.  SOT table upload
0067     C. ********************************
0068   . C. < Stop FG table >
0069   +. DC 07-F0 MDP_FG_CTRL_MANU
0070     BC       (51)
0071   . C. ------------------------------------
0072     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0073     C. ------------------------------------
0074     C.
0075   . C. <Upload FG Observation Table>
0076   . S. RAM   ram-266:MDP_OBS_F
0077     ()
0078     C.
0079   . C. < Dump RAMID=MDP_OBS_F >
0080   +. DC 07-F0 MDP_DUMP_FGTBL
0081     BC       (82 07 00 00 00 38 b8)
0082     C. ------------------------------------
0083     C.  MDP_OBS_F verify = OK/NG        [ ]
0084     C. ------------------------------------
0085     C.
0086   . C. < Stop SP table >
0087   +. DC 07-F0 MDP_SP_CTRL_MANU
0088     BC       (61)
0089     C. ------------------------------------
0090     C.  MDP_SP_CTRL_MODE      = MANU    [ ]
0091     C. ------------------------------------
0092     C.
0093   . C. <Upload SP Observation Table>
0094   . S. RAM   ram-282:MDP_OBS_S
0095     ()
```

```
0096     C.
0097   . C. < Dump RAMID=MDP_OBS_S >
0098  +. DC 07-F0 MDP_DUMP_SPTBL
0099     BC       (83 07 00 00 00 38 b8)
0100     C. ------------------------------------
0101     C.  MDP_OBS_S verify = OK/NG        [ ]
0102     C. ------------------------------------
0103     C.
0104     C. ******************************
0105     C.  SOT TI command set
0106     C. ******************************
0107     C. Execute, after the success of TBL upload.
0108  +. TI 2009-02-05 10:40:18.0
0109     DC 07-F0 MDP_SOT_MODE_OBSV
0110     BC       (40)
0111   . C. ------------------------------------
0112     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0113     C. ------------------------------------
0114     C.
0115     C.
0116   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¤ëDCBC•×²è *****
0117     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¤ë)
0118   . S. DC-BC dcbc-402:DCBC
0119     (MDP_known_event)
0120     C.
0121     C.
0122   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¤ëDCBC•×²è *****
0123   . S. DC-BC dcbc-153:DCBC
0124     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0125     C.
0126     C.
0127   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0128     C.
0129   . C. ********** LOS **********
0130     C.
```

```
*** OP Sequence for XRT ***

2009/02/05  10:50:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/02/05  10:50:56.0  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2009/02/05  10:51:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 00 00 54 00
2009/02/05  10:51:16.0  XRT_QT_PROG_SET_413_OG [0x19d]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2009/02/05  10:51:18.0  XRT_AEC_RESET_415_OG [0x19f]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2009/02/05  10:51:20.0  XRT_ARS_DIS_422_OG [0x1a6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2009/02/05  10:51:22.0  XRT_FLD_DIS_445_OG [0x1bd]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2009/02/05  10:51:24.0  XRT_FLRCTRL_DIS_449_OG [0x1c1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/02/05  10:53:00.0  XRT_CTRL_AUTO_403_OG [0x193]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/02/05  15:00:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/02/05  17:57:24.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/02/05  17:57:26.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2009/02/05  17:57:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2009/02/05  17:57:46.0  XRT_QT_PROG_SET_408_OG [0x198]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 07
2009/02/05  17:57:48.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2009/02/05  17:57:50.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/02/05  17:57:52.0  XRT_ARS_DIS_427_OG [0x1ab]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2009/02/05  17:59:30.0  XRT_CTRL_AUTO_444_OG [0x1bc]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/02/05  18:07:24.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/02/05  18:07:30.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 01 00 00 00 00
2009/02/06  06:30:24.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/02/06  06:30:26.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2009/02/06  06:30:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2009/02/06  06:30:46.0  XRT_QT_PROG_SET_421_OG [0x1a5]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2009/02/06  06:30:48.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2009/02/06  06:30:50.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/02/06  06:30:52.0  XRT_ARS_DIS_427_OG [0x1ab]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2009/02/06  06:32:30.0  XRT_CTRL_AUTO_444_OG [0x1bc]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/02/06  06:40:24.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/02/06  06:40:30.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 00 00 54 00
2009/02/06  18:16:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/02/06  18:16:56.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2009/02/06  18:17:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2009/02/06  18:17:16.0  XRT_QT_PROG_SET_408_OG [0x198]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 07
2009/02/06  18:17:18.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2009/02/06  18:17:20.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/02/06  18:17:22.0  XRT_ARS_DIS_427_OG [0x1ab]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2009/02/06  18:19:00.0  XRT_CTRL_AUTO_444_OG [0x1bc]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/02/06  18:26:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/02/06  18:26:56.0  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2009/02/06  18:27:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 55 ca 00 00
2009/02/06  18:27:16.0  XRT_QT_PROG_SET_404_OG [0x194]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 10
2009/02/06  18:27:18.0  XRT_AEC_RESET_415_OG [0x19f]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2009/02/06  18:27:20.0  XRT_ARS_DIS_422_OG [0x1a6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2009/02/06  18:27:22.0  XRT_FLD_DIS_445_OG [0x1bd]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2009/02/06  18:27:24.0  XRT_FLRCTRL_DIS_449_OG [0x1c1]
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```
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/02/06  18:29:00.0  XRT_CTRL_AUTO_403_OG [0x193]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/02/06  19:30:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/02/06  19:54:00.0  XRT_Custom_430_OG [0x1ae]
2009/02/06  19:55:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/02/06  21:08:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/02/06  21:31:30.0  XRT_Custom_430_OG [0x1ae]
2009/02/06  21:32:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/02/06  22:45:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/02/06  23:10:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/02/06  23:10:02.0  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2009/02/06  23:10:22.0  XRT_QT_PROG_SET_412_OG [0x19c]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 08
2009/02/06  23:10:24.0  XRT_AEC_RESET_415_OG [0x19f]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2009/02/06  23:10:26.0  XRT_ARS_DIS_422_OG [0x1a6]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2009/02/06  23:10:28.0  XRT_FLD_DIS_445_OG [0x1bd]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2009/02/06  23:10:30.0  XRT_FLRCTRL_DIS_449_OG [0x1c1]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/02/06  23:12:06.0  XRT_CTRL_AUTO_403_OG [0x193]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/02/07  00:23:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/02/07  00:31:30.0  XRT_Custom_430_OG [0x1ae]
2009/02/07  00:32:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/02/07  01:47:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/02/07  06:02:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/02/07  06:02:56.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2009/02/07  06:03:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2009/02/07  06:03:16.0  XRT_QT_PROG_SET_421_OG [0x1a5]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2009/02/07  06:03:18.0  XRT_FLD_DIS_419_OG [0x1a3]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2009/02/07  06:03:20.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/02/07  06:03:22.0  XRT_ARS_DIS_427_OG [0x1ab]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2009/02/07  06:05:00.0  XRT_CTRL_AUTO_444_OG [0x1bc]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/02/07  06:12:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/02/07  06:13:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                        AOCU_NM              5 02-76 00 55 ca 00 00
2009/02/07  09:54:00.5  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
```