

XRT Timeline to be uploaded on 2009/02/10

Period: 2009/02/10 10:31:00 - 2009/02/14 09:44:00

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Normal mode

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XOB #1579: Coronal cavity (Al/poly, Ti/poly, Thin Be)-DPCM 2x2-768X768-FOV offset for South-East -longer exposures													
Term		Pointing (x, y)				Comment							
02/10 15:43:00 - 02/10 18:01:24		Fixed (-835.0, -482.0)				CCD test 4/4							
PROG= 19 Inf.-time(s)													
└ Subr= 1		1-time(s)		300.0sec									
└ Seqn= 46		1-time(s)		4.0sec									
└ Al-poly/Open		Al-poly/thick-Al close		Safe	Norm	11.3s	Obs	2x2	768x768 (896, 904)	DPCM	0	0	2.0sec
└ Seqn= 64		1-time(s)		2.0sec									
└ Open/Ti-poly		Open/Ti-poly close		Safe	Norm	16.0s	Obs	2x2	768x768 (896, 904)	DPCM	0	0	2.0sec
└ Seqn= 97		1-time(s)		2.0sec									
└ thin-Be/Open		med-Be/Open close		Safe	Norm	45.2s	Obs	2x2	768x768 (896, 904)	DPCM	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

XOB #15A3: Synoptic Q95 2x2 - Al/poly(512/5795) + Dark cal(512 Q98) + Ti-poly(723/11571) + G-band(16)												
Term		Pointing (x, y)				Comment						
02/10 18:03:30 - 02/10 18:11:24		Fixed (0.0, 0.0)				synoptic, shifted 1.5 min						
02/11 19:02:00 - 02/11 19:09:24		Fixed (0.0, 0.0)				synoptic, shifted manually						
PROG= 07 1-time(s)												
└ Subr= 1 1-time(s) 12.0sec												
└ Seqn= 36 1-time(s) 4.0sec												
└┐ Al-poly/Open Al-poly/Open close		Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└┐ Al-poly/Open Al-poly/thick-Al close		Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ Seqn= 6 1-time(s) 2.0sec												
└┐ Open/Ti-poly Open/thick-Al close		Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└ Seqn= 88 1-time(s) 4.0sec												
└┐ Open/Ti-poly Open/thick-Al close		Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└┐ Open/Ti-poly Open/thick-Al close		Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ Seqn= 92 1-time(s) 2.0sec												
└┐ Open/G-band Open/G-band open		Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #15CF: Axion Study_no-AEC_Med-Be-12s-2														
Term			Pointing (x, y)					Comment						
02/10 18:13:30 - 02/10 23:59:54			Fixed (0.0, 0.0)					# XRT: hot plasma obs for 5 hr						
PROG= 03 Inf.-time(s)														
Subr= 1		1-time(s)		2.0sec										
Seqn= 15		12-time(s)		4.0sec										
		med-Be/Open		med-Be/Open		close	Safe	Norm	11.3s	Obs	8x8	2048x2048 (1024, 1024)		DPCM 0 0 20.0sec
Subr= 2		1-time(s)		2.0sec										
Seqn= 61		1-time(s)		4.0sec										
		med-Be/Open		med-Be/Open		close	Safe	Dark	11.3s	Obs	8x8	2048x2048 (1024, 1024)		DPCM 0 0 2.0sec
Default Filter			Thicker Filter			VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp. AEC Buffer Interval

XOB #164E: Flux cancellation - Al/poly - 384x384 Q95 - full Gband every hour-2												
Term		Pointing (x, y)					Comment					
02/11 00:02:00 - 02/11 05:34:54		Track (-56.0, -30.4) @ 02/11 00:00:00					QS cancellation obs					
02/11 19:11:30 - 02/12 05:53:24		Track (122.7, -30.5) @ 02/11 19:10:00					# QS cancellation obs					
PROG= 14 Inf.-time(s)												
Subr= 1		1-time(s)		2.0sec								
Seqn= 24		90-time(s)		40.0sec								
Al-poly/Open		Al-poly/thick-Al close		Safe	Norm	16.0s	Obs	1x1	384x384 (1024, 1024)	Q=95	4	0 2.0sec
Subr= 2		1-time(s)		2.0sec								
Seqn= 78		1-time(s)		4.0sec								
Open/G-band		Open/G-band open		Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0 2.0sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp. AEC Buffer Interval

XOB #15A4: Synoptic Q95 2x2 - Al/mesh(512/5795) + Dark cal(512 Q98) + Ti-poly(723/11571) + G-band(16)												
Term		Pointing (x, y)				Comment						
02/11 05:37:00 - 02/11 05:44:54		Fixed (0.0, 0.0)				synoptic, shifted 25.0 min						
02/12 05:55:30 - 02/12 06:03:24		Fixed (0.0, 0.0)				synoptic, shifted -5.5 min						
PROG= 11		1-time(s)										
└ Subr= 1		1-time(s) 12.0sec										
└ Seqn= 47		1-time(s) 4.0sec										
└└ Open/Al-mesh		Open/Ti-poly		close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95 0 0 2.0sec
└└ Open/Al-mesh		Open/Ti-poly		close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)		Q=95 0 0 2.0sec
└ Seqn= 6		1-time(s) 2.0sec										
└└ Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98 0 0 2.0sec
└ Seqn= 88		1-time(s) 4.0sec										
└└ Open/Ti-poly		Open/thick-Al		close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95 0 0 2.0sec
└└ Open/Ti-poly		Open/thick-Al		close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)		Q=95 0 0 2.0sec

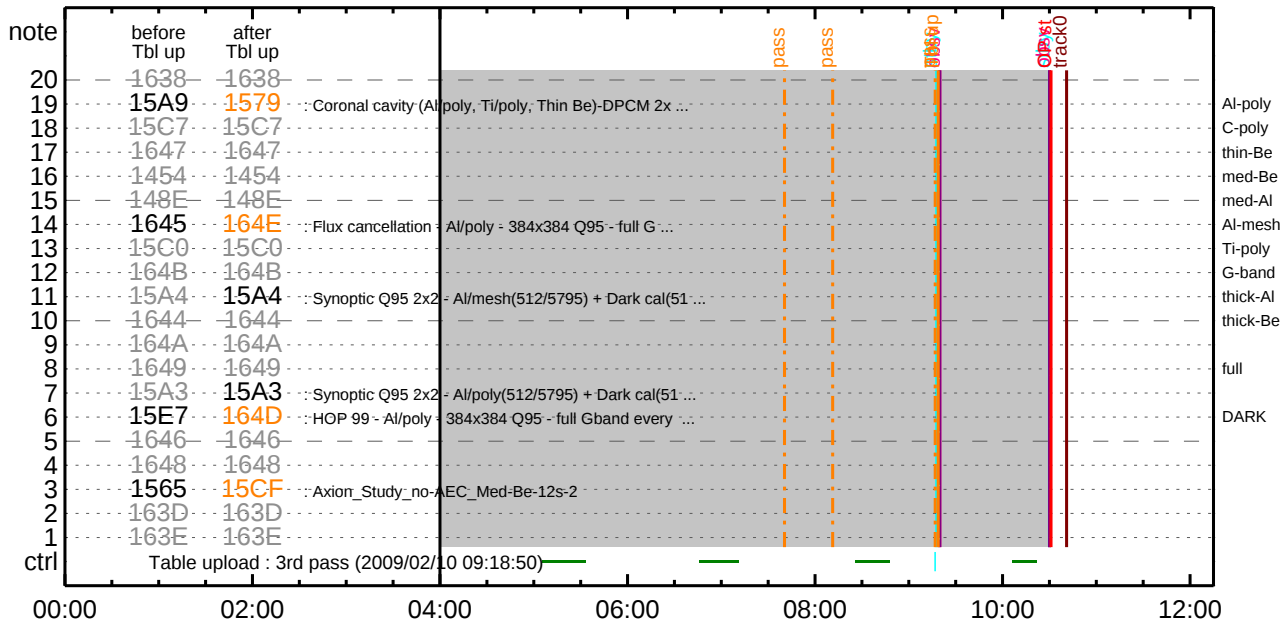
└ Seqn= 92		1-time(s)		2.0sec										
└ Open/G-band		Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

XOB #164D: HOP 99 - Al/poly - 384x384 Q95 - full Gband every hour														
Term			Pointing (x, y)							Comment				
02/11 05:47:00 - 02/11 14:59:54			Track (-2.3, -30.0) @ 02/11 05:45:00							# Cont QS obs				
02/11 15:02:00 - 02/11 18:59:54			Track (300.0, 99.9) @ 02/11 15:00:00							HOP99				

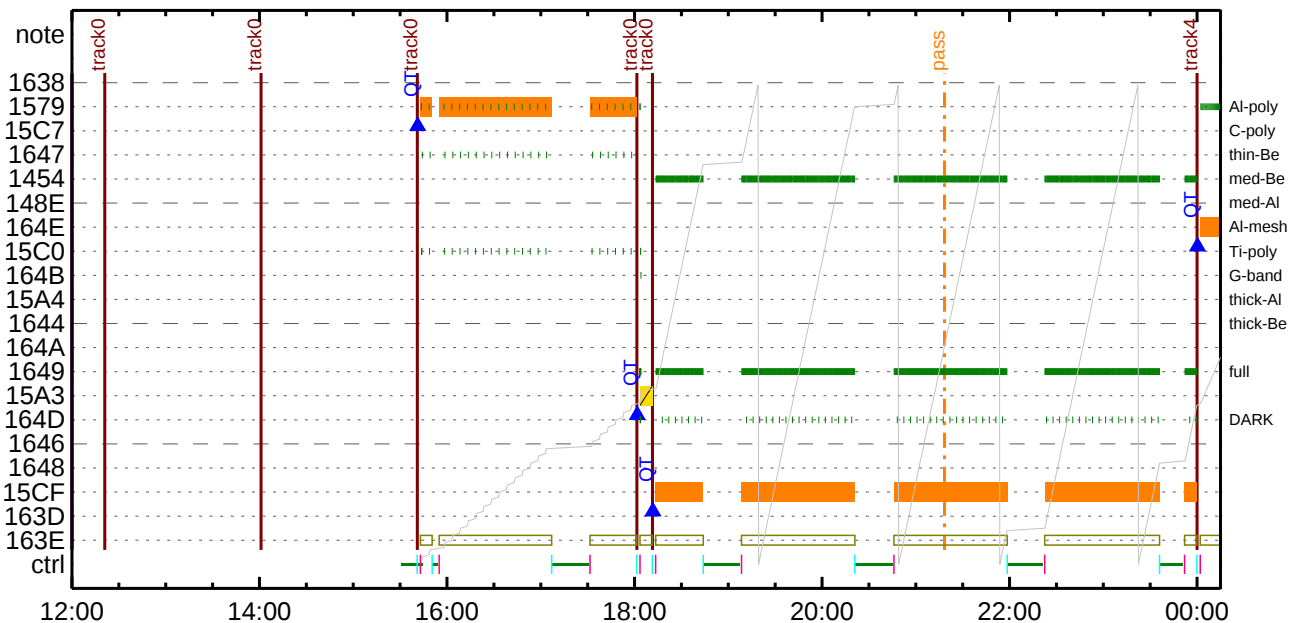
PROG= 06															Inf.-time(s)				
└ Subr= 1		1-time(s)		2.0sec															
└ Seqn= 24		90-time(s)		30.0sec															
		Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1024, 1024)		Q=95	4	0	2.0sec				
└ Subr= 2		1-time(s)		2.0sec															
└ Seqn= 78		1-time(s)		4.0sec															
		Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec				
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval					

* * * * *	Flare mode	* * * * *
	NOT USED	
* * * * *	Active Region Search	* * * * *
	NOT USED	
* * * * *	Flare Detection	* * * * *
	NOT USED	

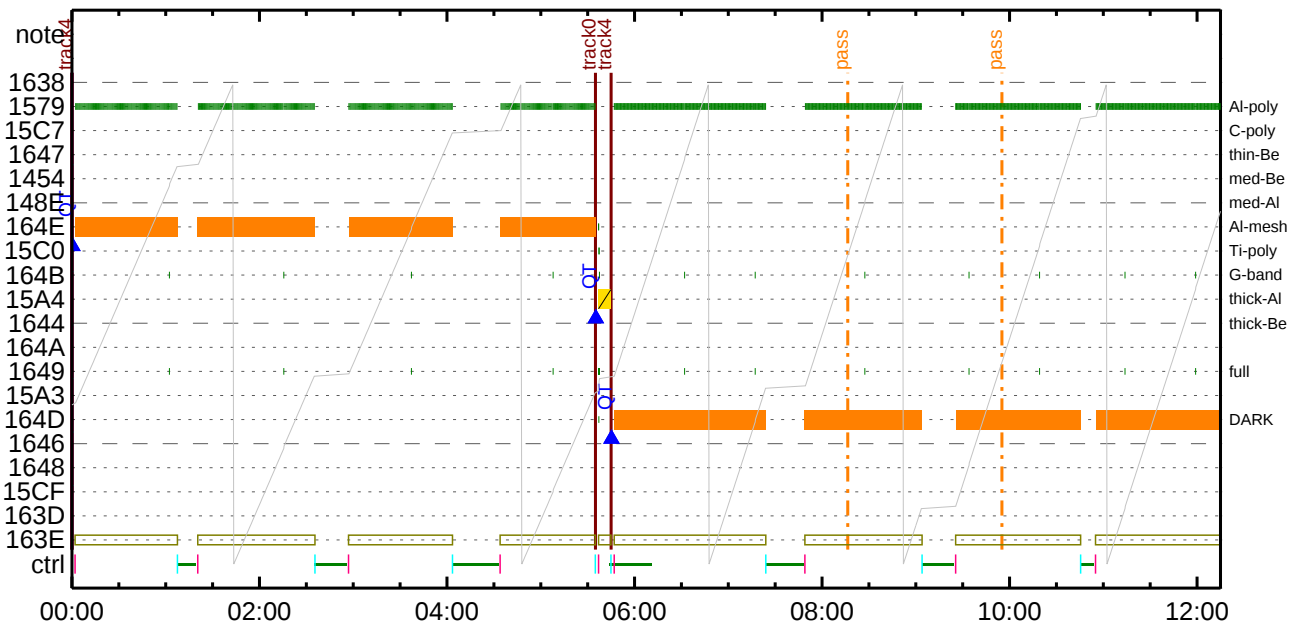
CMDI #0398 2009/02/10



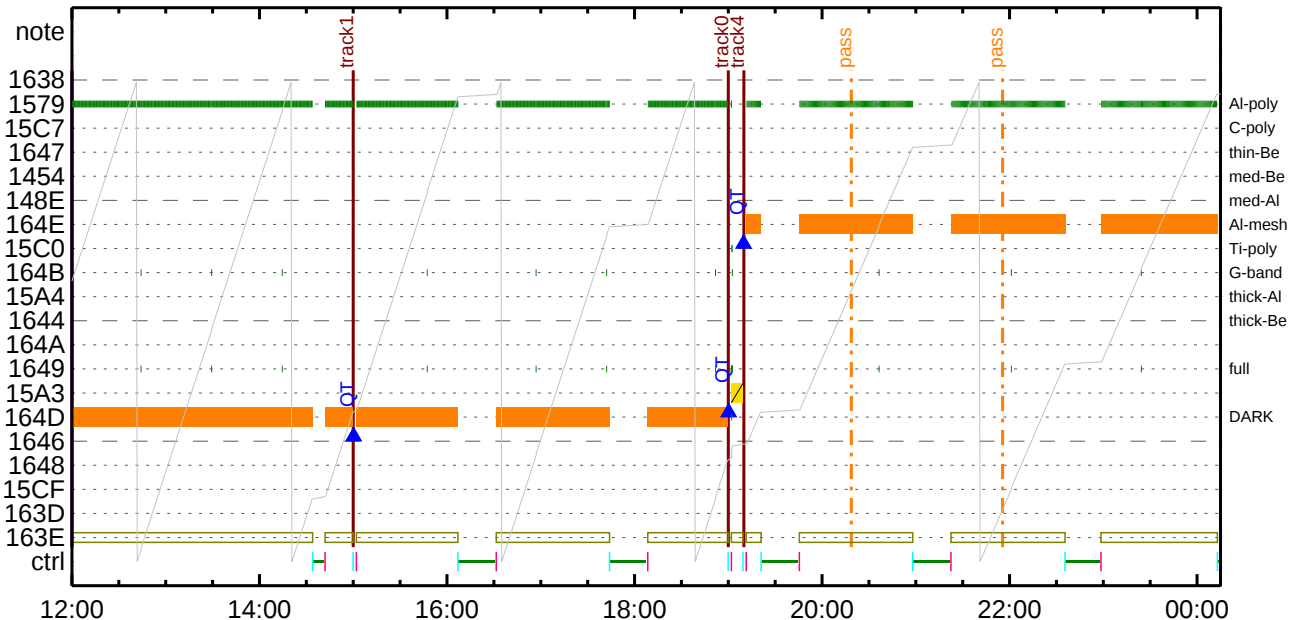
CMDI #0398 2009/02/10



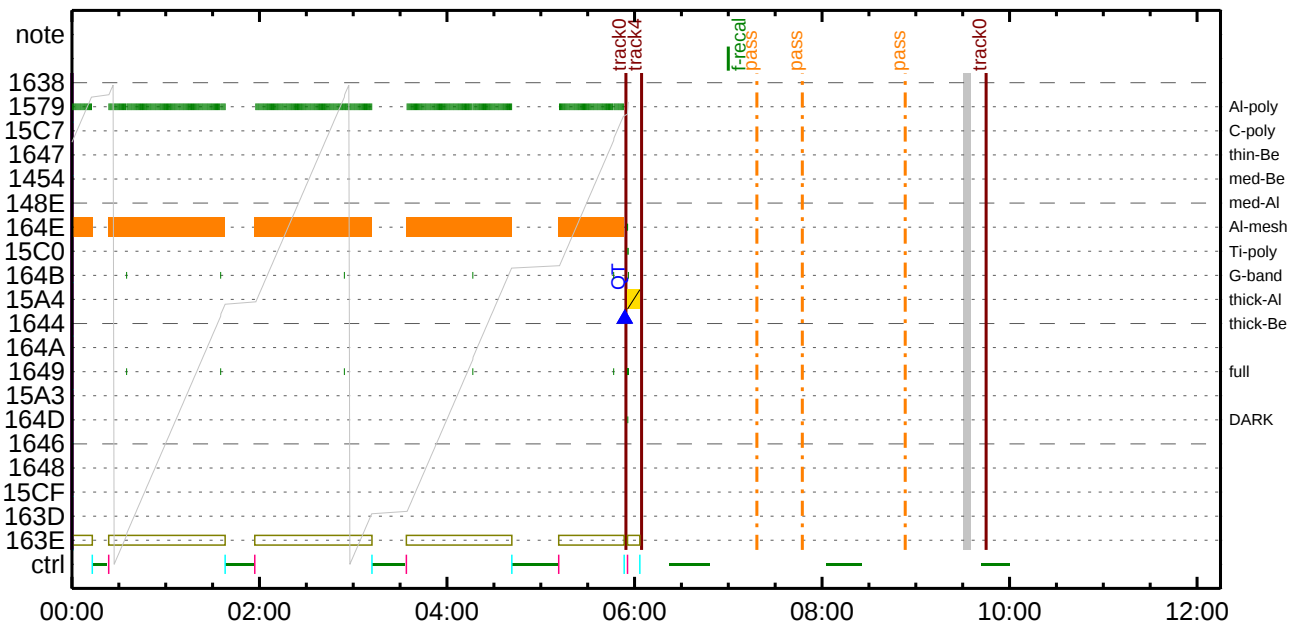
CMDI #0398 2009/02/11



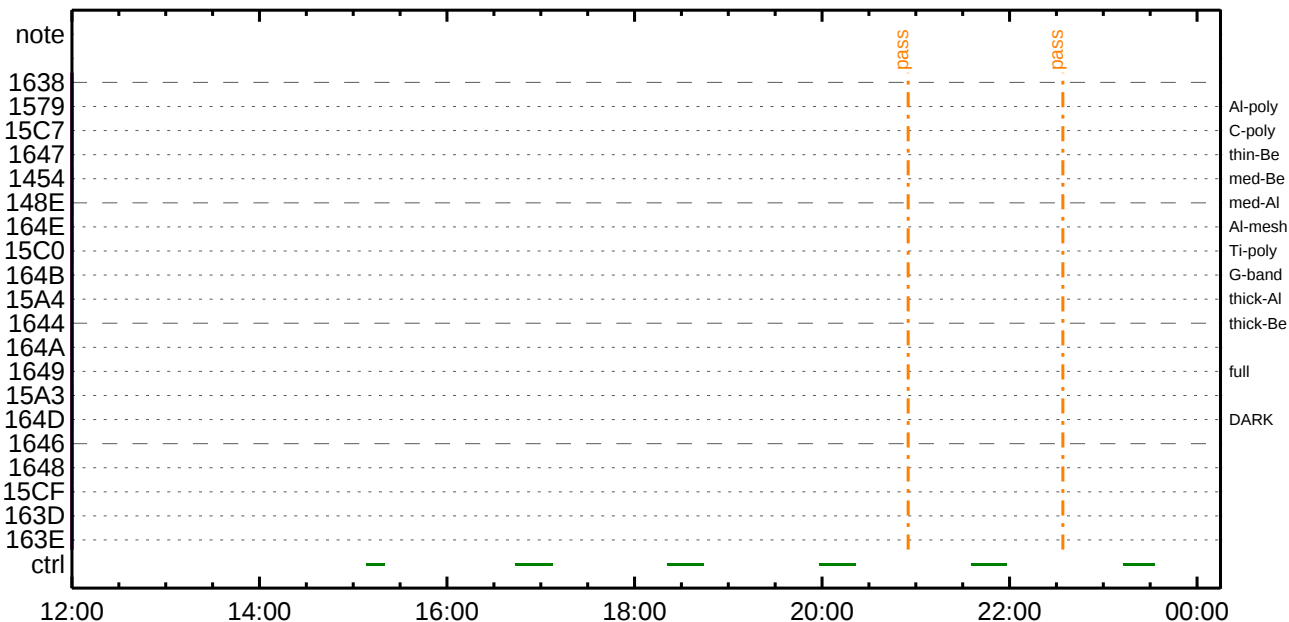
CMDI #0398 2009/02/11



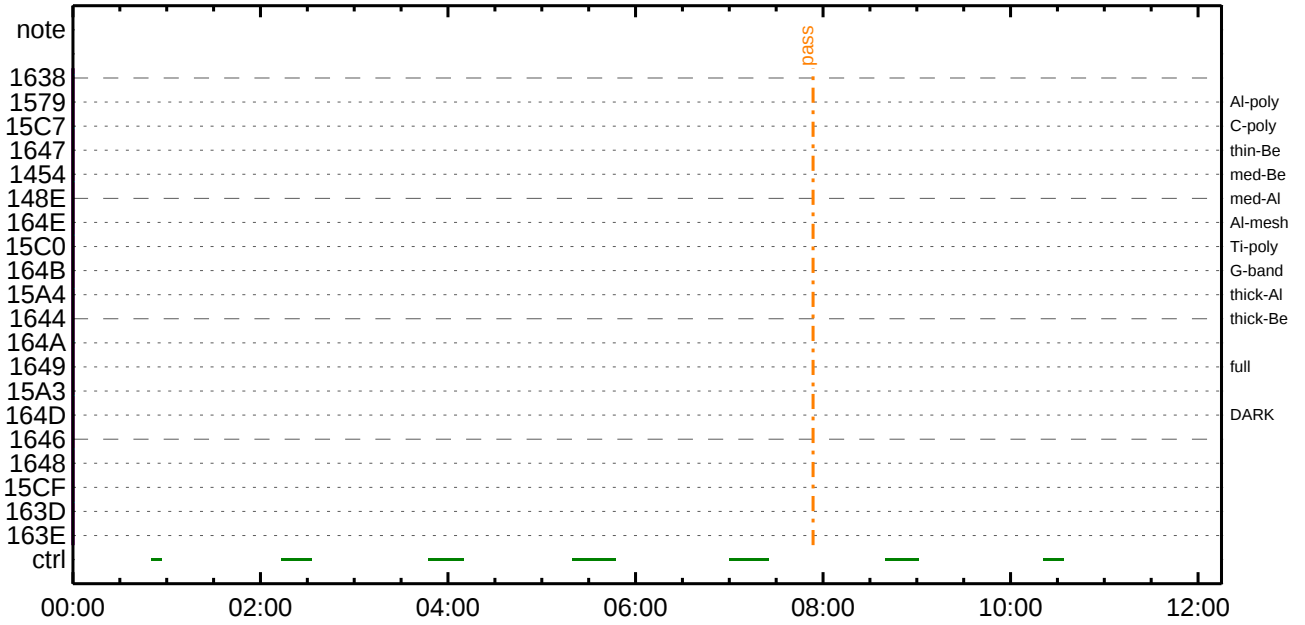
CMDI #0398 2009/02/12



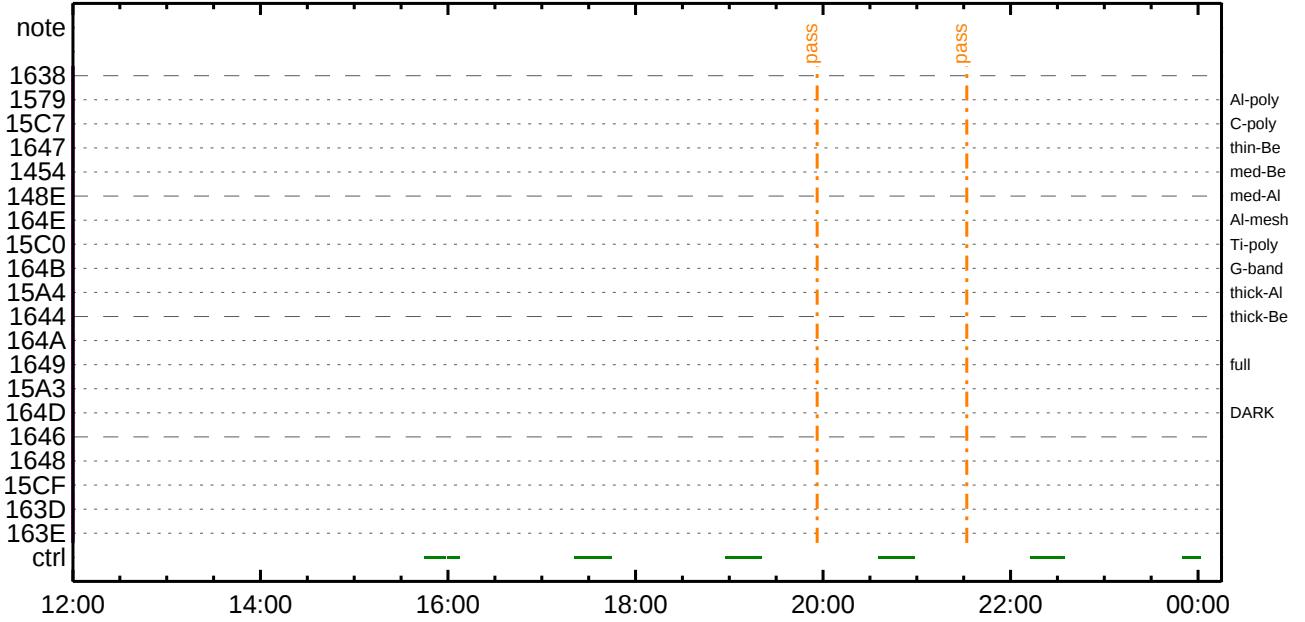
CMDI #0398 2009/02/12



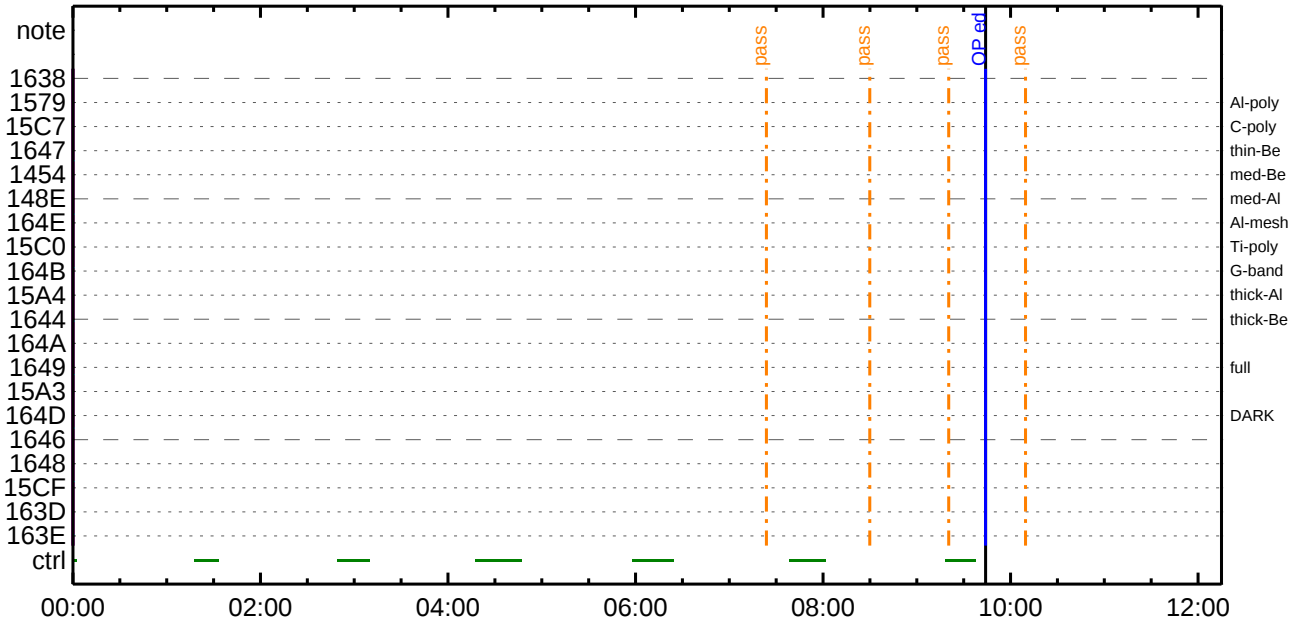
CMDI #0398 2009/02/13



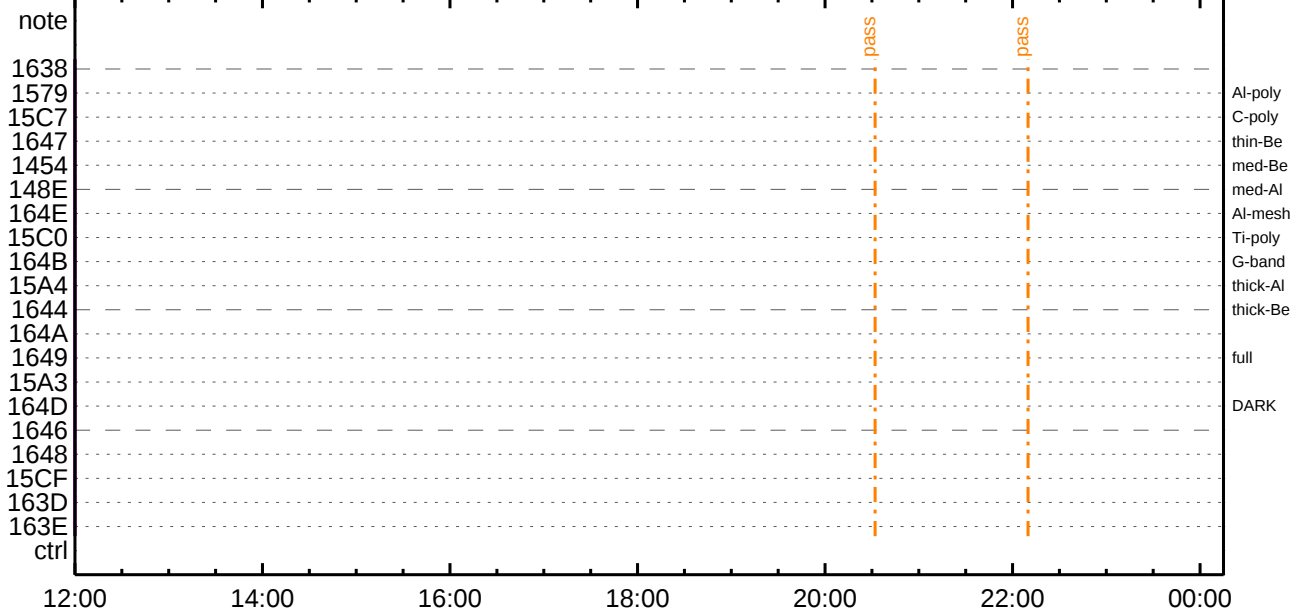
CMDI #0398 2009/02/13



CMDI #0398 2009/02/14



CMDI #0398 2009/02/14



(a) Spacecraft Operation Procedure (real-commands)

```
main-404 2009-02-10 12:28:04 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Ä»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥P¥Ó¥ÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äi;Ê¿¿A¤•µ°Æ»I×AÇ¤I¥Ç¥Á¥×¥i;¼¥ÉjÊÊ½µ•iÍÊjÊÊ¼Ç¿¤•¤¿¼¹¿¤IjÇÄ®, Ü¤¹¤Ê¤P¤ÇÄ÷¿®¤•¤Ê¤¤¤³¤Èj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿®µjON
0016 C. *****
0017 C. ¨ °ÆÄ, Í×ÊÝ¤äLOS¤P¤Ç¤I»p´Ö¤ð¹ÍÍ, ¤•jÇÉÖÍ×¤ÊXÄÓON¤I¹Ö¤Ê¤I¤Ê¤¤¤³¤Èj£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. X¥Ð¥Ó¥É¥i¥Ã¥¼ÖÄÖ¤¬°ÄÄ¤¤•¤¿¤¤jÇ°Ê²¼¤I°ÆÄ, ¼Ê½Ç¤ð¼Ä¹Ö¤¹¤Êj£
0030 C.
0031 . C. *****
0032 C. DR PT1 ÄI¼I°ÆÄ
0033 C. *****
0034 C. ¨ RESTARTjÊPT1jÊ¤•¤¿¤¤¼¹¿¤IjÇ°Ê²¼¤I¼Ä¹Ö¤¤¤•jÇDCBC-150¤Ø¿Ê¤¤¤j£
0035 C.
0036 . C. iãPT1°ÆÄ, ³«»Ijã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, i¼Ü)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, i¼Ü)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, i¼Ü)
0045 C.
0046 . C. iã¥Ç¥Ó¥Æ¥ÊÄÜÄ¤;ÊÄ•Ä²ÓÊðjÊ, ä¤I°ÆÄ, °Æ³«jã
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, i¼Ü)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, i¼Ü)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, i¼Ü)
0052 C.
0053 C.
0054 . C. PT1°ÆÄ, ¤¬¼«Æ°Ää»B¤•¤¿, äjÇ°Ê²¼¤ð¼Ä¹Ö¤¹¤Êj£
0055 C. ¥Ç¥Ó¥Æ¥ÊÄÜÄ¤¤¤Ä•Ä²ÓÊð¤¬¤¤¤¼¹¿¤I°I»¤¹¤Ê¤P¤ÇÄÖ¤Äj£
0056 C.
0057 . C. *****
0058 C. DR PT2 ÄI¼I°ÆÄ
0059 C. *****
0060 C. ¨ RESTARTjÊPT2jÊ¤•¤¿¤¤¼¹¿¤IjÇ°Ê²¼¤I¼Ä¹Ö¤¤¤•jÇDCBC-151¤Ø¿Ê¤¤¤j£
0061 C.
0062 . C. iãPT2°ÆÄ, ³«»Ijã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, i¼Ü)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, i¼Ü)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, i¼Ü)
0071 C.
0072 . C. iã¥Ç¥Ó¥Æ¥ÊÄÜÄ¤;ÊÄ•Ä²ÓÊðjÊ, ä¤I°ÆÄ, °Æ³«jã
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, i¼Ü)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, i¼Ü)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, i¼Ü)
0078 C.
0079 . C. *****
0080 C. DR°ÆÄ, Ää»BjÇXÁ÷¿®µjOFF
0081 C. *****
0082 C.
0083 . C. iãDR°ÆÄ, Ää»Bjã
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. iãXÁ÷¿®µjOFFjã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 C.
0099 C. OP/OG%1j%¥Éj|¥A¥0¥x
0100 C.
0101 C.
0102 C. jãOP/OG%1j%¥Éjã
0103 S. OP op-404:OP
0104 ( )
0105 S. OG og-404:OG
0106 ( )
0107 C.
0108 C. jãNMOG&OPÎî°è¥A¥0¥xjã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0120 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. ¥A¥0¥x½ªÎ»ð³ÎÇ§
0125 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG¤Î%Ê¹Ç•è²IOK¤ð³ÎÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0139 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. ¥A¥0¥x½ªÎ»ð³ÎÇ§
0144 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG¤Î%Ê¹Ç•è²IOK¤ð³ÎÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0158 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. ¥A¥0¥x½ªÎ»ð³ÎÇ§
0163 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP¤Î%Ê¹Ç•è²IOK¤ð³ÎÇ§
0165 C.
0166 C. ***** °Ê²¼¤Î¼Ã·¶Á°¤ÊÉ¬¤°Á÷¿® (%ãµ-¥A¥0¥x½ªè½Ç¤ðÁÖ¤Ç½¤°¬¤è%î¹Ç¤Ç¤â) *****
0167 C. DHU¥ãj%¥ÉjÊ¼¥½, ¥îj%¥ÉjÊ¤ðÎã¤¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0171 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE j§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¼¹Çj¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Î¼¹Ô¤°¤Ê¤¤¤³¤Êj£
0180 C. ¤¤¤¿j¢SET¤ÊDUMP¤Î¤±°î¥N¥¹¤Ç¹Ô¤¤¤³¤Êj£
0181 C.
0182 C. TI¥³¥¤¥0¥¥¤¤ðÁ¤Î¿(UT)
0183 +. TI 2009-02-10 10:26:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2009-02-10 10:26:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2009-02-10 10:26:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP

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(a) Spacecraft Operation Procedure (real-commands)

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main-405 2009-02-10 12:28:04 91 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Ä»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Äi¿¼¿¼Aa•µ°Æ»I×AÇ¼I¥C¥A¥×¥íj¼¥ÉjÉÉè¼µ•ííÉjÉ¼É¼°ÇÖ¼•¼¿¼i¹¿¼IjÇÀ®, Ü¼¹¼è¼¼¼ÇÁ÷¿®¼•¼É¼¼¼³¼Èj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ()
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCs Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. ***** AOCs Commands (Orbital Element Update) *****
0046 C. Update the orbital element
0047 +. DC 02-50 AOCU_ORB_PRPGT_START
0048 BC (16)
0049 +. DC 02-8E AOCU_ORB_UPD
0050 C.
0051 C. <A_ORB>[ORBIT] EPC = 7819125.4 +- 1.0 (s) [ ]
0052 C.
0053 C.
0054 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055 +. DC 07-FC EIS_MODE_MANU
0056 BC (21 02)
0057 . C. Verify EIS in MANUAL mode
0058 . C. Estimated OBSTBL upload time is 49s
0059 C. *****
0060 C. EIS START OBSTBL LOAD
0061 C. *****
0062 . S. RAM ram-820:EIS_OBSTBL
0063 ()
0064 +. DC 07-FC EIS_DUMP_OBSTBL
0065 BC (07 07 07 00 00 70 00)
0066 C.
0067 C. Execute, after the success of OBSTBL upload.
0068 C. Set EIS TI-commands
0069 +. TI 2009-02-10 10:30:50.0
0070 DC 07-FC EIS_MODE_CHG_ENA
0071 BC (20)
0072 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0073 C. *****
0074 C. EIS END OBSTBL LOAD
0075 C. *****
0076 C.
0077 . C. ***** MDP ´ÜÄI¼I»Ö¼Y¼ÉÄ¼¹¼èDCBC•x²è *****
0078 C. (¼Ä°I¥Ó¥A¥É¥¼¥É¥Ä¥C¥è¼¼¼¼¼Ä»Ü¼¹¼è)
0079 . S. DC-BC dcbc-402:DCBC
0080 (MDP_known_event)
0081 C.
0082 C.
0083 . C. ***** ¥Ð¥¹•I Daily±¿IÑ¼É´Ø¼¹¼èDCBC•x²è *****
0084 . S. DC-BC dcbc-153:DCBC
0085 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0086 C.
0087 C.
0088 . C. iãLOS¥Á¥S¥Ã¥¼Ä»Üjã
0089 C.
0090 . C. ***** LOS *****
0091 C.
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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 +. DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 +. DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 +. DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 06 70 71 0c 0c)
0126 +. DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 07 80 80 20 20)
0128 +. DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 08 80 80 06 06)
0130 +. DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 0f 80 80 04 04)
0132 +. DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 10 80 80 10 10)
0134 . C. ----- Success Verify ? OK / NG ____
0135 C.
0136 C.
0137 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0138 C.
0139 +. DC 07-F0 MDP_XRT_MODE_OBSV
0140 BC (c2)
0141 +. TI 2009-02-10 10:30:02.0
0142 DC 07-F0 MDP_XRT_MODE_OBSV
0143 BC (c2)
0144 . C. ----- Success Verify ? OK / NG ____
0145 C.
0146 C. ***** XRT END *****
0147 C. *****
0148 C. SOT table upload
0149 C. *****
0150 . C. < Stop FG table >
0151 +. DC 07-F0 MDP_FG_CTRL_MANU
0152 BC (51)
0153 . C. -----
0154 C. MDP_FG_CTRL_MODE = MANU [ ]
0155 C. -----
0156 C.
0157 . C. <Upload FG Observation Table>
0158 . S. RAM ram-268:MDP_OBS_F
0159 ( )
0160 C.
0161 . C. < Dump RAMID=MDP_OBS_F >
0162 +. DC 07-F0 MDP_DUMP_FGTBL
0163 BC (82 07 00 00 00 38 b8)
0164 C. -----
0165 C. MDP_OBS_F verify = OK/NG [ ]
0166 C. -----
0167 C.
0168 . C. < Upload DPL table >
0169 C.
0170 C. ¥¢¥Ã¥×¥¡;¥¥É°ÎÁ°¤ESTS_CHK¤òOFF¤Ě¤¹¤ě
0171 C.
0172 . S. RAM ram-271:MDP_DPL
0173 ( )
0174 C.
0175 . C. < Dump RAMID=MDP_DPL >
0176 +. DC 07-F0 MDP_DUMP_FGTBL
0177 BC (82 07 00 38 b8 00 40)
0178 C. -----
0179 C. MDP_DPL verify = OK [ ]
0180 C. -----
0181 C.
0182 C. STS_CHK¤òON¤Ě¤¹¤ě
0183 C.
0184 . C. < Update MDP DSC PAR1 >
0185 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0186 BC (4c)
0187 C. MDP_CMD_CODE = F04C0700 [ ]
0188 C. MDP_CMD_CNT (count-up 1) [ ]
0189 C. -----
0190 C.
0191 . C.
0192 C. *****
0193 C. SOT TI command set

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0194 C. *****
0195 C. Execute, after the success of TBL upload.
0196 +. TI 2009-02-10 10:30:18.0
0197 DC 07-F0 MDP_SOT_MODE_OBSV
0198 BC (40)
0199 . C. -----
0200 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0201 C. -----
0202 C.
0203 C.
0204 . C. ***** MDP '0ÃÎ«0%Ÿ«ÊÂ0¹«èDCBC•x²è *****
0205 C. (%â°İŸ0ŸÃŸÈŸpŸÊŸâŸÇŸè«½¼«¼Ã»Û¹«è)
0206 . S. DC-BC dcbc-402:DCBC
0207 (MDP_known_event)
0208 C.
0209 C.
0210 . C. ***** ŸÐŸ¹•İ Daily±¿ÎŸ«Ê'0¹«èDCBC•x²è *****
0211 . S. DC-BC dcbc-153:DCBC
0212 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0213 C.
0214 C.
0215 . C. iãLOSŸÁŸSŸÃŸ¬¼Ã»Ûiã
0216 C.
0217 . C. ***** LOS *****
0218 C.
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*** OP Sequence for XRT ***

2009/02/10	10:41:00.0	AOCS_ORe-point_Start_1_OG [0x097]	
		AOCU_NM	5 02-76 00 bc 73 33 1a
2009/02/10	12:21:00.0	AOCS_ORe-point_Start_2_OG [0x098]	
		AOCU_NM	5 02-76 00 43 8d 33 1a
2009/02/10	14:01:00.0	AOCS_ORe-point_Start_3_OG [0x099]	
		AOCU_NM	5 02-76 00 d5 27 4a 35
2009/02/10	15:40:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	
		MDP_XRT_CTRL_MANU	1 07-F0 c1
2009/02/10	15:40:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	
		XRT_FOCUS_POSITION	4 07-F8 22 fe 97 00
2009/02/10	15:41:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	
		AOCU_NM	5 02-76 00 2a d9 4a 35
2009/02/10	15:41:16.0	XRT_QT_PROG_SET_406_OG [0x196]	
		MDP_XRT_QT_PROG_SET	2 07-F0 c4 13
2009/02/10	15:41:18.0	XRT_AEC_RESET_415_OG [0x19f]	
		MDP_XRT_AEC_RESET	1 07-F0 d0
2009/02/10	15:41:20.0	XRT_ARS_DIS_422_OG [0x1a6]	
		MDP_XRT_ARS_DIS	1 07-F0 d5
2009/02/10	15:41:22.0	XRT_FLD_DIS_445_OG [0x1bd]	
		MDP_XRT_FLD_DIS	1 07-F0 d9
2009/02/10	15:41:24.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]	
		MDP_XRT_FLRCTRL_DIS	1 07-F0 c9
2009/02/10	15:43:00.0	XRT_CTRL_AUTO_403_OG [0x193]	
		MDP_XRT_CTRL_AUTO	1 07-F0 c0
2009/02/10	15:50:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	
		MDP_XRT_CTRL_MANU	1 07-F0 c1
2009/02/10	15:54:00.0	XRT_Custom_430_OG [0x1ae]	
2009/02/10	15:55:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	
		MDP_XRT_CTRL_AUTO	1 07-F0 c0
2009/02/10	17:07:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	
		MDP_XRT_CTRL_MANU	1 07-F0 c1
2009/02/10	17:30:30.0	XRT_Custom_430_OG [0x1ae]	
2009/02/10	17:31:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	
		MDP_XRT_CTRL_AUTO	1 07-F0 c0
2009/02/10	18:01:24.0	XRT_CTRL_MANU_428_OG [0x1ac]	
		MDP_XRT_CTRL_MANU	1 07-F0 c1
2009/02/10	18:01:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	
		XRT_FOCUS_POSITION	4 07-F8 22 ff aa 00
2009/02/10	18:01:30.0	AOCS_ORe-point_Start_5_OG [0x09b]	
		AOCU_NM	5 02-76 00 00 00 00 00
2009/02/10	18:01:46.0	XRT_QT_PROG_SET_408_OG [0x198]	
		MDP_XRT_QT_PROG_SET	2 07-F0 c4 07
2009/02/10	18:01:48.0	XRT_FLD_DIS_419_OG [0x1a3]	
		MDP_XRT_FLD_DIS	1 07-F0 d9
2009/02/10	18:01:50.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	
		MDP_XRT_FLRCTRL_DIS	1 07-F0 c9
2009/02/10	18:01:52.0	XRT_ARS_DIS_427_OG [0x1ab]	
		MDP_XRT_ARS_DIS	1 07-F0 d5
2009/02/10	18:03:30.0	XRT_CTRL_AUTO_444_OG [0x1bc]	
		MDP_XRT_CTRL_AUTO	1 07-F0 c0
2009/02/10	18:11:24.0	XRT_CTRL_MANU_428_OG [0x1ac]	
		MDP_XRT_CTRL_MANU	1 07-F0 c1
2009/02/10	18:11:26.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	
		XRT_FOCUS_POSITION	4 07-F8 22 fe 97 00
2009/02/10	18:11:30.0	AOCS_ORe-point_Start_5_OG [0x09b]	
		AOCU_NM	5 02-76 00 00 00 00 00
2009/02/10	18:11:46.0	XRT_QT_PROG_SET_443_OG [0x1bb]	
		MDP_XRT_QT_PROG_SET	2 07-F0 c4 03
2009/02/10	18:11:48.0	XRT_AEC_RESET_415_OG [0x19f]	
		MDP_XRT_AEC_RESET	1 07-F0 d0
2009/02/10	18:11:50.0	XRT_ARS_DIS_422_OG [0x1a6]	
		MDP_XRT_ARS_DIS	1 07-F0 d5
2009/02/10	18:11:52.0	XRT_FLD_DIS_445_OG [0x1bd]	
		MDP_XRT_FLD_DIS	1 07-F0 d9
2009/02/10	18:11:54.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]	
		MDP_XRT_FLRCTRL_DIS	1 07-F0 c9
2009/02/10	18:13:30.0	XRT_CTRL_AUTO_403_OG [0x193]	
		MDP_XRT_CTRL_AUTO	1 07-F0 c0
2009/02/10	18:44:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	
		MDP_XRT_CTRL_MANU	1 07-F0 c1
2009/02/10	19:07:30.0	XRT_Custom_430_OG [0x1ae]	
2009/02/10	19:08:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	
		MDP_XRT_CTRL_AUTO	1 07-F0 c0
2009/02/10	20:21:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	
		MDP_XRT_CTRL_MANU	1 07-F0 c1
2009/02/10	20:45:00.0	XRT_Custom_430_OG [0x1ae]	
2009/02/10	20:46:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	
		MDP_XRT_CTRL_AUTO	1 07-F0 c0
2009/02/10	21:58:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	
		MDP_XRT_CTRL_MANU	1 07-F0 c1
2009/02/10	22:21:30.0	XRT_Custom_430_OG [0x1ae]	
2009/02/10	22:22:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	
		MDP_XRT_CTRL_AUTO	1 07-F0 c0
2009/02/10	23:36:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	
		MDP_XRT_CTRL_MANU	1 07-F0 c1
2009/02/10	23:51:00.0	XRT_Custom_430_OG [0x1ae]	
2009/02/10	23:52:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	
		MDP_XRT_CTRL_AUTO	1 07-F0 c0
2009/02/10	23:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	
		MDP_XRT_CTRL_MANU	1 07-F0 c1
2009/02/10	23:59:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	

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2009/02/11	00:00:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
		AOCU_NM		5	02-76	04	00	00	00
2009/02/11	00:00:16.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e		
2009/02/11	00:00:18.0	XRT_AEC_RESET_415_OG [0x19f]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2009/02/11	00:00:20.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2009/02/11	00:00:22.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/02/11	00:00:24.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/02/11	00:02:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/02/11	01:07:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/11	01:19:30.0	XRT_Custom_430_OG [0x1ae]							
2009/02/11	01:20:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/02/11	02:35:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/11	02:56:00.0	XRT_Custom_430_OG [0x1ae]							
2009/02/11	02:57:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/02/11	04:03:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/11	04:33:00.0	XRT_Custom_430_OG [0x1ae]							
2009/02/11	04:34:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/02/11	05:34:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/11	05:34:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2009/02/11	05:35:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00	00	00	00
2009/02/11	05:35:16.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b		
2009/02/11	05:35:18.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/02/11	05:35:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/02/11	05:35:22.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2009/02/11	05:37:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/02/11	05:44:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/11	05:44:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2009/02/11	05:45:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	04	00	00	00
2009/02/11	05:45:16.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06		
2009/02/11	05:45:18.0	XRT_AEC_RESET_415_OG [0x19f]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2009/02/11	05:45:20.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2009/02/11	05:45:22.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/02/11	05:45:24.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/02/11	05:47:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/02/11	07:24:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/11	07:48:00.0	XRT_Custom_430_OG [0x1ae]							
2009/02/11	07:49:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/02/11	09:04:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/11	09:24:30.0	XRT_Custom_430_OG [0x1ae]							
2009/02/11	09:25:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/02/11	10:45:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/11	10:54:00.0	XRT_Custom_430_OG [0x1ae]							
2009/02/11	10:55:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/02/11	14:34:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/11	14:41:00.0	XRT_Custom_430_OG [0x1ae]							
2009/02/11	14:42:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/02/11	14:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/11	14:59:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2009/02/11	15:00:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	01	00	00	00
2009/02/11	15:00:16.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06		
2009/02/11	15:00:18.0	XRT_AEC_RESET_415_OG [0x19f]							

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2009/02/11	15:00:20.0	XRT_ARS_DIS_422_0G [0x1a6]	MDP_XRT_AEC_RESET	1	07-F0	d0
2009/02/11	15:00:22.0	XRT_FLD_DIS_445_0G [0x1bd]	MDP_XRT_ARS_DIS	1	07-F0	d5
2009/02/11	15:00:24.0	XRT_FLRCTRL_DIS_449_0G [0x1c1]	MDP_XRT_FLD_DIS	1	07-F0	d9
2009/02/11	15:02:00.0	XRT_CTRL_AUTO_403_0G [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/02/11	16:07:00.0	XRT_CTRL_MANU_435_0G [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/11	16:30:30.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/11	16:31:30.0	XRT_CTRL_AUTO_432_0G [0x1b0]	XRT_CTRL_AUTO	1	07-F0	c0
2009/02/11	17:44:00.0	XRT_CTRL_MANU_435_0G [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/11	18:07:30.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/11	18:08:30.0	XRT_CTRL_AUTO_432_0G [0x1b0]	XRT_CTRL_AUTO	1	07-F0	c0
2009/02/11	18:59:54.0	XRT_CTRL_MANU_428_0G [0x1ac]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/11	18:59:56.0	XRT_FOCUS_POSITION_442_0G [0x1ba]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/11	19:00:00.0	AOCS_ORe-point_Start_5_0G [0x09b]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2009/02/11	19:00:16.0	XRT_QT_PROG_SET_408_0G [0x198]	AOCU_NM	5	02-76	00 00 00 00 00
2009/02/11	19:00:18.0	XRT_FLD_DIS_419_0G [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2009/02/11	19:00:20.0	XRT_FLRCTRL_DIS_447_0G [0x1bf]	MDP_XRT_FLD_DIS	1	07-F0	d9
2009/02/11	19:00:22.0	XRT_ARS_DIS_427_0G [0x1ab]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/02/11	19:02:00.0	XRT_CTRL_AUTO_444_0G [0x1bc]	MDP_XRT_ARS_DIS	1	07-F0	d5
2009/02/11	19:09:24.0	XRT_CTRL_MANU_428_0G [0x1ac]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/11	19:09:26.0	XRT_FOCUS_POSITION_441_0G [0x1b9]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/11	19:09:46.0	XRT_QT_PROG_SET_426_0G [0x1aa]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/02/11	19:09:48.0	XRT_AEC_RESET_415_0G [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2009/02/11	19:09:50.0	XRT_ARS_DIS_422_0G [0x1a6]	MDP_XRT_AEC_RESET	1	07-F0	d0
2009/02/11	19:09:52.0	XRT_FLD_DIS_445_0G [0x1bd]	MDP_XRT_ARS_DIS	1	07-F0	d5
2009/02/11	19:09:54.0	XRT_FLRCTRL_DIS_449_0G [0x1c1]	MDP_XRT_FLD_DIS	1	07-F0	d9
2009/02/11	19:10:00.0	AOCS_ORe-point_Start_6_0G [0x09c]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/02/11	19:11:30.0	XRT_CTRL_AUTO_403_0G [0x193]	AOCU_NM	5	02-76	04 00 00 00 00
2009/02/11	19:21:00.0	XRT_CTRL_MANU_435_0G [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/11	19:44:30.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/11	19:45:30.0	XRT_CTRL_AUTO_432_0G [0x1b0]	XRT_CTRL_AUTO	1	07-F0	c0
2009/02/11	20:58:00.0	XRT_CTRL_MANU_435_0G [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/11	21:21:30.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/11	21:22:30.0	XRT_CTRL_AUTO_432_0G [0x1b0]	XRT_CTRL_AUTO	1	07-F0	c0
2009/02/11	22:35:30.0	XRT_CTRL_MANU_435_0G [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/11	22:57:30.5	XRT_Custom_430_0G [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/11	22:58:30.5	XRT_CTRL_AUTO_432_0G [0x1b0]	XRT_CTRL_AUTO	1	07-F0	c0
2009/02/12	00:13:00.0	XRT_CTRL_MANU_435_0G [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/12	00:22:30.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/12	00:23:30.0	XRT_CTRL_AUTO_432_0G [0x1b0]	XRT_CTRL_AUTO	1	07-F0	c0
2009/02/12	01:38:00.0	XRT_CTRL_MANU_435_0G [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/12	01:56:00.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/12	01:57:00.0	XRT_CTRL_AUTO_432_0G [0x1b0]	XRT_CTRL_AUTO	1	07-F0	c0
2009/02/12	03:12:00.0	XRT_CTRL_MANU_435_0G [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/12	03:33:00.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/12	03:34:00.0	XRT_CTRL_AUTO_432_0G [0x1b0]	XRT_CTRL_AUTO	1	07-F0	c0
2009/02/12	04:41:30.0	XRT_CTRL_MANU_435_0G [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/12	05:10:30.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/12	05:11:30.0	XRT_CTRL_AUTO_432_0G [0x1b0]	XRT_CTRL_AUTO	1	07-F0	c0
2009/02/12	05:53:24.0	XRT_CTRL_MANU_428_0G [0x1ac]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/12	05:53:26.0	XRT_FOCUS_POSITION_442_0G [0x1ba]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/12	05:53:46.0	XRT_QT_PROG_SET_421_0G [0x1a5]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00

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2009/02/12	05:53:48.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/02/12	05:53:50.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/02/12	05:53:52.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/02/12	05:54:30.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00	00 00 00 00
2009/02/12	05:55:30.0	XRT_CTRL_AUTO_444_OG [0x1bc]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/02/12	06:03:24.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/02/12	06:04:30.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	04	00 00 00 00
2009/02/12	07:00:00.0	XRT_FOCUS_RECALIBRATE_446_OG [0x1be]	XRT_FOCUS_RECAL	2	07-F8	78	00
2009/02/12	09:45:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00	00 00 00 00