

# XRT Timeline to be uploaded on 2009/02/21

Period: 2009/02/21 10:14:00 - 2009/02/26 11:09:00

\* \* \* \* \*

Normal mode

\* \* \* \* \*

<b>XOB #1563: CCD Monitor During Bakeout - G-band + dark - wide FOV</b>												
Term		Pointing (x, y)						Comment				
02/21 10:38:30 - 02/21 13:05:00		Track ( 866.4, -36.3) <sup>02/21 10:24:00</sup>						# OP start + 10min, track remnants of AR.				
<b>PROG= 06 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 600.0sec												
└─ Seqn= 98 1-time(s) 4.0sec												
└─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 2048x256 (1024, 1024) DPCM 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 2048x256 (1024, 1024) DPCM 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												
<b>XOB #1565: CCD Monitor During Bakeout - G-band + dark - wide FOV - lower cadence (30min)</b>												
Term		Pointing (x, y)						Comment				
02/21 13:09:10 - 02/22 02:26:00		Track ( 866.4, -36.3) <sup>02/21 10:24:00</sup>						# OP start + 10min, track remnants of AR.				
<b>PROG= 01 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 1800.0sec												
└─ Seqn= 98 1-time(s) 4.0sec												
└─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 2048x256 (1024, 1024) DPCM 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 2048x256 (1024, 1024) DPCM 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												
<b>XOB #15A8: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant</b>												
Term		Pointing (x, y)						Comment				
02/22 15:23:00 - 02/22 15:39:54		Fixed ( -528.4, -528.4)						* XRT post-bakeout four-quadrant pointing (4 in total).				
<b>PROG= 03 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 28 1-time(s) 12.0sec												
└─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												
<b>XOB #15A9: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant</b>												
Term		Pointing (x, y)						Comment				
02/22 15:43:00 - 02/22 15:49:54		Fixed ( 528.4, -528.4)										
<b>PROG= 10 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 41 1-time(s) 12.0sec												
└─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												
<b>XOB #15AA: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant</b>												
Term		Pointing (x, y)						Comment				
02/22 15:53:00 - 02/22 15:59:54		Fixed ( 528.4, 528.4)										
<b>PROG= 02 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 8 1-time(s) 12.0sec												
└─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												
<b>XOB #15AB: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant</b>												
Term		Pointing (x, y)						Comment				
02/22 16:03:00 - 02/22 17:03:54		Fixed ( -528.4, 528.4)										
<b>PROG= 18 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 40 1-time(s) 12.0sec												
└─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #165A: Synoptic Q95 2x2 - Al/poly(362/2048) + Dark cal(256 Q98) + Ti-poly(512/4096) + G-band(16)												
Term	Pointing (x, y)		Comment									
02/22 17:07:00 - 02/22 17:13:54	Fixed ( 0.0, 0.0)		synoptic, shifted manually.									
02/23 18:03:00 - 02/23 18:09:54	Fixed ( 0.0, 0.0)		synoptic									
<b>PROG= 13 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 37 1-time(s) 4.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 49 1-time(s) 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Dark 250ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 89 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 92 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1653: AR Dynamics/ CME watch - thin Be 256x256 - 20s cadence												
Term	Pointing (x, y)		Comment									
02/22 17:17:00 - 02/23 05:55:54	Fixed ( 930.0, -57.0)		# Fixed pointing at AR on W limb.									
02/23 06:09:00 - 02/23 09:59:54	Fixed ( 930.0, -57.0)		# Cont.									
02/23 15:39:30 - 02/23 17:59:54	Fixed ( 930.0, -57.0)		* Fixed pointing of AR at W limb.									
02/23 18:13:00 - 02/24 06:13:24	Fixed ( 930.0, -57.0)		# Fixed pointing at AR on W limb.									
02/24 06:27:00 - 02/24 11:11:54	Fixed ( 930.0, -57.0)		# Cont.									
<b>PROG= 17 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 83 6-time(s) 20.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 256x256 (1024, 1024) Q=90 1 0 2.0sec												
└─ Seqn= 84 1-time(s) 10.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 5.66s Obs 2x2 1024x1024 (1024, 1024) Q=95 2 0 2.0sec												
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #165B: Synoptic Q95 2x2 - Al/mesh(256/2048) + Dark cal(256 Q98) + Ti-poly(512/4096) + G-band(16)												
Term	Pointing (x, y)		Comment									
02/23 05:59:00 - 02/23 06:05:54	Fixed ( 0.0, 0.0)		synoptic, shifted -4.0 min									
02/24 06:16:30 - 02/24 06:23:54	Fixed ( 0.0, 0.0)		synoptic, shifted 14.5 min									
<b>PROG= 14 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 68 1-time(s) 4.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 50 1-time(s) 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Dark 250ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 89 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 92 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #162D: Full Sun - CME Watch - C/Poly + Thin-Be + Med-Be - 4x4 - AEC2												
Term	Pointing (x, y)		Comment									
02/23 10:17:30 - 02/23 15:25:30	Track ( -28.1, -0.1) @ 02/23 10:00:00		* Disk-center tracking for EIS sensitivity monitoring.									
<b>PROG= 15 Inf.-time(s)</b>												
└─ Subr= 1 4-time(s) 480.0sec												
└─ Seqn= 73 1-time(s) 60.0sec												
└─ C-poly/Open med-Be/Open close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
└─ Seqn= 71 1-time(s) 60.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 4.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
└─ Seqn= 44 1-time(s) 60.0sec												
└─ med-Be/Open med-Be/Open close Safe Norm 22.6s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
└─ Subr= 2 1-time(s) 60.0sec												
└─ Seqn= 87 1-time(s) 30.0sec												
└─ C-poly/Open C-poly/Open close Safe Dark 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

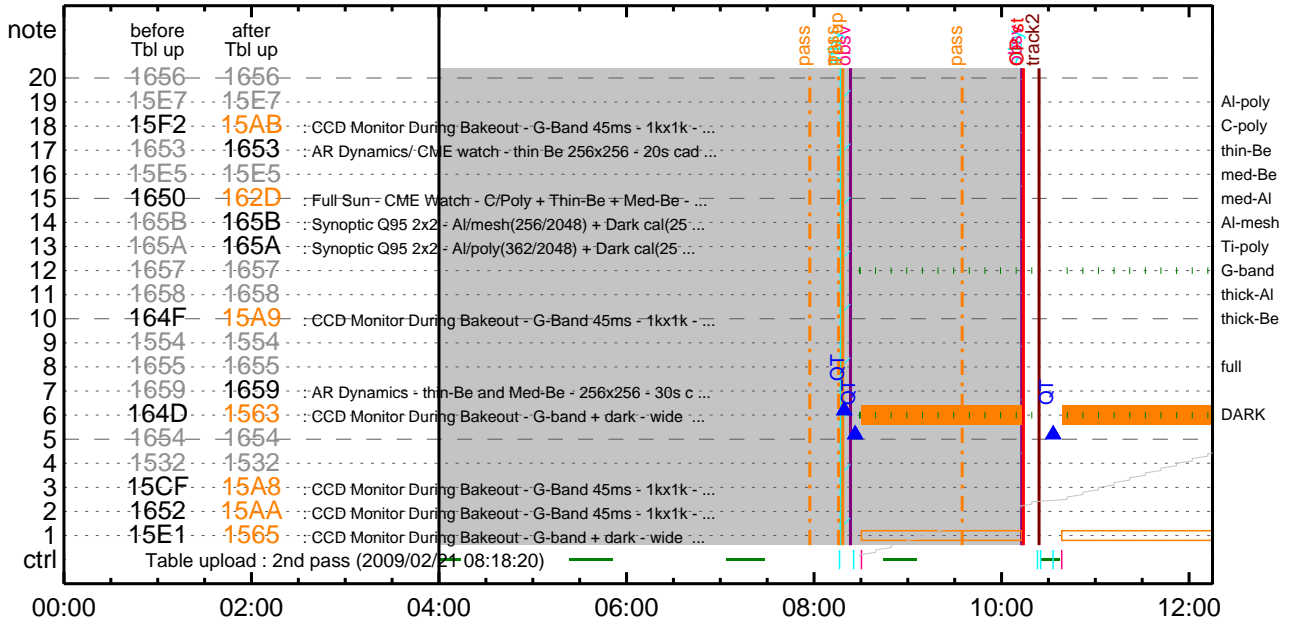
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* * * * *
                                Flare mode                                * * * * *
                                NOT USED
* * * * *
                                Active Region Search                        * * * * *
                                NOT USED
* * * * *
                                Flare Detection                            * * * * *

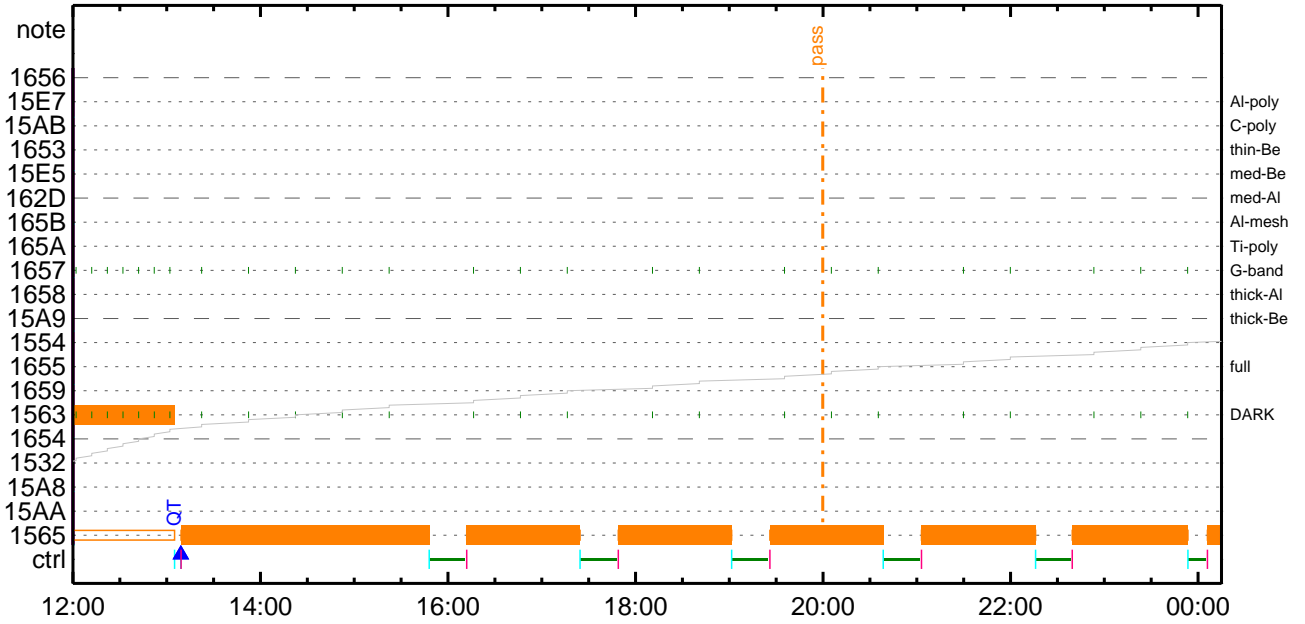
```

NOT USED

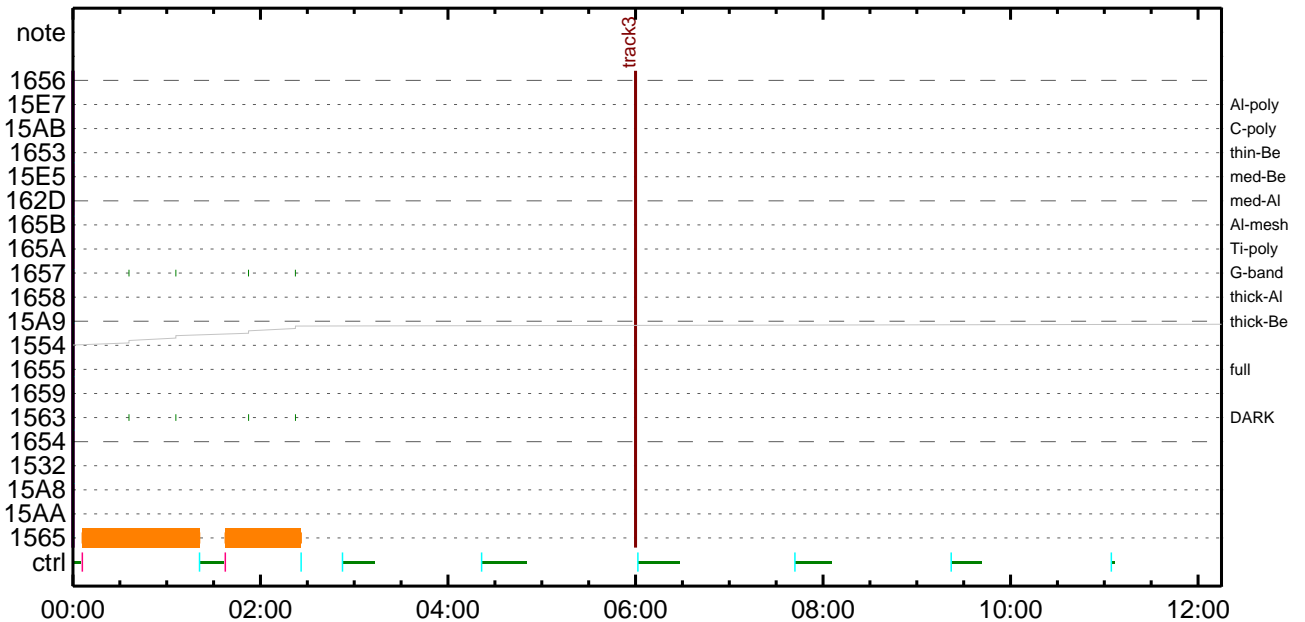
### CMDI #0417 2009/02/21



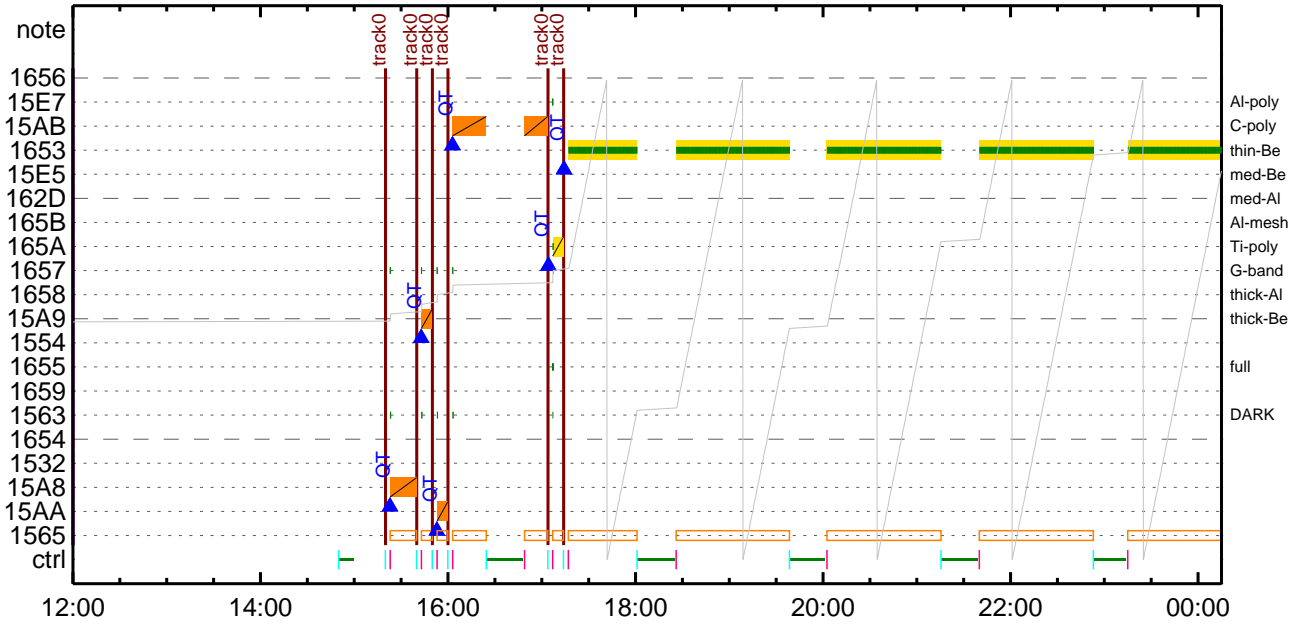
### CMDI #0417 2009/02/21



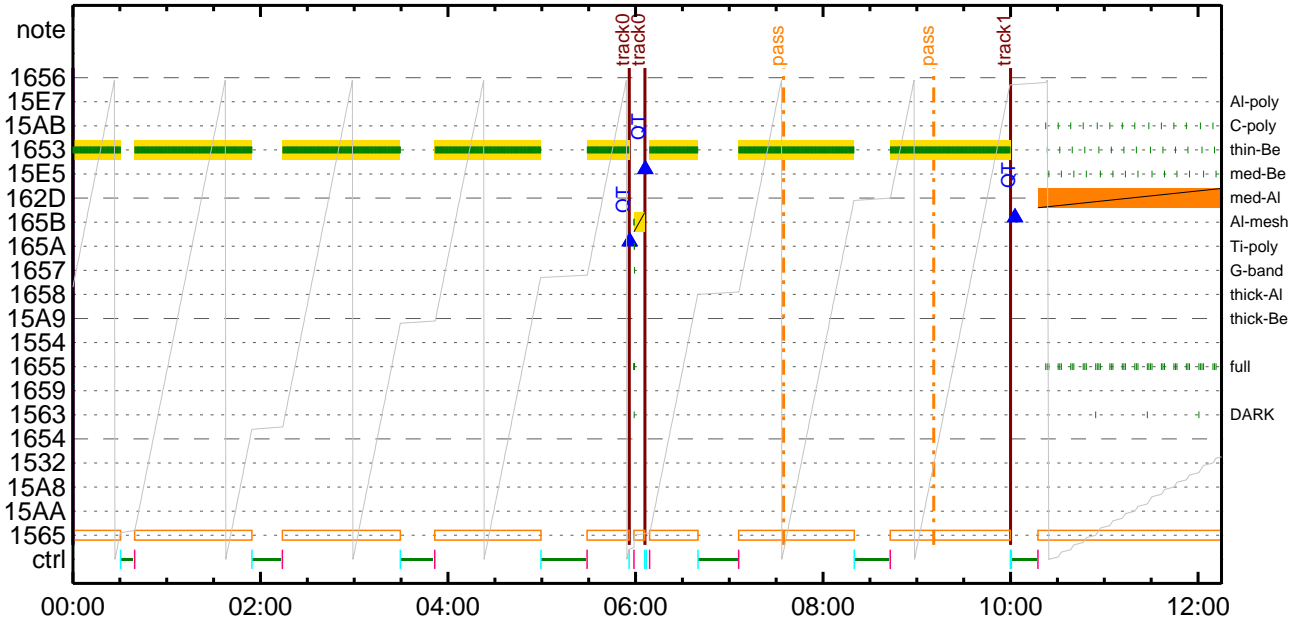
### CMDI #0417 2009/02/22



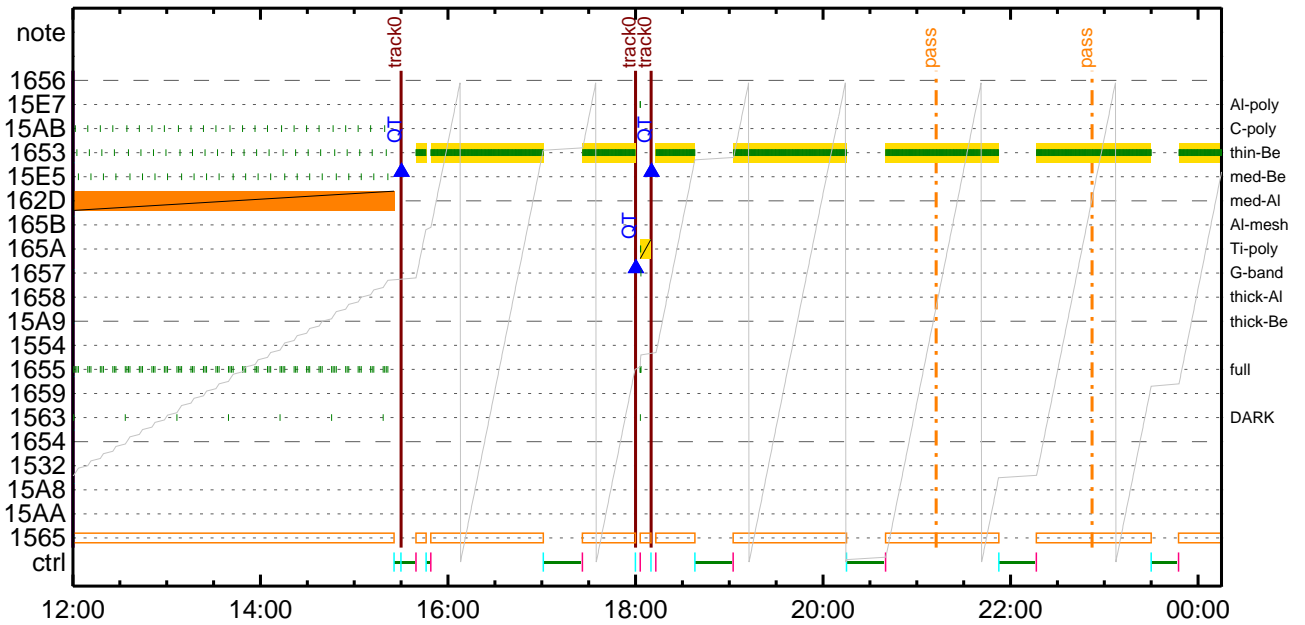
CMDI #0417 2009/02/22



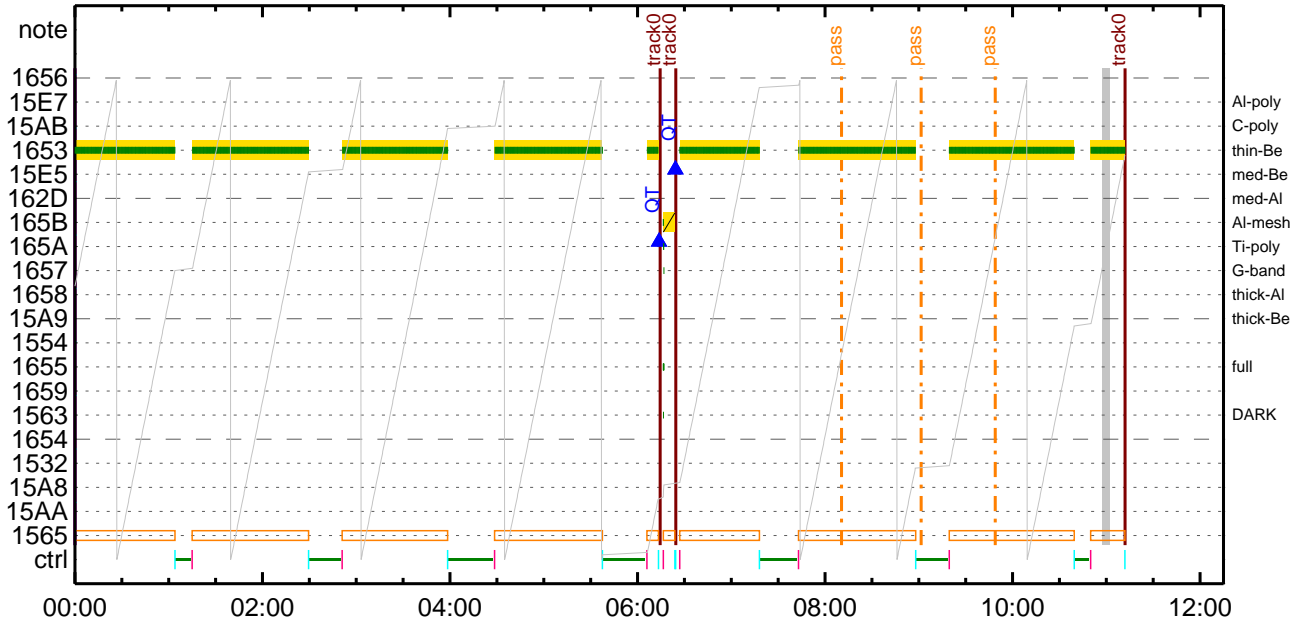
CMDI #0417 2009/02/23



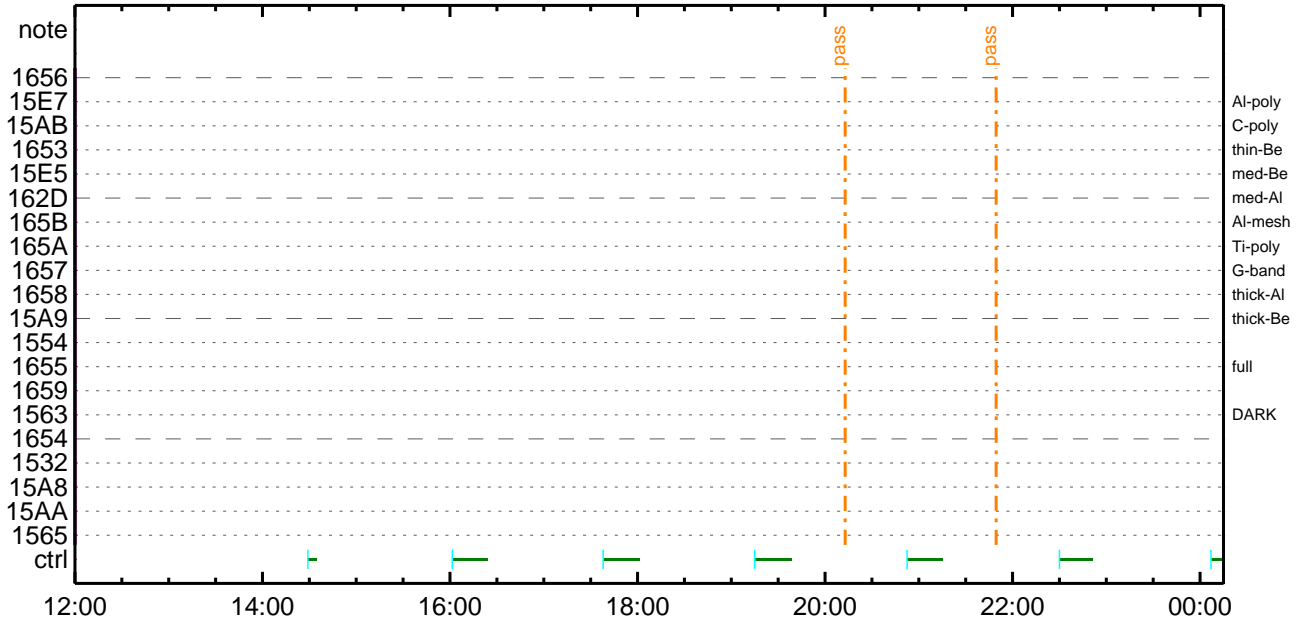
CMDI #0417 2009/02/23



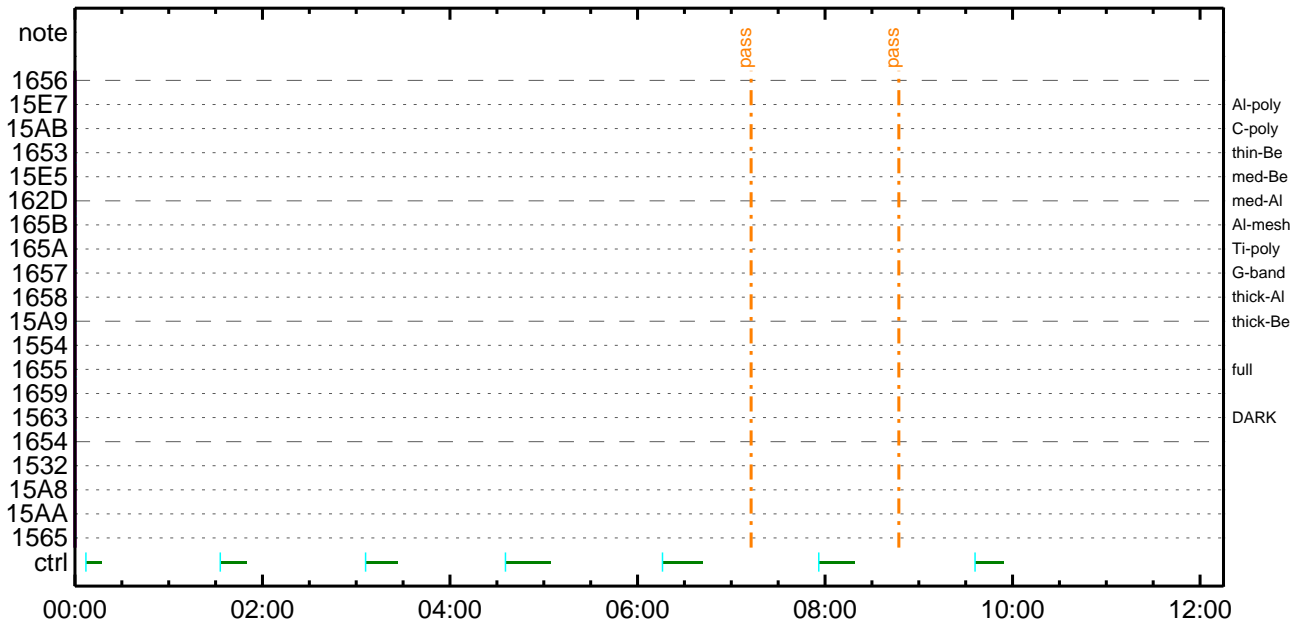
CMDI #0417 2009/02/24



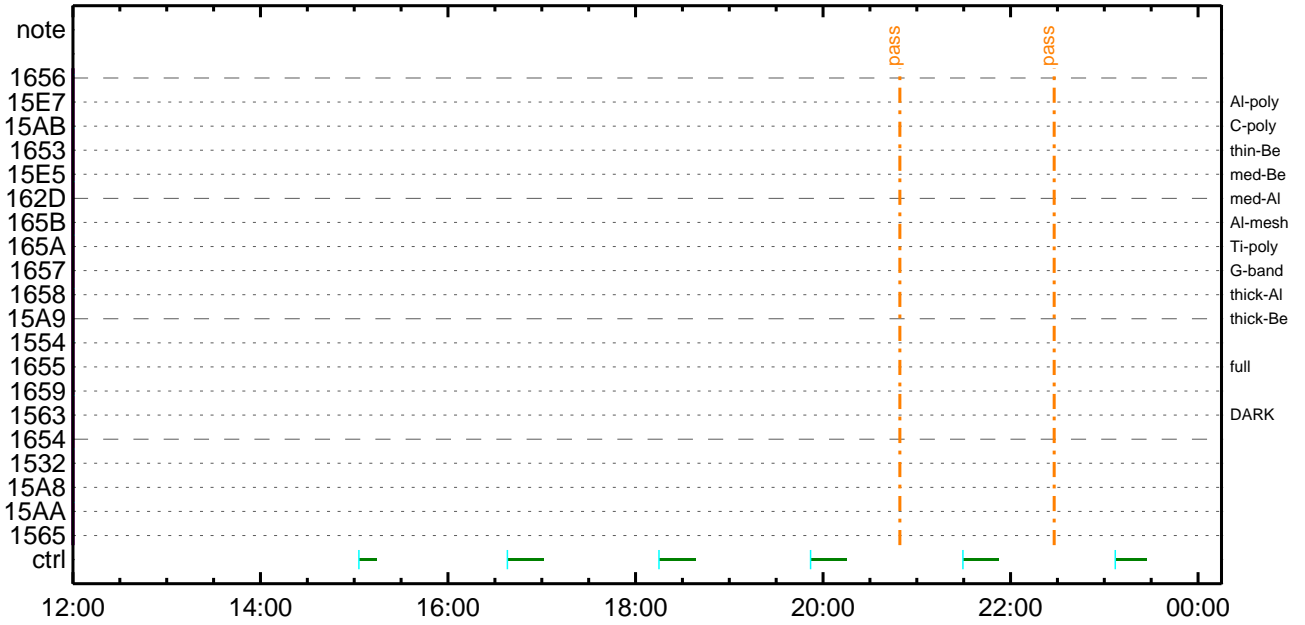
CMDI #0417 2009/02/24



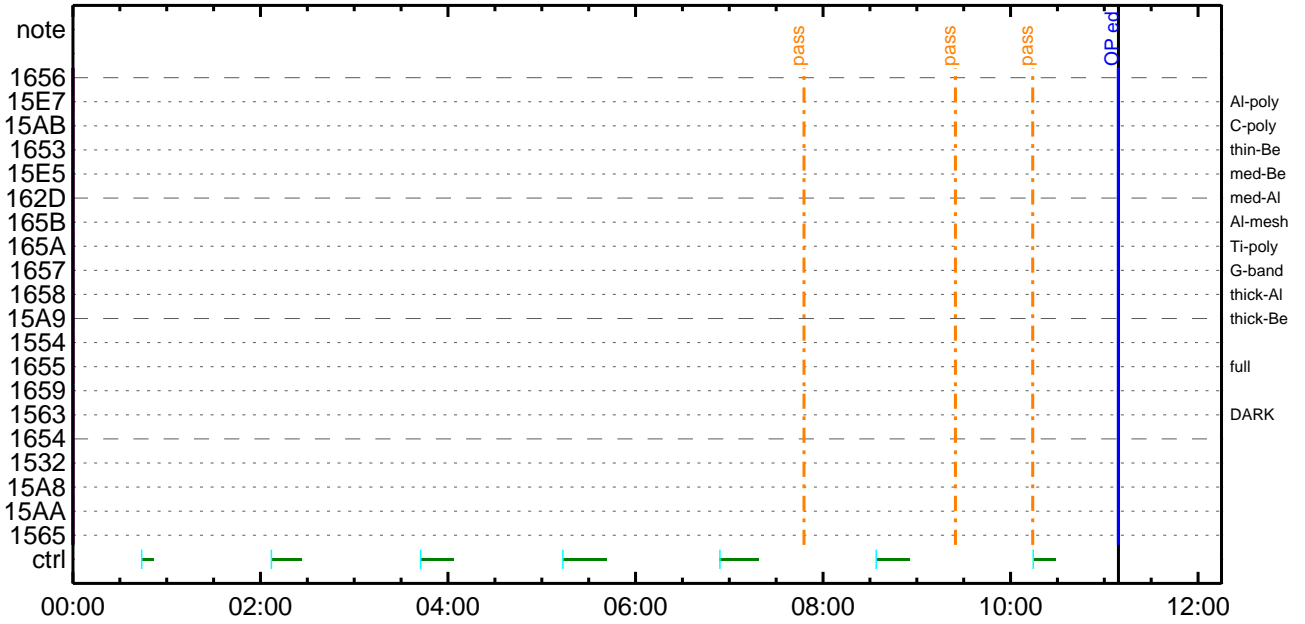
CMDI #0417 2009/02/25



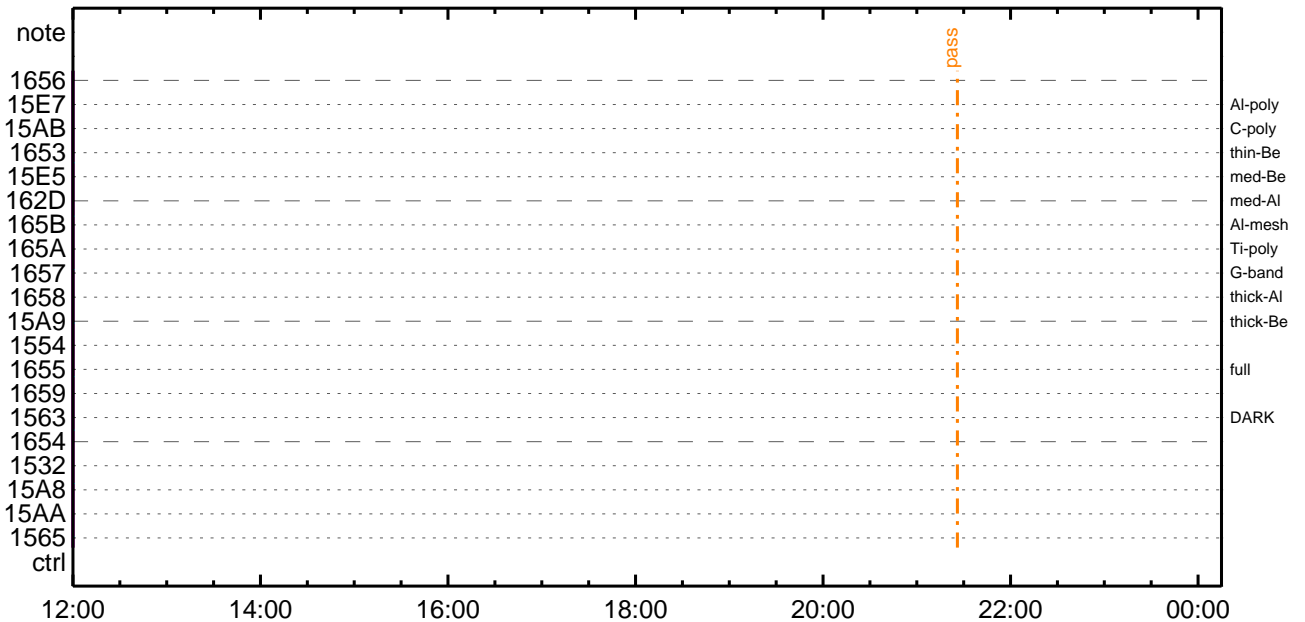
CMDI #0417 2009/02/25



CMDI #0417 2009/02/26



CMDI #0417 2009/02/26







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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-428:OP
0104 ( )
0105 S. OG og-428:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½A´¶Á°òEÉ-ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÓÆòÇ¼ª°"òè¼i¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½i¹ç;ç°E²¼òî½TI-CMDÁ÷¿@²î½Á¹Ôª°²E²ò³òE;f
0180 C. ²²ò¿;çSET²E²DUMP²î½±°iYNY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²Y½YÉòðÁDî¿(UT)
0183 +. TI 2009-02-21 10:09:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2009-02-21 10:09:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2009-02-21 10:09:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2009-02-21 10:13:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄë%îíñøîîŷÄŷ§ŷÄŷ-¹àîÛ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]                   EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]                       EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]                    EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]                     EQ      0xB3
0204 C.
0205 C. *****
0206 C. Tîîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]                   EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]                   EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]                    EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]                   EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]                      EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]                      EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]                     EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]                   EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]                   EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]                      EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»αò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]                      EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLαîîÊ¹ç•è²îOKαò³îç§
0229 C.
0230 C. DHUŷä;¼ŷÉ;Ê¼ŷ¼.ŷî;¼ŷÉ;Êøðîäα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]                      EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]                     EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]                   EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]                   EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2009-02-21 10:13:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2009-02-21 10:13:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [      ] [HK1_TI_CMD_NUM]                EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2009-02-21 10:13:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 C.          [      ] [HK1_TI_CMD_NUM]                EQ      1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP `ûÄîòî»ò¼ŷøÈÄð¹αèDCBC•×²è *****
0265 C. (¼ä°îŷÖŷÄŷËŷËŷËŷäŷçŷèøÊ¼αα¼Ä»Ûα¹αè)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ŷĐŷ¹.İ Daily±;îñøîîË´øα¹αèDCBC•×²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. ðäLOSŷÄŷŷŷÄŷ-¼Ä»Û;ä
0276 C.
0277 C. ***** LOS *****
0278 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```

main-429 2009-02-21 11:55:24 187 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿øÁø•µ°È»ÍxÁÇøíYçYÁY×Yí;¼YÉ;ÈÈ%µ•íÉ;ÈøÈ¼°ÇÔø•ø¿¼l¹çøí;çÁ®, ùø¹øÈøBøÇÁ+¿®ø•øÈøøø³øÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCs Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 C.
0046 C. ***** XRT START *****
0047 C.
0048 +. DC 07-F0 MDP_XRT_CTRL_MANU
0049 BC (c1)
0050 + DC 07-F0 MDP_XRT_MODE_STBY
0051 BC (c3)
0052 . C. ----- Success Verify ? OK / NG____
0053 C.
0054 C. XRT Obs. Table Upload
0055 . S. RAM ram-291:MDP_OBS_X
0056 ( )
0057 C.
0058 +. DC 07-F0 MDP_DUMP_XRTTBL
0059 BC (84 07 00 00 00 3a d4)
0060 . C. ----- Comparison Check ? OK / ERR _____
0061 C.
0062 C.
0063 +. DC 07-F0 MDP_XRT_ROI_SET
0064 BC (cd 01 b1 b1 04 04)
0065 + DC 07-F0 MDP_XRT_ROI_SET
0066 BC (cd 02 b1 b1 08 08)
0067 + DC 07-F0 MDP_XRT_ROI_SET
0068 BC (cd 03 b1 b1 08 08)
0069 + DC 07-F0 MDP_XRT_ROI_SET
0070 BC (cd 04 b1 b1 06 06)
0071 + DC 07-F0 MDP_XRT_ROI_SET
0072 BC (cd 06 80 80 20 04)
0073 + DC 07-F0 MDP_XRT_ROI_SET
0074 BC (cd 07 c0 c0 10 10)
0075 + DC 07-F0 MDP_XRT_ROI_SET
0076 BC (cd 08 40 c0 10 10)
0077 + DC 07-F0 MDP_XRT_ROI_SET
0078 BC (cd 09 40 40 10 10)
0079 + DC 07-F0 MDP_XRT_ROI_SET
0080 BC (cd 0a c0 40 10 10)
0081 + DC 07-F0 MDP_XRT_ROI_SET
0082 BC (cd 0b 80 80 20 20)
0083 + DC 07-F0 MDP_XRT_ROI_SET
0084 BC (cd 0c 80 80 04 04)
0085 + DC 07-F0 MDP_XRT_ROI_SET
0086 BC (cd 0d 80 80 10 10)
0087 + DC 07-F0 MDP_XRT_ROI_SET
0088 BC (cd 0f 80 80 04 04)
0089 + DC 07-F0 MDP_XRT_ROI_SET
0090 BC (cd 10 80 80 10 10)
0091 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0092 BC (c4 07)
0093 +. DC 07-F0 MDP_XRT_AEC_RESET
0094 BC (d0)
0095 +. DC 07-F0 MDP_XRT_ARS_DIS

```





```

0096 . C.
0097 . C.
0098 . C. *****
0099 . C. SOT table upload
0100 . C. *****
0101 . C. < Upload DPL table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 . C. -----
0107 . C.
0108 C. ¥ç¥Ã¥×¥í;¼¥É°îÁ°æESTS_CHKαðOFFαÈα¹æ
0109 . C.
0110 . S. RAM ram-271:MDP_DPL
0111 ( )
0112 . C.
0113 . C. < Dump RAMID=MDP_DPL >
0114 +. DC 07-F0 MDP_DUMP_FGTBL
0115 BC (82 07 00 38 b8 00 40)
0116 . C. -----
0117 C. MDP_DPL verify = OK [ ]
0118 . C. -----
0119 . C.
0120 C. STS_CHKαðONαÈα¹æ
0121 . C.
0122 . C. < Update MDP DSC PAR1 >
0123 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0124 BC (4c)
0125 C. MDP_CMD_CODE = F04C0700[ ]
0126 C. MDP_CMD_CNT (count-up 1) [ ]
0127 . C. -----
0128 . C.
0129 . C.
0130 . C.
0131 . C. ***** MDP `úÃîâî»ð¼ÝαÈÄðα¹æDCBC•×²è *****
0132 C. (%ã°îÏÖ¥Ã¥È¥Þ¥È¥ã¥ç¥ÈαÈ¼αα¼Ã»Ûα¹æ)
0133 . S. DC-BC dcbc-402:DCBC
0134 (MDP_known_event)
0135 . C.
0136 . C.
0137 . C. ***** ¥Ð¥¹•Ï Daily±;îÑαÈ´Øα¹æDCBC•×²è *****
0138 . S. DC-BC dcbc-153:DCBC
0139 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0140 . C.
0141 . C.
0142 . C. ;ãLOS¥Ã¥$¥Ã¥¹¼Ã»Û;ä
0143 . C.
0144 . C. ***** LOS *****
0145 . C.

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\*\*\* OP Sequence for XRT \*\*\*

2009/02/21	10:23:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/21	10:24:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2009/02/21	10:25:01.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/21	10:33:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/21	10:33:02.0	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2009/02/21	10:33:04.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/02/21	10:33:06.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/02/21	10:33:08.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/02/21	10:37:30.0	XRT_Custom_430_OG [0x1ae]							
2009/02/21	10:38:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/02/21	13:05:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/21	13:09:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/21	13:09:02.0	XRT_QT_PROG_SET_400_OG [0x190]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2009/02/21	13:09:04.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/02/21	13:09:06.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/02/21	13:09:08.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/02/21	13:09:10.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/02/21	15:48:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/21	16:11:00.0	XRT_Custom_430_OG [0x1ae]							
2009/02/21	16:12:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/02/21	17:24:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/21	17:48:00.0	XRT_Custom_430_OG [0x1ae]							
2009/02/21	17:49:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/02/21	19:01:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/21	19:25:00.0	XRT_Custom_430_OG [0x1ae]							
2009/02/21	19:26:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/02/21	20:38:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/21	21:02:00.0	XRT_Custom_430_OG [0x1ae]							
2009/02/21	21:03:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/02/21	22:16:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/21	22:38:30.0	XRT_Custom_430_OG [0x1ae]							
2009/02/21	22:39:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/02/21	23:53:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/22	00:05:00.0	XRT_Custom_430_OG [0x1ae]							
2009/02/22	00:06:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/02/22	01:21:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/22	01:36:30.0	XRT_Custom_430_OG [0x1ae]							
2009/02/22	01:37:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/02/22	02:26:00.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/22	02:26:02.0	XRT_TCIB_XRT_S_HTR_A_DIS_417_OG [0x1a1]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2009/02/22	02:52:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/22	04:21:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/22	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2009/02/22	06:01:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/22	07:42:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/22	09:22:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/22	11:04:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/22	14:50:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/22	15:19:54.0	XRT_CTRL_MANU_448_OG [0x1c0]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/22	15:20:00.0	AOCS_ORe-point_Start_3_OG [0x099]							

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2009/02/22	15:22:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	5	02-76	00	2e f9	2e f9
		XRT_FOCUS_POSITION	4	07-F8	22	ff aa	00
2009/02/22	15:22:52.0	XRT_QT_PROG_SET_443_OG [0x1bb]	2	07-F0	c4	03	
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03	
2009/02/22	15:22:54.0	XRT_FLD_DIS_419_OG [0x1a3]	1	07-F0	d9		
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2009/02/22	15:22:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	1	07-F0	c9		
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2009/02/22	15:22:58.0	XRT_ARS_DIS_418_OG [0x1a2]	1	07-F0	d5		
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2009/02/22	15:23:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	1	07-F0	c0		
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/02/22	15:39:54.0	XRT_CTRL_MANU_448_OG [0x1c0]	1	07-F0	c1		
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/02/22	15:40:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	5	02-76	00	2e f9	d1 07
		AOCU_NM	5	02-76	00	2e f9	d1 07
2009/02/22	15:42:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	4	07-F8	22	ff aa	00
		XRT_FOCUS_POSITION	4	07-F8	22	ff aa	00
2009/02/22	15:42:52.0	XRT_QT_PROG_SET_411_OG [0x19b]	2	07-F0	c4	0a	
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a	
2009/02/22	15:42:54.0	XRT_FLD_DIS_419_OG [0x1a3]	1	07-F0	d9		
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2009/02/22	15:42:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	1	07-F0	c9		
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2009/02/22	15:42:58.0	XRT_ARS_DIS_418_OG [0x1a2]	1	07-F0	d5		
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2009/02/22	15:43:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	1	07-F0	c0		
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/02/22	15:49:54.0	XRT_CTRL_MANU_448_OG [0x1c0]	1	07-F0	c1		
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/02/22	15:50:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	5	02-76	00	d1 07	d1 07
		AOCU_NM	5	02-76	00	d1 07	d1 07
2009/02/22	15:52:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	4	07-F8	22	ff aa	00
		XRT_FOCUS_POSITION	4	07-F8	22	ff aa	00
2009/02/22	15:52:52.0	XRT_QT_PROG_SET_414_OG [0x19e]	2	07-F0	c4	02	
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	02	
2009/02/22	15:52:54.0	XRT_FLD_DIS_419_OG [0x1a3]	1	07-F0	d9		
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2009/02/22	15:52:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	1	07-F0	c9		
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2009/02/22	15:52:58.0	XRT_ARS_DIS_418_OG [0x1a2]	1	07-F0	d5		
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2009/02/22	15:53:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	1	07-F0	c0		
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/02/22	15:59:54.0	XRT_CTRL_MANU_448_OG [0x1c0]	1	07-F0	c1		
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/02/22	16:00:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	5	02-76	00	d1 07	2e f9
		AOCU_NM	5	02-76	00	d1 07	2e f9
2009/02/22	16:02:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	4	07-F8	22	ff aa	00
		XRT_FOCUS_POSITION	4	07-F8	22	ff aa	00
2009/02/22	16:02:52.0	XRT_QT_PROG_SET_424_OG [0x1a8]	2	07-F0	c4	12	
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12	
2009/02/22	16:02:54.0	XRT_FLD_DIS_419_OG [0x1a3]	1	07-F0	d9		
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2009/02/22	16:02:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	1	07-F0	c9		
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2009/02/22	16:02:58.0	XRT_ARS_DIS_418_OG [0x1a2]	1	07-F0	d5		
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2009/02/22	16:03:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	1	07-F0	c0		
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/02/22	16:24:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	1	07-F0	c1		
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/02/22	16:48:00.0	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0		
		XRT_CTRL_AUTO_432_OG [0x1b0]	1	07-F0	c0		
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/02/22	17:03:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	1	07-F0	c1		
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/02/22	17:03:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	4	07-F8	22	ff aa	00
		XRT_FOCUS_POSITION	4	07-F8	22	ff aa	00
2009/02/22	17:04:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	5	02-76	00	00 00	00 00
		AOCU_NM	5	02-76	00	00 00	00 00
2009/02/22	17:04:16.0	XRT_QT_PROG_SET_405_OG [0x195]	2	07-F0	c4	0d	
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d	
2009/02/22	17:04:18.0	XRT_FLD_DIS_419_OG [0x1a3]	1	07-F0	d9		
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2009/02/22	17:04:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	1	07-F0	c9		
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2009/02/22	17:04:22.0	XRT_ARS_DIS_410_OG [0x19a]	1	07-F0	d5		
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2009/02/22	17:07:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]	1	07-F0	c0		
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/02/22	17:13:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	1	07-F0	c1		
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/02/22	17:13:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	4	07-F8	22	fe 97	00
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97	00
2009/02/22	17:14:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	5	02-76	00	05 0e	ad 59
		AOCU_NM	5	02-76	00	05 0e	ad 59
2009/02/22	17:14:16.0	XRT_QT_PROG_SET_423_OG [0x1a7]	2	07-F0	c4	11	
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11	
2009/02/22	17:14:18.0	XRT_AEC_RESET_415_OG [0x19f]	1	07-F0	d0		
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2009/02/22	17:14:20.0	XRT_ARS_DIS_422_OG [0x1a6]	1	07-F0	d0		



2009/02/22	17:14:22.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_ARS_DIS	1	07-F0	d5	
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/02/22	17:14:24.0	XRT_FLRCTRL_DIS_406_OG [0x196]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/02/22	17:17:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/02/22	18:01:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/02/22	18:25:00.0	XRT_Custom_430_OG [0x1ae]					
2009/02/22	18:26:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/02/22	19:38:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/02/22	20:01:30.0	XRT_Custom_430_OG [0x1ae]					
2009/02/22	20:02:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/02/22	21:15:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/02/22	21:39:00.0	XRT_Custom_430_OG [0x1ae]					
2009/02/22	21:40:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/02/22	22:53:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/02/22	23:14:00.0	XRT_Custom_430_OG [0x1ae]					
2009/02/22	23:15:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/02/23	00:30:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/02/23	00:38:30.0	XRT_Custom_430_OG [0x1ae]					
2009/02/23	00:39:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/02/23	01:54:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/02/23	02:13:00.0	XRT_Custom_430_OG [0x1ae]					
2009/02/23	02:14:00.5	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/02/23	03:29:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/02/23	03:50:30.0	XRT_Custom_430_OG [0x1ae]					
2009/02/23	03:51:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/02/23	04:59:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/02/23	05:28:00.0	XRT_Custom_430_OG [0x1ae]					
2009/02/23	05:29:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/02/23	05:55:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/02/23	05:55:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2009/02/23	05:56:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 00 00 00 00	
2009/02/23	05:56:16.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2009/02/23	05:56:18.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/02/23	05:56:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/02/23	05:56:22.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/02/23	05:59:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/02/23	06:05:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/02/23	06:05:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2009/02/23	06:06:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00 05 0e ad 59	
2009/02/23	06:06:16.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2009/02/23	06:06:18.0	XRT_AEC_RESET_415_OG [0x19f]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2009/02/23	06:06:20.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/02/23	06:06:22.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/02/23	06:06:24.0	XRT_FLRCTRL_DIS_406_OG [0x196]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/02/23	06:06:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/02/23	06:09:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/02/23	06:40:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/02/23	07:05:00.0	XRT_Custom_430_OG [0x1ae]					
2009/02/23	07:06:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/02/23	08:20:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/02/23	08:42:00.0	XRT_Custom_430_OG [0x1ae]					
2009/02/23	08:43:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	

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2009/02/23	09:59:54.0	XRT_CTRL_MANU_448_OG [0x1c0] MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/23	10:00:00.0	AOCs_OrE-point_Start_9_OG [0x09f] AOCU_NM	5	02-76	01 00 00 00 00
2009/02/23	10:00:30.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/23	10:02:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2009/02/23	10:02:52.0	XRT_QT_PROG_SET_409_OG [0x199] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2009/02/23	10:02:54.0	XRT_ARS_DIS_422_OG [0x1a6] MDP_XRT_ARS_DIS	1	07-F0	d5
2009/02/23	10:02:56.0	XRT_FLD_DIS_419_OG [0x1a3] MDP_XRT_FLD_DIS	1	07-F0	d9
2009/02/23	10:02:58.0	XRT_FLRCTRL_DIS_447_OG [0x1bf] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/02/23	10:03:00.0	XRT_ARS_DIS_418_OG [0x1a2] MDP_XRT_ARS_DIS	1	07-F0	d5
2009/02/23	10:16:30.0	XRT_Custom_430_OG [0x1ae]			
2009/02/23	10:17:30.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/23	15:25:30.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/23	15:29:54.0	XRT_CTRL_MANU_428_OG [0x1ac] MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/23	15:29:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/02/23	15:30:00.0	AOCs_OrE-point_Start_8_OG [0x09e] AOCU_NM	5	02-76	00 05 0e ad 59
2009/02/23	15:30:16.0	XRT_QT_PROG_SET_423_OG [0x1a7] MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2009/02/23	15:30:18.0	XRT_AEC_RESET_415_OG [0x19f] MDP_XRT_AEC_RESET	1	07-F0	d0
2009/02/23	15:30:20.0	XRT_ARS_DIS_422_OG [0x1a6] MDP_XRT_ARS_DIS	1	07-F0	d5
2009/02/23	15:30:22.0	XRT_FLD_DIS_445_OG [0x1bd] MDP_XRT_FLD_DIS	1	07-F0	d9
2009/02/23	15:30:24.0	XRT_FLRCTRL_DIS_406_OG [0x196] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/02/23	15:38:30.0	XRT_Custom_430_OG [0x1ae]			
2009/02/23	15:39:30.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/23	15:46:00.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/23	15:48:00.0	XRT_Custom_430_OG [0x1ae]			
2009/02/23	15:49:00.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/23	17:01:00.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/23	17:25:00.0	XRT_Custom_430_OG [0x1ae]			
2009/02/23	17:26:00.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/23	17:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac] MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/23	17:59:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2009/02/23	18:00:00.0	AOCs_OrE-point_Start_7_OG [0x09d] AOCU_NM	5	02-76	00 00 00 00 00
2009/02/23	18:00:16.0	XRT_QT_PROG_SET_405_OG [0x195] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2009/02/23	18:00:18.0	XRT_FLD_DIS_419_OG [0x1a3] MDP_XRT_FLD_DIS	1	07-F0	d9
2009/02/23	18:00:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/02/23	18:00:22.0	XRT_ARS_DIS_410_OG [0x19a] MDP_XRT_ARS_DIS	1	07-F0	d5
2009/02/23	18:03:00.0	XRT_CTRL_AUTO_444_OG [0x1bc] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/23	18:09:54.0	XRT_CTRL_MANU_428_OG [0x1ac] MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/23	18:09:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/02/23	18:10:00.0	AOCs_OrE-point_Start_8_OG [0x09e] AOCU_NM	5	02-76	00 05 0e ad 59
2009/02/23	18:10:16.0	XRT_QT_PROG_SET_423_OG [0x1a7] MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2009/02/23	18:10:18.0	XRT_AEC_RESET_415_OG [0x19f] MDP_XRT_AEC_RESET	1	07-F0	d0
2009/02/23	18:10:20.0	XRT_ARS_DIS_422_OG [0x1a6] MDP_XRT_ARS_DIS	1	07-F0	d5
2009/02/23	18:10:22.0	XRT_FLD_DIS_445_OG [0x1bd] MDP_XRT_FLD_DIS	1	07-F0	d9
2009/02/23	18:10:24.0	XRT_FLRCTRL_DIS_406_OG [0x196] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/02/23	18:13:00.0	XRT_CTRL_AUTO_403_OG [0x193] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/23	18:38:00.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/23	19:01:30.0	XRT_Custom_430_OG [0x1ae]			
2009/02/23	19:02:30.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/23	20:15:00.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1

2009/02/23	20:39:00.0	XRT_Custom_430_OG [0x1ae]			
2009/02/23	20:40:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/23	21:52:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/23	22:15:30.0	XRT_Custom_430_OG [0x1ae]			
2009/02/23	22:16:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/23	23:30:00.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/23	23:46:30.0	XRT_Custom_430_OG [0x1ae]			
2009/02/23	23:47:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/24	01:04:00.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/24	01:14:00.0	XRT_Custom_430_OG [0x1ae]			
2009/02/24	01:15:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/24	02:29:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/24	02:50:00.0	XRT_Custom_430_OG [0x1ae]			
2009/02/24	02:51:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/24	03:58:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/24	04:27:30.0	XRT_Custom_430_OG [0x1ae]			
2009/02/24	04:28:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/24	05:37:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/24	06:05:00.0	XRT_Custom_430_OG [0x1ae]			
2009/02/24	06:06:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/24	06:13:24.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/24	06:13:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2009/02/24	06:13:46.0	XRT_QT_PROG_SET_426_OG [0x1aa]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2009/02/24	06:13:48.0	XRT_FLD_DIS_419_OG [0x1a3]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/02/24	06:13:50.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/02/24	06:13:52.0	XRT_ARS_DIS_410_OG [0x19a]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/02/24	06:14:30.0	AOCS_Ore-point_Start_7_OG [0x09d]			
		AOCU_NM	5	02-76	00 00 00 00 00
2009/02/24	06:16:30.0	XRT_CTRL_AUTO_444_OG [0x1bc]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/24	06:23:54.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/24	06:23:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/02/24	06:24:16.0	XRT_QT_PROG_SET_423_OG [0x1a7]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2009/02/24	06:24:18.0	XRT_AEC_RESET_415_OG [0x19f]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2009/02/24	06:24:20.0	XRT_ARS_DIS_422_OG [0x1a6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/02/24	06:24:22.0	XRT_FLD_DIS_445_OG [0x1bd]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/02/24	06:24:24.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/24	06:24:24.5	XRT_FLRCTRL_DIS_406_OG [0x196]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/02/24	06:24:30.0	AOCS_Ore-point_Start_8_OG [0x09e]			
		AOCU_NM	5	02-76	00 05 0e ad 59
2009/02/24	06:27:00.5	XRT_CTRL_AUTO_403_OG [0x193]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/24	07:18:00.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/24	07:42:00.0	XRT_Custom_430_OG [0x1ae]			
2009/02/24	07:43:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/24	08:58:00.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/24	09:18:30.0	XRT_Custom_430_OG [0x1ae]			
2009/02/24	09:19:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/24	10:39:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/24	10:49:00.0	XRT_Custom_430_OG [0x1ae]			
2009/02/24	10:50:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/02/24	11:11:54.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/24	11:12:00.0	AOCS_Ore-point_Start_7_OG [0x09d]			
		AOCU_NM	5	02-76	00 00 00 00 00
2009/02/24	14:29:00.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/24	16:01:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/24	17:38:00.0	XRT_CTRL_MANU_435_OG [0x1b3]			

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2009/02/24	19:15:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/24	20:52:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/24	22:30:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/25	00:07:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/25	01:33:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/25	03:06:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/25	04:35:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/25	06:16:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/25	07:56:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/25	09:36:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/25	15:03:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/25	16:38:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/25	18:15:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/25	19:52:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/25	21:29:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/25	23:07:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/26	00:44:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/26	02:07:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/26	03:42:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/26	05:13:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/26	06:54:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/26	08:34:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/02/26	10:14:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1