

# XRT Timeline to be uploaded on 2009/02/24

Period: 2009/02/24 11:02:00 - 2009/02/28 09:42:00

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## Normal mode

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XOB #15E7: HOP81 - Al/poly, G-band - 16s - 384x384													
Term		Pointing (x, y)					Comment						
02/24 11:12:26 - 02/24 17:38:00		Fixed ( 0.0, -954.0)					# OP start + 10min, Observe S pole.						
02/26 06:10:26 - 02/26 11:18:54		Fixed ( 0.0, -954.0)					# Observe S pole.						
<b>PROG= 19 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 1 1-time(s) 2.0sec													
Open/G-band		Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 99 30-time(s) 60.0sec													
Al-poly/Open		Al-poly/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #165A: Synoptic Q95 2x2 - Al/poly(362/2048) + Dark cal(256 Q98) + Ti-poly(512/4096) + G-band(16)													
Term		Pointing (x, y)					Comment						
02/24 18:04:30 - 02/24 18:11:24		Fixed ( 0.0, 0.0)					synoptic, shifted 1.5 min						
02/25 19:20:00 - 02/25 19:52:00		Fixed ( 0.0, 0.0)					synoptic, shifted manually.						
<b>PROG= 13 1-time(s)</b>													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 37 1-time(s) 4.0sec													
Al-poly/Open		Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open		Al-poly/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 49 1-time(s) 2.0sec													
Al-poly/Open		Al-poly/Open	close	Safe	Dark	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 89 1-time(s) 4.0sec													
Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 92 1-time(s) 2.0sec													
Open/G-band		Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1649: CH Jets - Al/poly - 32s exp - FOV384													
Term		Pointing (x, y)					Comment						
02/24 18:14:30 - 02/25 19:16:54		Fixed ( -8.0, -912.0)					# HOP 101 and 103, helioseismology and EIS sit-and-stare, at S limb for 25 hrs.						
<b>PROG= 04 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 32.0sec													
└─ Seqn= 17 1-time(s) 2.0sec													
Al-poly/Open		Al-poly/thick-Al	close	Safe	Norm	22.6s	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #165B: Synoptic Q95 2x2 - Al/mesh(256/2048) + Dark cal(256 Q98) + Ti-poly(512/4096) + G-band(16)													
Term		Pointing (x, y)					Comment						
02/26 06:03:00 - 02/26 06:09:54		Fixed ( 0.0, 0.0)					synoptic						
<b>PROG= 14 1-time(s)</b>													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 68 1-time(s) 4.0sec													
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 50 1-time(s) 2.0sec													
Open/Al-mesh		Open/Al-mesh	close	Safe	Dark	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 89 1-time(s) 4.0sec													
Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 92 1-time(s) 2.0sec													
Open/G-band		Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

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## Flare mode

\* \* \* \* \*

NOT USED

\* \* \* \* \*

## Active Region Search

\* \* \* \* \*

NOT USED

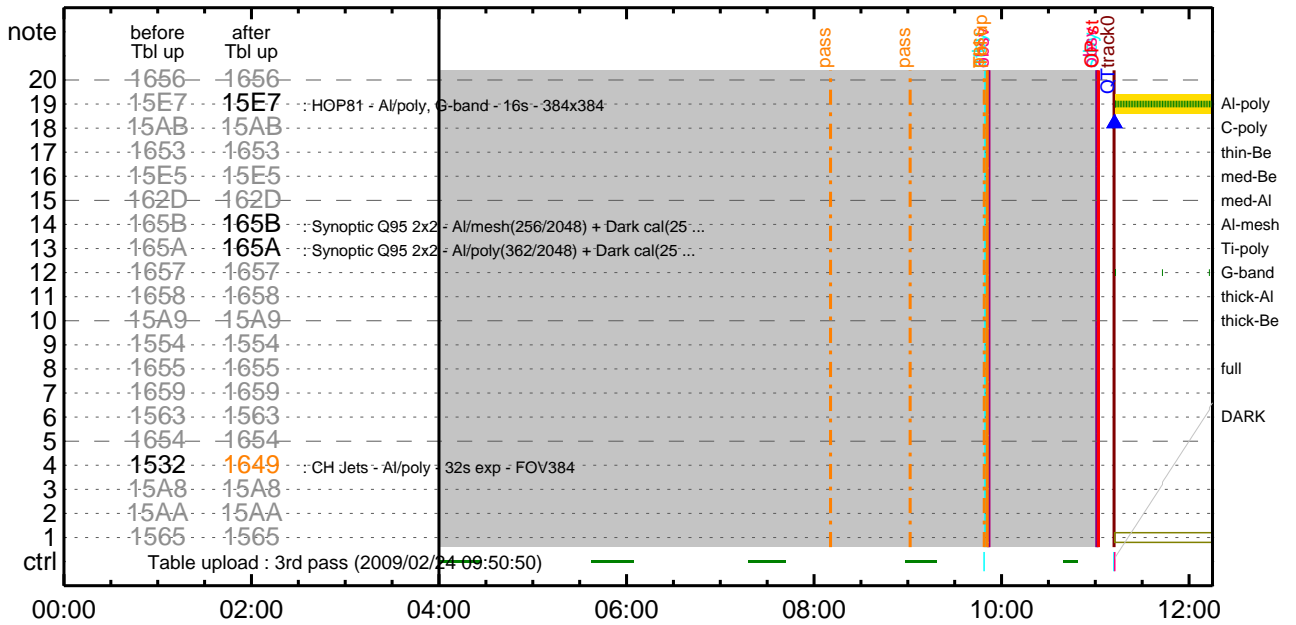
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## Flare Detection

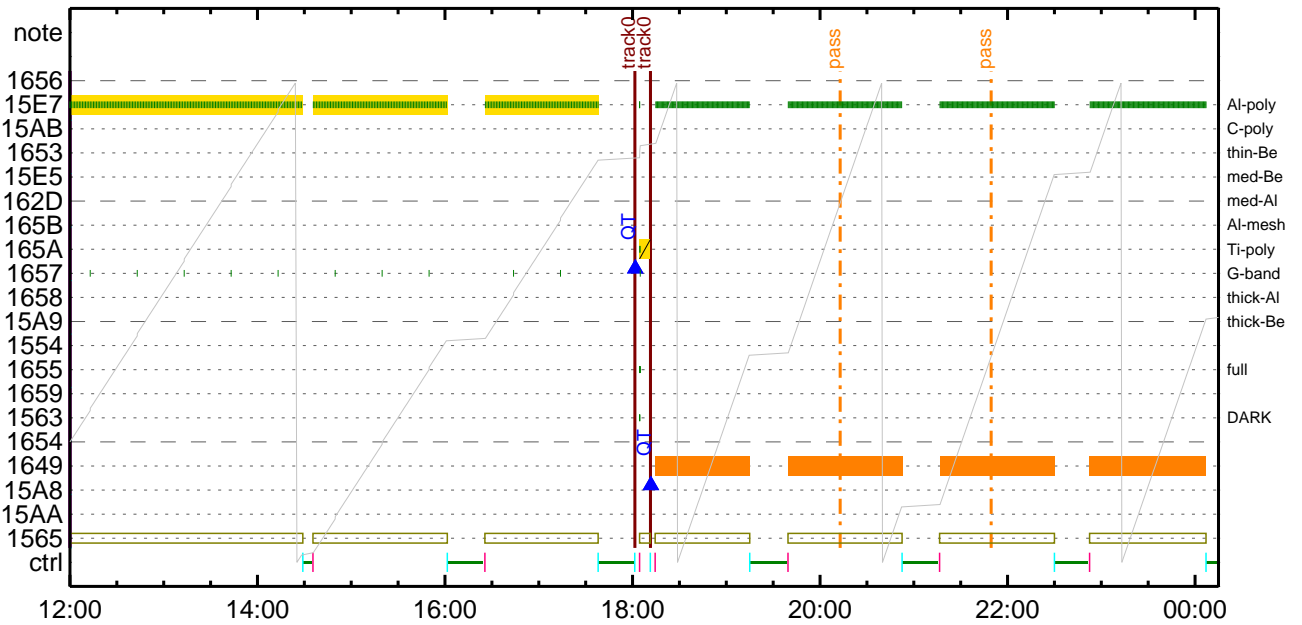
\* \* \* \* \*

NOT USED

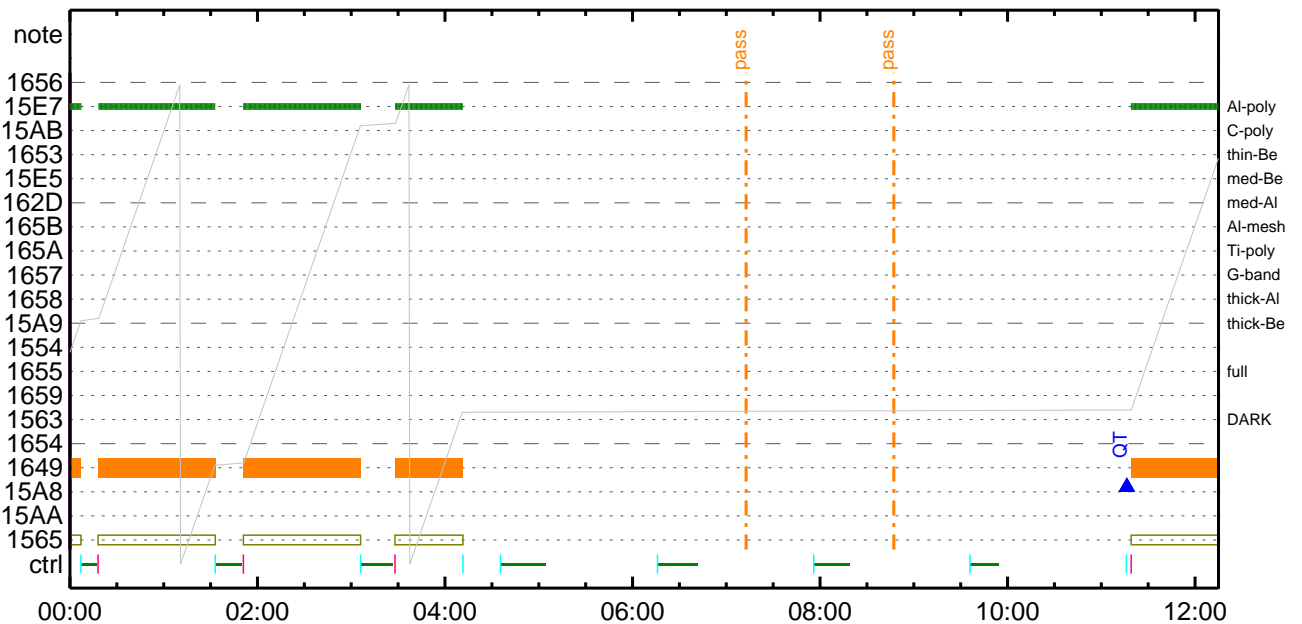
### CMDI #0420 2009/02/24



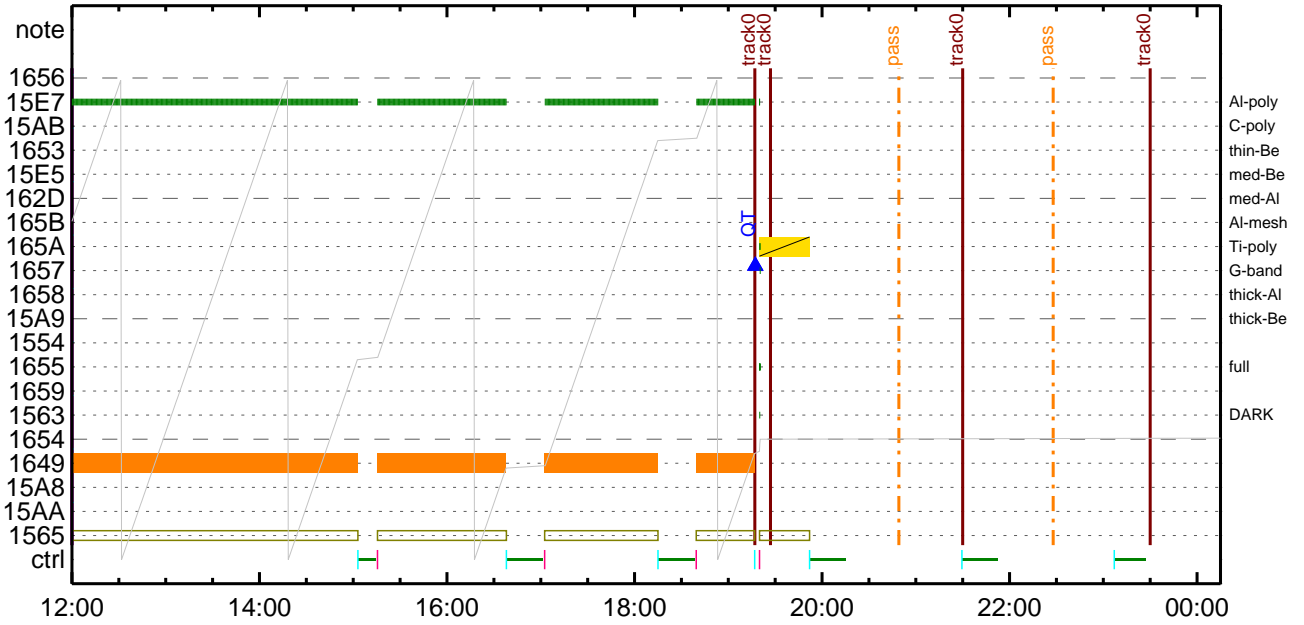
### CMDI #0420 2009/02/24



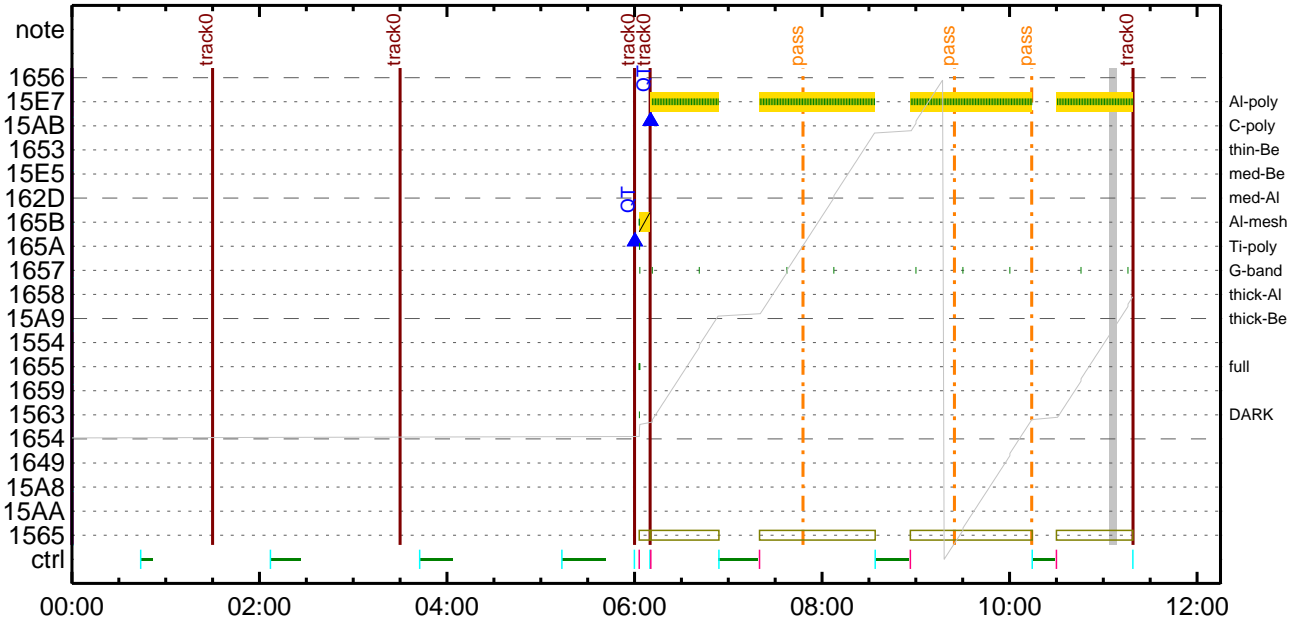
### CMDI #0420 2009/02/25



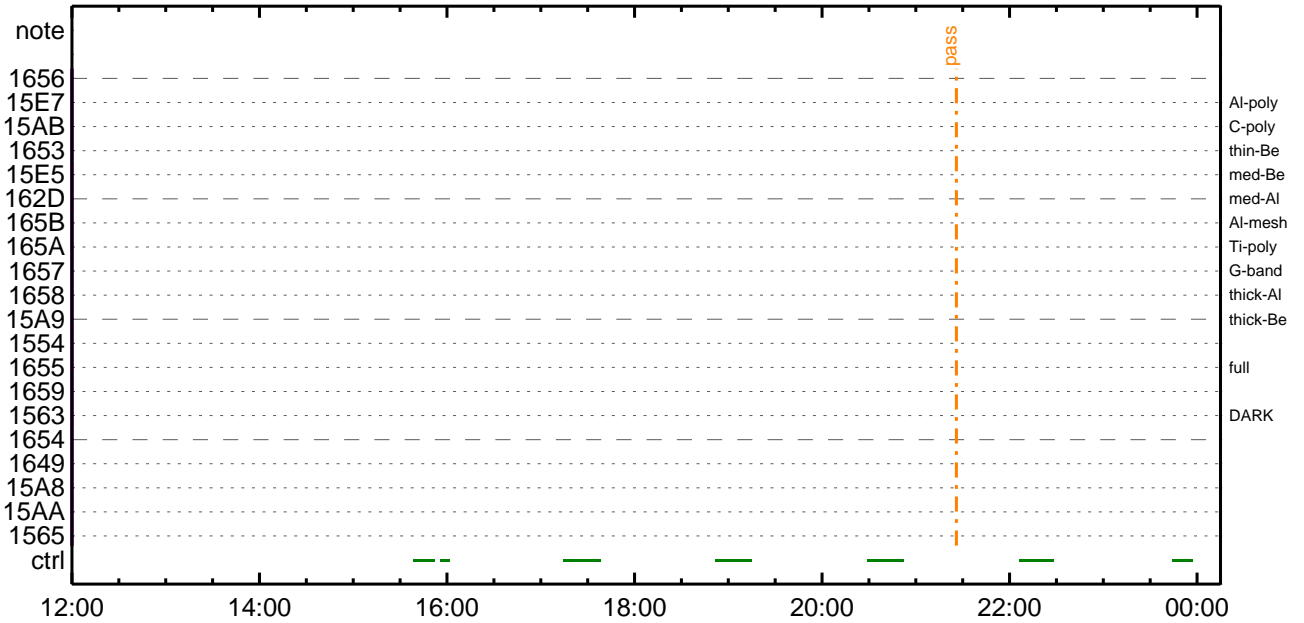
CMDI #0420 2009/02/25



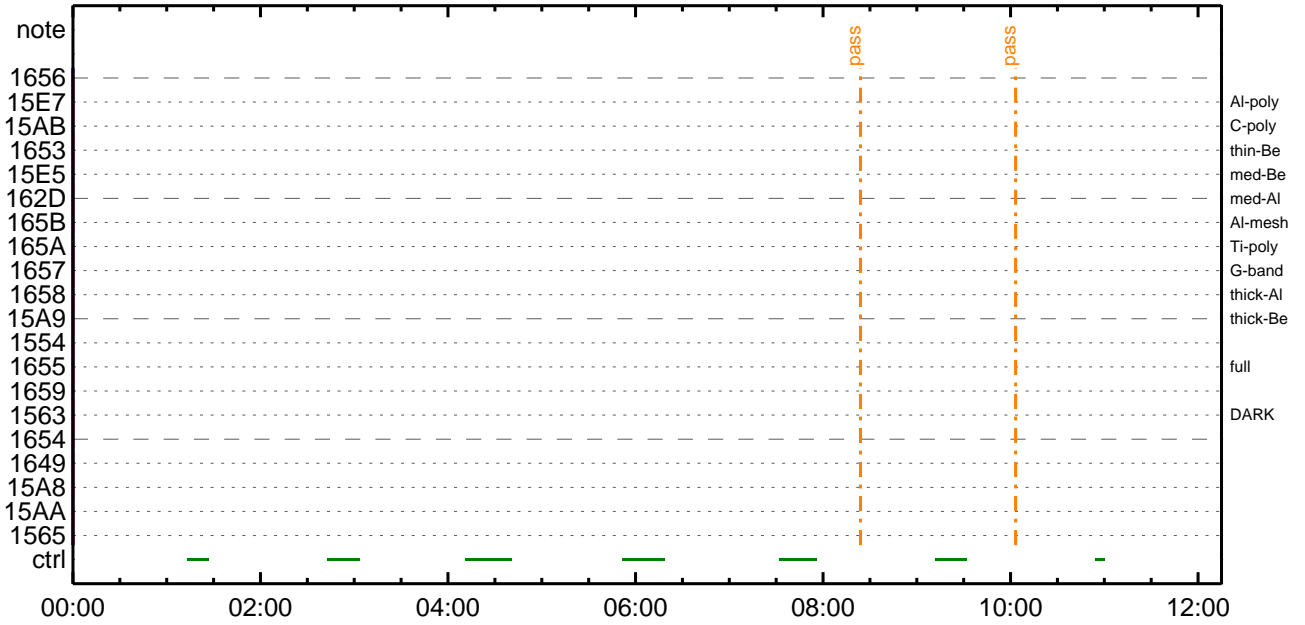
CMDI #0420 2009/02/26



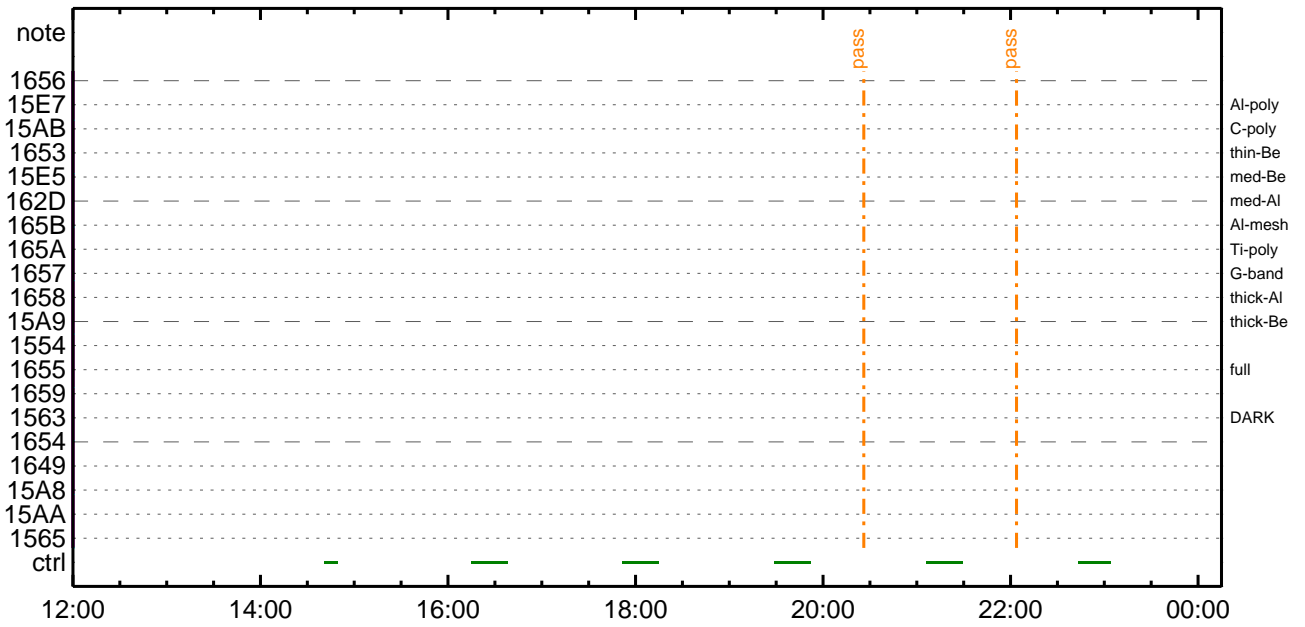
CMDI #0420 2009/02/26



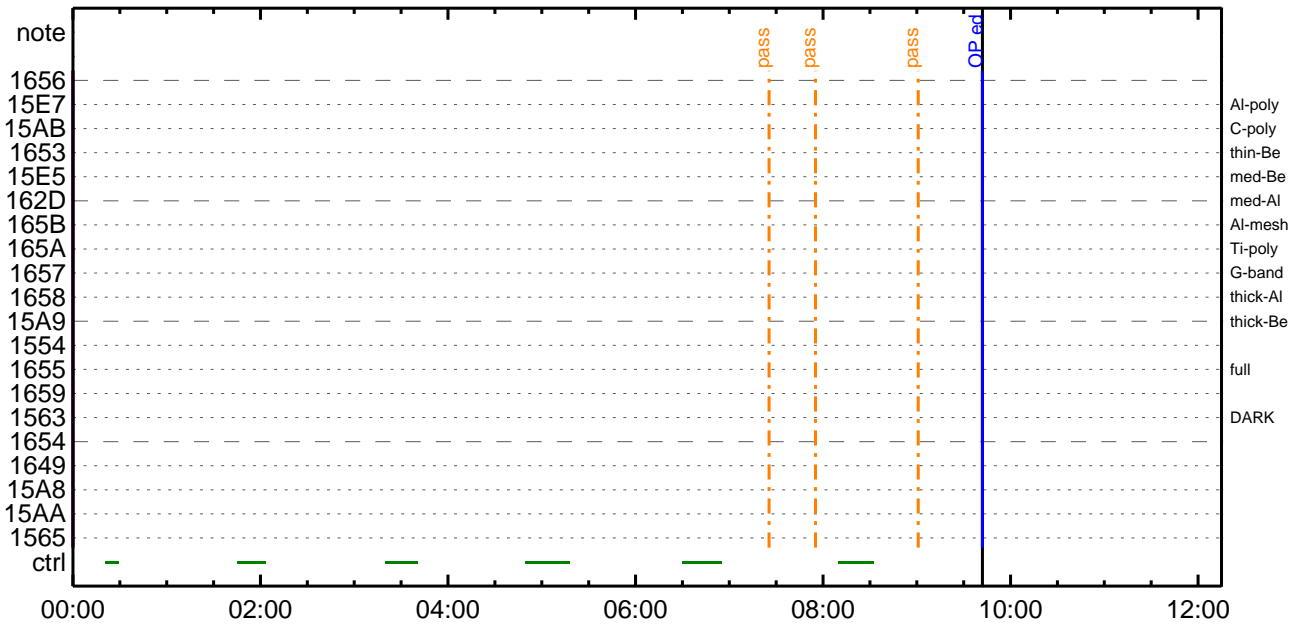
CMDI #0420 2009/02/27



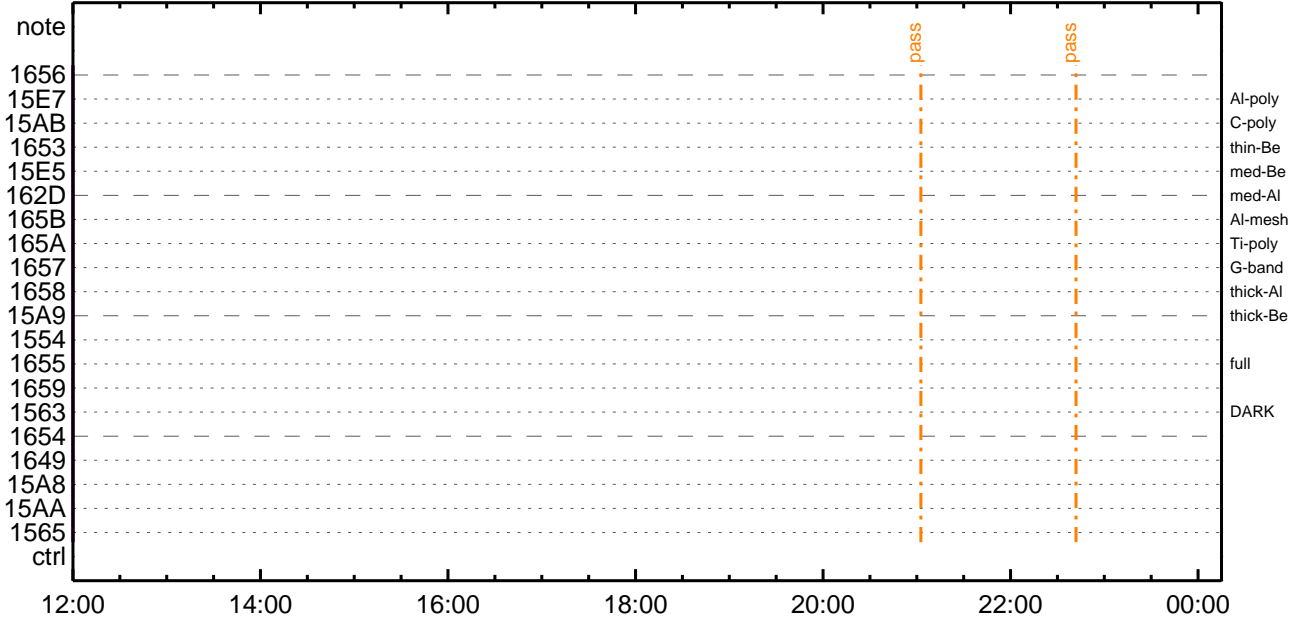
CMDI #0420 2009/02/27



CMDI #0420 2009/02/28



CMDI #0420 2009/02/28





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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-433:OP
0104 ( )
0105 S. OG og-433:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYôYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¼E¹ç•è²îOK²³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYôYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¼E¹ç•è²îOK²³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYôYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î¼E¹ç•è²îOK²³îÇ§
0165 C.
0166 C. ***** °E²¼²î¼E¹ç•è²îOK²³îÇ§ *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eòîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷çîNG²î¼E¹ç•è²¼²îTI-CMDÁ÷çî¼Á¹Ô²•²E²³²E;f
0180 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0181 C.
0182 C. TIY³YpYôYE²òðÁDîç(UT)
0183 +. TI 2009-02-24 10:57:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2009-02-24 10:57:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2009-02-24 10:57:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 06 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 07 80 80 20 20)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 08 80 80 06 06)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 0f 80 80 04 04)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 10 80 80 10 10)
0134 . C. ----- Success Verify ? OK / NG ____
0135 C.
0136 C.
0137 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0138 C.
0139 +. DC 07-F0 MDP_XRT_MODE_OBSV
0140 BC (c2)
0141 +. TI 2009-02-24 11:01:02.0
0142 DC 07-F0 MDP_XRT_MODE_OBSV
0143 BC (c2)
0144 . C. ----- Success Verify ? OK / NG ____
0145 C.
0146 C. ***** XRT END *****
0147 . C. *****
0148 C. SOT table upload
0149 C. *****
0150 . C. < Stop FG table >
0151 +. DC 07-F0 MDP_FG_CTRL_MANU
0152 BC (51)
0153 . C. -----
0154 C. MDP_FG_CTRL_MODE = MANU [ ]
0155 C. -----
0156 C.
0157 . C. <Upload FG Observation Table>
0158 . S. RAM ram-264:MDP_OBS_F
0159 ( )
0160 C.
0161 . C. < Dump RAMID=MDP_OBS_F >
0162 +. DC 07-F0 MDP_DUMP_FGTBL
0163 BC (82 07 00 00 00 38 b8)
0164 C. -----
0165 C. MDP_OBS_F verify = OK/NG [ ]
0166 C. -----
0167 C.
0168 C. *****
0169 C. SOT TI command set
0170 C. *****
0171 C. Execute, after the success of TBL upload.
0172 +. TI 2009-02-24 11:01:18.0
0173 DC 07-F0 MDP_SOT_MODE_OBSV
0174 BC (40)
0175 . C. -----
0176 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0177 C. -----
0178 C.
0179 C.
0180 . C. ***** MDP `uãîcî»ö%ýñÊÄðñ¹ñèDCBC•x²è *****
0181 C. (%ã°îÿÓYÁYÈYËYÏYáYçYèñE%¼ññ%Ä»Ûñ¹è)
0182 . S. DC-BC dcbc-402:DCBC
0183 (MDP_known_event)
0184 C.
0185 C.
0186 . C. ***** ¥ÐY¹.İ Daily±¿İÑñÊ´Øñ¹ñèDCBC•x²è *****
0187 . S. DC-BC dcbc-153:DCBC
0188 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0189 C.
0190 C.
0191 . C. ;ãLOS¥Á¥§¥Ã¥¹¼Ä»Û;ã
0192 C.
0193 . C. ***** LOS *****
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Feb 24, 09 12:00

## XRT\_OGLIST\_0420.chk

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\*\*\* OP Sequence for XRT \*\*\*

2009/02/24	11:11:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/24	11:11:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2009/02/24	11:12:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 54 cc 00 00				
2009/02/24	11:12:16.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2009/02/24	11:12:18.0	XRT_AEC_RESET_415_OG [0x19f]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2009/02/24	11:12:20.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/02/24	11:12:22.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/02/24	11:12:24.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/02/24	11:12:26.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/02/24	14:29:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/24	14:34:30.0	XRT_Custom_430_OG [0x1ae]							
2009/02/24	14:35:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/02/24	16:01:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/24	16:24:30.0	XRT_Custom_430_OG [0x1ae]							
2009/02/24	16:25:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/02/24	17:38:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/24	18:01:24.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/24	18:01:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/02/24	18:01:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2009/02/24	18:01:46.0	XRT_QT_PROG_SET_405_OG [0x195]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2009/02/24	18:01:48.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/02/24	18:01:50.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/02/24	18:01:52.0	XRT_ARS_DIS_410_OG [0x19a]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/02/24	18:04:30.0	XRT_CTRL_AUTO_444_OG [0x1bc]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/02/24	18:11:24.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/24	18:11:26.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2009/02/24	18:11:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 51 0e 00 b4				
2009/02/24	18:11:46.0	XRT_QT_PROG_SET_413_OG [0x19d]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2009/02/24	18:11:48.0	XRT_AEC_RESET_415_OG [0x19f]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2009/02/24	18:11:50.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/02/24	18:11:52.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/02/24	18:11:54.0	XRT_FLRCTRL_DIS_406_OG [0x196]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/02/24	18:14:30.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/02/24	19:15:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/24	19:38:30.0	XRT_Custom_430_OG [0x1ae]							
2009/02/24	19:39:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/02/24	20:52:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/24	21:15:30.0	XRT_Custom_430_OG [0x1ae]							
2009/02/24	21:16:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/02/24	22:30:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/24	22:51:30.5	XRT_Custom_430_OG [0x1ae]							
2009/02/24	22:52:30.5	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/02/25	00:07:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/25	00:17:00.0	XRT_Custom_430_OG [0x1ae]							
2009/02/25	00:18:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/02/25	01:33:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/02/25	01:50:00.0	XRT_Custom_430_OG [0x1ae]							
2009/02/25	01:51:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/02/25	03:06:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2009/02/25	03:27:00.0	XRT_Custom_430_OG [0x1ae]						
2009/02/25	03:28:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/02/25	04:11:30.0	XRT_CTRL_MANU_428_OG [0x1ac]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/25	04:35:30.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/25	06:16:00.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/25	07:56:00.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/25	09:36:00.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/25	11:16:00.0	XRT_CTRL_MANU_428_OG [0x1ac]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/25	11:16:02.0	XRT_FOCUS_POSITION_441_OG [0x1b9]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2009/02/25	11:16:22.0	XRT_QT_PROG_SET_413_OG [0x19d]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04			
2009/02/25	11:16:24.0	XRT_AEC_RESET_415_OG [0x19f]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2009/02/25	11:16:26.0	XRT_ARS_DIS_422_OG [0x1a6]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2009/02/25	11:16:28.0	XRT_FLD_DIS_445_OG [0x1bd]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/02/25	11:16:30.0	XRT_FLRCTRL_DIS_406_OG [0x196]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/02/25	11:19:06.0	XRT_CTRL_AUTO_403_OG [0x193]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/02/25	15:03:00.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/25	15:14:30.0	XRT_Custom_430_OG [0x1ae]						
2009/02/25	15:15:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/02/25	16:38:00.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/25	17:01:30.0	XRT_Custom_430_OG [0x1ae]						
2009/02/25	17:02:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/02/25	18:15:00.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/25	18:38:30.0	XRT_Custom_430_OG [0x1ae]						
2009/02/25	18:39:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/02/25	19:16:54.0	XRT_CTRL_MANU_428_OG [0x1ac]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/25	19:16:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2009/02/25	19:17:00.0	AOCS_ORe-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2009/02/25	19:17:16.0	XRT_QT_PROG_SET_405_OG [0x195]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d			
2009/02/25	19:17:18.0	XRT_FLD_DIS_419_OG [0x1a3]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/02/25	19:17:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/02/25	19:17:22.0	XRT_ARS_DIS_410_OG [0x19a]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2009/02/25	19:20:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/02/25	19:27:00.0	AOCS_ORe-point_Start_4_OG [0x09a]						
		AOCU_NM	5	02-76	00 00 00 00 b4 73			
2009/02/25	19:52:00.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/25	21:29:30.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/25	21:30:00.0	AOCS_ORe-point_Start_5_OG [0x09b]						
		AOCU_NM	5	02-76	00 00 00 00 d8 00			
2009/02/25	23:07:00.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/25	23:30:00.0	AOCS_ORe-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00 00			
2009/02/26	00:44:00.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/26	01:30:00.0	AOCS_ORe-point_Start_6_OG [0x09c]						
		AOCU_NM	5	02-76	00 00 00 00 28 00			
2009/02/26	02:07:00.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/26	03:30:00.0	AOCS_ORe-point_Start_7_OG [0x09d]						
		AOCU_NM	5	02-76	00 00 00 00 4b 8d			
2009/02/26	03:42:30.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/26	05:13:30.0	XRT_CTRL_MANU_435_OG [0x1b3]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/26	05:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/02/26	05:59:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2009/02/26	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00 00			
2009/02/26	06:00:16.0	XRT_QT_PROG_SET_426_OG [0x1aa]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e			
2009/02/26	06:00:18.0	XRT_FLD_DIS_419_OG [0x1a3]						

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2009/02/26	06:00:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLD_DIS	1	07-F0	d9	
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/02/26	06:00:22.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/02/26	06:03:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/02/26	06:09:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/02/26	06:09:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2009/02/26	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 54 cc 00 00	
2009/02/26	06:10:16.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2009/02/26	06:10:18.0	XRT_AEC_RESET_415_OG [0x19f]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2009/02/26	06:10:20.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/02/26	06:10:22.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/02/26	06:10:24.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/02/26	06:10:26.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/02/26	06:54:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/02/26	07:19:00.0	XRT_Custom_430_OG [0x1ae]					
2009/02/26	07:20:00.5	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/02/26	08:34:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/02/26	08:55:30.0	XRT_Custom_430_OG [0x1ae]					
2009/02/26	08:56:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/02/26	10:14:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/02/26	10:29:00.0	XRT_Custom_430_OG [0x1ae]					
2009/02/26	10:30:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/02/26	11:18:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/02/26	11:19:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	