

XRT Timeline to be uploaded on 2009/03/03

Period: 2009/03/03 10:46:00 - 2009/03/07 09:59:00

* * * * *

Normal mode

* * * * *

XOB #1639: AR multifilter - Al/mesh,Ti/Poly,Al/Poly,Thin-Be FOV384 AEC4 Q95- 5 min cadence												
Term	Pointing (x, y)	Comment										
03/03 10:59:00 - 03/03 15:48:30	Track (780.2, 463.9) ^{© 03/03 10:56:00}	# OP start + 10min Track Active Region Remnant 11013										
03/04 08:15:00 - 03/04 17:41:30	Fixed (810.0, 450.0)	# Active Region Remnant 11013, fixed pointing at W limb.										
PROG= 17 Inf.-time(s)												
└ Subr= 1	1-time(s) 300.0sec											
└└ Seqn= 35	1-time(s) 4.0sec											
└└└ Open/Al-mesh	Open/Ti-poly close Safe Norm 8.00s	Obs 1x1	384x384 (1024, 1024)	Q=98	4	0	2.0sec					
└└└ Open/Ti-poly	Open/thick-Al close Safe Norm 11.3s	Obs 1x1	384x384 (1024, 1024)	Q=98	4	0	2.0sec					
└└└ Al-poly/Open	thin-Be/Open close Safe Norm 8.00s	Obs 1x1	384x384 (1024, 1024)	Q=98	4	0	2.0sec					
└└└ thin-Be/Open	thin-Be/Open close Safe Norm 22.6s	Obs 1x1	384x384 (1024, 1024)	Q=98	4	0	2.0sec					
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #165A: Synoptic Q95 2x2 - Al/poly(362/2048) + Dark cal(256 Q98) + Ti-poly(512/4096) + G-band(16)												
Term	Pointing (x, y)	Comment										
03/03 15:54:00 - 03/03 16:00:54	Fixed (0.0, 0.0)	synoptic										
03/04 18:08:30 - 03/05 05:51:54	Fixed (0.0, 0.0)	synoptic, shifted 5.5 min										
PROG= 13 1-time(s)												
└ Subr= 1	1-time(s) 12.0sec											
└└ Seqn= 37	1-time(s) 4.0sec											
└└└ Al-poly/Open	Al-poly/Open close Safe Norm 354ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					
└└└ Al-poly/Open	Al-poly/Open close Safe Norm 2.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec					
└└└ Seqn= 49	1-time(s) 2.0sec											
└└└└ Al-poly/Open	Al-poly/Open close Safe Dark 250ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec					
└└└└ Seqn= 89	1-time(s) 4.0sec											
└└└└└ Open/Ti-poly	Open/Ti-poly close Safe Norm 500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					
└└└└└ Open/Ti-poly	Open/Ti-poly close Safe Norm 4.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec					
└└└└└ Seqn= 92	1-time(s) 2.0sec											
└└└└└└ Open/G-band	Open/G-band open Safe Norm 16ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1649: CH Jets - Al/poly - 32s exp - FOV384												
Term	Pointing (x, y)	Comment										
03/03 16:04:00 - 03/04 02:01:00	Fixed (-8.0, -912.0)	# HOP 101, helioseismology at the S pole.										
PROG= 16 Inf.-time(s)												
└ Subr= 1	1-time(s) 32.0sec											
└└ Seqn= 17	1-time(s) 2.0sec											
└└└ Al-poly/Open	Al-poly/thick-Al close Safe Norm 22.6s	Obs 1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec					
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #165B: Synoptic Q95 2x2 - Al/mesh(256/2048) + Dark cal(256 Q98) + Ti-poly(512/4096) + G-band(16)												
Term	Pointing (x, y)	Comment										
03/04 08:05:00 - 03/04 08:11:54	Fixed (0.0, 0.0)	synoptic, shifted 18.5 min										
03/05 05:55:00 - 03/07 09:59:00	Fixed (0.0, 0.0)	synoptic, shifted -8.0 min										
PROG= 14 1-time(s)												
└ Subr= 1	1-time(s) 12.0sec											
└└ Seqn= 68	1-time(s) 4.0sec											
└└└ Open/Al-mesh	Open/Al-mesh close Safe Norm 250ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					
└└└ Open/Al-mesh	Open/Al-mesh close Safe Norm 2.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec					
└└└ Seqn= 50	1-time(s) 2.0sec											
└└└└ Open/Al-mesh	Open/Al-mesh close Safe Dark 250ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec					
└└└└ Seqn= 89	1-time(s) 4.0sec											
└└└└└ Open/Ti-poly	Open/Ti-poly close Safe Norm 500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					
└└└└└ Open/Ti-poly	Open/Ti-poly close Safe Norm 4.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec					
└└└└└ Seqn= 92	1-time(s) 2.0sec											
└└└└└└ Open/G-band	Open/G-band open Safe Norm 16ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec					
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

* * * * *

Flare mode

* * * * *

NOT USED

* * * * *

Active Region Search

* * * * *

NOT USED

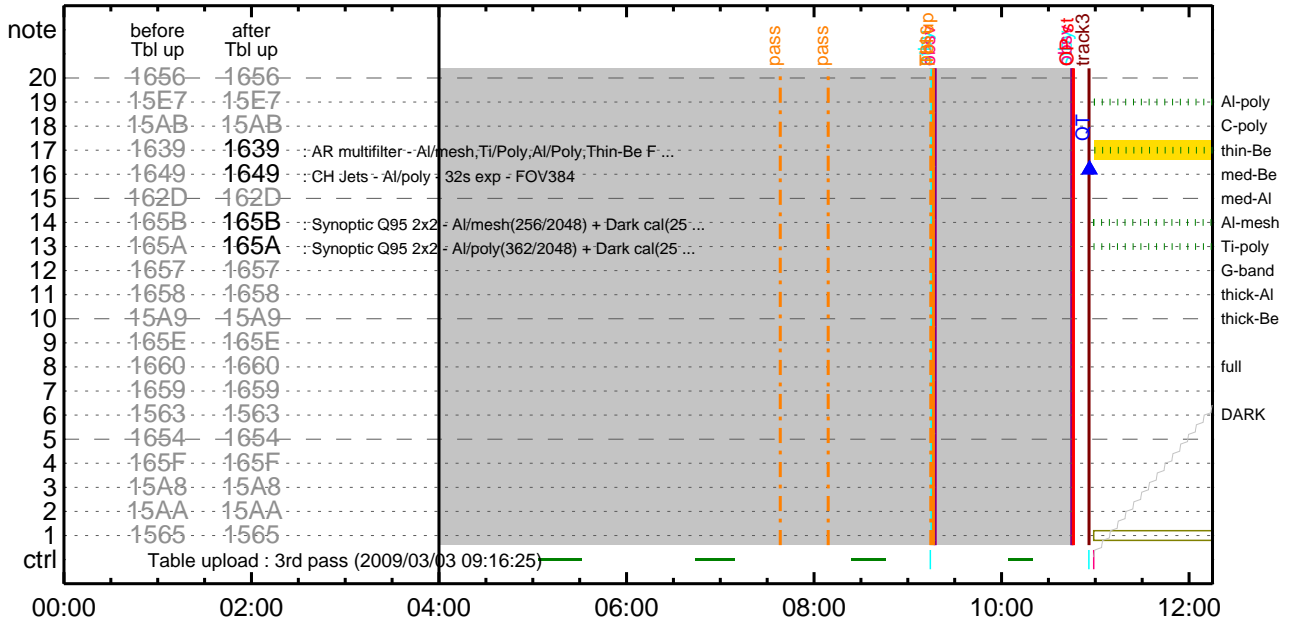
* * * * *

Flare Detection

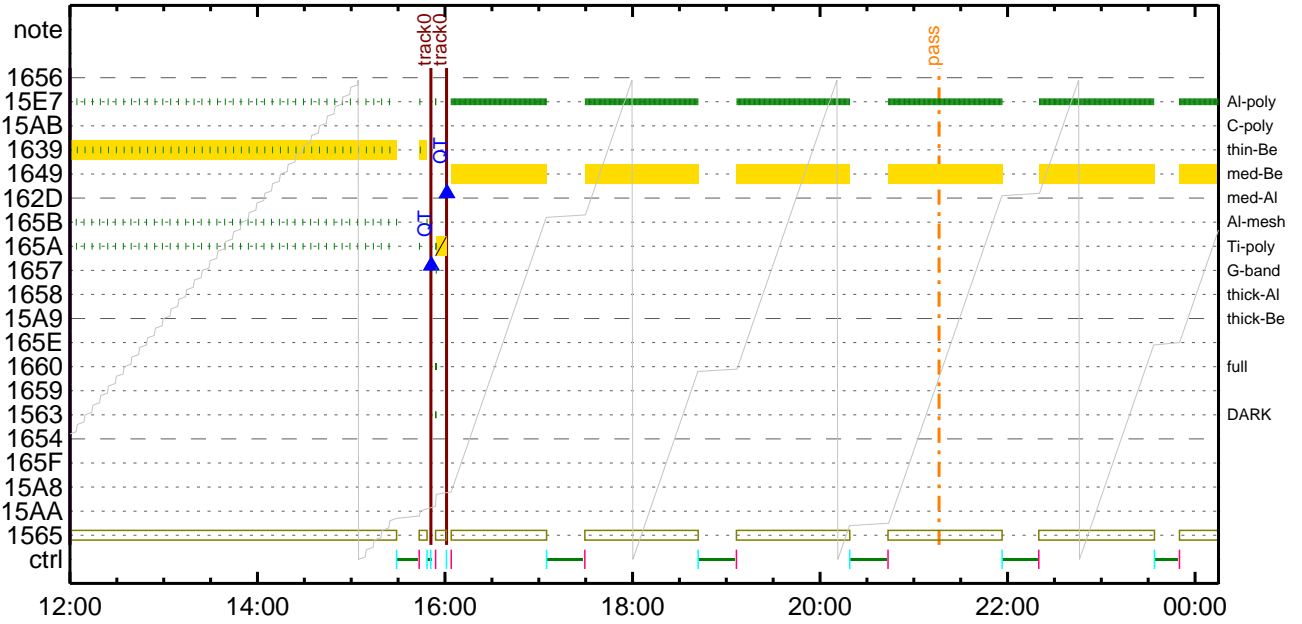
* * * * *

NOT USED

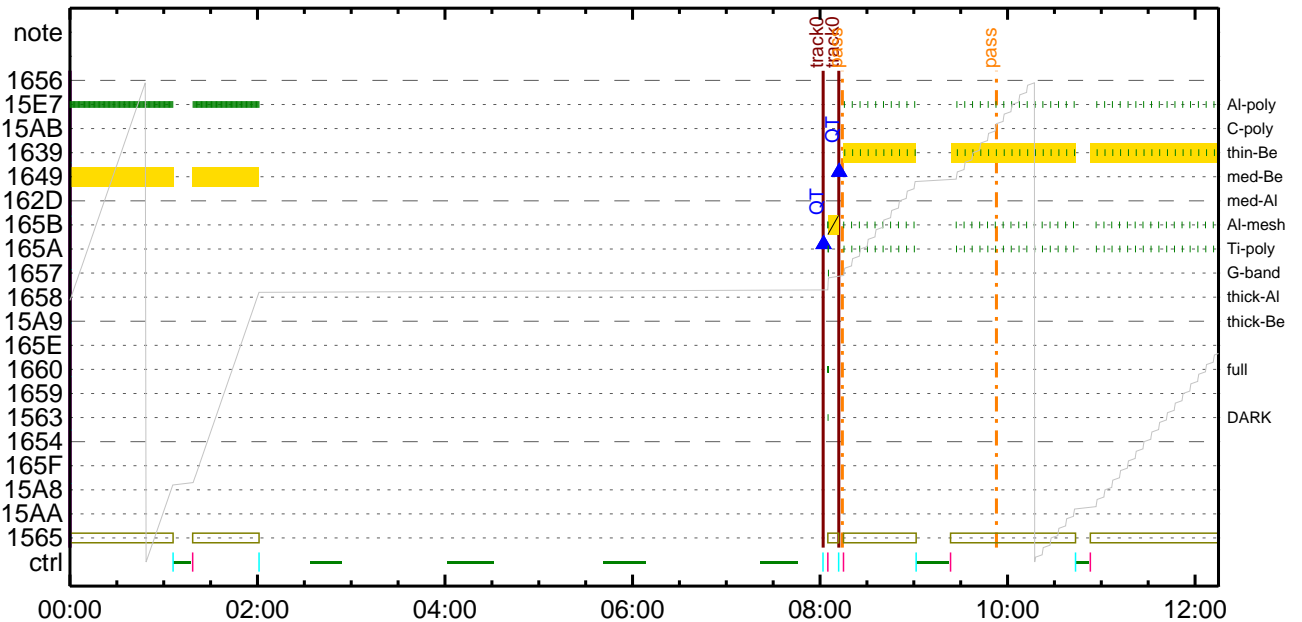
CMDI #0431 2009/03/03



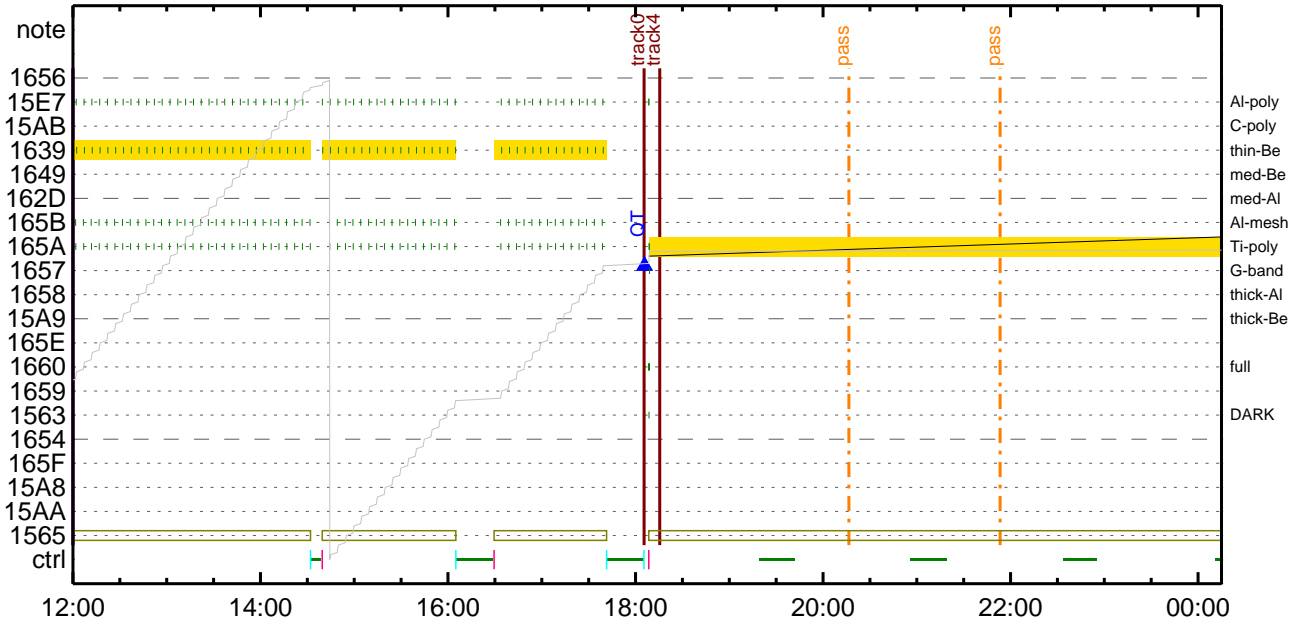
CMDI #0431 2009/03/03



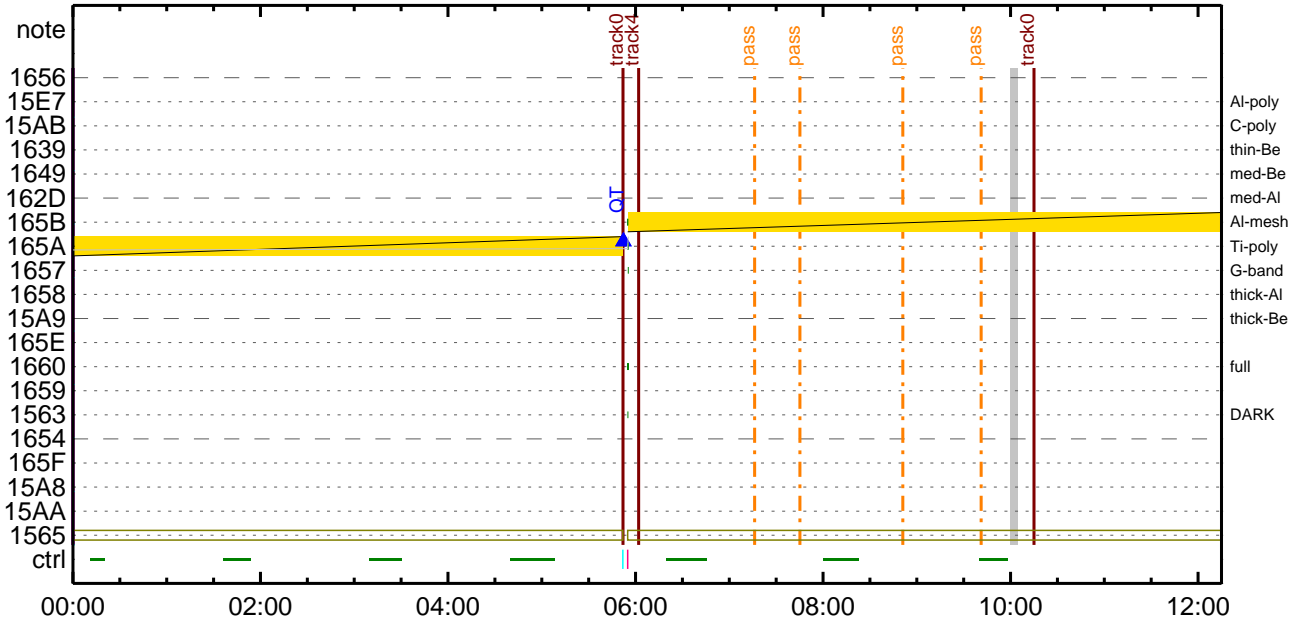
CMDI #0431 2009/03/04



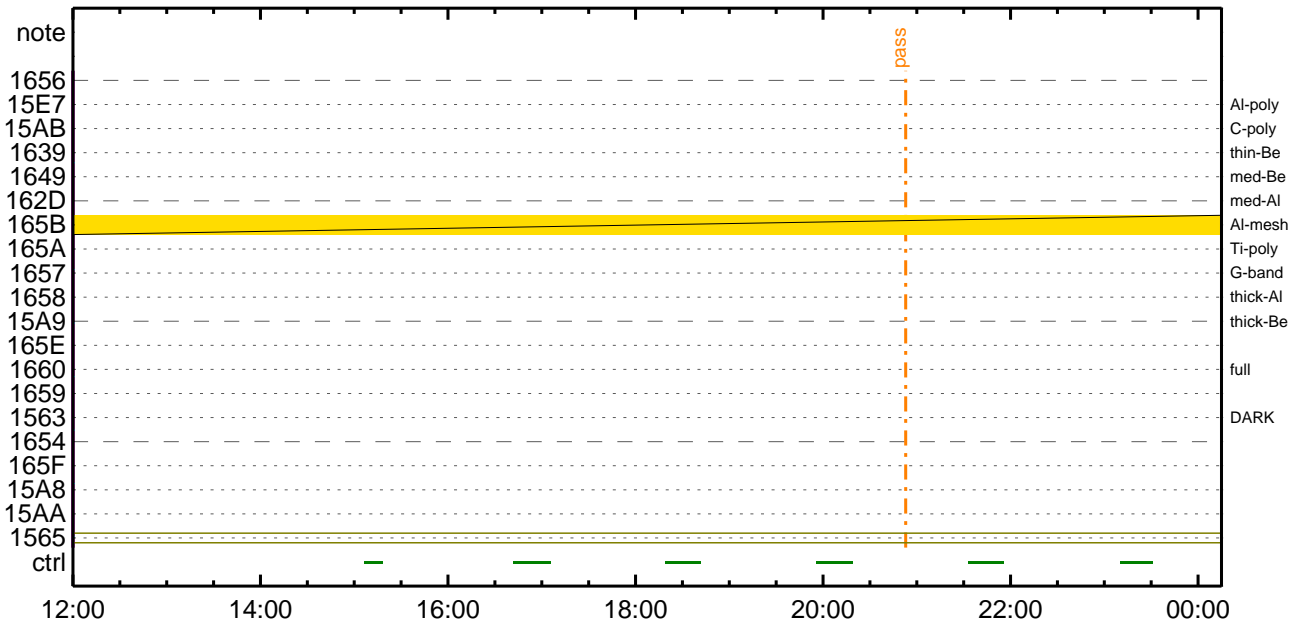
CMDI #0431 2009/03/04



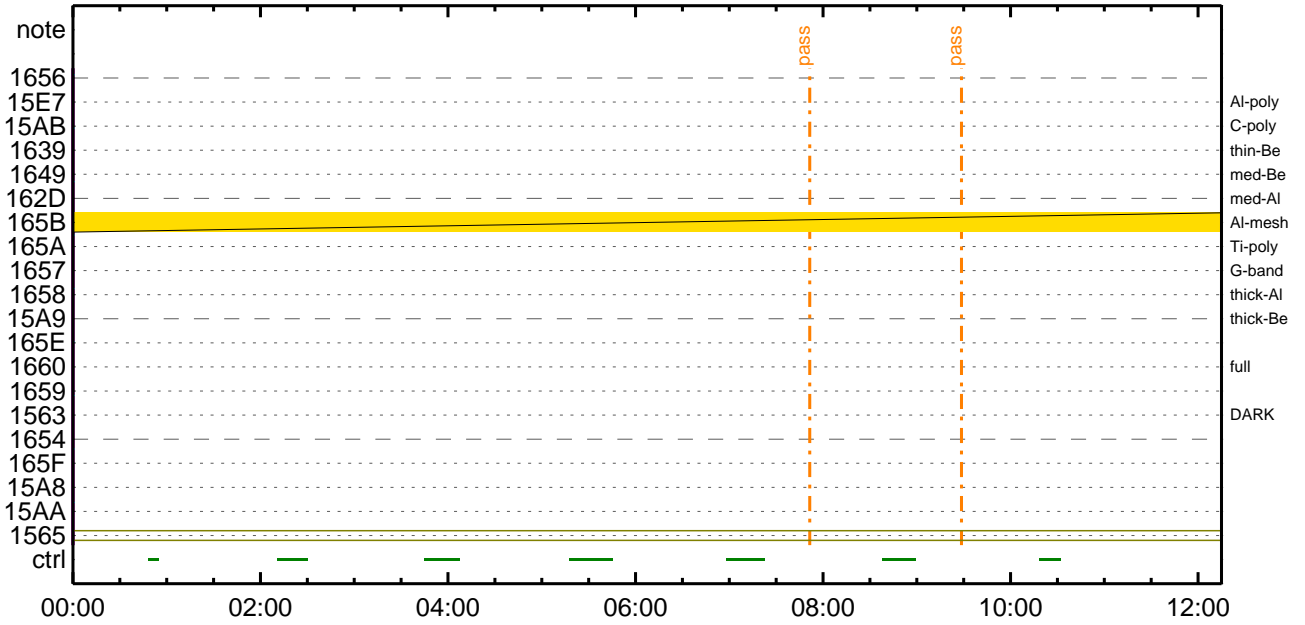
CMDI #0431 2009/03/05



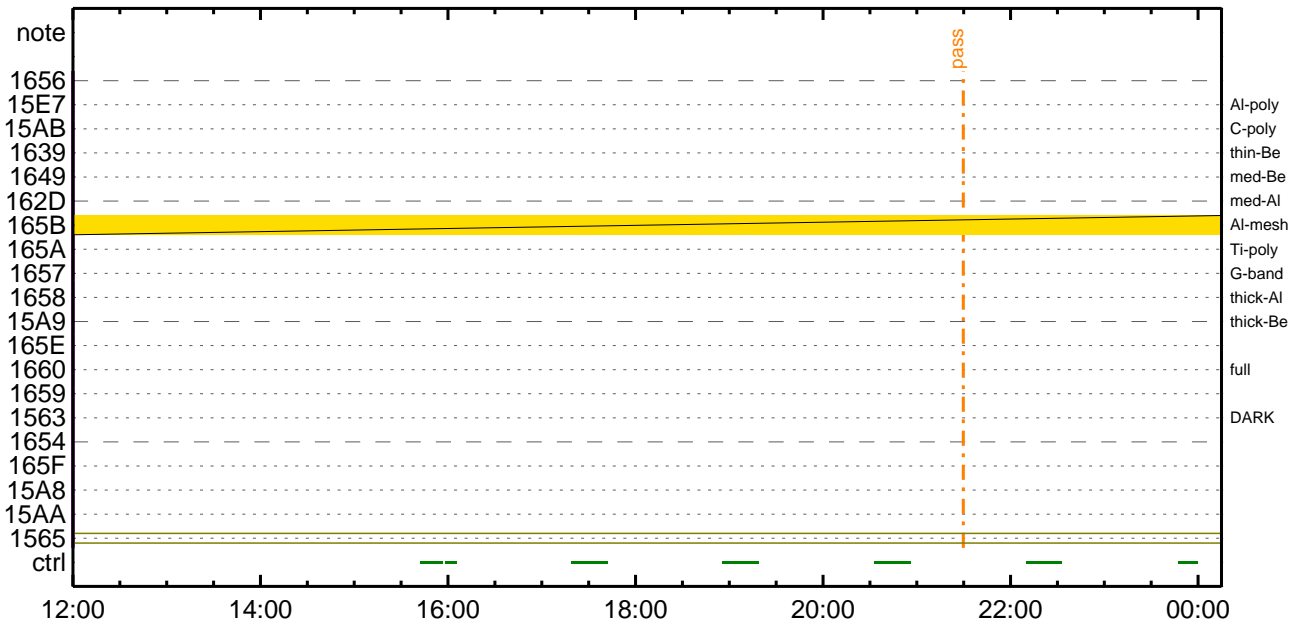
CMDI #0431 2009/03/05



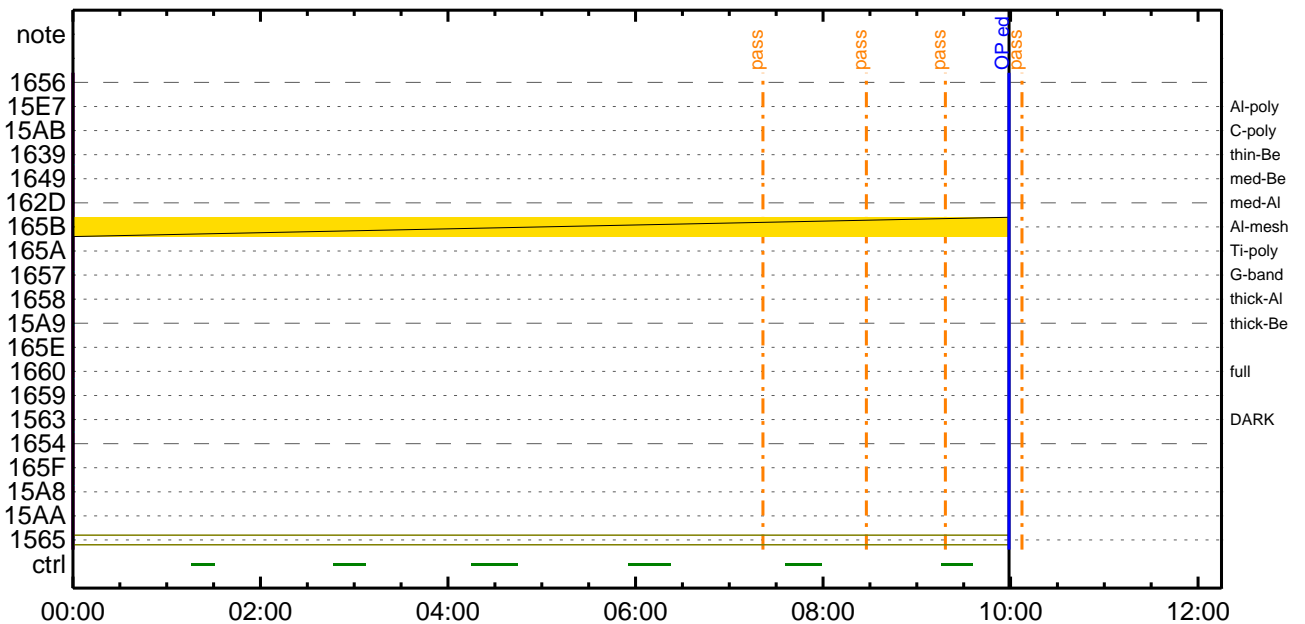
CMDI #0431 2009/03/06



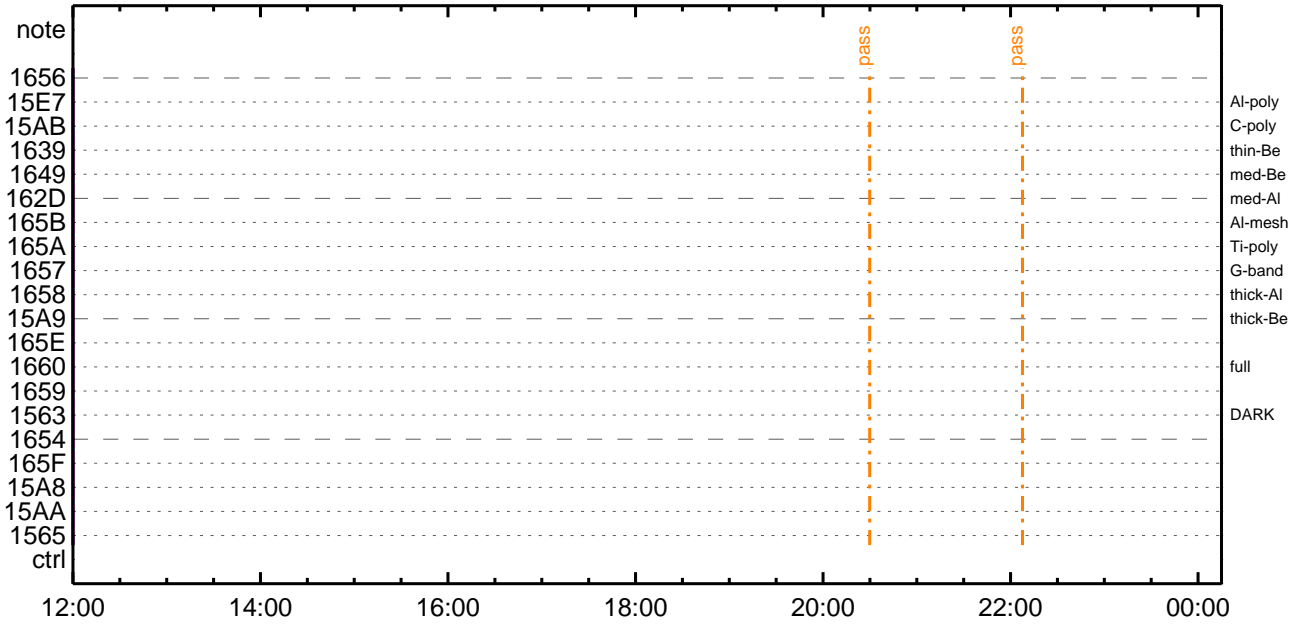
CMDI #0431 2009/03/06



CMDI #0431 2009/03/07



CMDI #0431 2009/03/07




```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGYI;YÉ;YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGYI;YÉ;ä
0103 S. OP op-446:OP
0104 ( )
0105 S. OG og-446:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPf^°èYAYOYx;ä
0109 C. NMOG(0x200000-0x207FFF;s 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ª î»oð³ ÎÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGαî½ª¹ç•ê² İOKαð³ ÎÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;s 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªª î»oð³ ÎÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGαî½ª¹ç•ê² İOKαð³ ÎÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;s 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªª î»oð³ ÎÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPαî½ª¹ç•ê² İOKαð³ ÎÇ§
0165 C.
0166 C. ***** °ê²¼o î½ª¹ç•ê² İOKαð³ ÎÇ§ *****
0167 C. DHUYâ;½YÉ;ê½Y½;Yi;½YÉ;ëoðîãα¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADα-Á÷çîNGUî½ª¹ç;ç°ê²¼o î½ª¹ç•ê² İOKαð³ ÎÇ§ *****
0180 C. αÞαç;çSETαEDUMPAİÆ±°iYNY¹ç¹Ço|α³αÊ;f
0181 C.
0182 C. TIY³YþYóYÉoððÁDîç (UT)
0183 +. TI 2009-03-03 10:41:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2009-03-03 10:41:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2009-03-03 10:41:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```



```

0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 06 80 80 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 07 80 80 20 20)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 0f 80 80 04 04)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 10 80 80 10 10)
0132 . C. ----- Success Verify ? OK / NG ____
0133 C.
0134 C.
0135 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0136 C.
0137 +. DC 07-F0 MDP_XRT_MODE_OBSV
0138 BC (c2)
0139 +. TI 2009-03-03 10:45:02.0
0140 DC 07-F0 MDP_XRT_MODE_OBSV
0141 BC (c2)
0142 . C. ----- Success Verify ? OK / NG ____
0143 C.
0144 C. ***** XRT END *****
0145 . C. *****
0146 C. SOT table upload
0147 C. *****
0148 . C. < Stop FG table >
0149 +. DC 07-F0 MDP_FG_CTRL_MANU
0150 BC (51)
0151 . C. -----
0152 C. MDP_FG_CTRL_MODE = MANU [ ]
0153 C. -----
0154 C.
0155 . C. <Upload FG Observation Table>
0156 . S. RAM ram-266:MDP_OBS_F
0157 ( )
0158 C.
0159 . C. < Dump RAMID=MDP_OBS_F >
0160 +. DC 07-F0 MDP_DUMP_FGTBL
0161 BC (82 07 00 00 00 38 b8)
0162 C. -----
0163 C. MDP_OBS_F verify = OK/NG [ ]
0164 C. -----
0165 C.
0166 . C. < Upload DPL table >
0167 C.
0168 C. ¥¢¥Ã¥×¥í;¼¥É°îÁ°¤ESTS_CHK¤ðOFF¤È¤¹¤ë
0169 C.
0170 . S. RAM ram-271:MDP_DPL
0171 ( )
0172 C.
0173 . C. < Dump RAMID=MDP_DPL >
0174 +. DC 07-F0 MDP_DUMP_FGTBL
0175 BC (82 07 00 38 b8 00 40)
0176 C. -----
0177 C. MDP_DPL verify = OK [ ]
0178 C. -----
0179 C.
0180 C. STS_CHK¤ðON¤È¤¹¤ë
0181 C.
0182 . C. < Update MDP DSC PAR1 >
0183 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0184 BC (4c)
0185 C. MDP_CMD_CODE = F04C0700[ ]
0186 C. MDP_CMD_CNT (count-up 1) [ ]
0187 C. -----
0188 C.
0189 . C.
0190 C. *****
0191 C. SOT TI command set
0192 C. *****
0193 C. Execute, after the success of TBL upload.

```

```
0194 +. TI 2009-03-03 10:45:18.0
0195 DC 07-F0 MDP_SOT_MODE_OBSV
0196 BC (40)
0197 . C. -----
0198 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0199 C. -----
0200 C.
0201 C.
0202 . C. ***** MDP 'úÃîñî»ô¼ÝñÈÃðñ¹ñèDCBC•x²è *****
0203 C. (¼ã°îÝÓÝÃÝÈÝÞÝÈÝÁÝçÝèñ¼ññ¼Ã»Ûñ¹ñè)
0204 . S. DC-BC dcbc-402:DCBC
0205 (MDP_known_event)
0206 C.
0207 C.
0208 . C. ***** ÝÐÝ¹•İ Daily±¿İÑñÈ´Øñ¹ñèDCBC•x²è *****
0209 . S. DC-BC dcbc-153:DCBC
0210 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0211 C.
0212 C.
0213 . C. ;ãLOSÝÁÝ§ÝÃÝ¬¼Ã»Û;ã
0214 C.
0215 . C. ***** LOS *****
0216 C.
```

Mar 03, 09 11:48

XRT_OGLIST_0431.chk

Page 1/3

*** OP Sequence for XRT ***

2009/03/03	10:55:54.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/03/03	10:55:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/03/03	10:56:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 ff f0
2009/03/03	10:56:16.0	XRT_QT_PROG_SET_423_OG [0x1a7]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2009/03/03	10:56:18.0	XRT_AEC_RESET_415_OG [0x19f]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2009/03/03	10:56:20.0	XRT_ARS_DIS_422_OG [0x1a6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/03/03	10:56:22.0	XRT_FLD_DIS_445_OG [0x1bd]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/03/03	10:56:24.0	XRT_FLRCTRL_DIS_406_OG [0x196]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/03/03	10:59:00.0	XRT_CTRL_AUTO_403_OG [0x193]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/03/03	15:29:00.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/03/03	15:42:30.0	XRT_Custom_430_OG [0x1ae]			
2009/03/03	15:43:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/03/03	15:48:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/03/03	15:50:54.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/03/03	15:50:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2009/03/03	15:51:00.0	AOCS_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2009/03/03	15:51:16.0	XRT_QT_PROG_SET_405_OG [0x195]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2009/03/03	15:51:18.0	XRT_FLD_DIS_419_OG [0x1a3]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/03/03	15:51:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/03/03	15:51:22.0	XRT_ARS_DIS_410_OG [0x19a]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/03/03	15:54:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/03/03	16:00:54.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/03/03	16:00:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/03/03	16:01:00.0	AOCS_Ore-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 51 0e 00 b4
2009/03/03	16:01:16.0	XRT_QT_PROG_SET_404_OG [0x194]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2009/03/03	16:01:18.0	XRT_AEC_RESET_415_OG [0x19f]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2009/03/03	16:01:20.0	XRT_ARS_DIS_422_OG [0x1a6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/03/03	16:01:22.0	XRT_FLD_DIS_445_OG [0x1bd]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/03/03	16:01:24.0	XRT_FLRCTRL_DIS_406_OG [0x196]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/03/03	16:04:00.0	XRT_CTRL_AUTO_403_OG [0x193]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/03/03	17:05:00.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/03/03	17:28:30.0	XRT_Custom_430_OG [0x1ae]			
2009/03/03	17:29:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/03/03	18:42:00.5	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/03/03	19:05:30.5	XRT_Custom_430_OG [0x1ae]			
2009/03/03	19:06:30.5	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/03/03	20:19:00.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/03/03	20:42:30.0	XRT_Custom_430_OG [0x1ae]			
2009/03/03	20:43:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/03/03	21:56:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/03/03	22:19:00.0	XRT_Custom_430_OG [0x1ae]			
2009/03/03	22:20:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/03/03	23:34:00.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/03/03	23:49:00.0	XRT_Custom_430_OG [0x1ae]			
2009/03/03	23:50:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/03/04	01:06:00.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/03/04	01:17:30.0	XRT_Custom_430_OG [0x1ae]			
2009/03/04	01:18:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/03/04	02:01:00.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1

Mar 03, 09 11:48

XRT_OGLIST_0431.chk

Page 2/3

2009/03/04	08:01:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/03/04	08:01:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2009/03/04	08:02:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2009/03/04	08:02:16.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2009/03/04	08:02:18.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/03/04	08:02:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/03/04	08:02:22.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/03/04	08:05:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/03/04	08:11:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/03/04	08:11:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2009/03/04	08:12:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 d8 00 b8 00	
2009/03/04	08:12:16.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2009/03/04	08:12:18.0	XRT_AEC_RESET_415_OG [0x19f]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2009/03/04	08:12:20.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/03/04	08:12:22.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/03/04	08:12:24.0	XRT_FLRCTRL_DIS_406_OG [0x196]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/03/04	08:15:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/03/04	09:01:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/03/04	09:22:30.0	XRT_Custom_430_OG [0x1ae]					
2009/03/04	09:23:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/03/04	10:43:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/03/04	10:52:00.0	XRT_Custom_430_OG [0x1ae]					
2009/03/04	10:53:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/03/04	14:32:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/03/04	14:38:30.0	XRT_Custom_430_OG [0x1ae]					
2009/03/04	14:39:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/03/04	16:05:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/03/04	16:28:30.0	XRT_Custom_430_OG [0x1ae]					
2009/03/04	16:29:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/03/04	17:41:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/03/04	18:05:24.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/03/04	18:05:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2009/03/04	18:05:30.5	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2009/03/04	18:05:46.0	XRT_QT_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d	
2009/03/04	18:05:48.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/03/04	18:05:50.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/03/04	18:05:52.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/03/04	18:08:30.0	XRT_CTRL_AUTO_444_OG [0x1bc]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/03/04	18:15:30.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	04 00 00 00 00	
2009/03/05	05:51:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/03/05	05:51:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2009/03/05	05:52:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2009/03/05	05:52:16.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2009/03/05	05:52:18.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/03/05	05:52:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/03/05	05:52:22.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/03/05	05:55:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/03/05	06:02:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	04 00 00 00 00	
2009/03/05	10:15:00.0	AOCS_ORe-point_Start_2_OG [0x098]					

AOCU_NM

5 02-76 00 00 00 00 00