

# XRT Timeline to be uploaded on 2009/03/17

Period: 2009/03/17 11:00:00 - 2009/03/21 09:40:00

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## Normal mode

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XOB #15E7: HOP81 - Al/poly, G-band - 16s - 384x384												
Term	Pointing (x, y)							Comment				
03/17 11:10:26 - 03/17 15:59:00	Fixed ( 0.0, -940.0)	# OP start + 10min HOP81 at S pole										
03/18 09:53:30 - 03/18 14:49:54	Fixed ( 0.0, -940.0)	HOP81 at S pole										
<b>PROG= 12 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 1 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 99 30-time(s) 60.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #15A3: Synoptic Q95 2x2 - Al/poly(512/5795) + Dark cal(512 Q98) + Ti-poly(723/11571) + G-band(16)												
Term	Pointing (x, y)							Comment				
03/17 16:23:30 - 03/17 16:29:54	Fixed ( 0.0, 0.0)	synoptic shifted manually										
03/18 14:53:00 - 03/18 14:59:54	Fixed ( 0.0, 0.0)	synoptic, shifted manually										
<b>PROG= 06 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 36 1-time(s) 4.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 6 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 88 1-time(s) 4.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 92 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1639: AR multifilter - Al/mesh,Ti/Poly,Al/Poly,Thin-Be FOV384 AEC4 Q95- 5 min cadence												
Term	Pointing (x, y)							Comment				
03/17 16:33:00 - 03/18 05:45:54	Track ( 393.0, -80.1) @ 03/17 16:30:00	# Track small enhanced area (AR?).										
03/18 05:59:00 - 03/18 09:33:30	Track ( 501.1, -87.5) @ 03/18 05:56:00	# Cont small AR										
03/18 15:13:00 - 03/19 02:05:00	Track ( 569.5, -93.4) @ 03/18 15:00:00	# Cont small AR										
<b>PROG= 17 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 300.0sec												
└─ Seqn= 35 1-time(s) 4.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	4	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	1x1	384x384 (1024, 1024)	Q=98	4	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	4	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	22.6s	Obs	1x1	384x384 (1024, 1024)	Q=98	4	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #15A4: Synoptic Q95 2x2 - Al/mesh(512/5795) + Dark cal(512 Q98) + Ti-poly(723/11571) + G-band(16)												
Term	Pointing (x, y)							Comment				
03/18 05:49:00 - 03/18 05:55:54	Fixed ( 0.0, 0.0)	synoptic, shifted -14.0 min										
03/19 06:03:00 - 03/21 09:40:00	Fixed ( 0.0, 0.0)	synoptic										
<b>PROG= 05 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 47 1-time(s) 4.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 6 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 88 1-time(s) 4.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 92 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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## Flare mode

\* \* \* \* \*

NOT USED

\* \* \* \* \*

## Active Region Search

\* \* \* \* \*

NOT USED

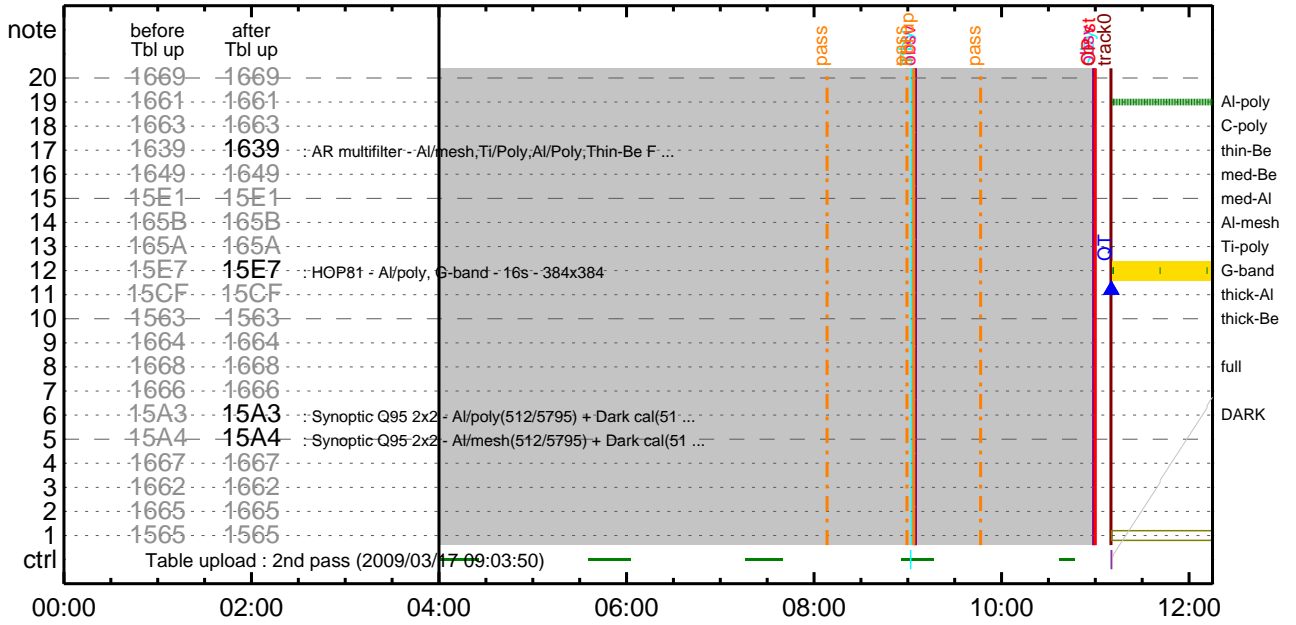
\* \* \* \* \*

## Flare Detection

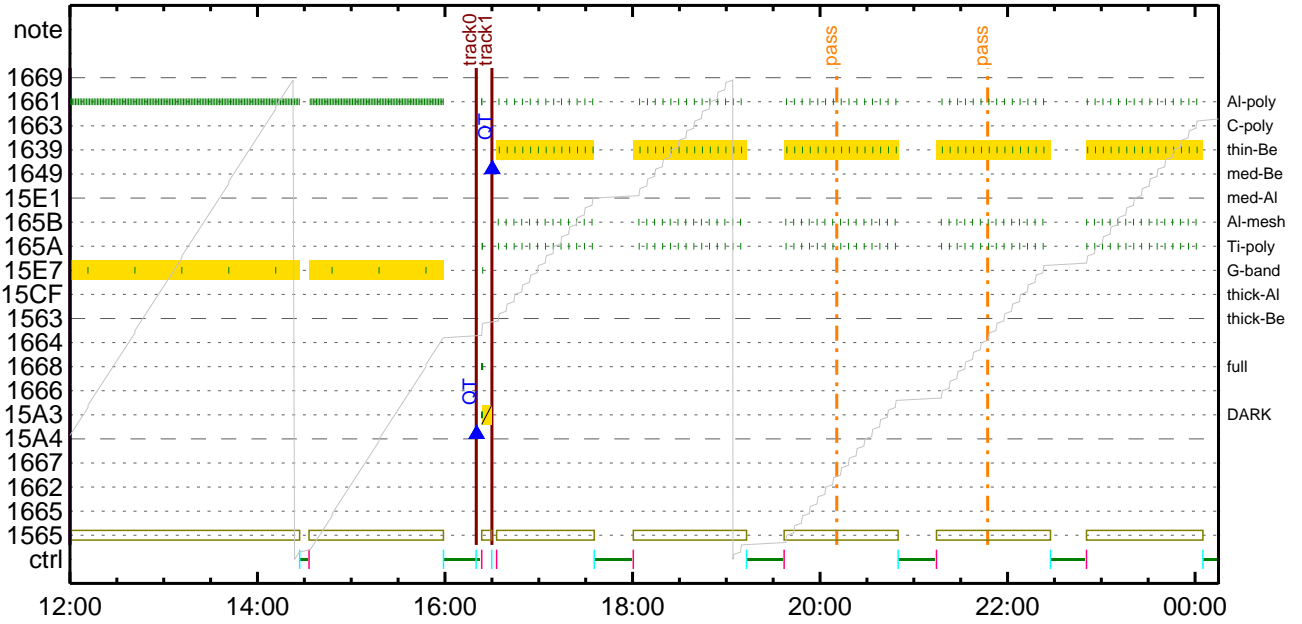
\* \* \* \* \*

NOT USED

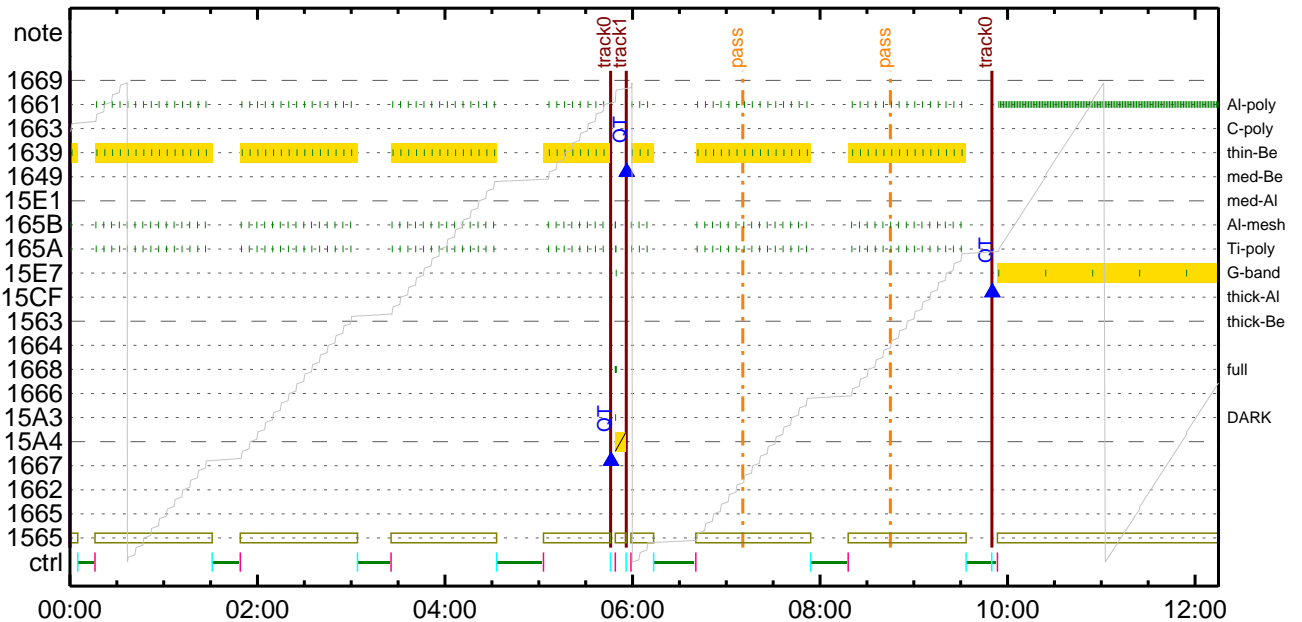
### CMDI #0454 2009/03/17



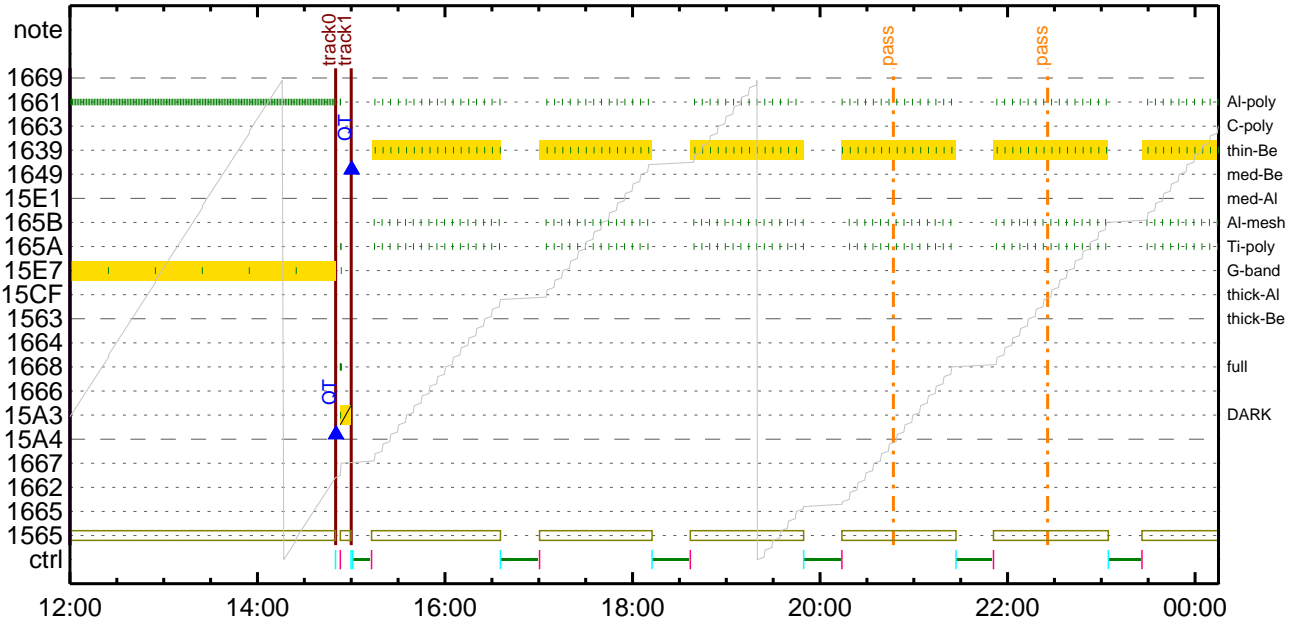
### CMDI #0454 2009/03/17



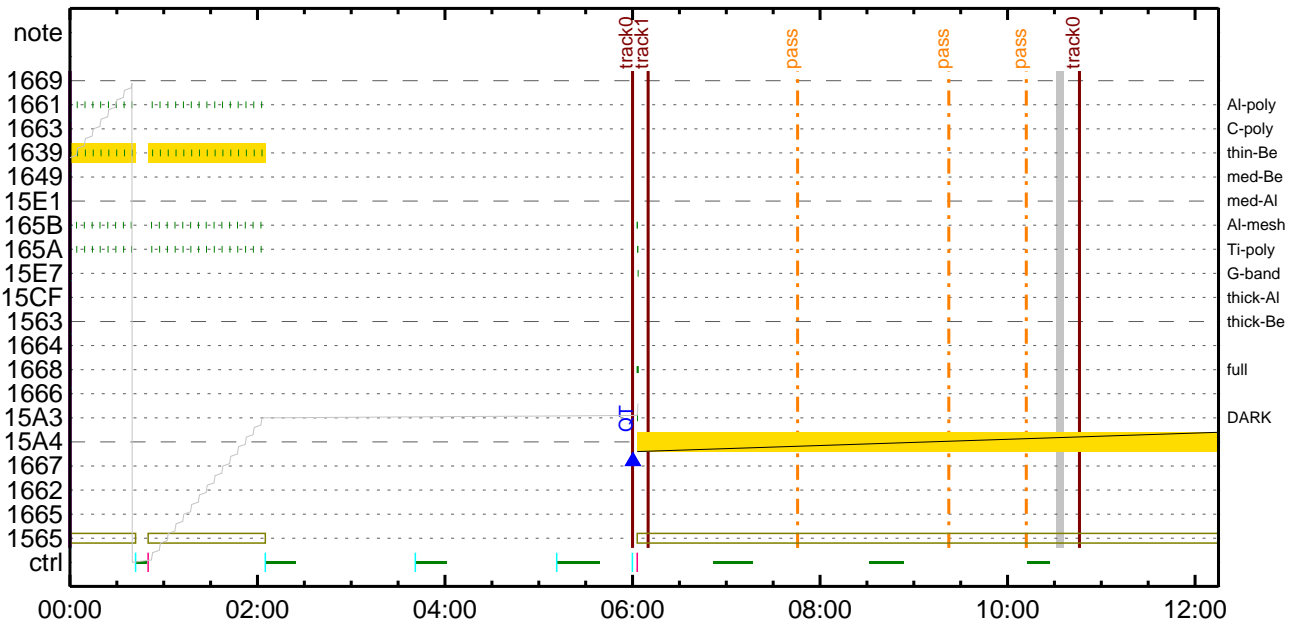
### CMDI #0454 2009/03/18



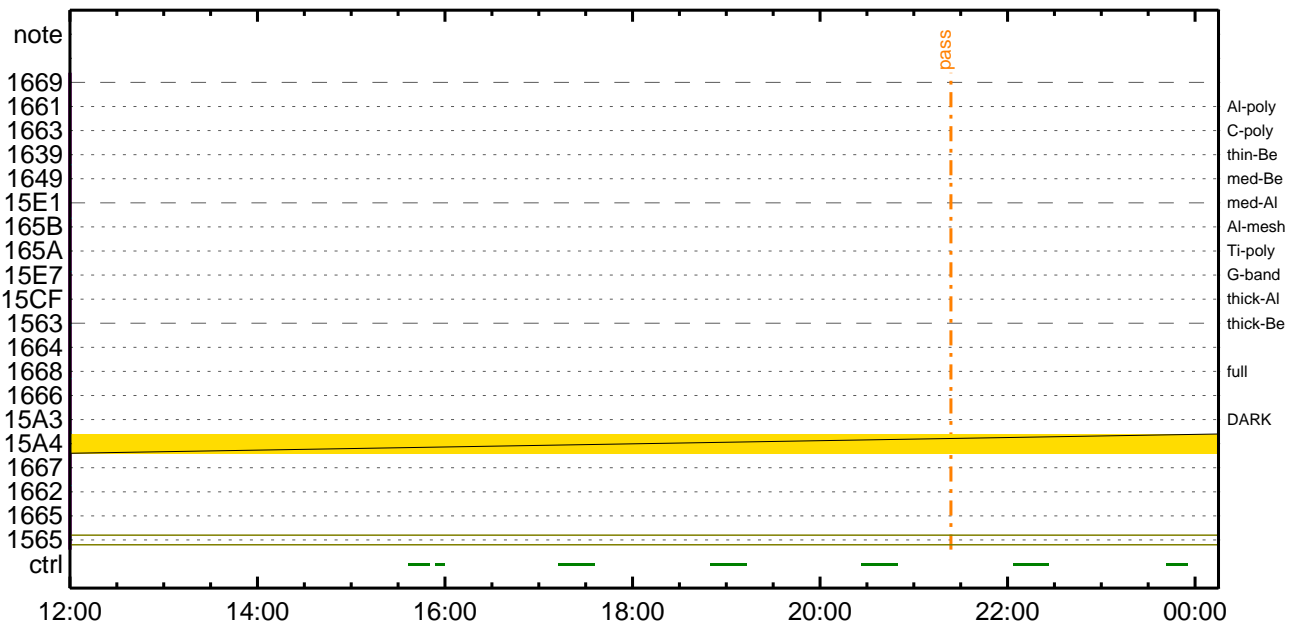
CMDI #0454 2009/03/18



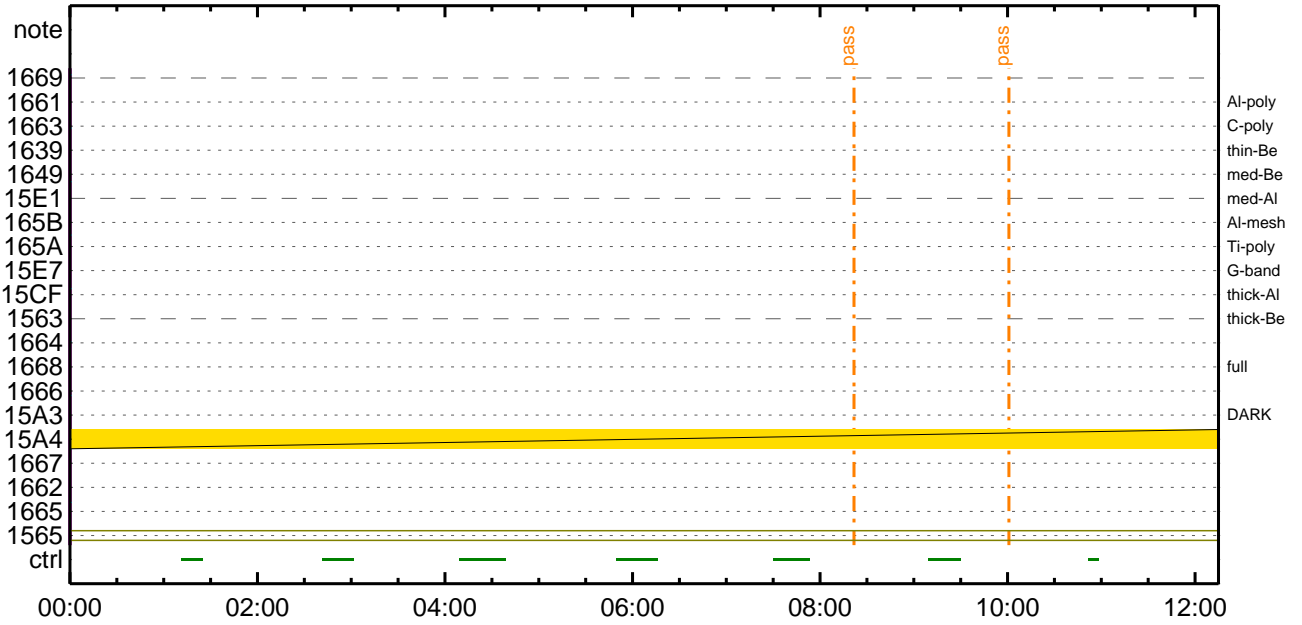
CMDI #0454 2009/03/19



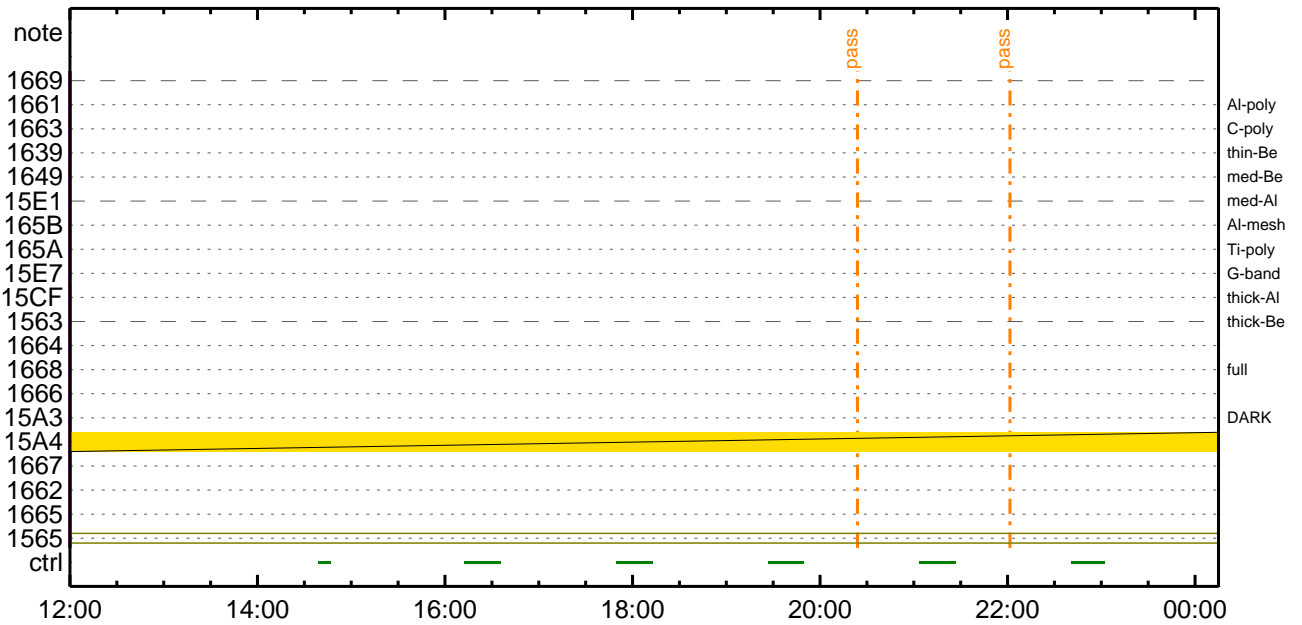
CMDI #0454 2009/03/19



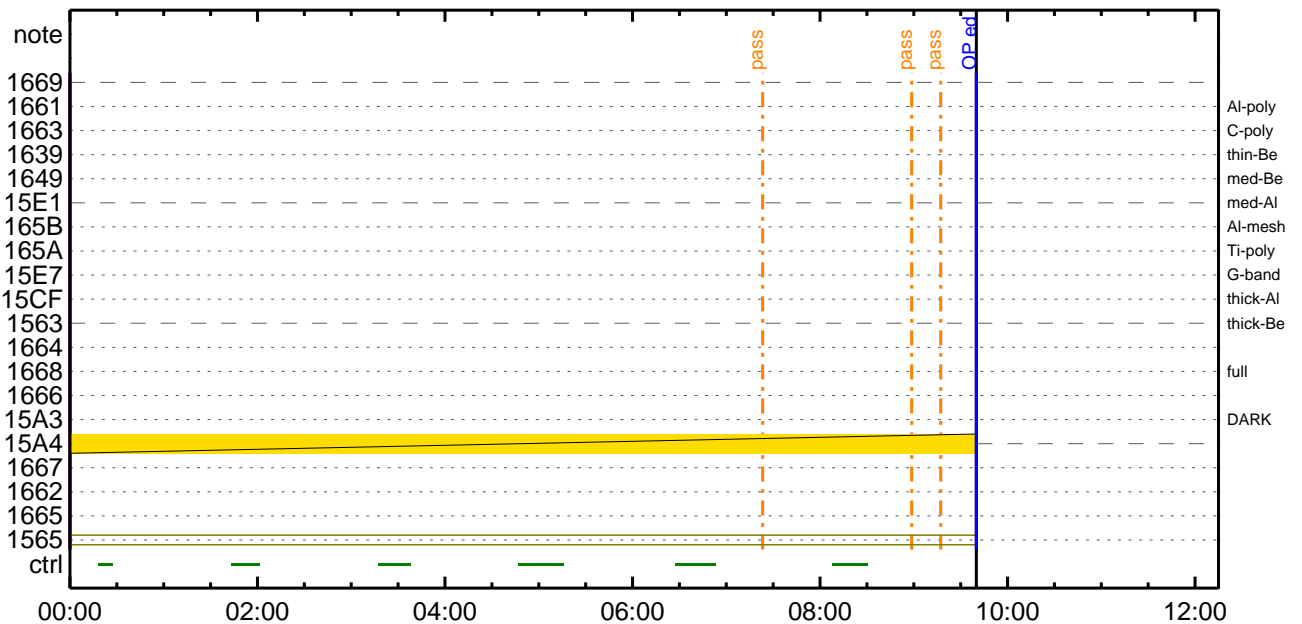
CMDI #0454 2009/03/20



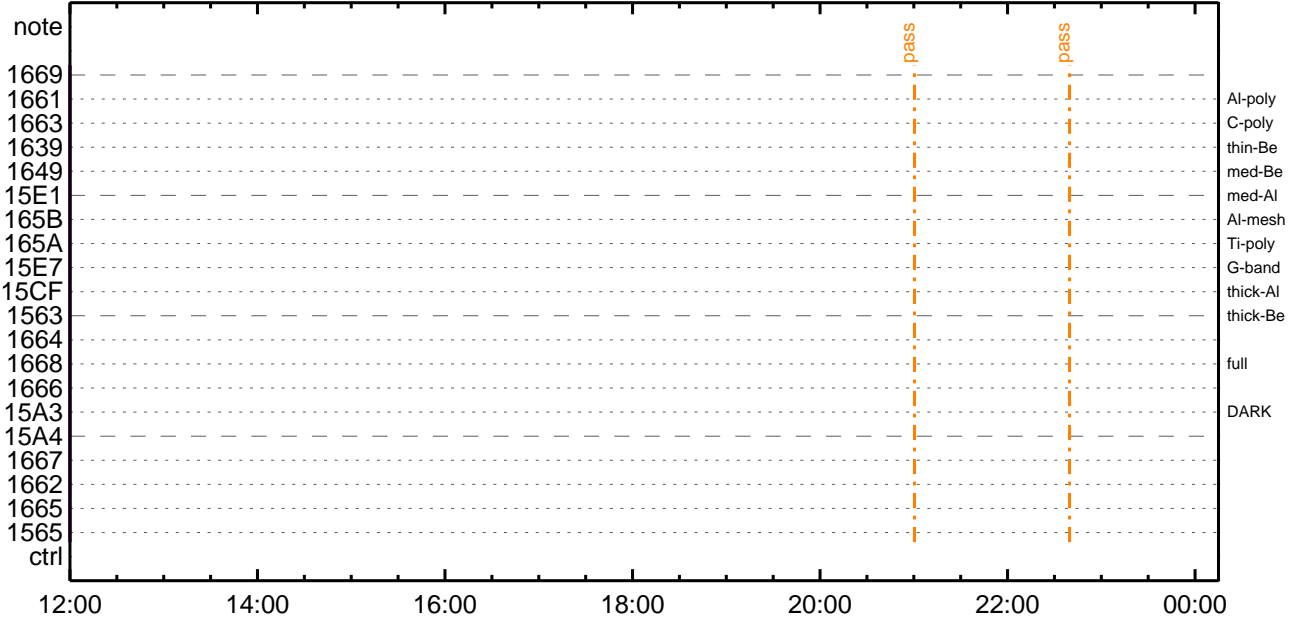
CMDI #0454 2009/03/20



CMDI #0454 2009/03/21



CMDI #0454 2009/03/21



(a) Spacecraft Operation Procedure (real-commands)

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main-477 2009-03-17 12:06:06 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Ä»Û;ã
0005 C.
0006 C. YÀYß;¼Y³YÞYÓYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ëð¿ðÄð•µ°Ë»Í×ÁÇðÍYÇYÁY×YÍ;¼YÉ;ËËÈ¼µ•ííË;ËðÈ¼°Çðð•ð¿¼í¹ÇðÍ;¿Ä®, ùð¹ðÈððÇÄ+¿®ð•ðËððð³ðÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿µ;ON
0016 C. *****
0017 C. ¿ ºÄ, í×ËYðáLOððÇðÍ»p´ððð¹íí, ð, ; ¿Ëðí×ðËXÁðONðí¹ððËð¹ðËððð³ðÈ;f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ¿¿[HK1_XPA_ON/OFF] EQ ON
0025 C. ¿¿[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ¿¿[HK1_XMOD_ON/OFF] EQ ON
0027 C. ¿¿[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÝÓYÉYÍYÁY-¾ðÁðð-ºÄÄðð•ð¿ðÉ; ¿ºË²¼ðíºÄ, ¼È¼Çðð¼Ä¹ðð¹ðÉ;f
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼íºÄ,
0033 C. *****
0034 C. ¿" RESTART;ËPT1;Ëð•ð¿ð¼¼í¹ÇðÍ; ¿ºË²¼ðí¼Ä¹ðð»º; ¿DCBC-150ðð¿ËÈðà;f
0035 C.
0036 . C. ;ãPT1ºÄ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ¿¿[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹ð, ;¼Ú)
0043 C. ¿¿[HK1_REP_STA/STP] EQ START (¼Ä¹ð, ;¼Ú)
0044 C. ¿¿[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹ð, ;¼Ú)
0045 C.
0046 . C. ;ãYÇYÓYÉYËÄÙÄØ;ËÄ•ÄºðÈð;Ë, ãðíºÄ, °Ë³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ¿¿[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹ð, ;¼Ú)
0050 C. ¿¿[HK1_REP_STA/STP] EQ START (¼Ä¹ð, ;¼Ú)
0051 C. ¿¿[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹ð, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1ºÄ, ð-¼«ºÄ»ßð•ð¿, ã; ¿ºË²¼ð¼¼Ä¹ðð¹ðÉ;f
0055 C. YÇYÓYÉYËÄÙÄØðÄÄ•ÄºðÈðð-¼áð¼¼í¹ÇðÍ´ºí»ð¹ðÈððÇÄððÄ;f
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼íºÄ,
0059 C. *****
0060 C. ¿" RESTART;ËPT2;Ëð•ð¿ð¼¼í¹ÇðÍ; ¿ºË²¼ðí¼Ä¹ðð»º; ¿DCBC-151ðð¿ËÈðà;f
0061 C.
0062 . C. ;ãPT2ºÄ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ¿¿[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹ð, ;¼Ú)
0069 C. ¿¿[HK1_REP_STA/STP] EQ START (¼Ä¹ð, ;¼Ú)
0070 C. ¿¿[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹ð, ;¼Ú)
0071 C.
0072 . C. ;ãYÇYÓYÉYËÄÙÄØ;ËÄ•ÄºðÈð;Ë, ãðíºÄ, °Ë³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ¿¿[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹ð, ;¼Ú)
0076 C. ¿¿[HK1_REP_STA/STP] EQ START (¼Ä¹ð, ;¼Ú)
0077 C. ¿¿[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹ð, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DRºÄ, Äã»ß;¿XÁ+¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDRºÄ, Äã»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ¿¿[HK1_REP_STA/STP] EQ STOP
0087 C. ¿¿[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ¿¿[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+¿µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ¿¿[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ¿¿[HK1_XPA_ON/OFF] EQ OFF

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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YA6Yx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-477:OP
0104 ( )
0105 S. OG og-477:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAY6Yx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YA6Yx½ª î»ò³ ÎÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è² İOK²ò³ ÎÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YA6Yx½ª î»ò³ ÎÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è² İOK²ò³ ÎÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YA6Yx½ª î»ò³ ÎÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è² İOK²ò³ ÎÇ§
0165 C.
0166 C. ***** °E²¼ò İ½Ã´ ¶Á°òEĒ-ò°Á÷¿@ (¼âµ-YA6Yx½ª ê½çòðÁŌĀæòÇ¼ª°"òE¼i¹çòçòâ) *****
0167 C. DHUªâ;¼YE;Ē½Y½;Yi;¼YE;Ēòðİã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½E¹ç;ç°E²¼ò İTI-CMDÁ÷¿@²İ½Ā¹Ōª°²Ē²²²³²E;f
0180 C. ²²²²;çSET²ĒDUMP²İĒ±°iYÑY¹²ç¹Ō²|²³²E;f
0181 C.
0182 C. TIY³Y²Y6YĒ²òðĀİ¿¿(UT)
0183 +. TI 2009-03-17 10:55:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2009-03-17 10:55:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2009-03-17 10:55:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
```





(a) Spacecraft Operation Procedure (real-commands)

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main-478 2009-03-17 12:06:06 140 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼ÁÛ;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Èα¿αÁα•μ°Æ»Í×ÁÇαíY¿YÁY×Yí;¼YÉ;ÈÈ%μ•ííÉ;ÈαÈ¼°ÇÔα•α¿¼í¹çαí;çÁ®, ùα¹αÈαBαÇÁ+¿®α•αÈααα³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. ***** AOCS Commands (Orbital Element Update) *****
0046 C. Update the orbital element
0047 +. DC 02-50 AOCU_ORB_PRPGT_START
0048 BC (16)
0049 +. DC 02-8E AOCU_ORB_UPD
0050 C.
0051 C. <A_ORB>[ORBIT] EPC = 2407739.5 +- 1.0 (s) [ ]
0052 C.
0053 . C.
0054 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055 +. DC 07-FC EIS_MODE_MANU
0056 BC (21 02)
0057 . C. Verify EIS in MANUAL mode
0058 . C. Estimated OBSTBL upload time is 21s
0059 C. *****
0060 C. EIS START OBSTBL LOAD
0061 C. *****
0062 . S. RAM ram-820:EIS_OBSTBL
0063 ( )
0064 +. DC 07-FC EIS_DUMP_OBSTBL
0065 BC (07 07 07 00 00 70 00)
0066 C.
0067 C. Execute, after the success of OBSTBL upload.
0068 C. Set EIS TI-commands
0069 +. TI 2009-03-17 10:59:50.0
0070 DC 07-FC EIS_MODE_CHG_ENA
0071 BC (20)
0072 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0073 C. *****
0074 C. EIS END OBSTBL LOAD
0075 C. *****
0076 C.
0077 C. ***** XRT START *****
0078 C.
0079 +. DC 07-F0 MDP_XRT_CTRL_MANU
0080 BC (c1)
0081 +. DC 07-F0 MDP_XRT_MODE_STBY
0082 BC (c3)
0083 . C. ----- Success Verify ? OK / NG____
0084 C.
0085 C. XRT Obs. Table Upload
0086 . S. RAM ram-291:MDP_OBS_X
0087 ( )
0088 C.
0089 +. DC 07-F0 MDP_DUMP_XRTTBL
0090 BC (84 07 00 00 00 3a d4)
0091 . C. ----- Comparison Check ? OK / ERR ____
0092 C.
0093 C.
0094 +. DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 01 b1 b1 04 04)
```

```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 02 b1 b1 08 08)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 03 b1 b1 08 08)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 04 b1 b1 06 06)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 06 85 83 06 06)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 07 80 80 20 20)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 08 80 80 06 06)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 0f 80 80 04 04)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 10 80 80 10 10)
0112 . C. ----- Success Verify ? OK / NG ____
0113 C.
0114 C.
0115 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0116 C.
0117 +. DC 07-F0 MDP_XRT_MODE_OBSV
0118 BC (c2)
0119 +. TI 2009-03-17 10:59:02.0
0120 DC 07-F0 MDP_XRT_MODE_OBSV
0121 BC (c2)
0122 . C. ----- Success Verify ? OK / NG ____
0123 C.
0124 C. ***** XRT END *****
0125 C.
0126 . C. ***** MDP `úÃîñî»ö%ÝðÊÃð¹ñèDCBC•x²è *****
0127 C. (%á°îÿÓÿÃÿÈÿbÿËÿáÿçÿèñÈ%¼ñ¼Ã»Ûñ¹ñè)
0128 . S. DC-BC dcbc-402:DCBC
0129 (MDP_known_event)
0130 C.
0131 C.
0132 . C. ***** ÿDÿ¹•Ï Daily±¿îÑñÈ´Øñ¹ñèDCBC•x²è *****
0133 . S. DC-BC dcbc-153:DCBC
0134 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0135 C.
0136 C.
0137 . C. ;ãLOSÿÁÿ$ÿÃÿ-¼Ã»Û;ã
0138 C.
0139 . C. ***** LOS *****
0140 C.
```



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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-262:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Upload DPL table >
0120 C.
0121 C. ¥çYÃ¥×¥í;¼¥É°îÁ°ñÈSTS_CHKñðOFFñÈñ¹ñè
0122 C.
0123 . S. RAM ram-271:MDP_DPL
0124 ( )
0125 C.
0126 . C. < Dump RAMID=MDP_DPL >
0127 +. DC 07-F0 MDP_DUMP_FGTBL
0128 BC (82 07 00 38 b8 00 40)
0129 C. -----
0130 C. MDP_DPL verify = OK [ ]
0131 C. -----
0132 C.
0133 C. STS_CHKñðONñÈñ¹ñè
0134 C.
0135 . C. < Update MDP DSC PAR1 >
0136 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0137 BC (4c)
0138 C. MDP_CMD_CODE = F04C0700[ ]
0139 C. MDP_CMD_CNT (count-up 1) [ ]
0140 C. -----
0141 C.
0142 . C.
0143 C. *****
0144 C. SOT TI command set
0145 C. *****
0146 C. Execute, after the success of TBL upload.
0147 +. TI 2009-03-17 10:59:18.0
0148 DC 07-F0 MDP_SOT_MODE_OBSV
0149 BC (40)
0150 . C. -----
0151 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0152 C. -----
0153 C.
0154 C.
0155 . C. ***** MDP `úÃîîî»ò¼YñÈÁñ¹ñèDCBC•x²è *****
0156 C. (¼ã°îYÓYÃYÉY¥YÉYáYçYèñÈ¼ñ¼Á»Ûñ¹ñè)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** ¥D¥¹•İ Daily±¿İÑñÈ´Øñ¹ñèDCBC•x²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;ãLOS¥Ã¥S¥Ã¥¹¼Á»Û;ã
0167 C.
0168 . C. ***** LOS *****
0169 C.

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\*\*\* OP Sequence for XRT \*\*\*

2009/03/17	11:09:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/03/17	11:09:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2009/03/17	11:10:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 53 8d 00 00				
2009/03/17	11:10:16.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2009/03/17	11:10:18.0	XRT_AEC_RESET_415_OG [0x19f]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2009/03/17	11:10:20.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/03/17	11:10:22.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/03/17	11:10:24.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/03/17	11:10:26.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/03/17	14:27:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/03/17	14:32:00.0	XRT_Custom_430_OG [0x1ae]							
2009/03/17	14:33:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/03/17	15:59:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/03/17	16:19:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/03/17	16:19:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/03/17	16:20:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2009/03/17	16:20:16.0	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2009/03/17	16:20:18.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/03/17	16:20:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/03/17	16:20:22.0	XRT_ARS_DIS_410_OG [0x19a]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/03/17	16:22:30.0	XRT_Custom_430_OG [0x1ae]							
2009/03/17	16:23:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/03/17	16:29:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/03/17	16:29:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2009/03/17	16:30:00.0	AOCS_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2009/03/17	16:30:16.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2009/03/17	16:30:18.0	XRT_AEC_RESET_415_OG [0x19f]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2009/03/17	16:30:20.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/03/17	16:30:22.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/03/17	16:30:24.0	XRT_FLRCTRL_DIS_406_OG [0x196]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/03/17	16:33:00.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/03/17	17:35:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/03/17	17:59:30.0	XRT_Custom_430_OG [0x1ae]							
2009/03/17	18:00:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/03/17	19:13:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/03/17	19:36:00.0	XRT_Custom_430_OG [0x1ae]							
2009/03/17	19:37:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/03/17	20:50:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/03/17	21:13:30.0	XRT_Custom_430_OG [0x1ae]							
2009/03/17	21:14:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/03/17	22:27:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/03/17	22:49:30.0	XRT_Custom_430_OG [0x1ae]							
2009/03/17	22:50:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/03/18	00:05:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/03/18	00:15:00.0	XRT_Custom_430_OG [0x1ae]							
2009/03/18	00:16:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/03/18	01:31:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/03/18	01:48:00.0	XRT_Custom_430_OG [0x1ae]							
2009/03/18	01:49:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/03/18	03:04:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							

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2009/03/18	03:24:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/03/18	03:25:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
2009/03/18	04:33:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/03/18	05:02:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/03/18	05:03:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
2009/03/18	05:45:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/03/18	05:45:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/03/18	05:46:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2009/03/18	05:46:16.0	XRT_QT_PROG_SET_402_OG [0x192]	AOCU_NM	5	02-76	00	00	00	00	00
2009/03/18	05:46:18.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2009/03/18	05:46:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/03/18	05:46:22.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/03/18	05:49:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/03/18	05:55:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/03/18	05:55:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/03/18	05:56:00.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2009/03/18	05:56:16.0	XRT_QT_PROG_SET_423_OG [0x1a7]	AOCU_NM	5	02-76	01	00	00	00	00
2009/03/18	05:56:18.0	XRT_AEC_RESET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2009/03/18	05:56:20.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2009/03/18	05:56:22.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/03/18	05:56:24.0	XRT_FLRCTRL_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/03/18	05:59:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/03/18	06:13:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/03/18	06:39:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/03/18	06:40:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
2009/03/18	07:54:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/03/18	08:17:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/03/18	08:18:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
2009/03/18	09:33:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/03/18	09:49:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/03/18	09:49:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/03/18	09:50:00.0	AOCS_ORe-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2009/03/18	09:50:16.0	XRT_QT_PROG_SET_438_OG [0x1b6]	AOCU_NM	5	02-76	00	53	8d	00	00
2009/03/18	09:50:18.0	XRT_AEC_RESET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2009/03/18	09:50:20.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2009/03/18	09:50:22.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/03/18	09:50:24.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/03/18	09:52:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/03/18	09:53:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
2009/03/18	14:49:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/03/18	14:49:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/03/18	14:50:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2009/03/18	14:50:16.0	XRT_QT_PROG_SET_431_OG [0x1af]	AOCU_NM	5	02-76	00	00	00	00	00
2009/03/18	14:50:18.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2009/03/18	14:50:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/03/18	14:50:22.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/03/18	14:53:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/03/18	14:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/03/18	14:59:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2009/03/18	15:00:00.0	AOCS_Or-e-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
		AOCU_NM		5	02-76	01	00	00	00
2009/03/18	15:00:16.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11		
2009/03/18	15:00:18.0	XRT_AEC_RESET_415_OG [0x19f]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2009/03/18	15:00:20.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2009/03/18	15:00:22.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/03/18	15:00:24.0	XRT_FLRCTRL_DIS_406_OG [0x196]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/03/18	15:01:01.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/03/18	15:12:00.0	XRT_Custom_430_OG [0x1ae]							
2009/03/18	15:13:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/03/18	16:35:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/03/18	16:59:30.0	XRT_Custom_430_OG [0x1ae]							
2009/03/18	17:00:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/03/18	18:12:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/03/18	18:36:00.0	XRT_Custom_430_OG [0x1ae]							
2009/03/18	18:37:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/03/18	19:49:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/03/18	20:13:00.0	XRT_Custom_430_OG [0x1ae]							
2009/03/18	20:14:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/03/18	21:27:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/03/18	21:50:01.0	XRT_Custom_430_OG [0x1ae]							
2009/03/18	21:51:01.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/03/18	23:04:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/03/18	23:25:00.0	XRT_Custom_430_OG [0x1ae]							
2009/03/18	23:26:00.5	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/03/19	00:42:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/03/19	00:49:00.0	XRT_Custom_430_OG [0x1ae]							
2009/03/19	00:50:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/03/19	02:05:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/03/19	03:41:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/03/19	05:11:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/03/19	05:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/03/19	05:59:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2009/03/19	06:00:00.0	AOCS_Or-e-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00
2009/03/19	06:00:16.0	XRT_QT_PROG_SET_402_OG [0x192]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05		
2009/03/19	06:00:18.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/03/19	06:00:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/03/19	06:00:22.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2009/03/19	06:03:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/03/19	06:10:00.0	AOCS_Or-e-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01	00	00	00
2009/03/19	10:46:00.0	AOCS_Or-e-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00