

XRT Timeline to be uploaded on 2009/03/24

Period: 2009/03/24 10:27:00 - 2009/03/28 11:17:00

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Normal mode

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XOB #1656: Synoptic Q95 2x2 - Al/poly(181/2048) + Dark cal(256 Q98) + Ti-poly(256/4096) + G-band(16)												
Term	Pointing (x, y)				Comment							
03/24 18:03:00 - 03/25 00:49:54	Fixed (0.0, 0.0)				synoptic							
03/25 18:06:00 - 03/25 18:12:54	Fixed (0.0, 0.0)				synoptic, shifted 3.0 min							
PROG= 01 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 14 1-time(s) 4.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 49 1-time(s) 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Dark 250ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 21 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 92 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1628: Full Sun - CME Watch - C/Poly + Thin-Be + Med-Be - 4x4 - AEC4												
Term	Pointing (x, y)				Comment							
03/25 00:53:02 - 03/25 06:15:54	Track (-168.1, 512.8) @ 03/25 00:50:00				* Ttsck AR							
03/25 06:29:02 - 03/25 17:39:30	Track (-122.9, 513.6) @ 03/25 06:26:00				# Cont track AR							
03/25 18:16:02 - 03/26 05:49:54	Track (-26.8, 514.2) @ 03/25 18:13:00				# Cont track AR							
03/26 06:03:02 - 03/26 09:37:30	Track (69.5, 513.6) @ 03/26 06:00:00				# Cont track AR							
PROG= 10 Inf.-time(s)												
└─ Subr= 1 4-time(s) 480.0sec												
└─ Seqn= 10 1-time(s) 60.0sec												
└─ C-poly/Open med-Be/Open close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 4 0 2.0sec												
└─ Seqn= 79 1-time(s) 60.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 4.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 4 0 2.0sec												
└─ Seqn= 76 1-time(s) 60.0sec												
└─ med-Be/Open med-Be/Open close Safe Norm 22.6s Obs 4x4 2048x2048 (1024, 1024) Q=98 4 0 2.0sec												
└─ Subr= 2 1-time(s) 60.0sec												
└─ Seqn= 13 1-time(s) 30.0sec												
└─ C-poly/Open C-poly/Open close Safe Dark 22.6s Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1657: Synoptic Q95 2x2 - Al/mesh(128/2048) + Dark cal(256 Q98) + Ti-poly(256/4096) + G-band(16)												
Term	Pointing (x, y)				Comment							
03/25 06:19:00 - 03/25 06:25:54	Fixed (0.0, 0.0)				synoptic, shifted 16.0 min							
03/26 05:53:00 - 03/26 05:59:54	Fixed (0.0, 0.0)				synoptic, shifted -10.0 min							
PROG= 09 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 26 1-time(s) 4.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 50 1-time(s) 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Dark 250ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 21 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 92 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

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Flare mode

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NOT USED

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Active Region Search

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NOT USED

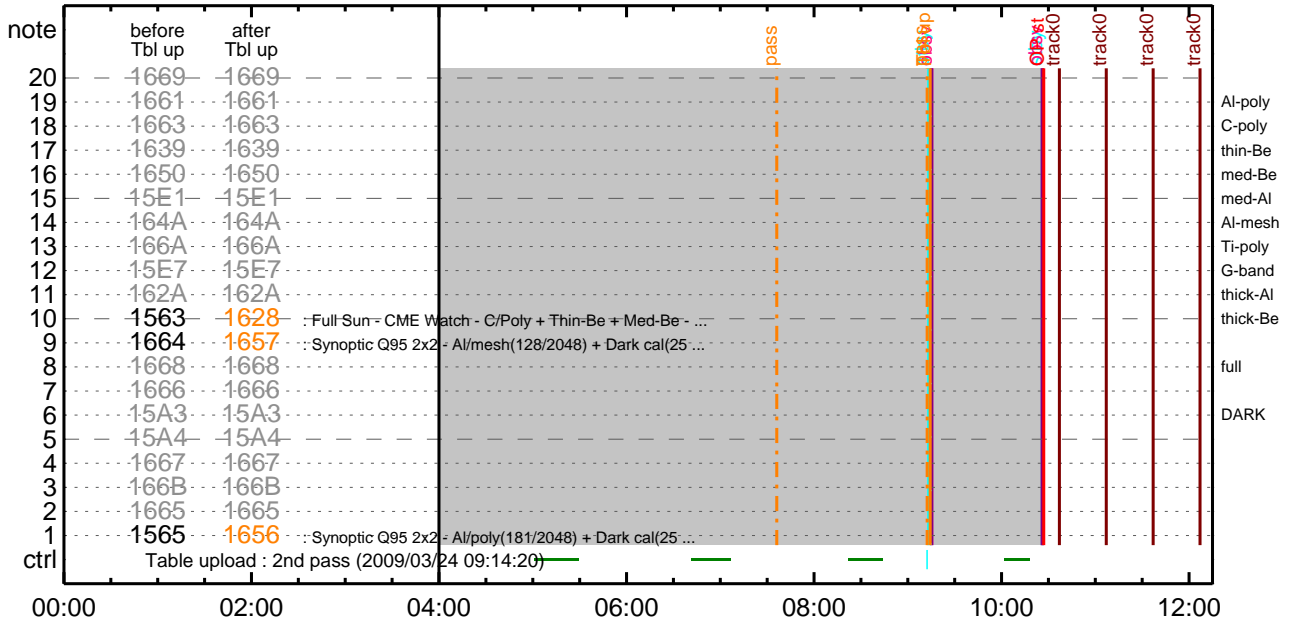
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Flare Detection

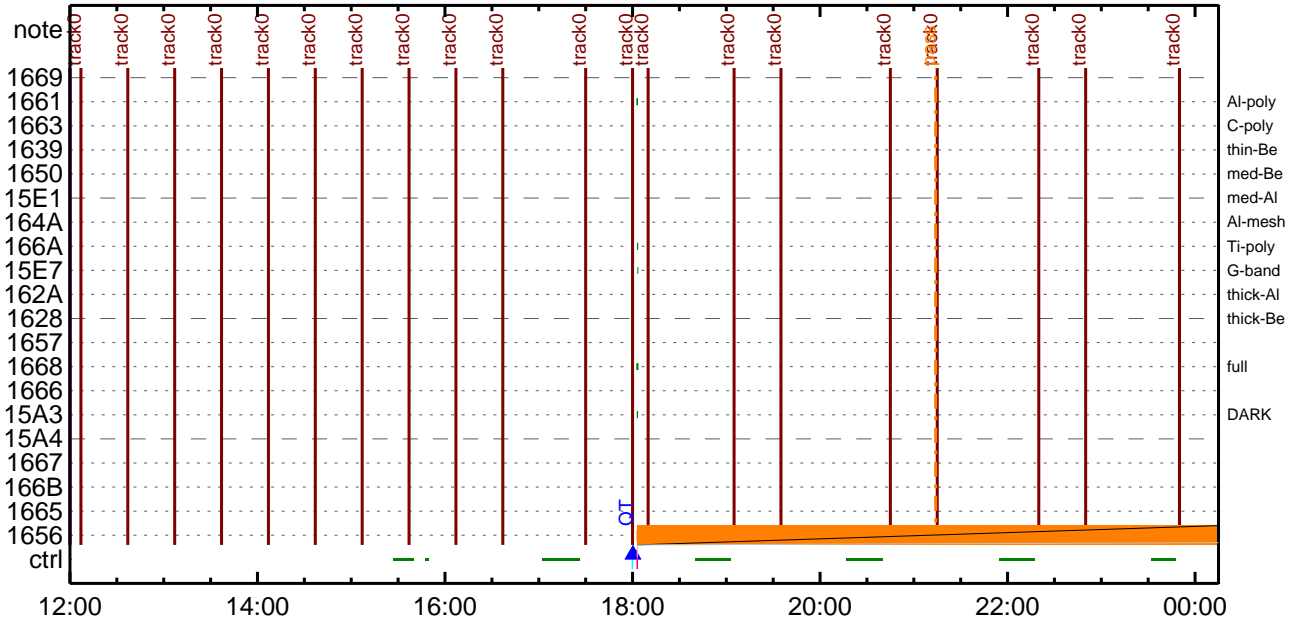
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NOT USED

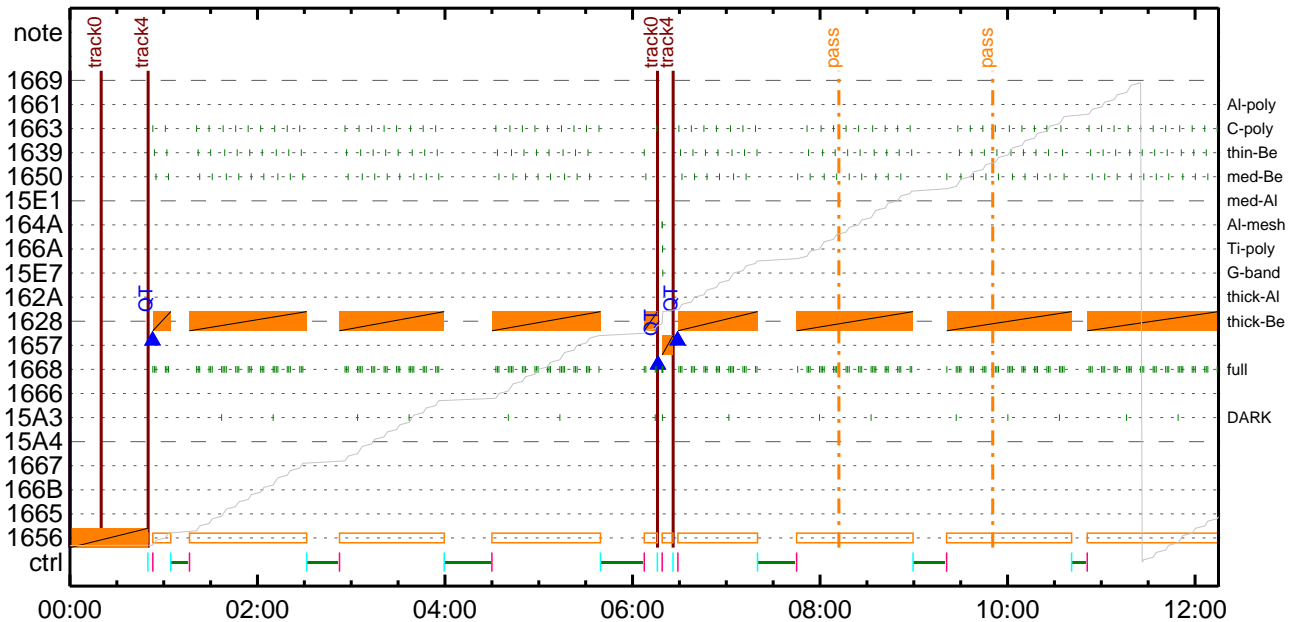
CMDI #0468 2009/03/24



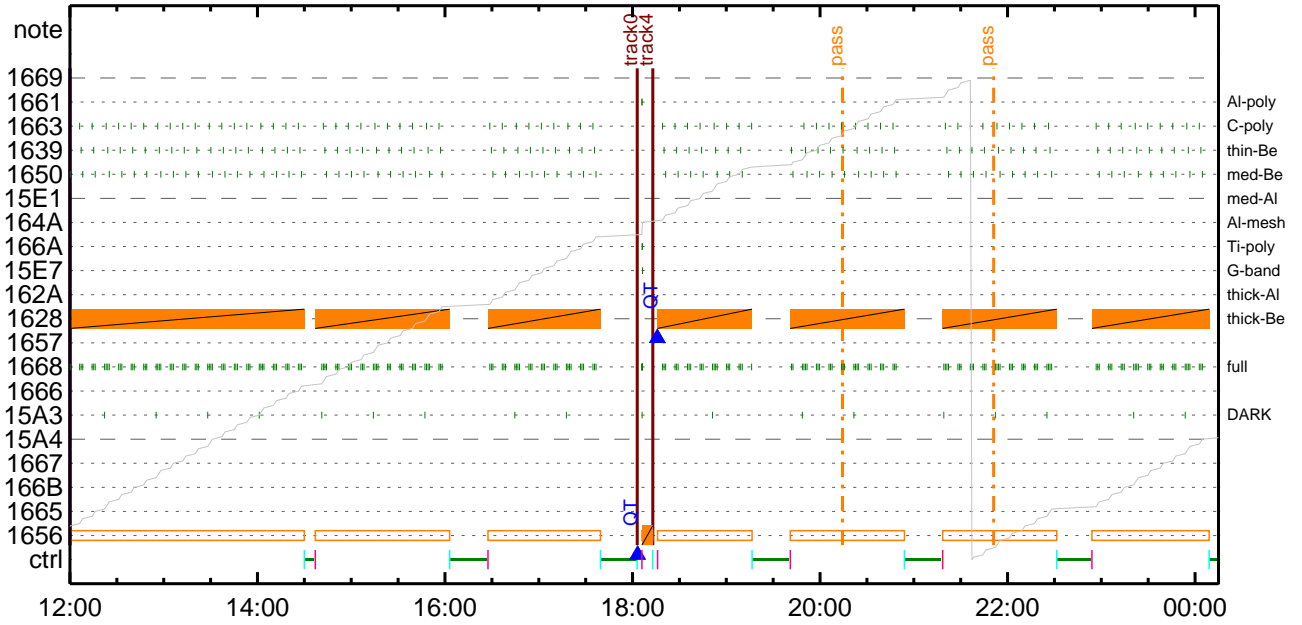
CMDI #0468 2009/03/24



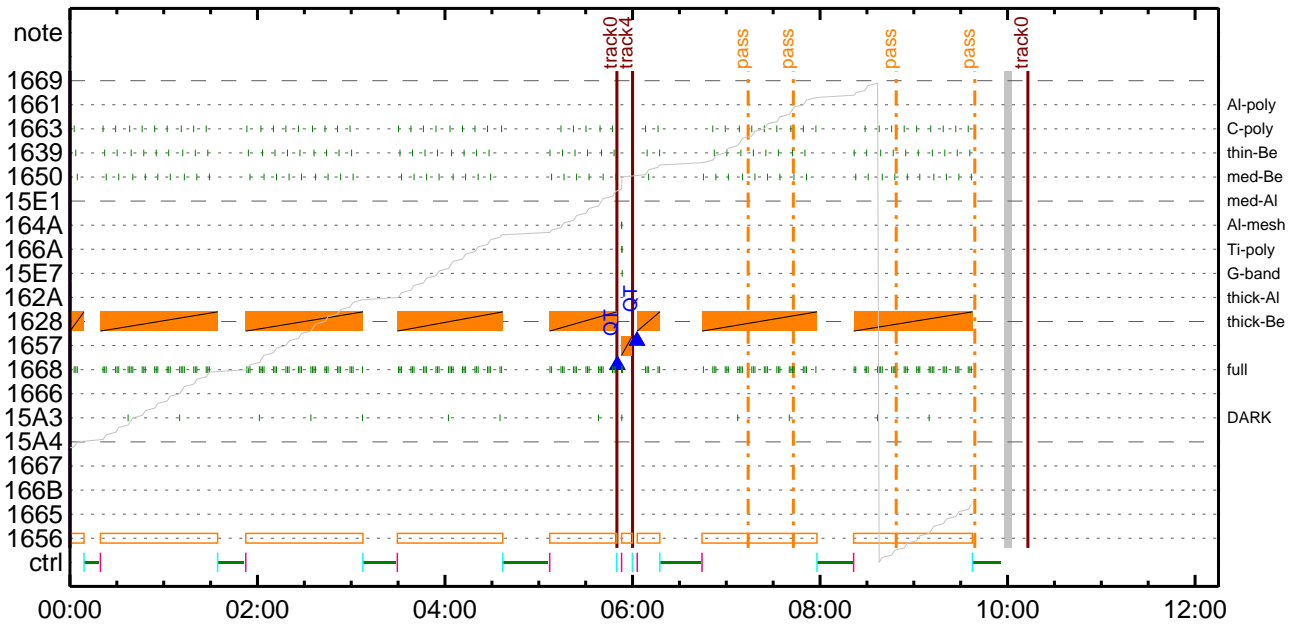
CMDI #0468 2009/03/25



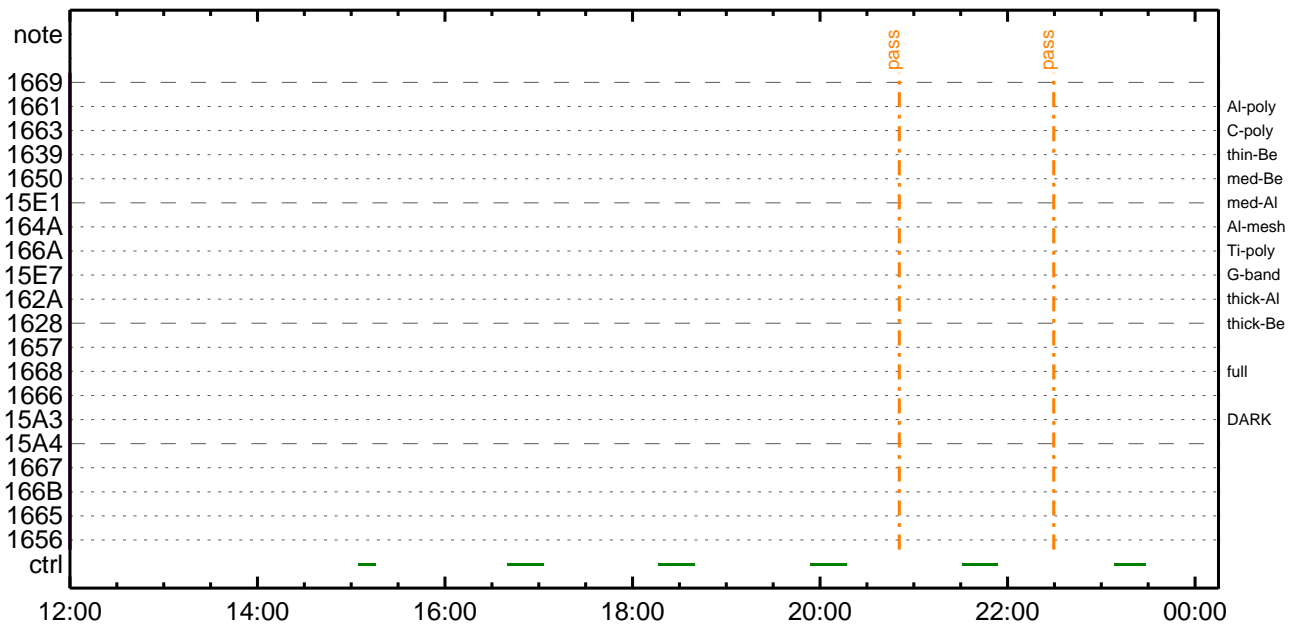
CMDI #0468 2009/03/25



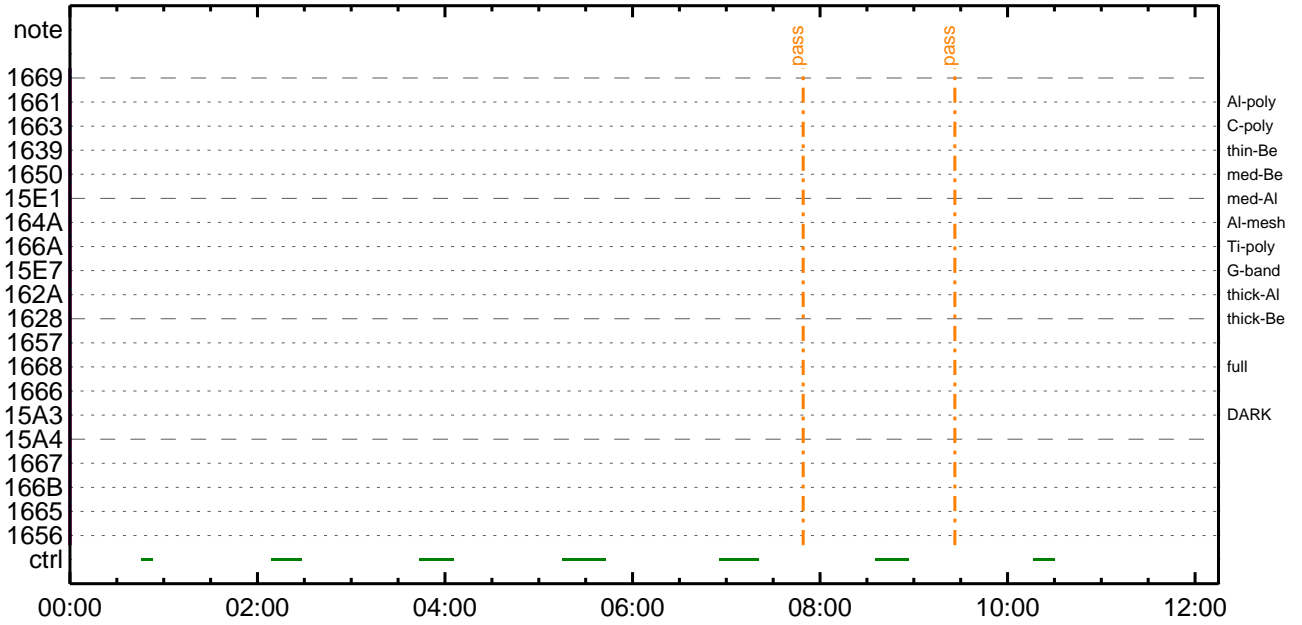
CMDI #0468 2009/03/26



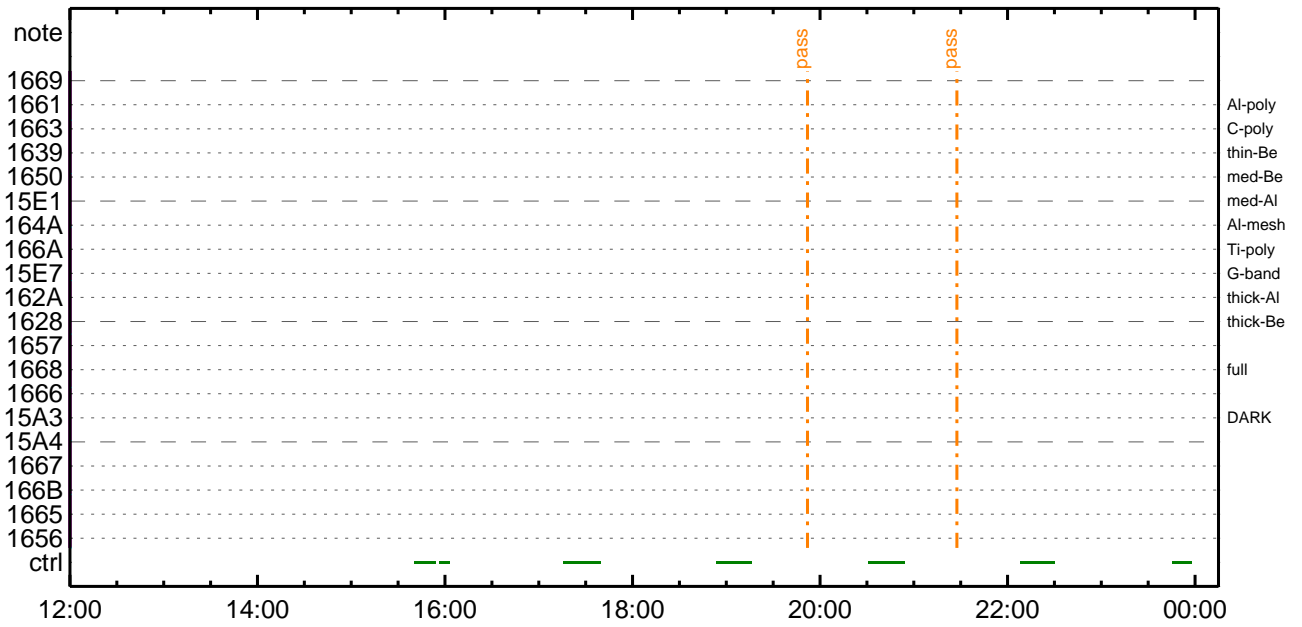
CMDI #0468 2009/03/26



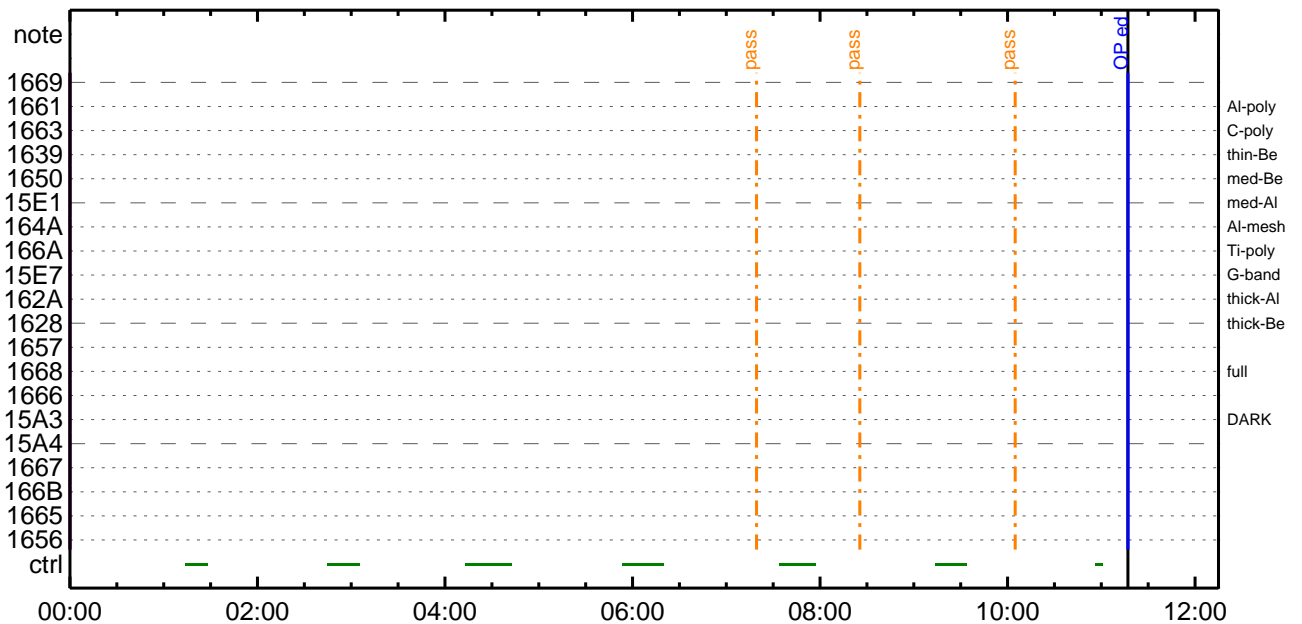
CMDI #0468 2009/03/27



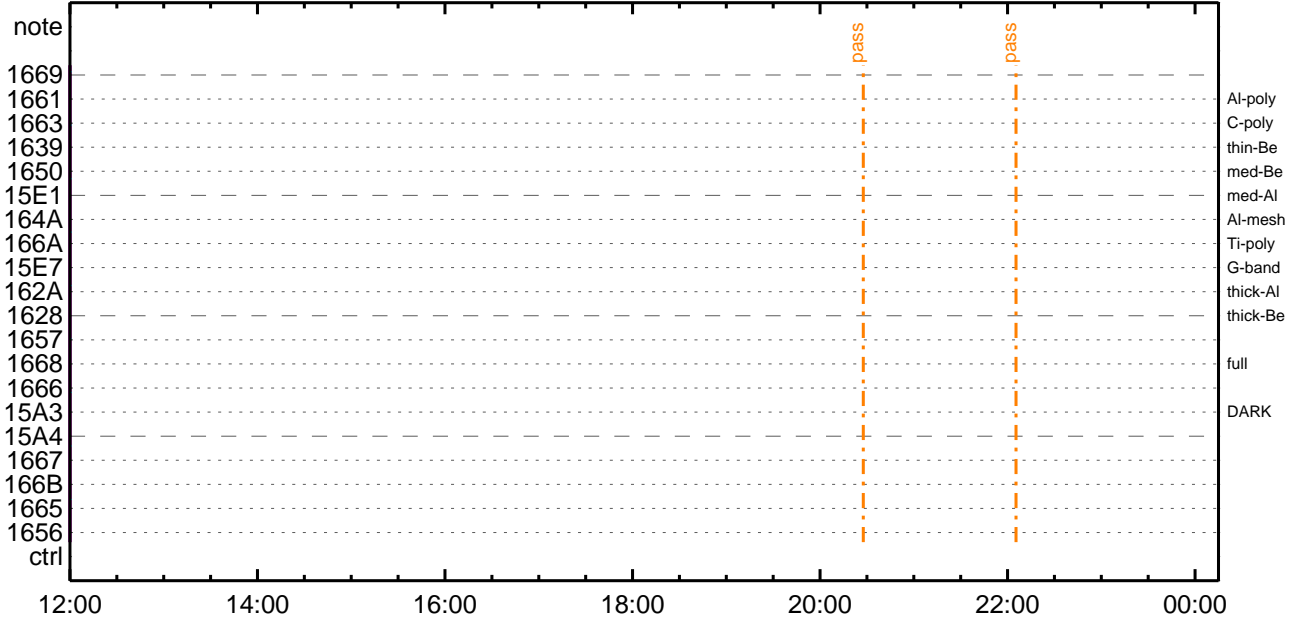
CMDI #0468 2009/03/27



CMDI #0468 2009/03/28



CMDI #0468 2009/03/28



(a) Spacecraft Operation Procedure (real-commands)

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main-490 2009-03-24 14:11:33 278 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSÝÄÝ$ÝÄÝ-¼Ä»Û;ä
0005 C.
0006 C. ÝÄÝß;¼Ý³ÝßÝóÝÉÄ+¿©
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Äí;Ë¿¿ãß•µ°Ë»í×ÁÇ¿íÝÇÝÄÝ×Ýí;¼ÝÉ;ËËË¼µ•ííË;ËßË¼°ÇÓã•¿¿¼í¹Çãí;çÄ®, ùã¹ãßãßãÇÄ+¿©ã•ßËããã³ãË;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ+¿@µ;ON
0016 C. *****
0017 C. ç" °ÄÄ, í×ËÝãäLOSãßãÇãí»p´Ïãð¹íí, ã•; çËÖí×ãËXÄÓONãí¹ÏãËãíãËãã³ãË;f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÝóÝÉÝíÝÄÝ-¼ÖÄÖã-°ÄÄËã•¿¿;ç°Ë²¼ãí°ËÄ, ¼Ë¼Çãð¼Ä¹Ïã¹ãË;f
0030 C.
0031 . C. *****
0032 C. DR PT1 Äí¼í°ËÄ,
0033 C. *****
0034 C. ç" RESTART;ËPT1;Ëã•¿¿¼¼í¹Çãí;ç°Ë²¼ãí°ËÄ¹Ïã»ã°; çDCBC-150ãØ¿ËËã;f
0035 C.
0036 . C. ;ãPT1°ËÄ, ³«»í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ï, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ï, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ï, ;¼Ú)
0045 C.
0046 . C. ;ãÝÇÝóÝÉÝËËÄÚÄÏ;ËÄ•Ä°²óËð;Ë, äãí°ËÄ, °Ë³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ï, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ï, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ï, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ËÄ, ã-¼«Ë°Äã»ßã•¿¿;ä;ç°Ë²¼ãð¼Ä¹Ïã¹ãË;f
0055 C. ÝÇÝóÝÉÝËËÄÚÄÏãÄ•Ä°²óËðã-¼ãã¼í¹Çãí°Ëí°Ëí»ã¹ãËãßãÇÄÖãÄ;f
0056 C.
0057 . C. *****
0058 C. DR PT2 Äí¼í°ËÄ,
0059 C. *****
0060 C. ç" RESTART;ËPT2;Ëã•¿¿¼¼í¹Çãí;ç°Ë²¼ãí°ËÄ¹Ïã»ã°; çDCBC-151ãØ¿ËËã;f
0061 C.
0062 . C. ;ãPT2°ËÄ, ³«»í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ï, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ï, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ï, ;¼Ú)
0071 C.
0072 . C. ;ãÝÇÝóÝÉÝËËÄÚÄÏ;ËÄ•Ä°²óËð;Ë, äãí°ËÄ, °Ë³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ï, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ï, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ï, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ËÄ, Äã»ß; çXÄ+¿@µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ËÄ, Äã»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ+¿@µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF

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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-490:OP
0104 ( )
0105 S. OG og-490:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¼E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¼E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î¼E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî¼Ã´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOYx½ê¼çòðÁÔÃæç¼ª°¬²è¼î¹çççðâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î¼î¹ç;ç°E²¼òî¼TI-CMDÁ÷¿@²î¼Á¹Ô²•²E²²²³²E;f
0180 C. ²²²¿;çSET²E²DUMP²î¼E²±²îY²¹²ç¹Ô²|²³²E;f
0181 C.
0182 C. TIY³Y²Y²Y²E²òðÁDî¿(UT)
0183 +. TI 2009-03-24 10:22:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2009-03-24 10:22:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2009-03-24 10:22:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCs Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 + DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 3059344.3 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_MANU
0140 BC (21 02)
0141 . C. Verify EIS in MANUAL mode
0142 . C. Estimated OBSTBL upload time is 37s
0143 C. *****
0144 C. EIS START OBSTBL LOAD
0145 C. *****
0146 . S. RAM ram-820:EIS_OBSTBL
0147 ( )
0148 +. DC 07-FC EIS_DUMP_OBSTBL
0149 BC (07 07 07 00 00 70 00)
0150 C.
0151 C. Execute, after the success of OBSTBL upload.
0152 C. Set EIS TI-commands
0153 +. TI 2009-03-24 10:26:50.0
0154 DC 07-FC EIS_MODE_CHG_ENA
0155 BC (20)
0156 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0157 C. *****
0158 C. EIS END OBSTBL LOAD
0159 C. *****
0160 C.
0161 C. ***** XRT START *****
0162 C.
0163 +. DC 07-F0 MDP_XRT_CTRL_MANU
0164 BC (c1)
0165 + DC 07-F0 MDP_XRT_MODE_STBY
0166 BC (c3)
0167 . C. ----- Success Verify ? OK / NG____
0168 C.
0169 C. XRT Obs. Table Upload
0170 . S. RAM ram-291:MDP_OBS_X
0171 ( )
0172 C.
0173 +. DC 07-F0 MDP_DUMP_XRTTBL
0174 BC (84 07 00 00 00 3a d4)
0175 . C. ----- Comparison Check ? OK / ERR ____
0176 C.
0177 C.
0178 +. DC 07-F0 MDP_XRT_ROI_SET
0179 BC (cd 01 b1 b1 04 04)
0180 + DC 07-F0 MDP_XRT_ROI_SET
0181 BC (cd 02 b1 b1 08 08)
0182 + DC 07-F0 MDP_XRT_ROI_SET
0183 BC (cd 03 b1 b1 08 08)
0184 + DC 07-F0 MDP_XRT_ROI_SET
0185 BC (cd 04 b1 b1 06 06)
0186 + DC 07-F0 MDP_XRT_ROI_SET
0187 BC (cd 06 80 80 20 20)
0188 + DC 07-F0 MDP_XRT_ROI_SET
0189 BC (cd 0f 80 80 04 04)
0190 + DC 07-F0 MDP_XRT_ROI_SET
0191 BC (cd 10 80 80 10 10)
0192 . C. ----- Success Verify ? OK / NG ____
0193 C.

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```
0194 C.
0195 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0196 C.
0197 +. DC 07-F0 MDP_XRT_MODE_OBSV
0198 BC (c2)
0199 +. TI 2009-03-24 10:26:02.0
0200 DC 07-F0 MDP_XRT_MODE_OBSV
0201 BC (c2)
0202 . C. ----- Success Verify ? OK / NG ____
0203 C.
0204 C. ***** XRT END *****
0205 C.
0206 . C. ***** MDP `úÃîñî»ò¼ÿñÊÂðñ¹ñèDCBC•x²è *****
0207 C. (¼á°îÿÓÿÃÿÈÿËÿËÿÁÿçÿèñË¼ññ¼Á»Ûñ¹ñè)
0208 . S. DC-BC dcbc-402:DCBC
0209 (MDP_known_event)
0210 C.
0211 C.
0212 . C. ***** ÿDÿ¹•Ï Daily±¿ÍññË´Øñ¹ñèDCBC•x²è *****
0213 . S. DC-BC dcbc-153:DCBC
0214 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0215 C.
0216 C.
0217 . C. ¡ãLOSÿÁÿSÿÃÿ¼Á»Ûñ¹ñè
0218 C.
0219 . C. ***** LOS *****
0220 C.
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Mar 24, 09 14:11

XRT_OGLIST_0468.chk

Page 1/4

*** OP Sequence for XRT ***

2009/03/24	10:37:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	14	a3	51	4f
2009/03/24	11:07:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	10	28	42	9f
2009/03/24	11:37:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	0e	35	32	d9
2009/03/24	12:07:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	0d	0e	22	7e
2009/03/24	12:37:00.5	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	0c	62	11	a1
2009/03/24	13:07:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	0c	28	00	62
2009/03/24	13:37:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	0c	28	ff	9e
2009/03/24	14:07:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	0c	62	ee	5f
2009/03/24	14:37:00.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	0d	0e	dd	82
2009/03/24	15:07:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	0e	35	cd	27
2009/03/24	15:37:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00	10	28	bd	61
2009/03/24	16:07:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00	14	a3	ae	b1
2009/03/24	16:37:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00	eb	5d	51	47
2009/03/24	17:30:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00	e3	b7	40	39
2009/03/24	17:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0					
2009/03/24	17:59:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2009/03/24	18:00:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00	00	00	00	00
2009/03/24	18:00:16.0	XRT_QT_PROG_SET_400_OG [0x190]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4			01
2009/03/24	18:00:18.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2009/03/24	18:00:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					c9
2009/03/24	18:00:22.0	XRT_ARS_DIS_410_OG [0x19a]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2009/03/24	18:03:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2009/03/24	18:10:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	00	e1	dc	2f	c6
2009/03/24	19:05:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	00	e0	bd	1f	1a
2009/03/24	19:35:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	00	e0	21	0e	1c
2009/03/24	20:45:00.0	AOCS_ORe-point_Start_19_OG [0x0a9]							
		AOCU_NM	5	02-76	00	e0	00	fc	bd
2009/03/24	21:15:00.0	AOCS_ORe-point_Start_20_OG [0x0aa]							
		AOCU_NM	5	02-76	00	e0	21	f1	e4
2009/03/24	22:20:00.0	AOCS_ORe-point_Start_21_OG [0x0ab]							
		AOCU_NM	5	02-76	00	e0	bd	e0	e6
2009/03/24	22:50:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	00	e1	dc	d0	3a
2009/03/24	23:50:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	00	e3	b7	bf	c7
2009/03/25	00:20:00.0	AOCS_ORe-point_Start_24_OG [0x0ae]							
		AOCU_NM	5	02-76	00	eb	5d	ae	b9
2009/03/25	00:49:54.0	XRT_CTRL_MANU_448_OG [0x1c0]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2009/03/25	00:50:00.0	AOCS_ORe-point_Start_25_OG [0x0af]							
		AOCU_NM	5	02-76	04	00	00	00	00
2009/03/25	00:52:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2009/03/25	00:52:52.0	XRT_QT_PROG_SET_411_OG [0x19b]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4			0a
2009/03/25	00:52:54.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2009/03/25	00:52:56.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2009/03/25	00:52:58.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					c9
2009/03/25	00:53:00.0	XRT_ARS_DIS_418_OG [0x1a2]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2009/03/25	00:53:02.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2009/03/25	01:04:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2009/03/25	01:15:30.0	XRT_Custom_430_OG [0x1ae]							
2009/03/25	01:16:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2009/03/25	02:31:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2009/03/25	02:51:30.0	XRT_Custom_430_OG [0x1ae]							
2009/03/25	02:52:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2009/03/25	03:59:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							

2009/03/25	04:29:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/03/25	04:30:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/03/25	05:39:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/03/25	06:06:30.0	XRT_Custom_430_OG [0x1ae]	XRT_CTRL_AUTO_432_OG [0x1b0]					
2009/03/25	06:07:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/03/25	06:15:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/03/25	06:15:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2009/03/25	06:16:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00 00 00 00 00		
2009/03/25	06:16:16.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09		
2009/03/25	06:16:18.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2009/03/25	06:16:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2009/03/25	06:16:22.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2009/03/25	06:19:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/03/25	06:25:54.0	XRT_CTRL_MANU_448_OG [0x1c0]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/03/25	06:26:00.0	AOCS_ORe-point_Start_25_OG [0x0af]	AOCU_NM	5	02-76	04 00 00 00 00		
2009/03/25	06:28:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2009/03/25	06:28:52.0	XRT_QT_PROG_SET_411_OG [0x19b]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a		
2009/03/25	06:28:54.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2009/03/25	06:28:56.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2009/03/25	06:28:58.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2009/03/25	06:29:00.0	XRT_ARS_DIS_418_OG [0x1a2]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2009/03/25	06:29:02.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/03/25	07:20:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/03/25	07:44:00.0	XRT_Custom_430_OG [0x1ae]	XRT_CTRL_AUTO_432_OG [0x1b0]					
2009/03/25	07:45:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/03/25	08:59:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/03/25	09:20:00.0	XRT_Custom_430_OG [0x1ae]	XRT_CTRL_AUTO_432_OG [0x1b0]					
2009/03/25	09:21:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/03/25	10:41:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/03/25	10:50:00.0	XRT_Custom_430_OG [0x1ae]	XRT_CTRL_AUTO_432_OG [0x1b0]					
2009/03/25	10:51:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/03/25	14:30:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/03/25	14:36:00.0	XRT_Custom_430_OG [0x1ae]	XRT_CTRL_AUTO_432_OG [0x1b0]					
2009/03/25	14:37:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/03/25	16:03:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/03/25	16:26:30.0	XRT_Custom_430_OG [0x1ae]	XRT_CTRL_AUTO_432_OG [0x1b0]					
2009/03/25	16:27:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/03/25	17:39:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/03/25	18:02:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/03/25	18:02:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2009/03/25	18:03:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00 00 00 00 00		
2009/03/25	18:03:16.0	XRT_QT_PROG_SET_400_OG [0x190]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01		
2009/03/25	18:03:18.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2009/03/25	18:03:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2009/03/25	18:03:22.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2009/03/25	18:06:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/03/25	18:12:54.0	XRT_CTRL_MANU_448_OG [0x1c0]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/03/25	18:13:00.0	AOCS_ORe-point_Start_25_OG [0x0af]	AOCU_NM	5	02-76	04 00 00 00 00		
2009/03/25	18:15:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2009/03/25	18:15:52.0	XRT_QT_PROG_SET_411_OG [0x19b]						

Mar 24, 09 14:11

XRT_OGLIST_0468.chk

Page 3/4

2009/03/25	18:15:54.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a	
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2009/03/25	18:15:56.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2009/03/25	18:15:58.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2009/03/25	18:16:00.0	XRT_ARS_DIS_418_OG [0x1a2]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2009/03/25	18:16:02.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/03/25	19:16:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/03/25	19:40:00.0	XRT_Custom_430_OG [0x1ae]						
2009/03/25	19:41:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/03/25	20:54:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/03/25	21:17:30.0	XRT_Custom_430_OG [0x1ae]						
2009/03/25	21:18:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/03/25	22:31:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/03/25	22:53:00.5	XRT_Custom_430_OG [0x1ae]						
2009/03/25	22:54:00.5	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/03/26	00:09:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/03/26	00:18:30.0	XRT_Custom_430_OG [0x1ae]						
2009/03/26	00:19:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/03/26	01:34:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/03/26	01:51:30.0	XRT_Custom_430_OG [0x1ae]						
2009/03/26	01:52:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/03/26	03:07:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/03/26	03:28:30.0	XRT_Custom_430_OG [0x1ae]						
2009/03/26	03:29:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/03/26	04:37:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/03/26	05:06:00.0	XRT_Custom_430_OG [0x1ae]						
2009/03/26	05:07:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/03/26	05:49:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/03/26	05:49:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2009/03/26	05:50:00.0	AOCS_Orе-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00	00 00 00 00	
2009/03/26	05:50:16.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09	
2009/03/26	05:50:18.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2009/03/26	05:50:20.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2009/03/26	05:50:22.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2009/03/26	05:53:00.0	XRT_CTRL_AUTO_444_OG [0x1bc]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/03/26	05:59:54.0	XRT_CTRL_MANU_448_OG [0x1c0]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/03/26	06:00:00.0	AOCS_Orе-point_Start_25_OG [0x0af]	AOCU_NM	5	02-76	04	00 00 00 00	
2009/03/26	06:02:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2009/03/26	06:02:52.0	XRT_QT_PROG_SET_411_OG [0x19b]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a	
2009/03/26	06:02:54.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2009/03/26	06:02:56.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2009/03/26	06:02:58.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2009/03/26	06:03:00.0	XRT_ARS_DIS_418_OG [0x1a2]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2009/03/26	06:03:02.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/03/26	06:17:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/03/26	06:43:30.0	XRT_Custom_430_OG [0x1ae]						
2009/03/26	06:44:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/03/26	07:58:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/03/26	08:20:30.0	XRT_Custom_430_OG [0x1ae]						
2009/03/26	08:21:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/03/26	09:37:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/03/26	10:13:00.0	AOCS_Orе-point_Start_15_OG [0x0a5]						

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