

# XRT Timeline to be uploaded on 2009/06/02

Period: 2009/06/02 09:46:00 - 2009/06/06 11:08:00

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Normal mode

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XOB #16BE: Al/poly(384x384) -Gband - Q95-AEC4(8192ms)-1min cadence														
Term	Pointing (x, y)		Comment											
06/02 09:59:02 - 06/02 11:03:00	Track (	-326.2, 385.7)	@ 06/02 09:56:00	# OP start + 10min HOP71										
06/03 06:07:32 - 06/03 08:18:00	Track (	-184.8, 398.9)	@ 06/03 06:04:30	Track AR										
06/03 08:56:30 - 06/03 11:44:30	Track (	-153.8, 384.5)	@ 06/03 07:50:00	* HOP71										
06/04 06:35:02 - 06/04 07:16:00	Track (	12.5, 397.2)	@ 06/04 06:32:00	# Track AR										
06/04 07:55:30 - 06/04 10:37:30	Track (	42.4, 382.7)	@ 06/04 07:50:00	# HOP71										
<b>PROG= 04 Inf.-time(s)</b>														
└─ Subr= 1 15-time(s) 60.0sec														
└─┬─ Seqn= 57 1-time(s) 4.0sec														
└─┬─ Al-poly/Open Al-poly/Open close Safe Norm 8.00s Obs 1x1 384x384 (1024, 1024) Q=95 4 0 2.0sec														
└─ Subr= 2 1-time(s) 2.0sec														
└─┬─ Seqn= 64 1-time(s) 4.0sec														
└─┬─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec														
└─┬─ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval														

XOB #16BC: AR dynamics - Thin-Be - 384x384 - Q95 - 25s - AEC4														
Term	Pointing (x, y)		Comment											
06/02 11:36:00 - 06/02 17:41:00	Track (	-330.0, 399.9)	@ 06/02 11:20:00	Track AR										
06/02 21:46:00 - 06/03 05:54:24	Track (	-250.6, 399.4)	@ 06/02 21:43:00	# Track AR										
06/03 12:13:36 - 06/03 17:59:54	Track (	-136.2, 398.5)	@ 06/03 12:10:00	# Track AR										
06/03 18:13:00 - 06/03 21:35:30	Track (	-87.9, 398.1)	@ 06/03 18:10:00	# Track AR and from 22 to 2UT for HOP123 obs										
<b>PROG= 05 Inf.-time(s)</b>														
└─ Subr= 1 36-time(s) 25.0sec														
└─┬─ Seqn= 30 1-time(s) 2.0sec														
└─┬─ thin-Be/Open thin-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 4 0 2.0sec														
└─ Subr= 2 1-time(s) 2.0sec														
└─┬─ Seqn= 64 1-time(s) 4.0sec														
└─┬─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec														
└─┬─ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval														

XOB #16A1: Synoptic Q95 2x2 - Al/poly(128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Ti-poly(181/2048) + G-band(16)													
Term	Pointing (x, y)		Comment										
06/02 18:26:00 - 06/02 21:42:54	Fixed (	0.0, 0.0)	synoptic, shifted 23.0 min and Post-bakeout for flat field for 3 hr										
06/03 18:03:00 - 06/03 18:09:54	Fixed (	0.0, 0.0)	synoptic										
<b>PROG= 15 1-time(s)</b>													
└─ Subr= 1 1-time(s) 12.0sec													
└─┬─ Seqn= 24 1-time(s) 4.0sec													
└─┬─ Al-poly/Open Al-poly/Open close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─┬─ Al-poly/Open Al-poly/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─┬─ Seqn= 7 1-time(s) 2.0sec													
└─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─┬─ Seqn= 71 1-time(s) 4.0sec													
└─┬─ Open/Ti-poly Open/Ti-poly close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─┬─ Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─┬─ Seqn= 92 1-time(s) 2.0sec													
└─┬─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─┬─ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #16A0: Synoptic Q95 2x2 - Al/mesh(88/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Ti-poly(181/2048) + G-band(16)													
Term	Pointing (x, y)		Comment										
06/03 05:57:30 - 06/03 06:04:24	Fixed (	0.0, 0.0)	synoptic, shifted -5.5 min										
06/04 06:25:00 - 06/04 06:31:54	Fixed (	0.0, 0.0)	synoptic, shifted 22.0 min										
<b>PROG= 17 1-time(s)</b>													
└─ Subr= 1 1-time(s) 12.0sec													
└─┬─ Seqn= 46 1-time(s) 4.0sec													
└─┬─ Open/Al-mesh Open/Al-mesh close Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─┬─ Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─┬─ Seqn= 7 1-time(s) 2.0sec													
└─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─┬─ Seqn= 71 1-time(s) 4.0sec													
└─┬─ Open/Ti-poly Open/Ti-poly close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─┬─ Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─┬─ Seqn= 92 1-time(s) 2.0sec													
└─┬─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─┬─ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

**XOB #16BB: Coronal Waves (HOP 123) - Al/poly - Ti/poly - 4 min cad - 1024x1024 - AEC 2**

Term	Pointing (x, y)	Comment
06/03 22:13:38 - 06/04 06:21:54	Track ( -87.9, 398.1) <sup>06/03 18:10:00</sup>	# Track AR and from 22 to 2UT for HOP123 obs
<b>PROG= 13 Inf.-time(s)</b>		
Subr= 1	6-time(s) 150.0sec	
└─ Seqn= 26	1-time(s) 4.0sec	
└─ Al-poly/Open	Al-poly/thick-Al close	Safe Norm 8.00s Obs 2x2 1024x1024 (1024, 1024) Q=98 2 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm 8.00s Obs 2x2 1024x1024 (1024, 1024) Q=98 2 0 2.0sec
Subr= 2	1-time(s) 2.0sec	
└─ Seqn= 64	1-time(s) 4.0sec	
└─ Open/G-band	Open/G-band open	Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Flare mode**

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NOT USED

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**Active Region Search**

\* \* \* \* \*

NOT USED

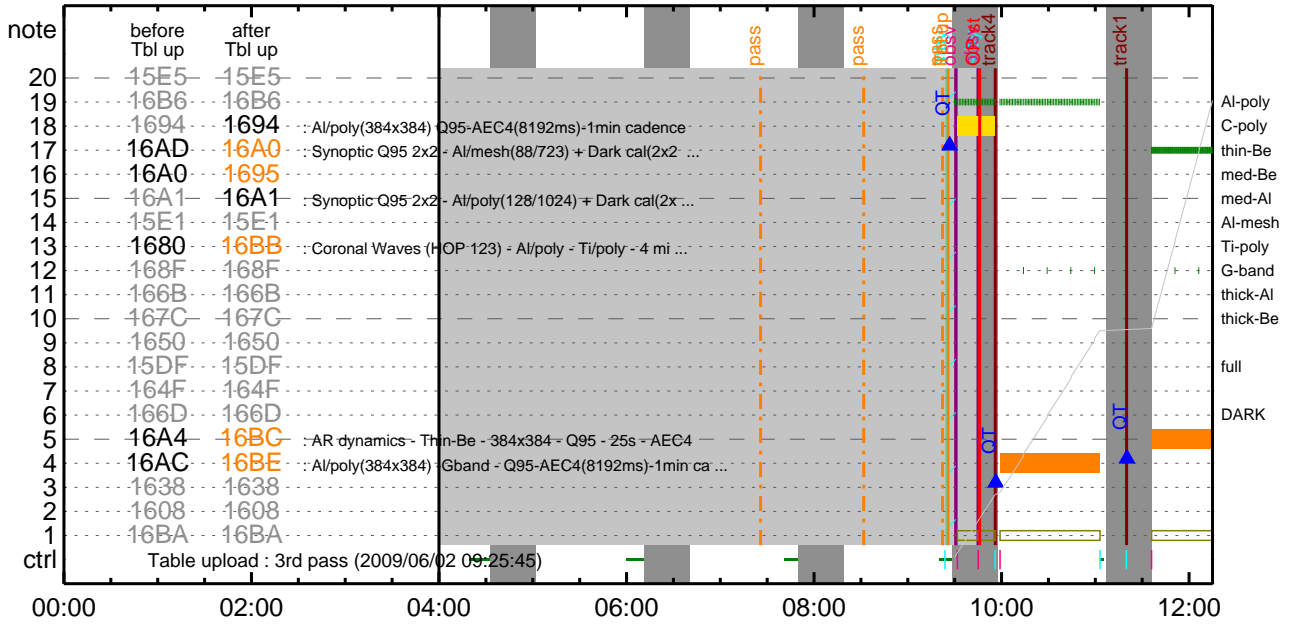
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**Flare Detection**

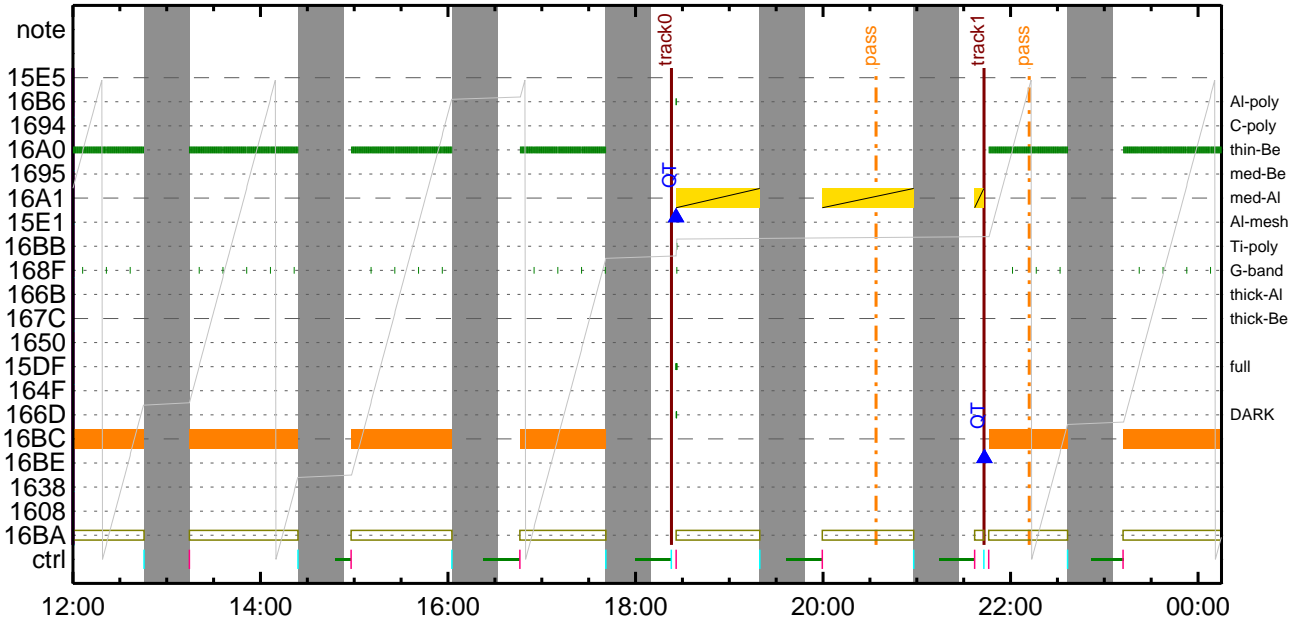
\* \* \* \* \*

NOT USED

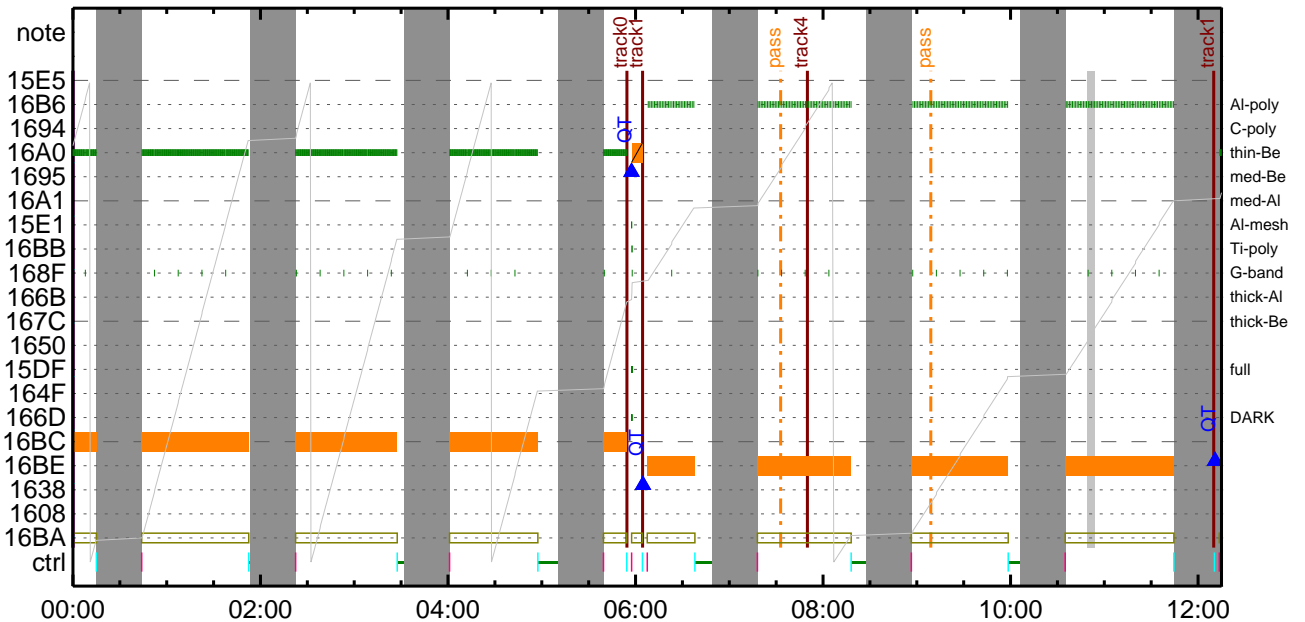
### CMDI #0591 2009/06/02



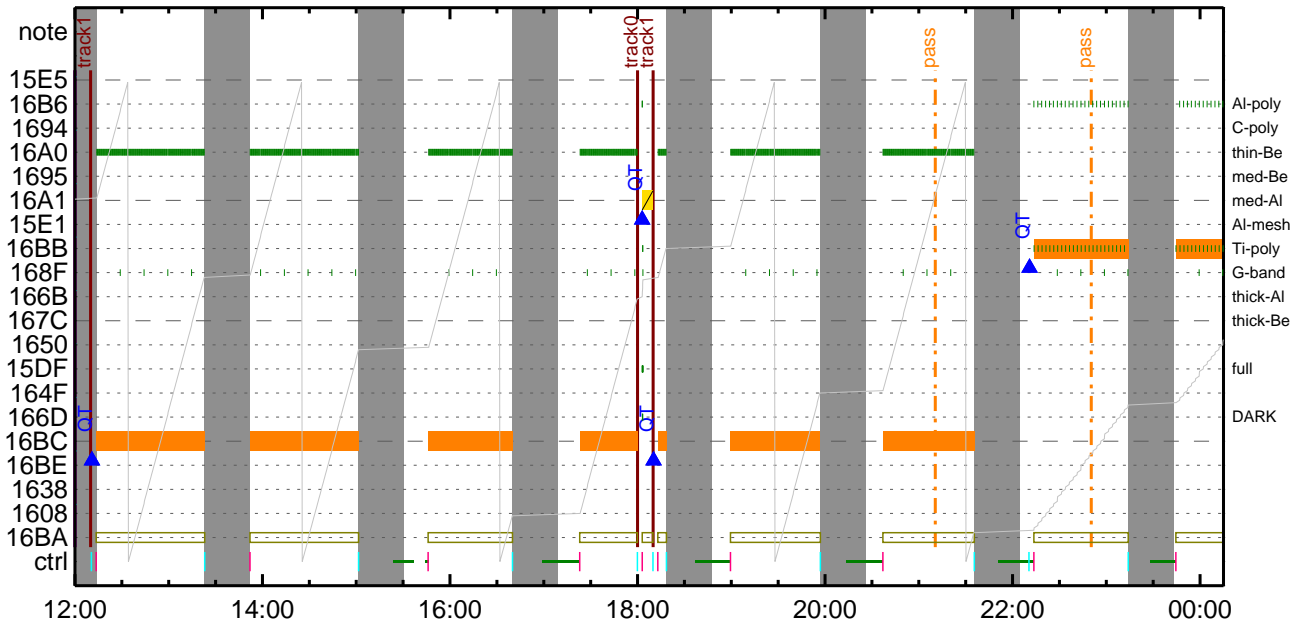
### CMDI #0591 2009/06/02



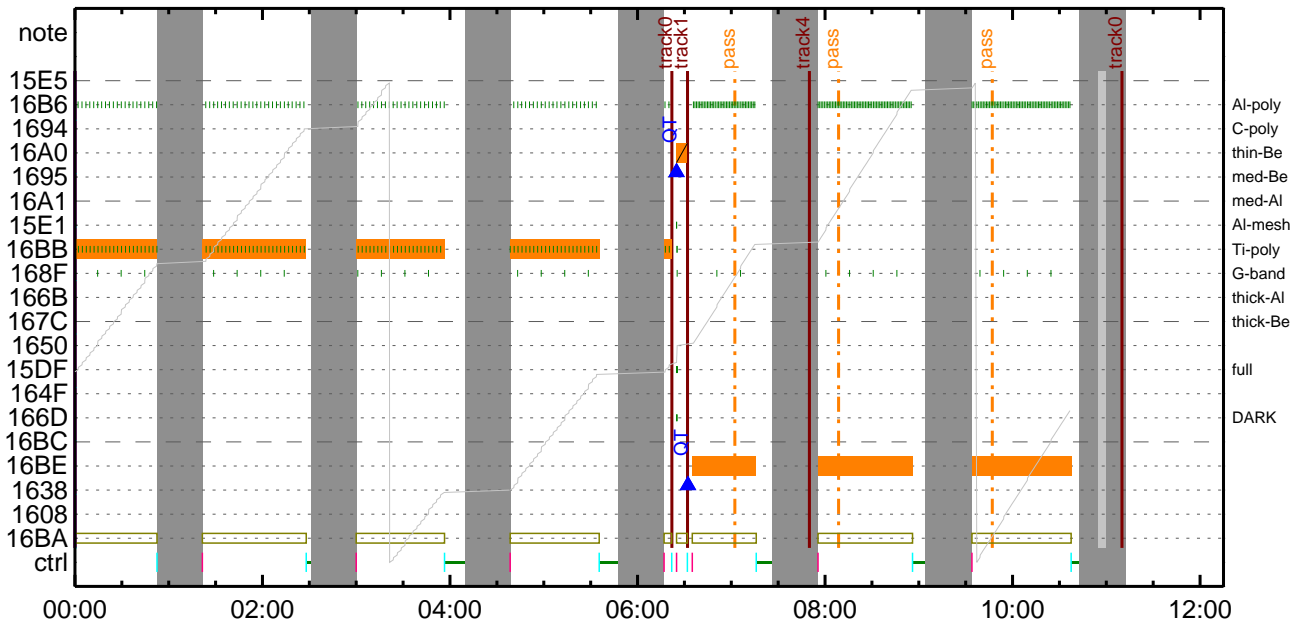
### CMDI #0591 2009/06/03



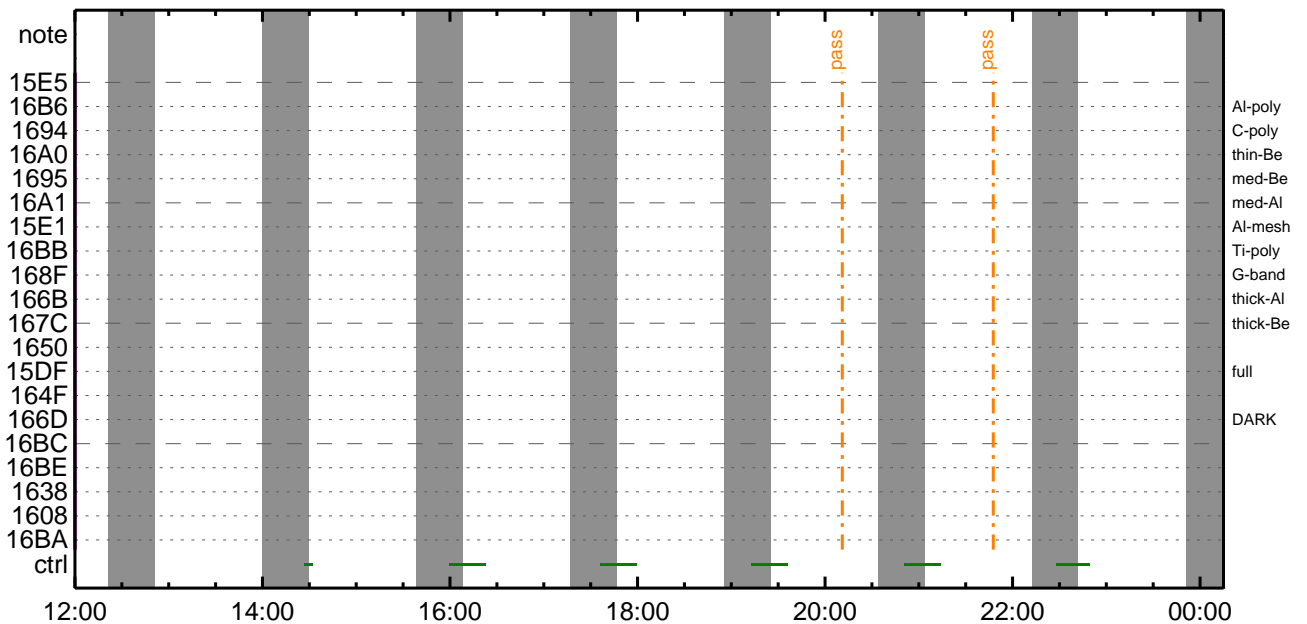
### CMDI #0591 2009/06/03



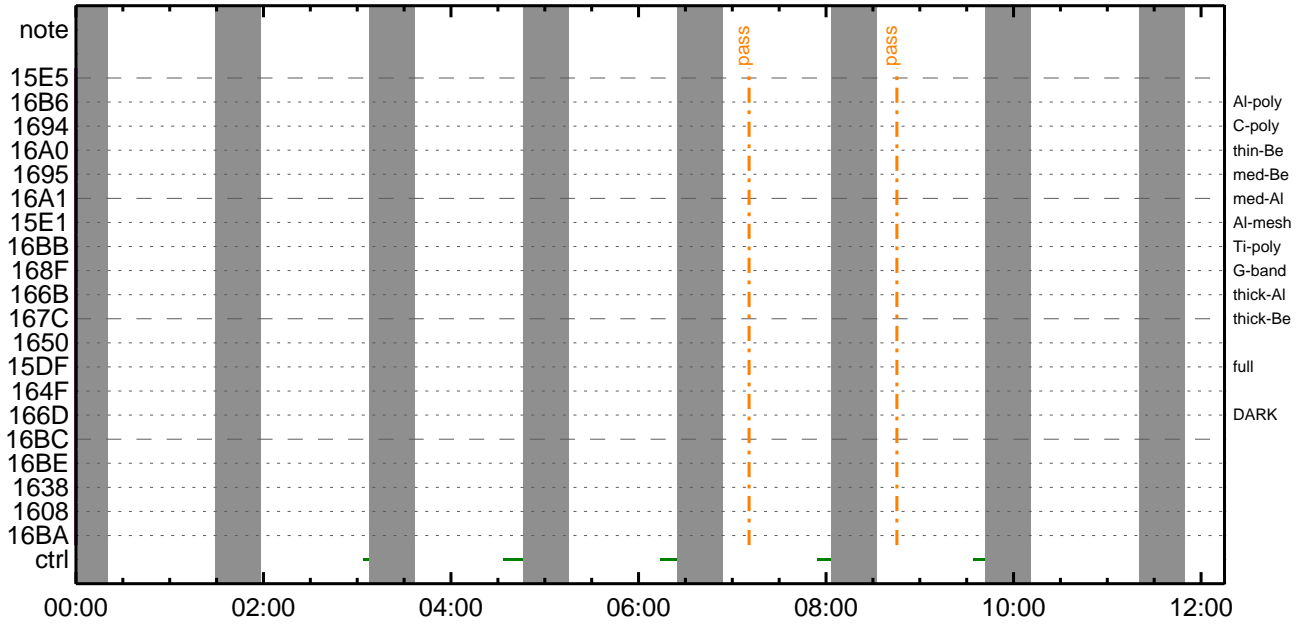
### CMDI #0591 2009/06/04



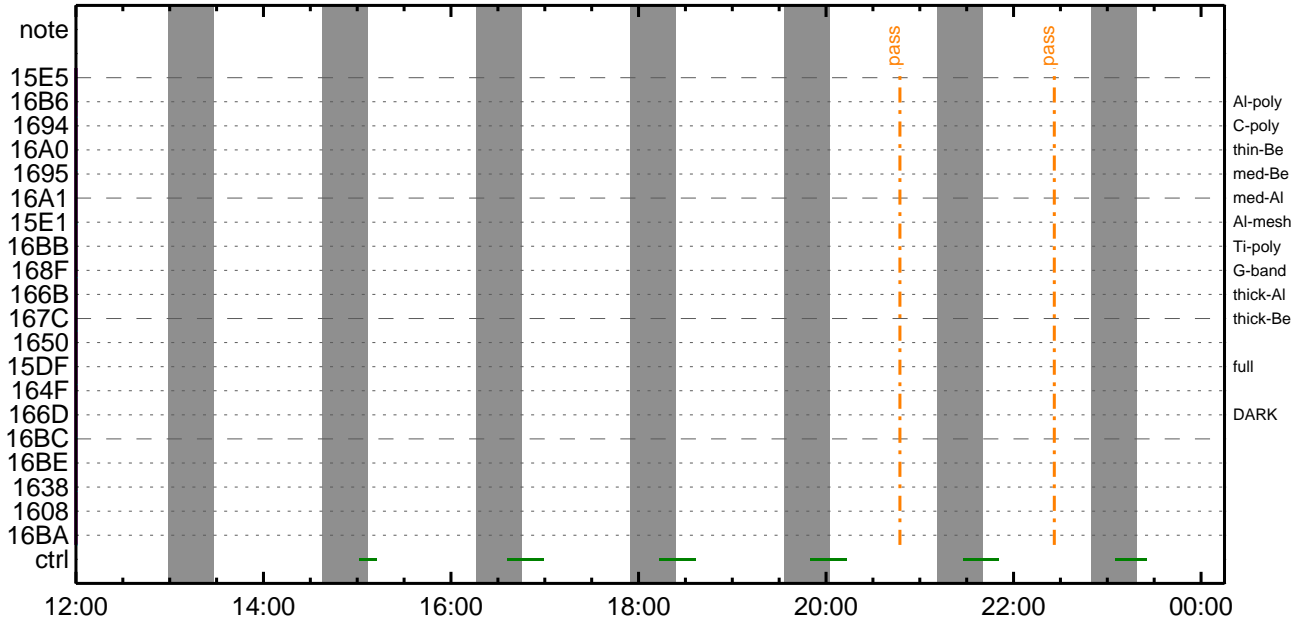
### CMDI #0591 2009/06/04



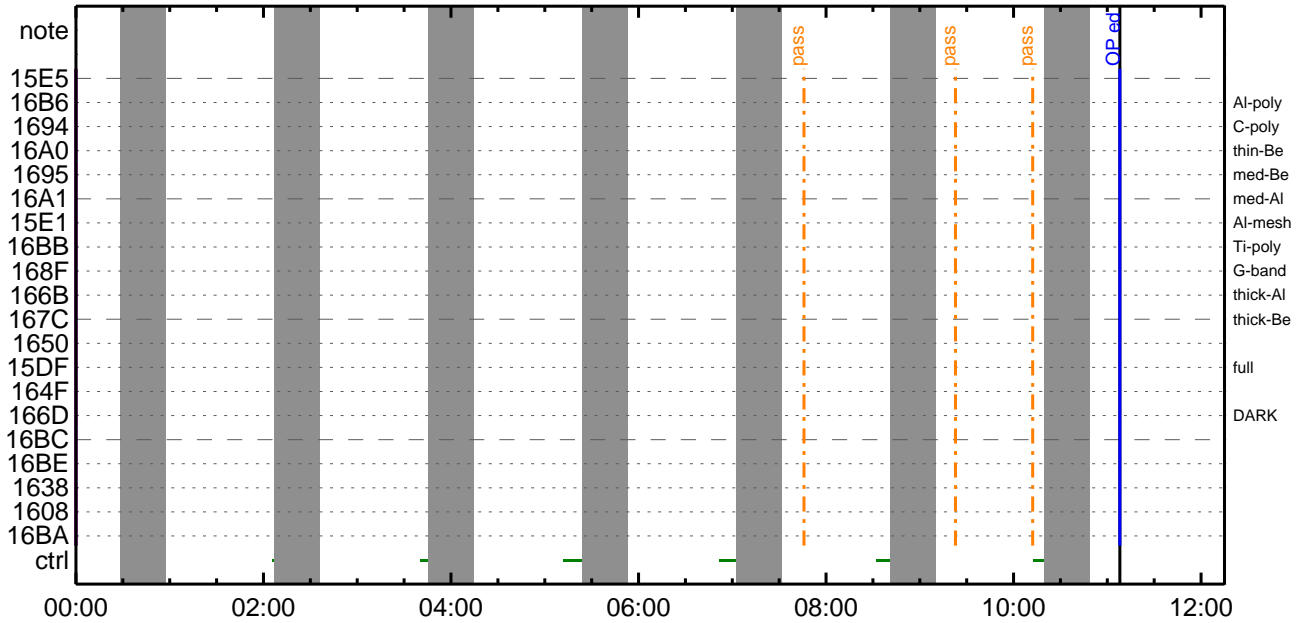
CMDI #0591 2009/06/05



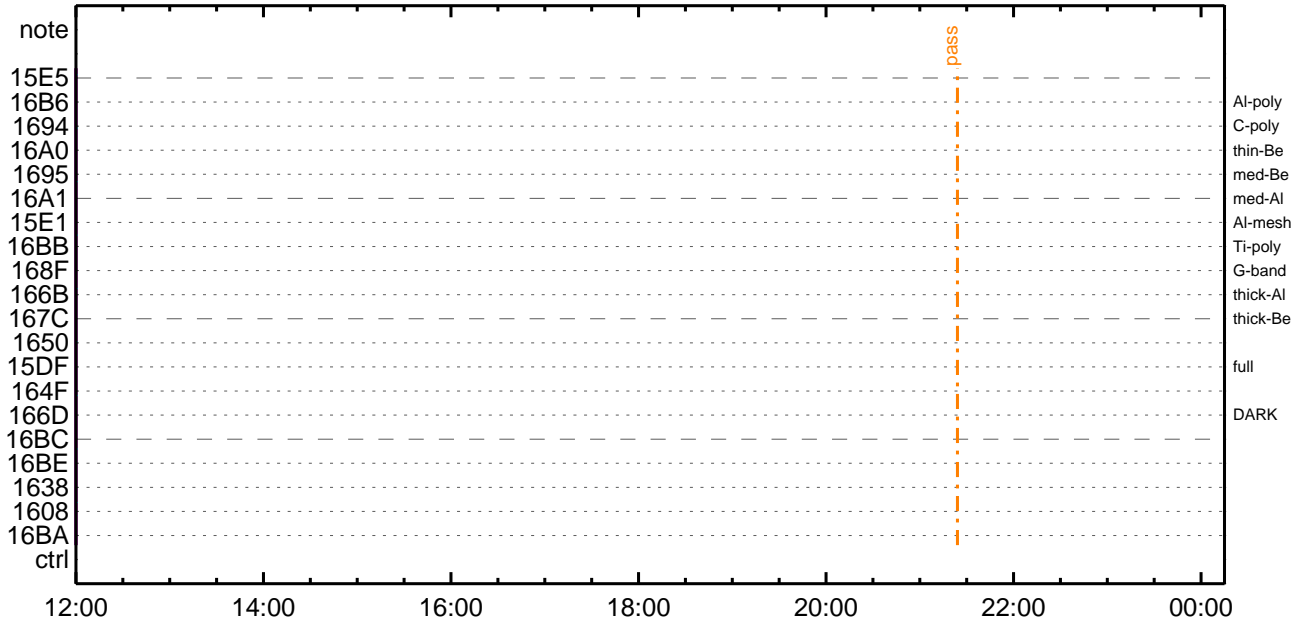
CMDI #0591 2009/06/05



CMDI #0591 2009/06/06



CMDI #0591 2009/06/06



(a) Spacecraft Operation Procedure (real-commands)

```
main-629 2009-06-02 13:58:05 245 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èò¿òÁò•µ°È»Í×ÁÇòÍYÇYÁY×Yí;¼YÉ;ÈÈèµ•íÉ;ÈòÈ¼°ÇÒò•ò¿¼í¹ÇòÍ;ÇÀ®, ùò¹òÈòÈòÇÁ+¿®ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCs Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. ***** AOCs Commands (Orbital Element Update) *****
0046 C. Update the orbital element
0047 +. DC 02-50 AOCU_ORB_PRPGT_START
0048 BC (16)
0049 +. DC 02-8E AOCU_ORB_UPD
0050 C.
0051 C. <A_ORB>[ORBIT] EPC = 715178.5 +- 1.0 (s) [ ]
0052 C.
0053 . C.
0054 . C. *****
0055 C. OP/OGYí;¼YÉ;|YÁYÓY×
0056 C. *****
0057 C.
0058 . C. ;ãOP/OGYí;¼YÉ;ã
0059 . S. OP op-629:OP
0060 ( )
0061 . S. OG og-629:OG
0062 ( )
0063 C.
0064 . C. ;ãNMOG&OPÍî°èYÁYÓY×;ã
0065 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0066 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0067 BC (20 00 7f 01 02)
0068 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40
0069 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0070 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0071 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0072 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0073 +. DC 01-22 DHU_MODE_CHNG
0074 BC (07 0b f8)
0075 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0076 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0077 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0078 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0079 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0080 . C. YÁYÓY×¼ªªî»òð³ÍÇ§
0081 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0082 . C. RAM ID=NMOGòÍ¼È¹Ç•è²ÍOKòð³ÍÇ§
0083 C.
0084 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0085 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0086 BC (20 80 7f 01 02)
0087 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41
0088 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0089 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0090 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0091 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0092 +. DC 01-22 DHU_MODE_CHNG
0093 BC (07 0b f8)
0094 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0095 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
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0096 C.        &[HK1_S_TLM_BIT_RATE]             EQ      32k
0097 C.        &[HK1_X_TLM_BIT_RATE]             EQ      4M
0098 C.        &[HK1_DMP_CHK_FLG]                EQ      EXEC
0099 . C.     ¶ÅÖÿ×½ª î»ò³ ÍÇ§
0100 C.        &[HK1_DMP_CHK_FLG]                EQ      NON
0101 . C.     RAM ID=NMOG²î¼È¹Ç•è² ÌOKò³ ÍÇ§
0102 C.
0103 C.     NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0104 +. DC    01-23 DHU_DMA_DMP_PRM_SET
0105 BC      (21 00 41 01 02)
0106 C.        &[HK1_DMP_TOP_ADRS_1]            EQ      42
0107 C.        &[HK1_DMP_TOP_ADRS_0]            EQ      0
0108 C.        &[HK1_DMP_BLOCK_NUM]            EQ      65
0109 C.        &[HK1_DMP_REPEAT_NUM]           EQ      0
0110 C.        &[HK1_DMA_DMP_PIM]              EQ      DHU
0111 +. DC    01-22 DHU_MODE_CHNG
0112 BC      (07 0b f8)
0113 C.        &[HK1_PKT_FORM_NO]               EQ      7
0114 C.        &[HK1_PKT_GEN_TIME]             EQ      0.25 s
0115 C.        &[HK1_S_TLM_BIT_RATE]           EQ      32k
0116 C.        &[HK1_X_TLM_BIT_RATE]           EQ      4M
0117 C.        &[HK1_DMP_CHK_FLG]            EQ      EXEC
0118 . C.     ¶ÅÖÿ×½ª î»ò³ ÍÇ§
0119 C.        &[HK1_DMP_CHK_FLG]              EQ      NON
0120 . C.     RAM ID=NMOG, RAM ID=OP²î¼È¹Ç•è² ÌOKò³ ÍÇ§
0121 C.
0122 . C.     ***** òÈ²¼Ò Ì¼Ã' ¶ÁºÒÈÈ-ÒºÁ:¿® (¼áu-¶ÅÖÿ×½ªè¼ÇÒÄÒÃæÒÇ½ªªÒ"òÈ¼ì¹ÇÒÇâ) *****
0123 C.     DHU¶â;¼¶È;È¼ÿ½, ¶ì;¼¶È;ÈòðÌáÒ¹
0124 +. DC    01-22 DHU_MODE_CHNG
0125 BC      (02 0a f8)
0126 C.        &[HK1_PKT_FORM_NO]               EQ      2
0127 C.        &[HK1_PKT_GEN_TIME]             EQ      0.5S
0128 C.        &[HK1_S_TLM_BIT_RATE]           EQ      32K
0129 C.        &[HK1_X_TLM_BIT_RATE]           EQ      4M
0130 C.
0131 . C.     *****
0132 C.     TI-CMD SET (OPOG STOP/COPY/START)
0133 C.     *****
0134 C.
0135 . C.     NOTICE ¡§ OPOG UPLOADÒ-Á:¿®NG²î¼È¹Ç;¿ºÈ²¼Ò ÌTI-CMDÁ:¿®Ò Ì¼Ã¹ÒªÒÈÒÒ³ÒÈ; £
0136 C.     ÒÒ¿¿;¿SETÒÈDUMPÒ ÌÈ±º ò Ì¶Û¼¹Ç¹Òª ò³ÒÈ; £
0137 C.
0138 . C.     TI¶³¶¶ÖÿÈòðÄÐ Ì¿ (UT)
0139 +. TI    2009-06-02 09:41:00.0
0140 DC     01-B3 DHU_OP_STOP
0141 C.        &[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0142 C.
0143 +. TI    2009-06-02 09:41:01.0
0144 DC     01-B4 DHU_OP_COPY
0145 C.        &[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0146 C.
0147 +. TI    2009-06-02 09:41:01.0
0148 DC     01-B5 DHU_OPOG_COPY
0149 C.        &[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0150 C.
0151 +. TI    2009-06-02 09:45:59.5
0152 DC     01-B2 DHU_OP_START
0153 C.        &[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0154 C.
0155 C.     òÈ²¼Ò Ì¼Ãè¼î Ì¶Á¶§¶Å¶-¹ àÌÙ
0156 C.        &[HK1_TI_CMD_ENA/DIS]            EQ      ENA
0157 C.        &[HK1_TI_CMD_NUM]                EQ      4
0158 C.        &[HK1_NEXT_EXEC_PIM]             EQ      DHU
0159 C.        &[HK1_NEXT_EXEC_DC]             EQ      0xB3
0160 C.
0161 . C.     *****
0162 C.     TI Ìîºè¶ÅÖÿ×
0163 C.     *****
0164 C.
0165 C.     TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0166 +. DC    01-23 DHU_DMA_DMP_PRM_SET
0167 BC      (03 ab 03 01 02)
0168 C.        &[HK1_DMP_TOP_ADRS_1]            EQ      07
0169 C.        &[HK1_DMP_TOP_ADRS_0]            EQ      2B
0170 C.        &[HK1_DMP_BLOCK_NUM]            EQ      3
0171 C.        &[HK1_DMP_REPEAT_NUM]           EQ      0
0172 C.        &[HK1_DMA_DMP_PIM]              EQ      DHU
0173 +. DC    01-22 DHU_MODE_CHNG
0174 BC      (07 0b f8)
0175 C.        &[HK1_PKT_FORM_NO]               EQ      7
0176 C.        &[HK1_PKT_GEN_TIME]             EQ      0.25 s
0177 C.        &[HK1_S_TLM_BIT_RATE]           EQ      32k
0178 C.        &[HK1_X_TLM_BIT_RATE]           EQ      4M
0179 C.        &[HK1_DMP_CHK_FLG]              EQ      EXEC
0180 C.
0181 . C.     ¶ÅÖÿ×½ª î»ò³ ÍÇ§
0182 C.        &[HK1_DMP_CHK_FLG]              EQ      NON
0183 C.
0184 . C.     RAM ID=TI_TBLÒ Ì¼È¹Ç•è² ÌOKò³ ÍÇ§
0185 C.
0186 . C.     DHU¶â;¼¶È;È¼ÿ½, ¶ì;¼¶È;ÈòðÌáÒ¹
0187 +. DC    01-22 DHU_MODE_CHNG
0188 BC      (02 0a f8)
0189 C.        &[HK1_PKT_FORM_NO]               EQ      2
0190 C.        &[HK1_PKT_GEN_TIME]             EQ      0.5S
0191 C.        &[HK1_S_TLM_BIT_RATE]           EQ      32K
0192 C.        &[HK1_X_TLM_BIT_RATE]           EQ      4M
0193 C.

```



```
0194 C. *****
0195 C. SOT TI command set
0196 C. *****
0197 C. Execute, after the success of OP upload.
0198 +. TI 2009-06-02 09:45:16.0
0199 DC 07-F0 MDP_SOT_MODE_STBY
0200 BC (41)
0201 . C. -----
0202 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0203 C. -----
0204 C. ***** SOT END *****
0205 . C. Stop EIS observation and temporarily disable EIS mode changes
0206 C.
0207 C.
0208 C. ***** Start EIS operation (TI set) *****
0209 C. Execute, after the success of OP upload.
0210 C. Set EIS TI-commands
0211 +. TI 2009-06-02 09:45:30.0
0212 DC 07-FC EIS_MODE_MANU
0213 BC (21 02)
0214 +. TI 2009-06-02 09:45:40.0
0215 DC 07-FC EIS_MODE_CHG_DIS
0216 BC (22)
0217 . C. [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0218 C. ***** End EIS operation (TI set) *****
0219 C.
0220 C.
0221 C.
0222 C. ***** XRT START *****
0223 C. Execute, after the success of OP upload.
0224 +. TI 2009-06-02 09:45:00.0
0225 DC 07-F0 MDP_XRT_MODE_STBY
0226 BC (c3)
0227 . C. [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0228 C.
0229 C. ***** XRT END *****
0230 C.
0231 . C. ***** MDP `ûÄîñî»ò¼ÿñÊÄñ¹ñèDCBC•x²è *****
0232 C. (¼ä°îÿÓÿÄÿÈÿÐÿËÿäÿçÿèñÊ¼ñ¼Ä»Ûñ¹ñè)
0233 . S. DC-BC dcbc-402:DCBC
0234 (MDP_known_event)
0235 C.
0236 C.
0237 . C. ***** ÿÐÿ¹·Ï Daily±;îÑñÊ`Øñ¹ñèDCBC•x²è *****
0238 . S. DC-BC dcbc-153:DCBC
0239 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0240 C.
0241 C.
0242 . C. ;ãLOSÿÁÿSÿÿÿ¼Ä»Û;ã
0243 C.
0244 . C. ***** LOS *****
0245 C.
```



```

0096 C.
0097 C.
0098 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0099 +. DC 07-FC EIS_MODE_MANU
0100 BC (21 02)
0101 . C. Verify EIS in MANUAL mode
0102 . C. Estimated OBSTBL upload time is 43s
0103 C. *****
0104 C. EIS START OBSTBL LOAD
0105 C. *****
0106 . S. RAM ram-820:EIS_OBSTBL
0107 ( )
0108 +. DC 07-FC EIS_DUMP_OBSTBL
0109 BC (07 07 07 00 00 70 00)
0110 C.
0111 C. Execute, after the success of OBSTBL upload.
0112 C. Set EIS TI-commands
0113 +. TI 2009-06-02 09:45:50.0
0114 DC 07-FC EIS_MODE_CHG_ENA
0115 BC (20)
0116 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0117 C. *****
0118 C. EIS END OBSTBL LOAD
0119 C. *****
0120 C.
0121 . C. ***** MDP `ûÃîñî»ö¼ÿñÊÃðñ¹ñèDCBC•x²è *****
0122 C. (¼ã°îÿÓÿÃÿËÿÏÿËÿÃÿÿÿèñ¼¼ã¼Ã»Ûñ¹ñè)
0123 . S. DC-BC dcbc-402:DCBC
0124 (MDP_known_event)
0125 C.
0126 C.
0127 . C. ***** ÿDÿ¹•î Daily±;îññÊ´øñ¹ñèDCBC•x²è *****
0128 . S. DC-BC dcbc-153:DCBC
0129 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0130 C.
0131 C.
0132 . C. ;ãLOSÿÃÿSÿÿÿÿ¼Ã»Û;ã
0133 C.
0134 . C. ***** LOS *****
0135 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-631 2009-06-02 13:58:05 180 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÁø•µ°È×ÁÇóíYçYÁY×Yí;¼YÉ;ÈÈè%µ•ííÉ;ÈøÈ¼°ÇÓø•ø¿¼í¹çøí;çÀ®, ùø¹øèøøøÇÁ+¿®ø•øÈøøøøøÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 06 80 80 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 07 80 80 20 20)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 08 80 80 10 10)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 0f 80 80 04 04)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 10 80 80 10 10)
0050 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0051 BC (c4 12)
0052 + DC 07-F0 MDP_XRT_ARS_DIS
0053 BC (d5)
0054 + DC 07-F0 MDP_XRT_FLD_DIS
0055 BC (d9)
0056 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0057 BC (c9)
0058 . C. ----- Success Verify ? OK / NG ____
0059 C.
0060 C.
0061 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0062 C.
0063 +. DC 07-F0 MDP_XRT_MODE_OBSV
0064 BC (c2)
0065 + DC 07-F0 MDP_XRT_CTRL_AUTO
0066 BC (c0)
0067 +. TI 2009-06-02 09:45:02.0
0068 DC 07-F0 MDP_XRT_MODE_OBSV
0069 BC (c2)
0070 +. TI 2009-06-02 09:45:04.0
0071 DC 07-F0 MDP_XRT_CTRL_AUTO
0072 BC (c0)
0073 . C. ----- Success Verify ? OK / NG ____
0074 C.
0075 C. ***** XRT END *****
0076 . C. *****
0077 C. SOT table upload
0078 C. *****
0079 . C. < Stop FG table >
0080 +. DC 07-F0 MDP_FG_CTRL_MANU
0081 BC (51)
0082 . C. -----
0083 C. MDP_FG_CTRL_MODE = MANU [ ]
0084 C. -----
0085 C.
0086 . C. <Upload FG Observation Table>
0087 . S. RAM ram-268:MDP_OBS_F
0088 ( )
0089 C.
0090 . C. < Dump RAMID=MDP_OBS_F >
0091 +. DC 07-F0 MDP_DUMP_FGTBL
0092 BC (82 07 00 00 00 38 b8)
0093 C. -----
0094 C. MDP_OBS_F verify = OK/NG [ ]
0095 C. -----
```

```
0096 C.
0097 . C. < Stop SP table >
0098 +. DC 07-F0 MDP_SP_CTRL_MANU
0099 BC (61)
0100 C. -----
0101 C. MDP_SP_CTRL_MODE = MANU [ ]
0102 C. -----
0103 C.
0104 . C. <Upload SP Observation Table>
0105 . S. RAM ram-289:MDP_OBS_S
0106 ( )
0107 C.
0108 . C. < Dump RAMID=MDP_OBS_S >
0109 +. DC 07-F0 MDP_DUMP_SPTBL
0110 BC (83 07 00 00 00 38 b8)
0111 C. -----
0112 C. MDP_OBS_S verify = OK/NG [ ]
0113 C. -----
0114 C.
0115 . C. < Upload DPL table >
0116 C.
0117 C. ¥ç¥Ã¥×¥í;¥É°îÁ°ãËSTS_CHKãðOFFãËã¹ãè
0118 C.
0119 . S. RAM ram-271:MDP_DPL
0120 ( )
0121 C.
0122 . C. < Dump RAMID=MDP_DPL >
0123 +. DC 07-F0 MDP_DUMP_FGTBL
0124 BC (82 07 00 38 b8 00 40)
0125 C. -----
0126 C. MDP_DPL verify = OK [ ]
0127 C. -----
0128 C.
0129 C. STS_CHKãðONãËã¹ãè
0130 C.
0131 . C. < Update MDP DSC PAR1 >
0132 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0133 BC (4c)
0134 C. MDP_CMD_CODE = F04C0700[ ]
0135 C. MDP_CMD_CNT (count-up 1) [ ]
0136 C. -----
0137 C.
0138 C.
0139 . C. < Resume FG table (auto mode) >
0140 +. DC 07-F0 MDP_FG_CTRL_AUTO
0141 BC (50)
0142 C. -----
0143 C. MDP_FG_CTRL_MODE = AUTO [ ]
0144 C. -----
0145 C.
0146 C. *****
0147 C. SOT TI command set
0148 C. *****
0149 C. Execute, after the success of TBL upload.
0150 +. TI 2009-06-02 09:45:18.0
0151 DC 07-F0 MDP_SOT_MODE_OBSV
0152 BC (40)
0153 C. -----
0154 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0155 C. -----
0156 C.
0157 C. Only when FG_CTRL_AUTO is used in RT.
0158 +. TI 2009-06-02 09:45:20.0
0159 DC 07-F0 MDP_FG_CTRL_AUTO
0160 BC (50)
0161 C. -----
0162 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 C.
0166 . C. ***** MDP `úÃîï»ó¼YãËã¹ãèDCBC•x²è *****
0167 C. (¼ã°îYÓYÄYÈYÉYËYáYçYèY¼ã¼Ã»Ûã¹ãè)
0168 . S. DC-BC dcbc-402:DCBC
0169 (MDP_known_event)
0170 C.
0171 C.
0172 . C. ***** ¥ÐY¹·Ï Daily±;îÑãË`Øã¹ãèDCBC•x²è *****
0173 . S. DC-BC dcbc-153:DCBC
0174 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0175 C.
0176 C.
0177 . C. ¡ãLOS¥Á¥S¥Ä¥-¼Ã»Û;ã
0178 C.
0179 . C. ***** LOS *****
0180 C.
```

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\*\*\* OP Sequence for XRT \*\*\*

2009/06/02	09:55:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/02	09:55:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2009/06/02	09:56:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2009/06/02	09:56:16.0	XRT_QT_PROG_SET_413_OG [0x19d]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2009/06/02	09:56:18.0	XRT_AEC_RESET_415_OG [0x19f]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2009/06/02	09:56:20.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/02	09:56:22.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/02	09:56:24.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/02	09:59:02.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/02	11:03:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/02	11:19:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/02	11:19:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2009/06/02	11:20:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2009/06/02	11:20:16.0	XRT_QT_PROG_SET_402_OG [0x192]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2009/06/02	11:20:18.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/02	11:20:20.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/02	11:20:22.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/02	11:36:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/02	12:45:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/02	13:14:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/02	14:24:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/02	14:58:00.5	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/02	16:02:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/02	16:46:00.5	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/02	17:41:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/02	18:22:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/02	18:22:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/06/02	18:23:00.5	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2009/06/02	18:23:16.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/02	18:23:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/02	18:23:20.0	XRT_ARS_DIS_410_OG [0x19a]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/02	18:25:58.0	XRT_QT_PROG_SET_409_OG [0x199]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2009/06/02	18:26:00.0	XRT_CTRL_AUTO_440_OG [0x1b8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/02	19:19:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/02	19:59:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/02	20:58:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/02	21:37:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/02	21:42:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/02	21:42:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2009/06/02	21:43:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2009/06/02	21:43:16.0	XRT_QT_PROG_SET_402_OG [0x192]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2009/06/02	21:43:18.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/02	21:43:20.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/02	21:43:22.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/02	21:46:00.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/02	22:36:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							

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2009/06/02	23:12:00.5	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/03	00:15:00.5	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/03	00:44:00.5	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/03	01:52:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/03	02:22:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/03	03:27:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/03	04:01:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/03	04:57:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/03	05:39:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/03	05:54:24.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/03	05:54:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/06/03	05:54:30.0	AOCs_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00				
2009/06/03	05:54:46.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/03	05:54:48.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/03	05:54:50.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/03	05:57:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2009/06/03	05:57:30.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/03	06:04:24.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/03	06:04:26.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2009/06/03	06:04:30.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00				
2009/06/03	06:04:46.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2009/06/03	06:04:48.0	XRT_AEC_RESET_415_OG [0x19f]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2009/06/03	06:04:50.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/03	06:04:52.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/03	06:04:54.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/03	06:07:32.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/03	06:38:00.5	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/03	07:18:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/03	07:50:00.0	AOCs_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00				
2009/06/03	08:18:00.5	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/03	08:56:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/03	09:58:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/03	10:35:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/03	11:44:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/03	12:10:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00				
2009/06/03	12:10:30.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/03	12:10:32.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2009/06/03	12:10:52.0	XRT_QT_PROG_SET_402_OG [0x192]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2009/06/03	12:10:54.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/03	12:10:56.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/03	12:10:58.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/03	12:13:36.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/03	13:23:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/03	13:52:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/03	15:01:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/03	15:46:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2009/06/03	16:40:00.5	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/03	17:23:00.5	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/03	17:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/03	17:59:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/06/03	18:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2009/06/03	18:00:16.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/03	18:00:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/03	18:00:20.0	XRT_ARS_DIS_410_OG [0x19a]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/03	18:02:58.0	XRT_QT_PROG_SET_409_OG [0x199]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2009/06/03	18:03:00.0	XRT_CTRL_AUTO_440_OG [0x1b8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/03	18:09:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/03	18:09:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2009/06/03	18:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2009/06/03	18:10:16.0	XRT_QT_PROG_SET_402_OG [0x192]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2009/06/03	18:10:18.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/03	18:10:20.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/03	18:10:22.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/03	18:13:00.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/03	18:18:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/03	18:59:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/03	19:57:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/03	20:37:00.5	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/03	21:35:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/03	22:10:30.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/03	22:10:32.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2009/06/03	22:10:52.0	XRT_QT_PROG_SET_405_OG [0x195]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2009/06/03	22:10:54.0	XRT_AEC_RESET_415_OG [0x19f]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2009/06/03	22:10:56.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/03	22:10:58.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/03	22:11:00.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/03	22:13:38.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/03	23:14:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/03	23:44:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/04	00:52:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/04	01:21:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/04	02:28:00.5	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/04	03:00:00.5	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/04	03:56:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/04	04:38:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/04	05:35:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/04	06:17:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/04	06:21:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/04	06:21:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/06/04	06:22:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2009/06/04	06:22:16.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/04	06:22:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/04	06:22:20.0	XRT_ARS_DIS_410_OG [0x19a]							



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2009/06/04	06:24:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2009/06/04	06:25:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/06/04	06:31:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/06/04	06:31:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/06/04	06:32:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00
2009/06/04	06:32:16.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2009/06/04	06:32:18.0	XRT_AEC_RESET_415_OG [0x19f]	MDP_XRT_AEC_RESET	1	07-F0	d0
2009/06/04	06:32:20.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2009/06/04	06:32:22.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9
2009/06/04	06:32:24.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/06/04	06:35:02.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/06/04	07:16:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/06/04	07:50:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00
2009/06/04	07:55:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/06/04	08:56:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/06/04	09:34:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/06/04	10:37:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/06/04	11:10:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00