

XRT Timeline to be uploaded on 2009/06/04

Period: 2009/06/04 11:00:00 - 2009/06/09 10:50:00

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Normal mode

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XOB #1563: CCD Monitor During Bakeout - G-band + dark - wide FOV													
Term	Pointing (x, y)		Comment										
06/04 11:12:30 - 06/04 16:30:01	Track (32.1, 405.8)	# OP start + 10min, track AR 11019, XRT in bakeout and post-bakeout until approx 2:30 UT										
PROG= 20 Inf.-time(s)													
└ Subr= 1 1-time(s) 600.0sec													
└ Seqn= 98 1-time(s) 4.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1565: CCD Monitor During Bakeout - G-band + dark - wide FOV - lower cadence (30min)													
Term	Pointing (x, y)		Comment										
06/04 16:30:11 - 06/04 17:17:00	Track (32.1, 405.8)	# OP start + 10min, track AR 11019, XRT in bakeout and post-bakeout until approx 2:30 UT										
06/04 18:00:30 - 06/04 22:12:30	Fixed (0.0, 0.0)	* Disk center pointing for SOT flat fields.										
06/04 22:50:30 - 06/05 02:27:54	Track (115.1, 405.0)	* Resume tracking AR. Flare-mode test during orbit over 5 June, 3:37 - 4:33 UT with flare-fla										
PROG= 02 Inf.-time(s)													
└ Subr= 1 1-time(s) 1800.0sec													
└ Seqn= 98 1-time(s) 4.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16B8: Flare Dynamics (normal mode, with preflare buffers) - Al/poly - Thin-Be -long/short pairs													
Term	Pointing (x, y)		Comment										
06/05 02:31:00 - 06/05 05:46:24	Track (115.1, 405.0)	* Resume tracking AR. Flare-mode test during orbit over 5 June, 3:37 - 4:33 UT with flare-fla										
PROG= 07 Inf.-time(s)													
└ Subr= 1 4-time(s) 2.0sec													
└ Seqn= 42 1-time(s) 15.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=92	3	0	3.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=92	2	0	3.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=92	3	0	3.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=92	2	0	3.0sec
└ Seqn= 82 4-time(s) 45.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	1	6.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	1	6.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	2	6.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	2	6.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	3	6.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	3	6.0sec
└ Subr= 2 1-time(s) 2.0sec													
└ Seqn= 64 1-time(s) 4.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16A0: Synoptic Q95 2x2 - Al/mesh(88/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Ti-poly(181/2048) + G-band(16)													
Term	Pointing (x, y)		Comment										
06/05 05:49:30 - 06/05 06:14:00	Fixed (0.0, 0.0)	synoptic, shifted -13.5 min										
06/05 06:54:00 - 06/05 06:59:54	Track (182.2, 404.3)	# Cont.										
06/06 06:03:00 - 06/06 06:09:54	Fixed (0.0, 0.0)	synoptic										
PROG= 17 1-time(s)													
└ Subr= 1 1-time(s) 12.0sec													
└ Seqn= 46 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└ Seqn= 7 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└ Seqn= 71 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└ Seqn= 92 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #15A8: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant												
Term	Pointing (x, y)		Comment									
06/05 07:03:00 - 06/05 07:04:54	Fixed (-528.4, -528.4)	* XRT post-bakeout four-quadrant sequence (four)..									
PROG= 11 1-time(s)												
└ Subr= 1 1-time(s) 12.0sec												

└─ Seqn= 28 1-time(s) 12.0sec													
┌	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
├	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
├	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
└	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #15A9: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant

Term	Pointing (x, y)	Comment
06/05 07:08:00 - 06/05 07:09:54	Fixed (528.4, -528.4)	

PROG= 08 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
┌─ Seqn= 41 1-time(s) 12.0sec													
├	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
├	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
├	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
└	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #15AA: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant

Term	Pointing (x, y)	Comment
06/05 07:13:00 - 06/05 07:14:54	Fixed (528.4, 528.4)	

PROG= 14 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
┌─ Seqn= 8 1-time(s) 12.0sec													
├	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
├	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
├	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
└	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #15AB: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant

Term	Pointing (x, y)	Comment
06/05 07:18:00 - 06/05 07:24:54	Fixed (-528.4, 528.4)	* Four-quadrant, last.

PROG= 19 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
┌─ Seqn= 40 1-time(s) 12.0sec													
├	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
├	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
├	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
└	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16BE: Al/poly(384x384) -Gband - Q95-AEC4(8192ms)-1min cadence

Term	Pointing (x, y)	Comment
06/05 07:28:02 - 06/05 11:19:54	Track (216.1, 420.8) ^{Ⓜ 06/05 07:25:00}	* Track HOP 71 AR 11019 (officially from 8:30 UT).
06/06 06:13:02 - 06/06 06:52:00	Track (366.9, 402.3) ^{Ⓜ 06/06 06:10:00}	# Cont.
06/06 07:31:30 - 06/06 10:12:30	Track (394.6, 418.9) ^{Ⓜ 06/06 07:30:00}	* HOP 71, horizontal fields in AR.

PROG= 04 Inf.-time(s)													
└─ Subr= 1 15-time(s) 60.0sec													
┌─ Seqn= 57 1-time(s) 4.0sec													
├	Al-poly/Open	Al-poly/Open	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	4	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
┌─ Seqn= 64 1-time(s) 4.0sec													
├	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16BF: AR dynamics - Thin-Be - 384x384 - Q95 - 18s - AEC1

Term	Pointing (x, y)	Comment
06/05 11:51:00 - 06/05 17:39:54	Track (224.5, 403.8) ^{Ⓜ 06/05 11:20:00}	* Track AR.
06/05 17:53:30 - 06/06 05:59:54	Track (274.7, 403.3) ^{Ⓜ 06/05 17:50:30}	# Cont.

PROG= 09 Inf.-time(s)													
└─ Subr= 1 50-time(s) 18.0sec													
┌─ Seqn= 30 1-time(s) 2.0sec													
├	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
┌─ Seqn= 64 1-time(s) 4.0sec													
├	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16A1: Synoptic Q95 2x2 - Al/poly(128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Ti-poly(181/2048) + G-band(16)

Term	Pointing (x, y)	Comment
06/05 17:43:00 - 06/05 17:49:54	Fixed (0.0, 0.0)	synoptic, shifted -19.5 min

PROG= 15 1-time(s)

Subr= 1													1-time(s)	12.0sec
Seqn= 24													1-time(s)	4.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Seqn= 7													1-time(s)	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Seqn= 71													1-time(s)	4.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	177ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Seqn= 92													1-time(s)	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval

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Flare mode

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XOB #16B9: Flare Response - Dynamics - Thin-Be - Med-Be - AEC 1 - 384x384 - Q95

Term	Pointing (x, y)	Comment
06/05 04:10:08 - 06/05 04:20:00	Track (115.1, 405.0) ^{® 06/04 21:30:00}	* Resume tracking AR. Flare-mode test during orbit over 5 June, 3:37 - 4:33 UT with flare-fla

PROG= 03 1-time(s)

Subr= 1													1-time(s)	2.0sec
Seqn= 3													15-time(s)	20.0sec
	thin-Be/Open	thin-Be/thick-Al	close	Safe	Norm	125ms	Obs	1x1	384x384	(1024, 1024)	Q=95	1	0	2.0sec
	med-Be/Open	med-Be/thick-Al	close	Safe	Norm	354ms	Obs	1x1	384x384	(1024, 1024)	Q=95	1	0	2.0sec
Seqn= 17													1-time(s)	4.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec
Subr= 2													8-time(s)	2.0sec
Seqn= 3													15-time(s)	60.0sec
	thin-Be/Open	thin-Be/thick-Al	close	Safe	Norm	125ms	Obs	1x1	384x384	(1024, 1024)	Q=95	1	0	2.0sec
	med-Be/Open	med-Be/thick-Al	close	Safe	Norm	354ms	Obs	1x1	384x384	(1024, 1024)	Q=95	1	0	2.0sec
Seqn= 17													1-time(s)	4.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec
Subr= 3													25-time(s)	2.0sec
Seqn= 3													1-time(s)	600.0sec
	thin-Be/Open	thin-Be/thick-Al	close	Safe	Norm	125ms	Obs	1x1	384x384	(1024, 1024)	Q=95	1	0	2.0sec
	med-Be/Open	med-Be/thick-Al	close	Safe	Norm	354ms	Obs	1x1	384x384	(1024, 1024)	Q=95	1	0	2.0sec
Seqn= 17													1-time(s)	4.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval

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Active Region Search

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NOT USED

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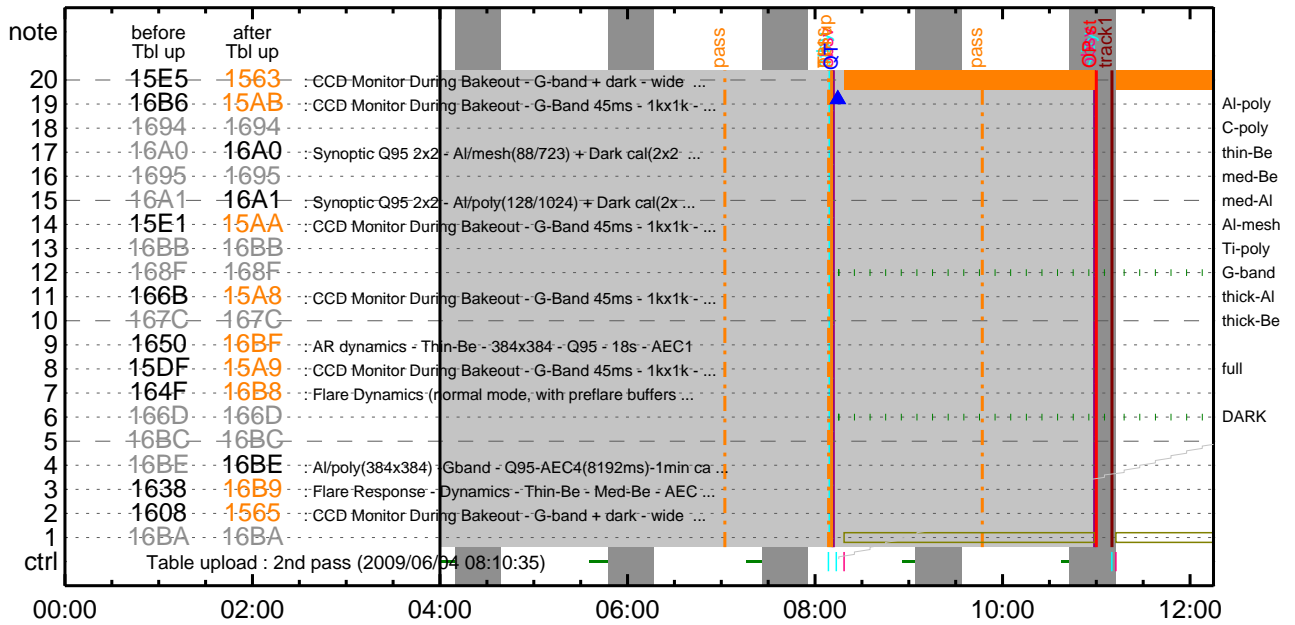
Flare Detection

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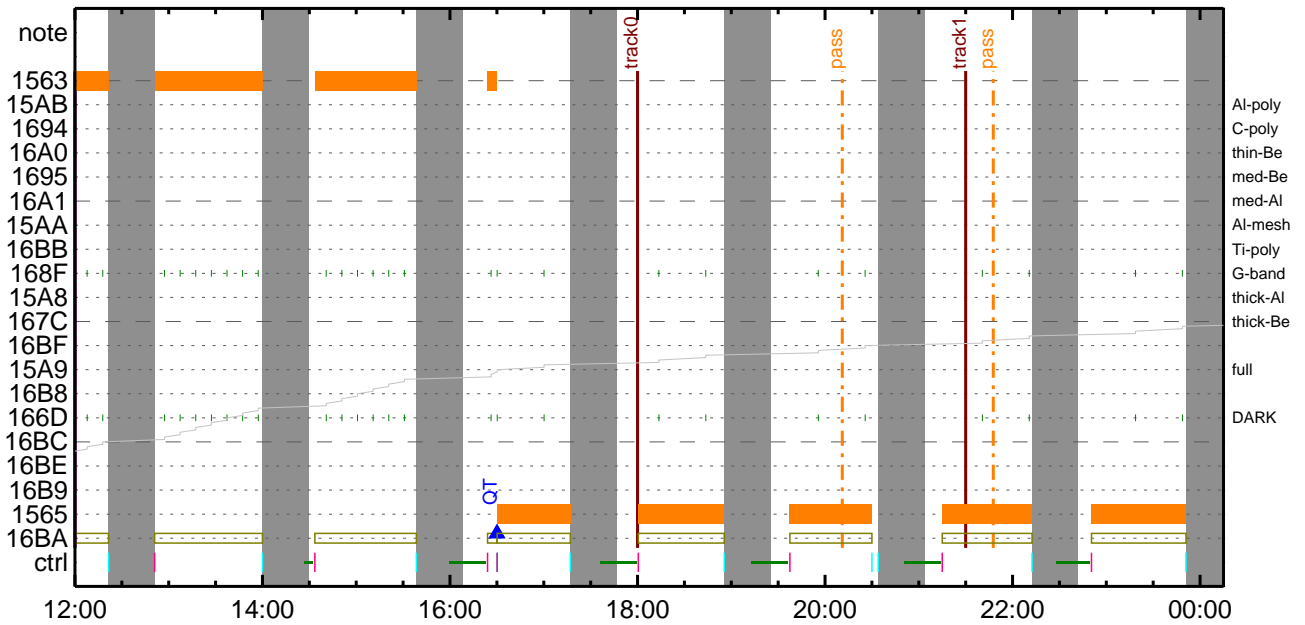
FLD Patrol

Term	Pointing (x, y)	Comment
06/05 04:10:04 - 06/05 04:20:04	Track (115.1, 405.0) ^{® 06/04 21:30:00}	* Resume tracking AR. Flare-mode test during orbit over 5 June, 3:37 - 4:33 UT with flare-fla
thin-Be/Open	thin-Be/Open close	Safe Norm 8ms Obs 8x8 DPCM 120sec
	Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

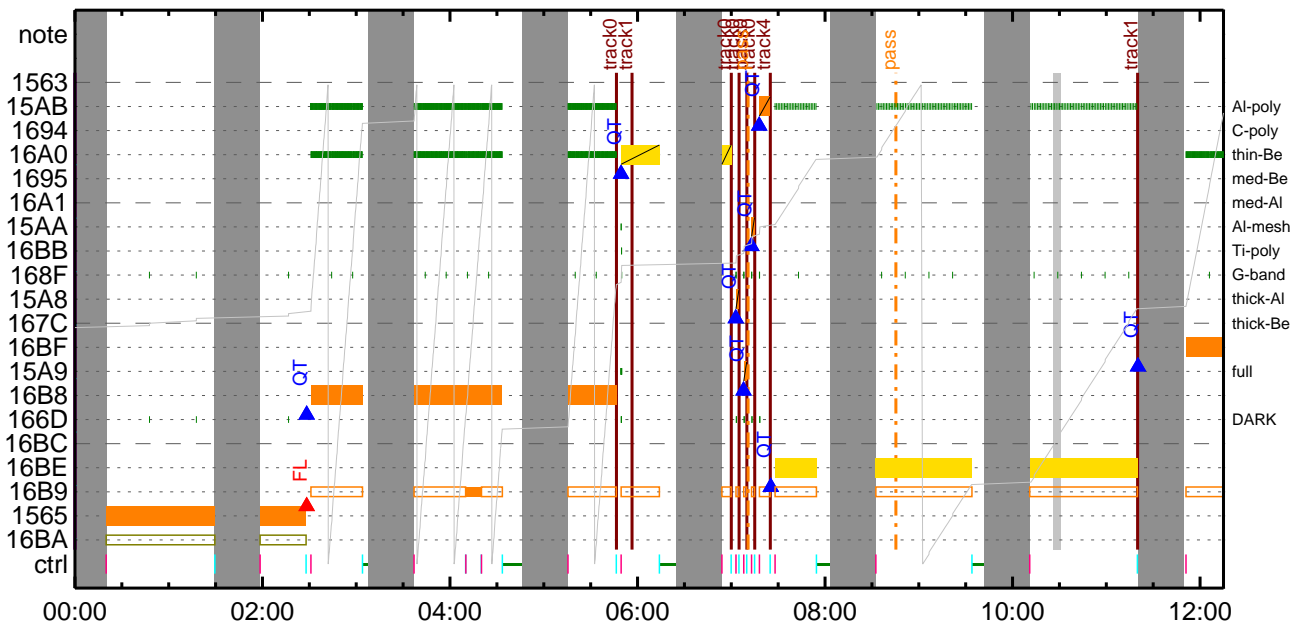
CMDI #0596 2009/06/04



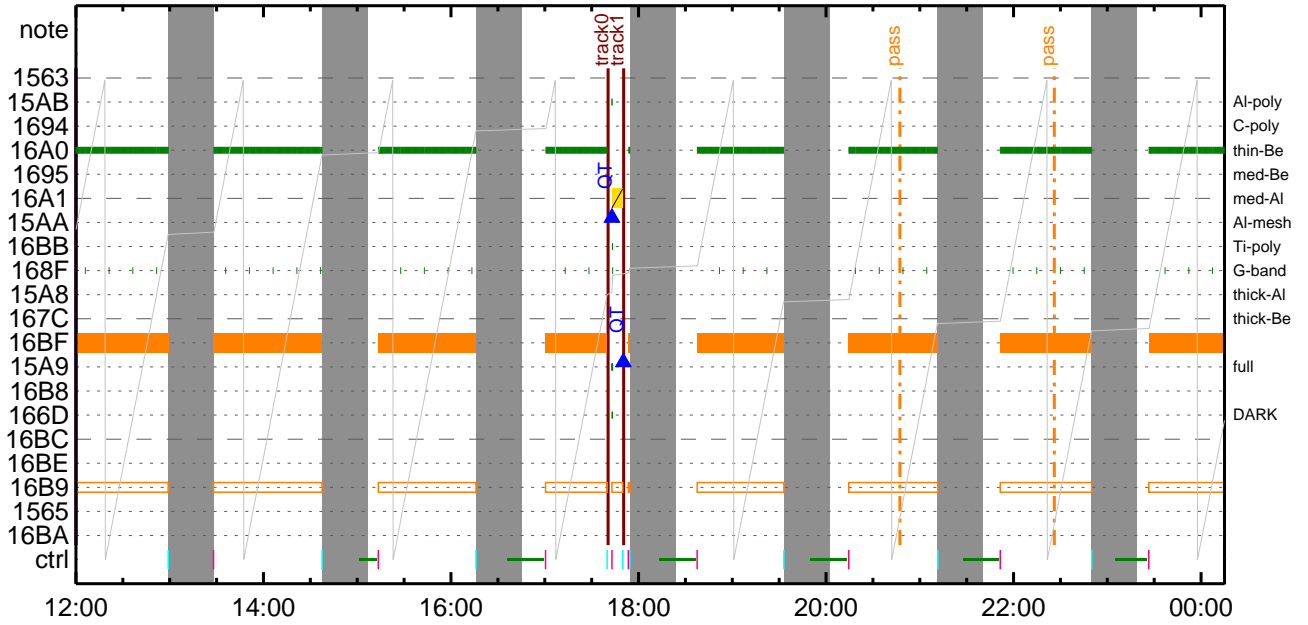
CMDI #0596 2009/06/04



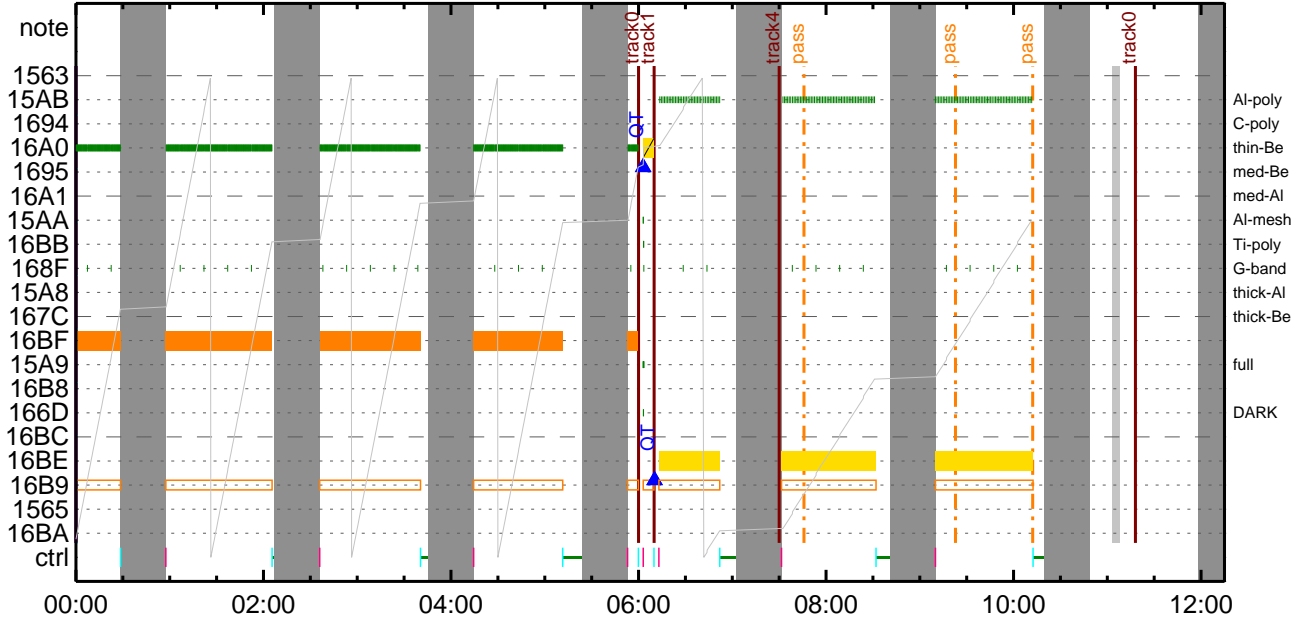
CMDI #0596 2009/06/05



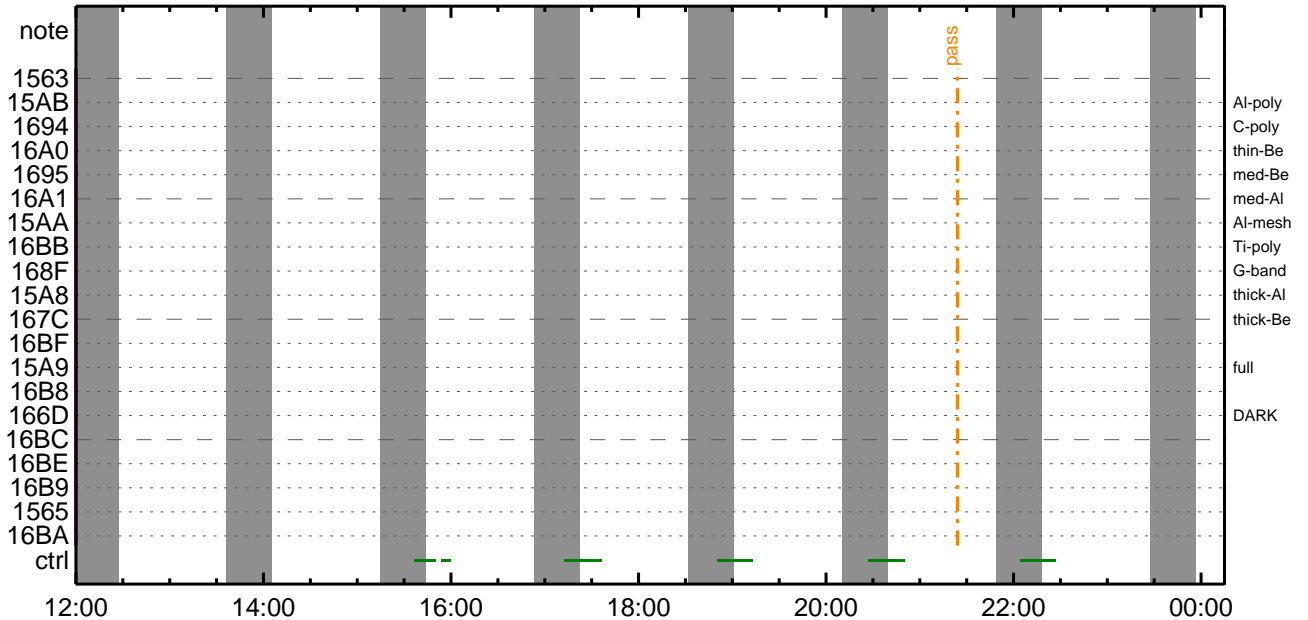
CMDI #0596 2009/06/05



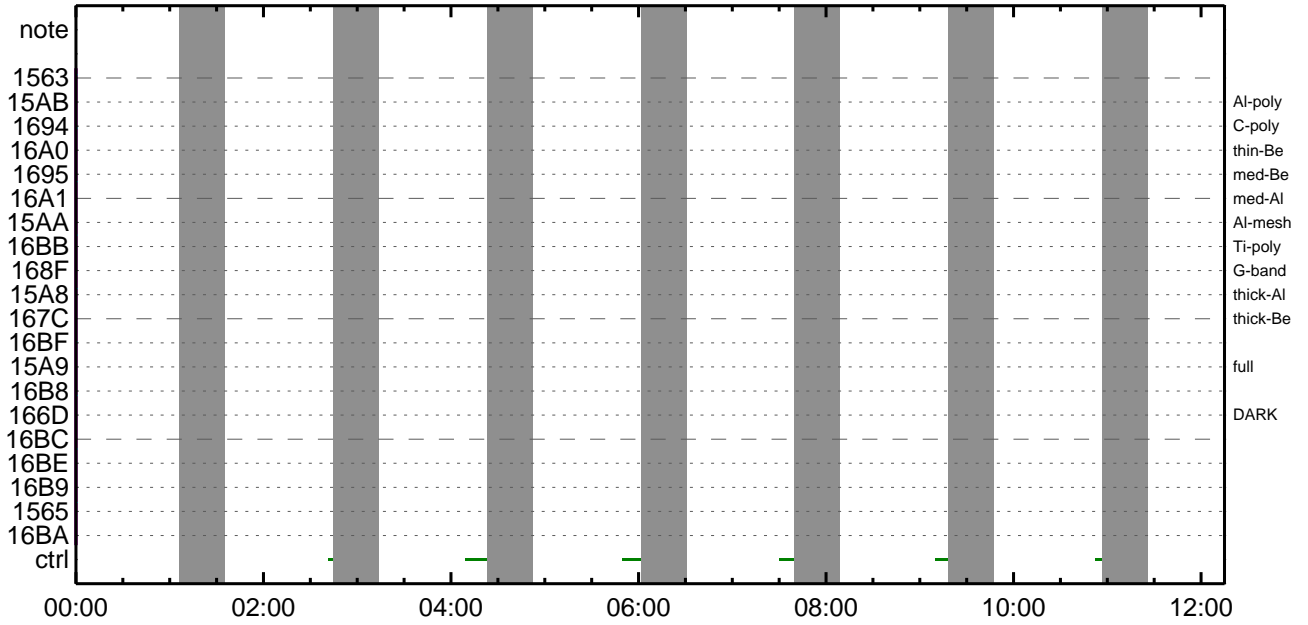
CMDI #0596 2009/06/06



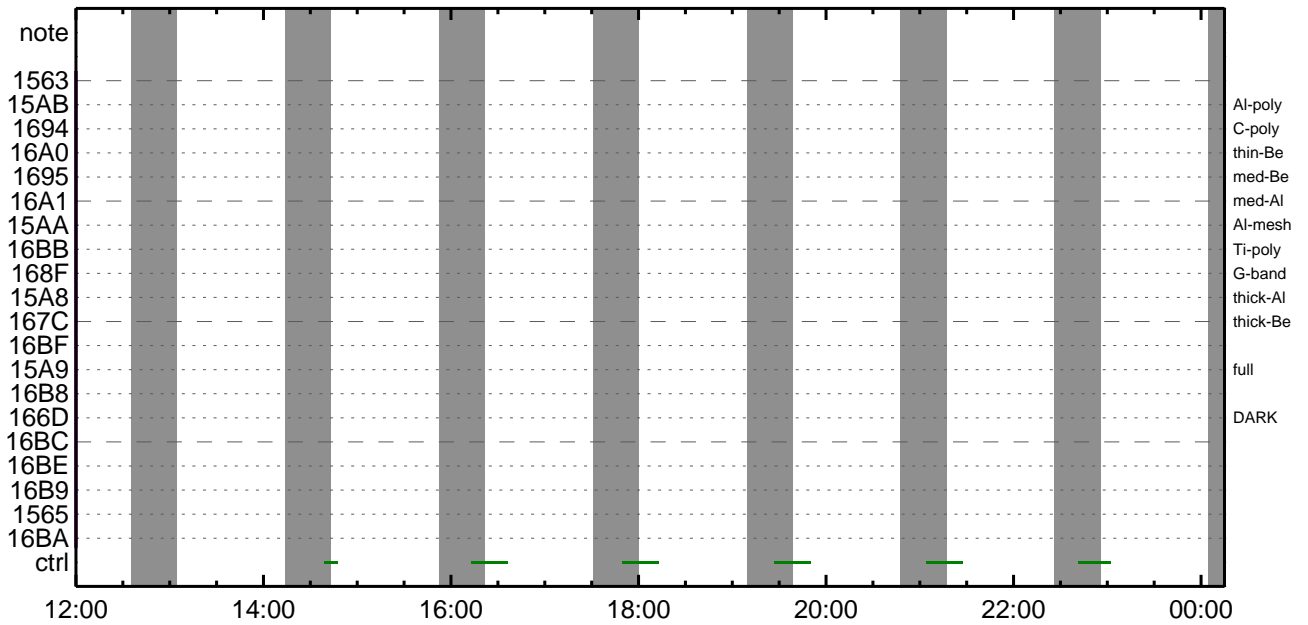
CMDI #0596 2009/06/06



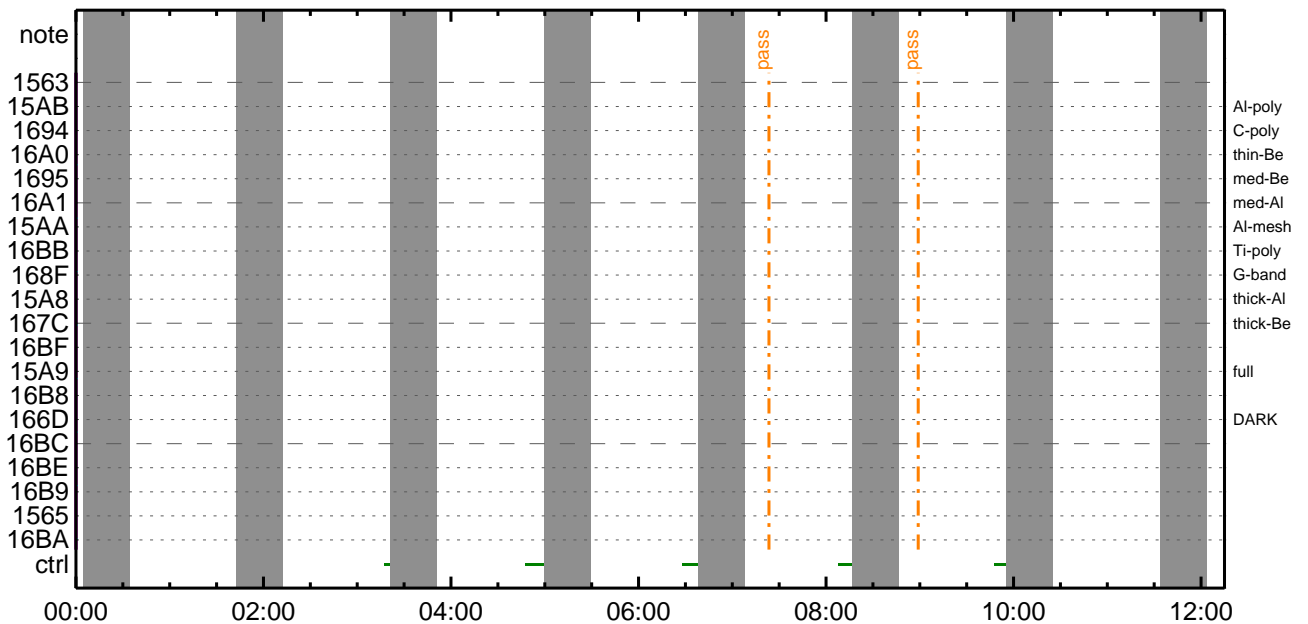
CMDI #0596 2009/06/07



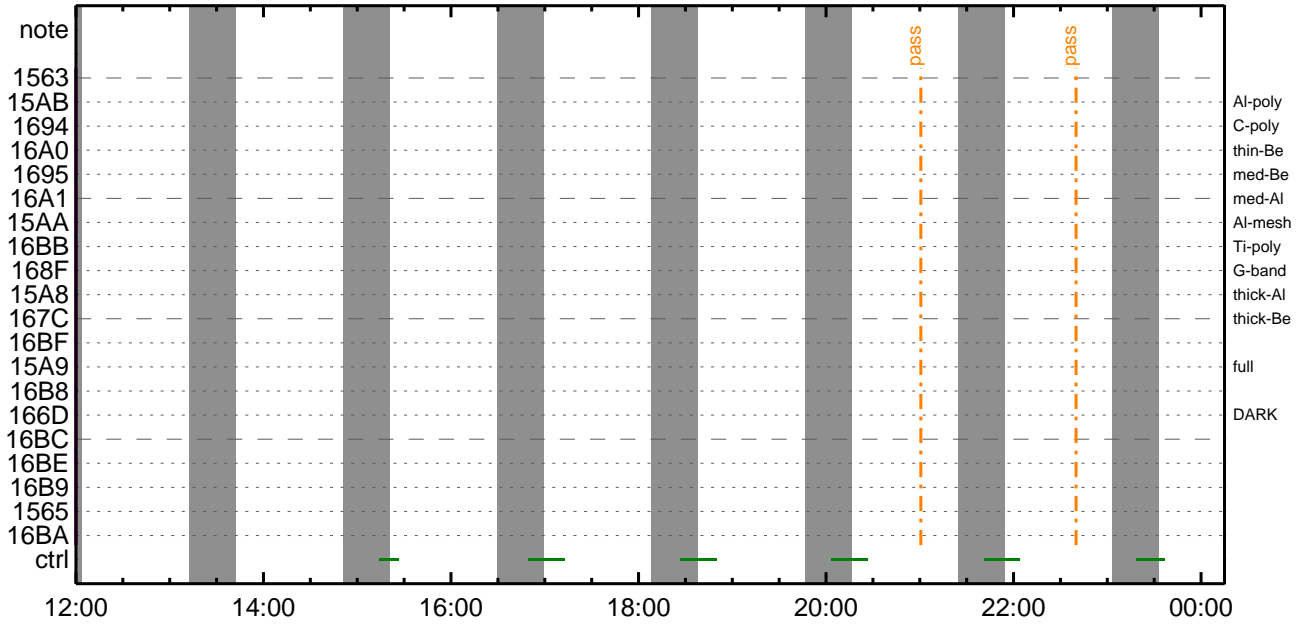
CMDI #0596 2009/06/07



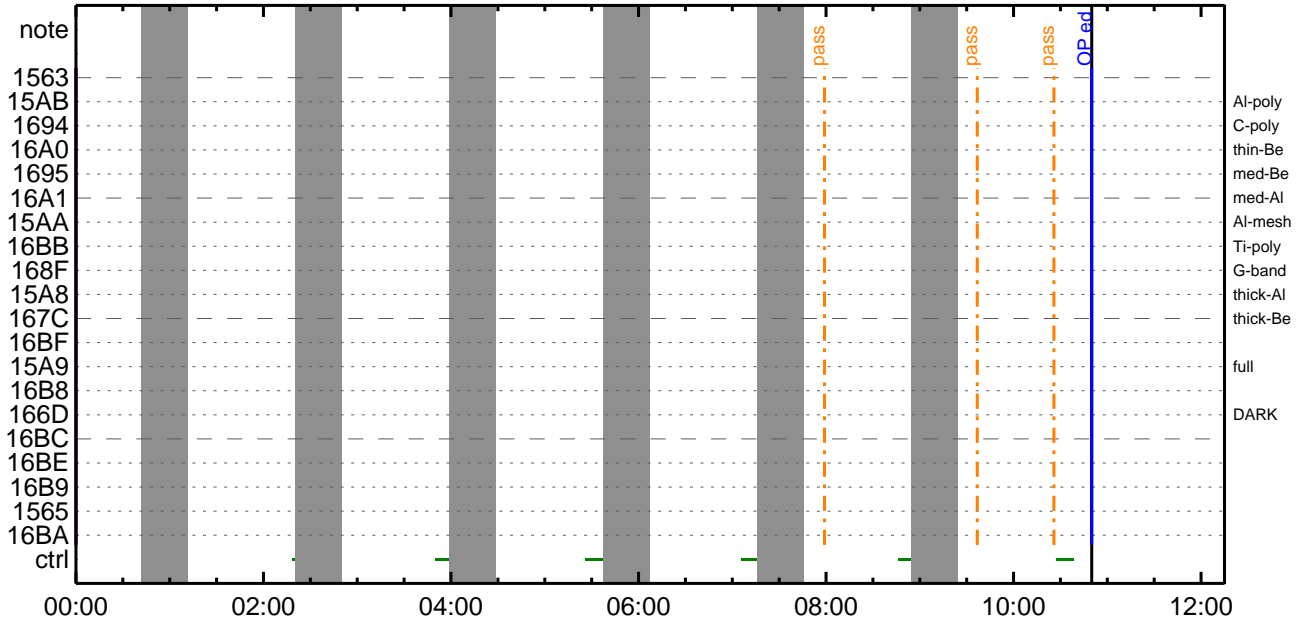
CMDI #0596 2009/06/08



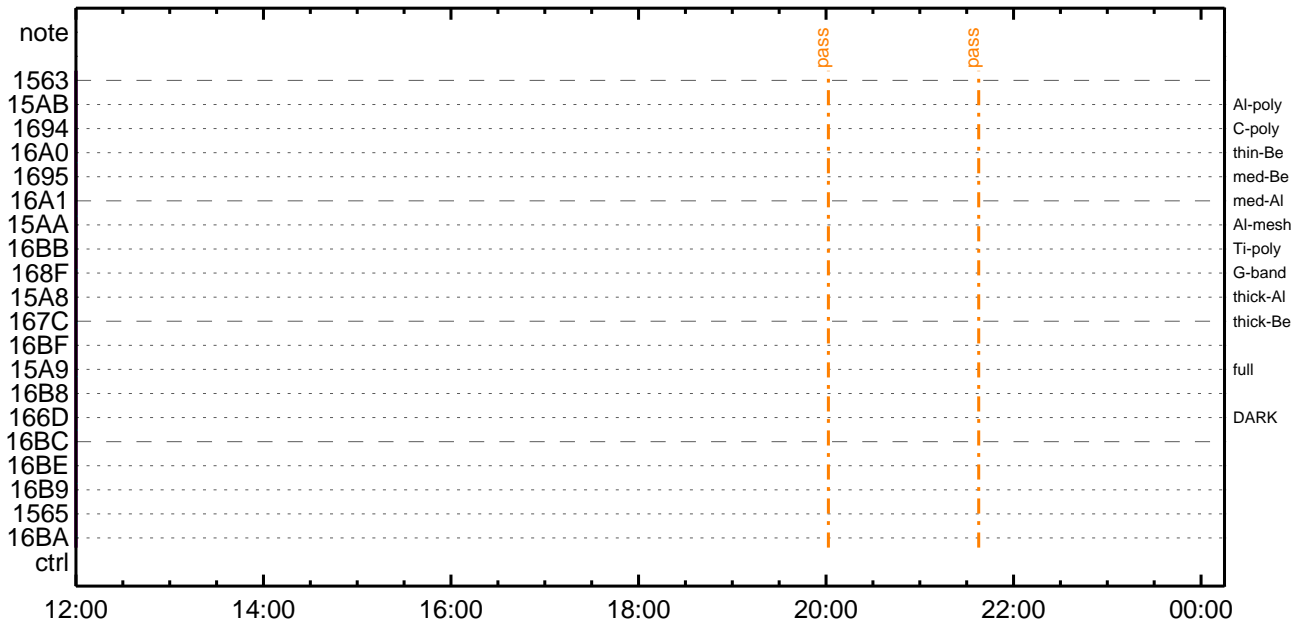
CMDI #0596 2009/06/08



CMDI #0596 2009/06/09



CMDI #0596 2009/06/09



(a) Spacecraft Operation Procedure (real-commands)

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main-634 2009-06-04 14:06:40 236 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOS¥Á¥S¥Á¥~¼Á»Û;ä
0005 C.
0006 C. ¥Á¥B;¼¥³¥F¥ó¥ÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;ËçðÅð•µ°E»Í×ÁÇçí¥ç¥Á¥×¥í;¼¥É;ËËèµ•ííÉ;ÈðE¼°ÇÖç•ñç¼l¹ççí;çÀ®, ùñ¹çðçðççÁ+ç®ç•ñËçççç³çÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS DUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. *****
0046 C. OP/OG¥í;¼¥É;¼¥Áó¥×
0047 C. *****
0048 C.
0049 . C. ;ãOP/OG¥í;¼¥É;ä
0050 . S. OP op-634:OP
0051 ( )
0052 . S. OG og-634:OG
0053 ( )
0054 C.
0055 . C. ;ãNMOG&OPíî°è¥Á¥ó¥×;ä
0056 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0057 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0058 BC (20 00 7f 01 02)
0059 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0060 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0061 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0062 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0063 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0064 +. DC 01-22 DHU_MODE_CHNG
0065 BC (07 0b f8)
0066 C. çç[HK1_PKT_FORM_NO] EQ 7
0067 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0068 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0069 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0070 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0071 . C. ¥Áó¥×½ªí»ðð³íÇ§
0072 C. çç[HK1_DMP_CHK_FLG] EQ NON
0073 . C. RAM ID=NMOGçí¼Ë¹ç•ë²ïOKçðð³íÇ§
0074 C.
0075 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0076 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0077 BC (20 80 7f 01 02)
0078 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0079 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0080 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0081 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0082 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0083 +. DC 01-22 DHU_MODE_CHNG
0084 BC (07 0b f8)
0085 C. çç[HK1_PKT_FORM_NO] EQ 7
0086 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0087 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0088 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0089 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0090 . C. ¥Áó¥×½ªí»ðð³íÇ§
0091 C. çç[HK1_DMP_CHK_FLG] EQ NON
0092 . C. RAM ID=NMOGçí¼Ë¹ç•ë²ïOKçðð³íÇ§
0093 C.
0094 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0095 +. DC 01-23 DHU_DMA_DMP_PRM_SET

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0096 BC (21 00 41 01 02)
0097 C.          ÷÷[HK1_DMP_TOP_ADRS_1]          EQ      42
0098 C.          ÷÷[HK1_DMP_TOP_ADRS_0]          EQ      0
0099 C.          ÷÷[HK1_DMP_BLOCK_NUM]         EQ      65
0100 C.          ÷÷[HK1_DMP_REPEAT_NUM]        EQ      0
0101 C.          ÷÷[HK1_DMA_DMP_PIM]          EQ      DHU
0102 +. DC 01-22 DHU_MODE_CHNG
0103 BC (07 0b f8)
0104 C.          ÷÷[HK1_PKT_FORM_NO]           EQ      7
0105 C.          ÷÷[HK1_PKT_GEN_TIME]         EQ      0.25 s
0106 C.          ÷÷[HK1_S_TLM_BIT_RATE]       EQ      32k
0107 C.          ÷÷[HK1_X_TLM_BIT_RATE]       EQ      4M
0108 C.          ÷÷[HK1_DMP_CHK_FLG]         EQ      EXEC
0109 . C.      ¥À¥Ö¥×½ªî»ðð³îÇ§
0110 C.          ÷÷[HK1_DMP_CHK_FLG]         EQ      NON
0111 . C.      RAM ID=NMOG, RAM ID=OPîîË¹ç•ë²îOKðð³îÇ§
0112 C.
0113 . C.      ***** òÊ²¼ðîî¼Ä´¶Á°ðÊÊ¬ò°Á÷ç® (¼åµ-¥À¥Ö¥×½ªê¼çððÄÖÃæðÇ¼ª°¬ðë¼î¹çðÇâ) *****
0114 C.      DHU¥â;¼¥Ê;Ê¼¥½, ¥ì;¼¥Ê;Êððîãð¹
0115 +. DC 01-22 DHU_MODE_CHNG
0116 BC (02 0a f8)
0117 C.          ÷÷[HK1_PKT_FORM_NO]           EQ      2
0118 C.          ÷÷[HK1_PKT_GEN_TIME]         EQ      0.5S
0119 C.          ÷÷[HK1_S_TLM_BIT_RATE]       EQ      32K
0120 C.          ÷÷[HK1_X_TLM_BIT_RATE]       EQ      4M
0121 C.
0122 . C.      *****
0123 C.      TI-CMD SET (OPOG STOP/COPY/START)
0124 C.      *****
0125 C.
0126 . C.      NOTICE ;§ OPOG UPLOADð¬Á÷ç®NGðîî¼ç;ç°Ê²¼ðîîTI-CMDÁ÷ç®ðîî¼Ä¹Òª•ðÊððð³ðÊ;£
0127 C.          ðªðç;çSETðÊDUMPðîîË±°î¥Ñ¥¹ç¹Òª|ð³ðÊ;£
0128 C.
0129 . C.      TI¥³¥Þ¥Ö¥ÊððÄðî¼ç(UT)
0130 +. TI 2009-06-04 10:55:00.0
0131 DC 01-B3 DHU_OP_STOP
0132 C.          ÷÷[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0133 C.
0134 +. TI 2009-06-04 10:55:01.0
0135 DC 01-B4 DHU_OP_COPY
0136 C.          ÷÷[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0137 C.
0138 +. TI 2009-06-04 10:55:01.0
0139 DC 01-B5 DHU_OPOG_COPY
0140 C.          ÷÷[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0141 C.
0142 +. TI 2009-06-04 10:59:59.5
0143 DC 01-B2 DHU_OP_START
0144 C.          ÷÷[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0145 C.
0146 C.      òÊ²¼ðîîÄê¼îîñðîî¥À¥§¥À¥¬¹àîÛ
0147 C.          ÷÷[HK1_TI_CMD_ENA/DIS]       EQ      ENA
0148 C.          ÷÷[HK1_TI_CMD_NUM]          EQ      4
0149 C.          ÷÷[HK1_NEXT_EXEC_PIM]        EQ      DHU
0150 C.          ÷÷[HK1_NEXT_EXEC_DC]        EQ      0xB3
0151 C.
0152 . C.      *****
0153 C.      TIîî°è¥À¥Ö¥×
0154 C.      *****
0155 C.
0156 C.      TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0157 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0158 BC (03 ab 03 01 02)
0159 C.          ÷÷[HK1_DMP_TOP_ADRS_1]       EQ      07
0160 C.          ÷÷[HK1_DMP_TOP_ADRS_0]       EQ      2B
0161 C.          ÷÷[HK1_DMP_BLOCK_NUM]        EQ      3
0162 C.          ÷÷[HK1_DMP_REPEAT_NUM]      EQ      0
0163 C.          ÷÷[HK1_DMA_DMP_PIM]         EQ      DHU
0164 +. DC 01-22 DHU_MODE_CHNG
0165 BC (07 0b f8)
0166 C.          ÷÷[HK1_PKT_FORM_NO]           EQ      7
0167 C.          ÷÷[HK1_PKT_GEN_TIME]         EQ      0.25 s
0168 C.          ÷÷[HK1_S_TLM_BIT_RATE]       EQ      32k
0169 C.          ÷÷[HK1_X_TLM_BIT_RATE]       EQ      4M
0170 C.          ÷÷[HK1_DMP_CHK_FLG]         EQ      EXEC
0171 C.
0172 . C.      ¥À¥Ö¥×½ªî»ðð³îÇ§
0173 C.          ÷÷[HK1_DMP_CHK_FLG]         EQ      NON
0174 C.
0175 . C.      RAM ID=TI_TBLðîîË¹ç•ë²îOKðð³îÇ§
0176 C.
0177 . C.      DHU¥â;¼¥Ê;Ê¼¥½, ¥ì;¼¥Ê;Êððîãð¹
0178 +. DC 01-22 DHU_MODE_CHNG
0179 BC (02 0a f8)
0180 C.          ÷÷[HK1_PKT_FORM_NO]           EQ      2
0181 C.          ÷÷[HK1_PKT_GEN_TIME]         EQ      0.5S
0182 C.          ÷÷[HK1_S_TLM_BIT_RATE]       EQ      32K
0183 C.          ÷÷[HK1_X_TLM_BIT_RATE]       EQ      4M
0184 C.
0185 . C.      Stop EIS observation and temporarily disable EIS mode changes
0186 C.
0187 C.
0188 C.      ***** Start EIS operation (TI set) *****
0189 C.      Execute, after the success of OP upload.
0190 C.      Set EIS TI-commands
0191 +. TI 2009-06-04 10:59:30.0
0192 DC 07-FC EIS_MODE_MANU
0193 BC (21 02)

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0194 +. TI 2009-06-04 10:59:40.0
0195 DC 07-FC EIS_MODE_CHG_DIS
0196 BC (22)
0197 . C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0198 C. ***** End EIS operation (TI set) *****
0199 C.
0200 C.
0201 C. *****
0202 C. SOT TI command set
0203 C. *****
0204 C. Execute, after the success of OP upload.
0205 +. TI 2009-06-04 10:59:16.0
0206 DC 07-F0 MDP_SOT_MODE_STBY
0207 BC (41)
0208 . C. -----
0209 C. HK1_TI_CMD_NUM = 1 CNTUP []
0210 C. -----
0211 C. ***** SOT END *****
0212 C.
0213 C. ***** XRT START *****
0214 C. Execute, after the success of OP upload.
0215 +. TI 2009-06-04 10:59:00.0
0216 DC 07-F0 MDP_XRT_MODE_STBY
0217 BC (c3)
0218 . C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0219 C.
0220 C. ***** XRT END *****
0221 C.
0222 . C. ***** MDP 'ûÃîñî»ô¼ÿòÊÂð¹àéDCBC•x²è *****
0223 C. (¼â°îÿÔÿÃÿÊÿÞÿËÿãÿçÿéðE¼ç¼¼¼»Ü¹¹è)
0224 . S. DC-BC dcbc-402:DCBC
0225 (MDP_known_event)
0226 C.
0227 C.
0228 . C. ***** ÿDÿ¹•î Daily±;îññE'ø¹¹èDCBC•x²è *****
0229 . S. DC-BC dcbc-153:DCBC
0230 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0231 C.
0232 C.
0233 . C. ;ãLOSÿÁÿSÿÿÿ¼¼¼»Ü;ã
0234 C.
0235 . C. ***** LOS *****
0236 C.


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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 06 80 80 20 04)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 07 80 80 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 08 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 09 c0 c0 10 10)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 0a 40 c0 10 10)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0b 40 40 10 10)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0c c0 40 10 10)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0f 80 80 06 06)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 10 80 80 10 10)
0142 . C. ----- Success Verify ? OK / NG ____
0143 C.
0144 C.
0145 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0146 C.
0147 +. DC 07-F0 MDP_XRT_MODE_OBSV
0148 BC (c2)
0149 +. TI 2009-06-04 10:59:02.0
0150 DC 07-F0 MDP_XRT_MODE_OBSV
0151 BC (c2)
0152 . C. ----- Success Verify ? OK / NG ____
0153 C.
0154 C. ***** XRT END *****
0155 . C. *****
0156 C. SOT FPP MEMORY UPLOAD
0157 C. *****
0158 C.
0159 . C. < Upload RAMID=FPP_CTRL >
0160 . S. RAM ram-421:FPP_CTRL
0161 ( )
0162 . C. -----
0163 C. Number of bytes = 1*128 [ ]
0164 C. -----
0165 C.
0166 . C. < Dump RAMID=FPP_CTRL >
0167 +. DC 07-F2 FPP_DUMP_MEM
0168 BC (08 07 0b 40 00 b5 7f)
0169 . C. -----
0170 C. (It takes ~1min)
0171 C. Dump Comparison = OK [ ]
0172 C. -----
0173 C.
0174 C. *****
0175 C. END of SOT sequence
0176 C. *****
0177 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0178 +. DC 07-FC EIS_MODE_MANU
0179 BC (21 02)
0180 . C. Verify EIS in MANUAL mode
0181 . C. Estimated OBSTBL upload time is 18s
0182 C. *****
0183 C. EIS START OBSTBL LOAD
0184 C. *****
0185 . S. RAM ram-820:EIS_OBSTBL
0186 ( )
0187 +. DC 07-FC EIS_DUMP_OBSTBL
0188 BC (07 07 07 00 00 70 00)
0189 C.
0190 C. Execute, after the success of OBSTBL upload.
0191 C. Set EIS TI-commands
0192 +. TI 2009-06-04 10:59:50.0
0193 DC 07-FC EIS_MODE_CHG_ENA

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0194 BC (20)
0195 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0196 C. *****
0197 C. EIS END OBSTBL LOAD
0198 C. *****
0199 C. *****
0200 C. START of XRT_CCD_HEATER_ON operation
0201 C. *****
0202 C.
0203 +. DC 07-F0 MDP_XRT_CTRL_MANU
0204 BC (c1)
0205 C. ----- Success Verify ? OK / NG;
0206 C.
0207 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0208 BC (c4 14)
0209 + DC 07-F0 MDP_XRT_FLD_DIS
0210 BC (d9)
0211 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0212 BC (c9)
0213 + DC 07-F0 MDP_XRT_ARS_DIS
0214 BC (d5)
0215 C. ----- Success Verify ? OK / NG ____
0216 C.
0217 C.
0218 C. All OK? Yes--> Please Proceed. / No --> Stop here.
0219 C.
0220 +. DC 07-F0 MDP_XRT_CTRL_AUTO
0221 BC (c0)
0222 C. ----- Success Verify ? OK / NG;
0223 C.
0224 +. DC 04-BC TCIB_XRT_S_HTR_A_ENA
0225 C. ----- Success Verify ? OK / NG;
0226 C.
0227 C. -----
0228 C. If anomalous situation appeared, execute TCIB_XRT_S_HTR_A_DIS using DCBC-441 (line 24)
0229 C. -----
0230 C. *****
0231 C. END of XRT_CCD_HEATER_ON operation
0232 C. *****
0233 C.
0234 C.
0235 C.
0236 . C. ***** MDP `úÃîï»ö¼ÝðĒĀð¹ñēDCBC•x²è *****
0237 C. (%á°îÝŌŸĀŸĒŸŦŸĒŸáŸçŸèñĒ¼ñ¼Ā»Ŧñ¹è)
0238 . S. DC-BC dcbc-402:DCBC
0239 (MDP_known_event)
0240 C.
0241 C.
0242 . C. ***** ȲĐŸ¹•Ī Daily±ĵĪññĒ'Øñ¹ñēDCBC•x²è *****
0243 . S. DC-BC dcbc-153:DCBC
0244 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0245 C.
0246 C.
0247 . C. ;ãLOSŸÁŸŸŸĀŸ~¼Ā»Ŧ;ä
0248 C.
0249 . C. ***** LOS *****
0250 C.

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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-269:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Stop SP table >
0120 +. DC 07-F0 MDP_SP_CTRL_MANU
0121 BC (61)
0122 C. -----
0123 C. MDP_SP_CTRL_MODE = MANU [ ]
0124 C. -----
0125 C.
0126 . C. <Upload SP Observation Table>
0127 . S. RAM ram-281:MDP_OBS_S
0128 ( )
0129 C.
0130 . C. < Dump RAMID=MDP_OBS_S >
0131 +. DC 07-F0 MDP_DUMP_SPTBL
0132 BC (83 07 00 00 00 38 b8)
0133 C. -----
0134 C. MDP_OBS_S verify = OK/NG [ ]
0135 C. -----
0136 C.
0137 C. *****
0138 C. SOT TI command set
0139 C. *****
0140 C. Execute, after the success of TBL upload.
0141 +. TI 2009-06-04 10:59:18.0
0142 DC 07-F0 MDP_SOT_MODE_OBSV
0143 BC (40)
0144 . C. -----
0145 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0146 C. -----
0147 C.
0148 C.
0149 . C. ***** MDP 'úÃîâî»ö¼ÝðËÄð¹ñèDCBC•x²è *****
0150 C. (¼á°îÝÓÝÄÝËÝÞÝËÝáÝ¼ÝèñË¼ð¼Ä»Û¹ñè)
0151 . S. DC-BC dcbc-402:DCBC
0152 (MDP_known_event)
0153 C.
0154 C.
0155 . C. ***** ÝÐÝ¹•î Daily±¿îÑñË'Ø¹ñèDCBC•x²è *****
0156 . S. DC-BC dcbc-153:DCBC
0157 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0158 C.
0159 C.
0160 . C. ;ãLOSÝÁÝ§ÝÄÝ¹¼Ä»Û;ã
0161 C.
0162 . C. ***** LOS *****
0163 C.

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*** OP Sequence for XRT ***

2009/06/04	11:09:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/04	11:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2009/06/04	11:12:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/04	12:21:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/04	12:51:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/04	14:00:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/04	14:32:30.0	XRT_Custom_430_OG [0x1ae]							
2009/06/04	14:33:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/04	15:38:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/04	16:23:00.0	XRT_Custom_430_OG [0x1ae]							
2009/06/04	16:24:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/04	16:30:01.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/04	16:30:03.0	XRT_QT_PROG_SET_414_OG [0x19e]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2009/06/04	16:30:05.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/04	16:30:07.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/04	16:30:09.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/04	16:30:11.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/04	17:17:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/04	17:59:30.0	XRT_Custom_430_OG [0x1ae]							
2009/06/04	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2009/06/04	18:00:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/04	18:55:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/04	19:36:30.0	XRT_Custom_430_OG [0x1ae]							
2009/06/04	19:37:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/04	20:30:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/04	20:30:02.0	XRT_TCIB_XRT_S_HTR_A_DIS_417_OG [0x1a1]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2009/06/04	20:34:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/04	21:14:00.0	XRT_Custom_430_OG [0x1ae]							
2009/06/04	21:15:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/04	21:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2009/06/04	22:12:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/04	22:49:30.0	XRT_Custom_430_OG [0x1ae]							
2009/06/04	22:50:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/04	23:51:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/05	00:20:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/05	01:29:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/05	01:58:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/05	02:27:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/05	02:27:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2009/06/05	02:28:16.0	XRT_QT_PROG_SET_408_OG [0x198]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2009/06/05	02:28:18.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2009/06/05	02:28:20.0	XRT_AEC_RESET_415_OG [0x19f]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2009/06/05	02:28:22.0	XRT_Custom_411_OG [0x19b]							
		MDP_XRT_PFBUF_RESET	1	07-F0	ea				
2009/06/05	02:28:32.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/05	02:28:34.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/05	02:28:36.0	XRT_FLRCTRL_DIS_436_OG [0x1b4]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/05	02:30:58.0	XRT_FLD_RESET_437_OG [0x1b5]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2009/06/05	02:31:00.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/05	03:04:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							

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2009/06/05	03:37:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/06/05	04:10:00.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/06/05	04:10:02.0	XRT_FLRCTRL_ENA_444_OG [0x1bc]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2009/06/05	04:10:04.0	XRT_FLD_ENA_449_OG [0x1c1]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2009/06/05	04:10:06.0	XRT_FLD_RESET_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da			
2009/06/05	04:10:08.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/06/05	04:20:00.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/06/05	04:20:02.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/06/05	04:20:04.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/06/05	04:20:06.0	XRT_FLD_RESET_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da			
2009/06/05	04:20:08.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/06/05	04:33:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/06/05	04:35:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2009/06/05	04:40:00.0	XRT_Custom_434_OG [0x1b2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2009/06/05	05:15:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/06/05	05:46:24.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/06/05	05:46:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2009/06/05	05:46:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2009/06/05	05:46:46.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/06/05	05:46:48.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/06/05	05:46:50.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2009/06/05	05:49:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11			
2009/06/05	05:49:30.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/06/05	05:56:30.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00			
2009/06/05	06:14:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/06/05	06:54:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/06/05	06:59:54.0	XRT_CTRL_MANU_448_OG [0x1c0]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/06/05	07:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 2e f9 2e f9			
2009/06/05	07:02:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2009/06/05	07:02:52.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b			
2009/06/05	07:02:54.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/06/05	07:02:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/06/05	07:02:58.0	XRT_ARS_DIS_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2009/06/05	07:03:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/06/05	07:04:54.0	XRT_CTRL_MANU_448_OG [0x1c0]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/06/05	07:05:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 2e f9 d1 07			
2009/06/05	07:07:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2009/06/05	07:07:52.0	XRT_QT_PROG_SET_412_OG [0x19c]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08			
2009/06/05	07:07:54.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/06/05	07:07:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/06/05	07:07:58.0	XRT_ARS_DIS_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2009/06/05	07:08:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/06/05	07:09:54.0	XRT_CTRL_MANU_448_OG [0x1c0]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/06/05	07:10:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 d1 07 d1 07			
2009/06/05	07:12:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2009/06/05	07:12:52.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e			

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2009/06/05	07:12:54.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/05	07:12:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/05	07:12:58.0	XRT_ARS_DIS_446_OG [0x1be]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/05	07:13:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/05	07:14:54.0	XRT_CTRL_MANU_448_OG [0x1c0]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/05	07:15:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 d1 07 2e f9				
2009/06/05	07:17:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/06/05	07:17:52.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2009/06/05	07:17:54.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/05	07:17:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/05	07:17:58.0	XRT_ARS_DIS_446_OG [0x1be]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/05	07:18:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/05	07:24:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/05	07:24:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2009/06/05	07:25:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2009/06/05	07:25:16.0	XRT_QT_PROG_SET_413_OG [0x19d]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2009/06/05	07:25:18.0	XRT_AEC_RESET_415_OG [0x19f]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2009/06/05	07:25:20.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/05	07:25:22.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/05	07:25:24.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/05	07:28:02.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/05	07:54:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/05	08:32:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/05	09:34:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/05	10:11:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/05	11:19:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/05	11:19:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2009/06/05	11:20:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2009/06/05	11:20:16.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2009/06/05	11:20:18.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/05	11:20:20.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/05	11:20:22.0	XRT_FLRCTRL_DIS_429_OG [0x1ad]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/05	11:51:00.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/05	12:59:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/05	13:28:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/05	14:37:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/05	15:12:30.0	XRT_Custom_430_OG [0x1ae]							
2009/06/05	15:13:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/05	16:16:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/05	16:59:30.0	XRT_Custom_430_OG [0x1ae]							
2009/06/05	17:00:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/05	17:39:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/05	17:39:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/06/05	17:40:16.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/06/05	17:40:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/06/05	17:40:20.0	XRT_ARS_DIS_410_OG [0x19a]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/06/05	17:40:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2009/06/05	17:42:58.0	XRT_QT_PROG_SET_409_OG [0x199]							

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2009/06/05	17:43:00.0	XRT_CTRL_AUTO_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/06/05	17:49:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/06/05	17:49:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2009/06/05	17:50:16.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09
2009/06/05	17:50:18.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/06/05	17:50:20.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/06/05	17:50:22.0	XRT_FLRCTRL_DIS_427_OG [0x1ab]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/06/05	17:50:30.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01	00 00 00 00
2009/06/05	17:53:30.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/06/05	17:54:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/06/05	18:36:30.5	XRT_Custom_430_OG [0x1ae]					
2009/06/05	18:37:30.5	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/06/05	19:33:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/06/05	20:13:30.0	XRT_Custom_430_OG [0x1ae]					
2009/06/05	20:14:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/06/05	21:11:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/06/05	21:50:30.0	XRT_Custom_430_OG [0x1ae]					
2009/06/05	21:51:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/06/05	22:50:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/06/05	23:25:30.0	XRT_Custom_430_OG [0x1ae]					
2009/06/05	23:26:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/06/06	00:28:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/06/06	00:57:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/06/06	02:05:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/06/06	02:36:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/06/06	03:40:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/06/06	04:14:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/06/06	05:11:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/06/06	05:53:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/06/06	05:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/06/06	05:59:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2009/06/06	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00
2009/06/06	06:00:16.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/06/06	06:00:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/06/06	06:00:20.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/06/06	06:02:58.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2009/06/06	06:03:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/06/06	06:09:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/06/06	06:09:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2009/06/06	06:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01	00 00 00 00
2009/06/06	06:10:16.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04
2009/06/06	06:10:18.0	XRT_AEC_RESET_415_OG [0x19f]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2009/06/06	06:10:20.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/06/06	06:10:22.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/06/06	06:10:24.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/06/06	06:13:02.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/06/06	06:52:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/06/06	07:30:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	04	00 00 00 00

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2009/06/06	07:31:30.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1 07-F0 c0
2009/06/06	08:32:00.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1 07-F0 c1
2009/06/06	09:10:00.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1 07-F0 c0
2009/06/06	10:12:30.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1 07-F0 c1
2009/06/06	11:18:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5 02-76 00 00 00 00 00