

XRT Timeline to be uploaded on 2009/06/30

Period: 2009/06/30 10:48:00 - 2009/07/04 11:37:00

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Normal mode

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XOB #16C5: AR monitor - 3FOV core_Q85 around_outre_Q92 - draft													
Term		Pointing (x, y)					Comment						
06/30 11:03:08 - 06/30 18:02:54	Track (-247.4, 376.8)	⑥ 06/30 10:58:00					# OP start + 10min; Track the boundary of bright and dark region						
06/30 18:16:02 - 07/01 05:33:54	Track (-191.6, 375.6)	⑥ 06/30 18:13:00					# Cont.						
07/01 05:47:02 - 07/01 10:29:54	Track (-101.4, 374.0)	⑥ 07/01 05:44:00					# Cont.						
07/01 18:37:02 - 07/02 05:55:24	Track (0.5, 372.8)	⑥ 07/01 18:34:00					# Track Bright and Dark region						
PROG= 03 1-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 81 1-time(s) 4.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	768x768 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	1024x1024 (1024, 1024)	Q=98	0	0	2.0sec
└─ Subr= 2 30-time(s) 2.0sec													
└─ Seqn= 70 2-time(s) 60.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	707ms	Obs	1x1	384x384 (1024, 1024)	Q=85	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=85	3	0	2.0sec
└─ Seqn= 79 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	768x768 (1024, 1024)	Q=92	2	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	768x768 (1024, 1024)	Q=92	2	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	768x768 (1024, 1024)	Q=92	2	0	2.0sec
└─ Seqn= 32 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	1024x1024 (1024, 1024)	Q=92	2	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	177ms	Obs	4x4	1024x1024 (1024, 1024)	Q=92	2	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #166D: Synoptic Q95 2x2 - Al/poly(181/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Ti-poly(256/4096) + G-band(16)													
Term		Pointing (x, y)					Comment						
06/30 18:06:00 - 06/30 18:12:54	Fixed (0.0, 0.0)						synoptic, shifted 3.0 min						
07/01 18:27:00 - 07/01 18:33:54	Fixed (0.0, 0.0)						synoptic, shifted 24.0 min						
PROG= 11 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 14 1-time(s) 4.0sec													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 7 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 21 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 92 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16BA: Synoptic Q95 2x2 - Al/mesh(256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Ti-poly(181/2048) + G-band(16)													
Term		Pointing (x, y)					Comment						
07/01 05:37:00 - 07/01 05:43:54	Fixed (0.0, 0.0)						synoptic, shifted -26.0 min						
07/02 05:58:30 - 07/02 06:05:24	Fixed (0.0, 0.0)						synoptic, shifted -4.5 min						
PROG= 01 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 18 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 7 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 68 1-time(s) 4.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 92 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16A4: Synoptic 9 Filter- 2x2 Q98 Shorter exp												
Term		Pointing (x, y)					Comment					
07/01 10:33:00 - 07/01 13:59:54	Track (-13.8, 0.1)	⑥ 07/01 10:30:00					EIS sensitivity monitoring					
PROG= 09 1-time(s)												
└─ Subr= 1 1-time(s) 180.0sec												

Seqn= 38 1-time(s) 25.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 5 1-time(s) 25.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 59 1-time(s) 25.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 2 1-time(s) 25.0sec												
C-poly/Open	C-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
C-poly/Open	C-poly/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 77 1-time(s) 25.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 67 1-time(s) 4.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 360.0sec												
Seqn= 20 1-time(s) 4.0sec												
med-Al/Open	med-Al/Open	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 45 1-time(s) 4.0sec												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 50 1-time(s) 4.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 92 1-time(s) 4.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16AC: G-Band Alignment with North Pole Q90 2x2(G-band only) - 5min cadence - Partial Sun-wNGT												
Term			Pointing (x, y)					Comment				
07/01 14:03:00 - 07/01 15:59:54			Fixed (0.0, 945.0)					Co-alignment program (N)				
PROG= 13 1-time(s)												
└ Subr= 1 1-time(s) 360.0sec												
└ Seqn= 10 24-time(s) 300.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

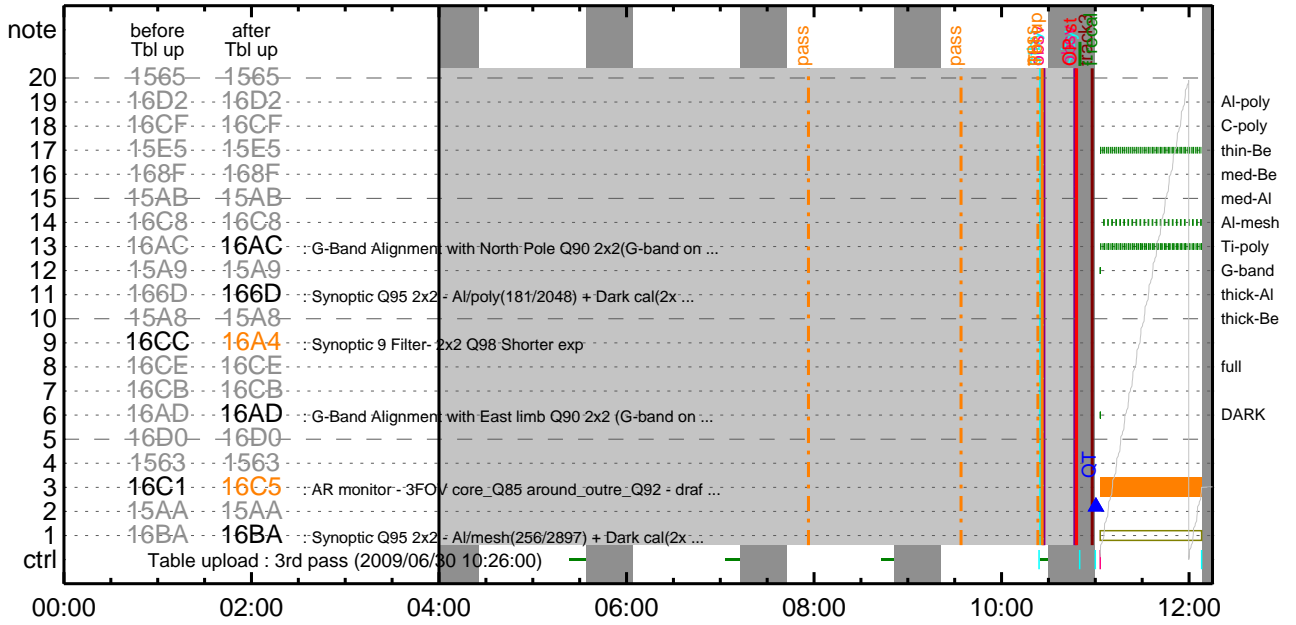
XOB #16AD: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8 min cadence-wNGT												
Term			Pointing (x, y)					Comment				
07/01 16:32:30 - 07/01 18:23:54			Fixed (-945.0, 0.0)					Co-alignment program (E)				
PROG= 06 1-time(s)												
└ Subr= 1 1-time(s) 360.0sec												
└ Seqn= 13 15-time(s) 480.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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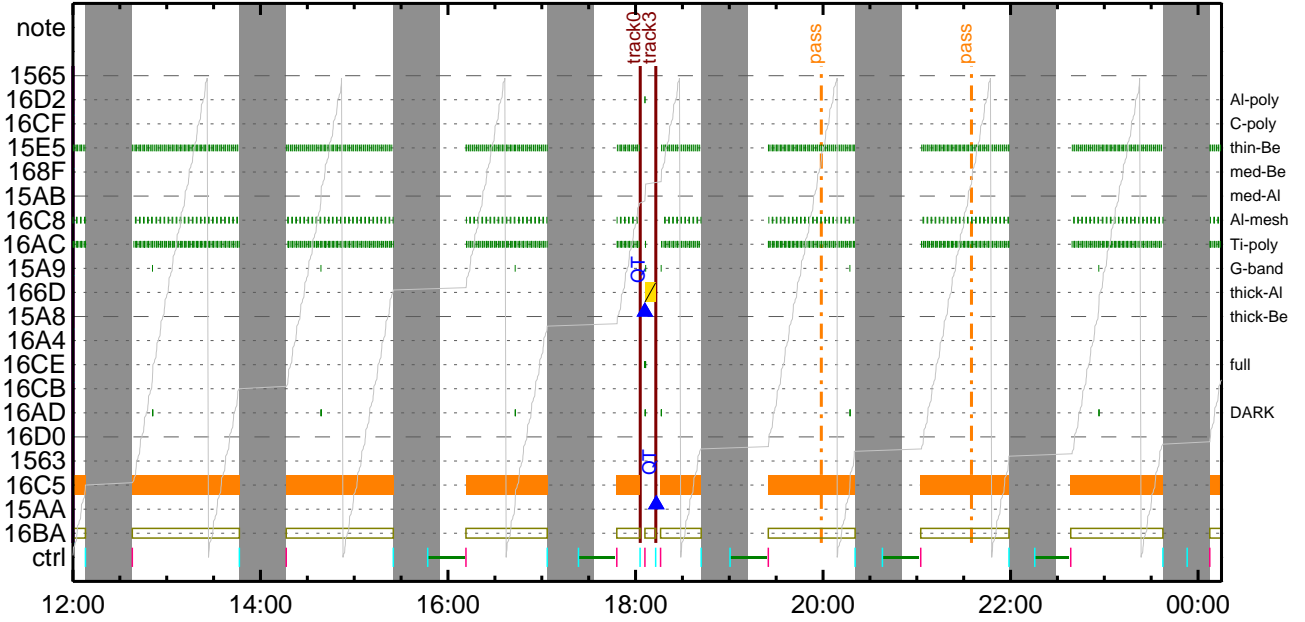
* * * * *
                                Flare mode                                * * * * *
                                NOT USED
* * * * *
                                Active Region Search                        * * * * *
                                NOT USED
* * * * *
                                Flare Detection                            * * * * *
                                NOT USED

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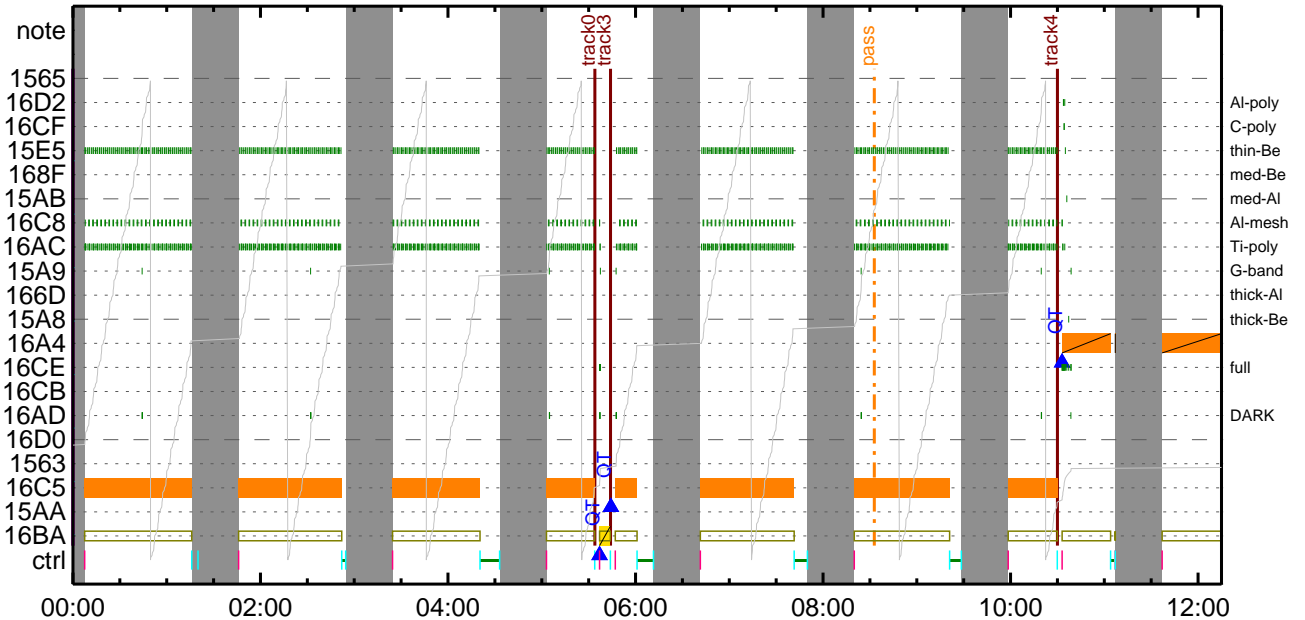
CMDI #0641 2009/06/30



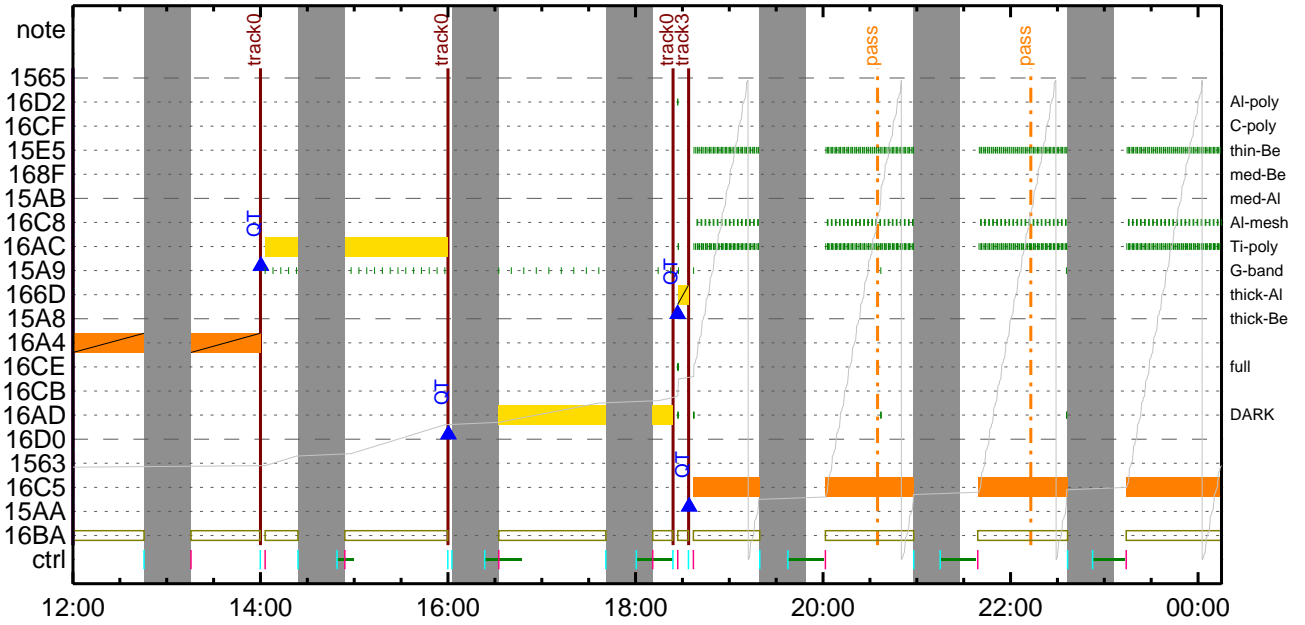
CMDI #0641 2009/06/30



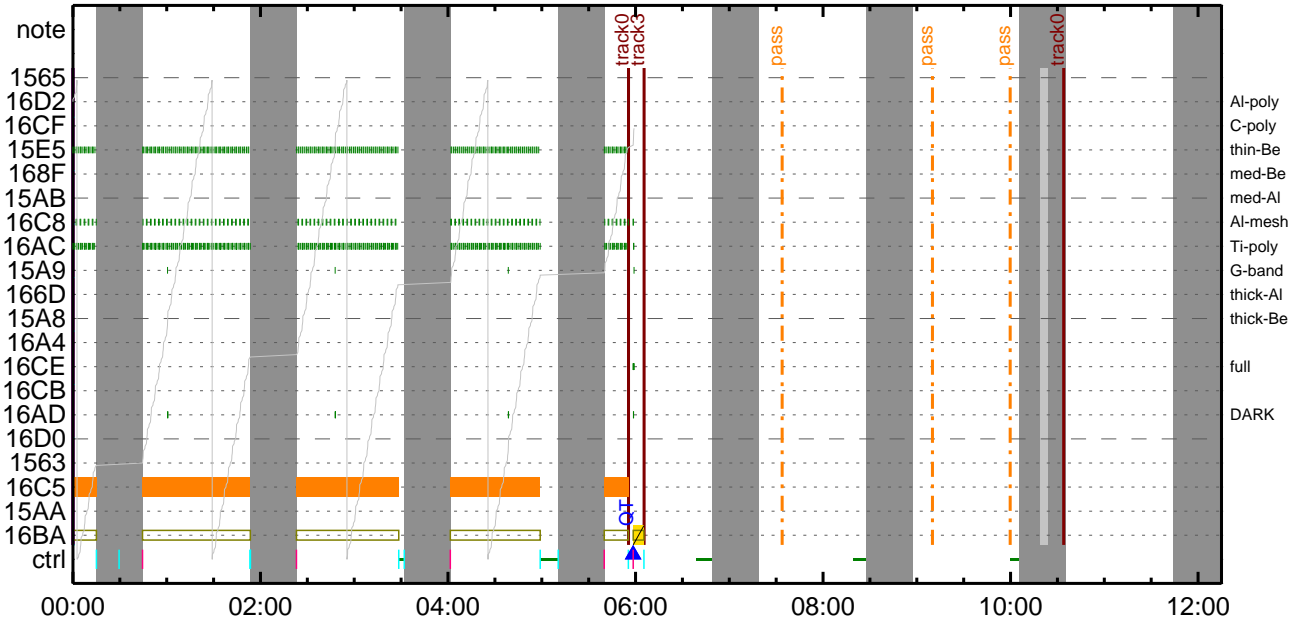
CMDI #0641 2009/07/01



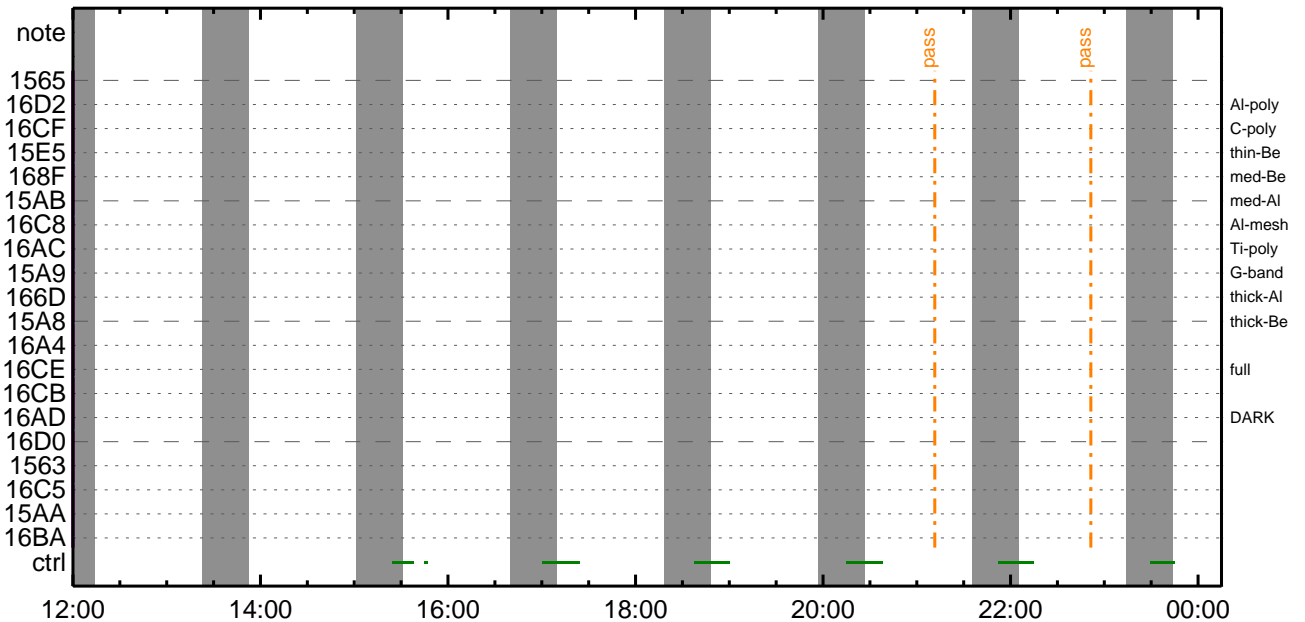
CMDI #0641 2009/07/01



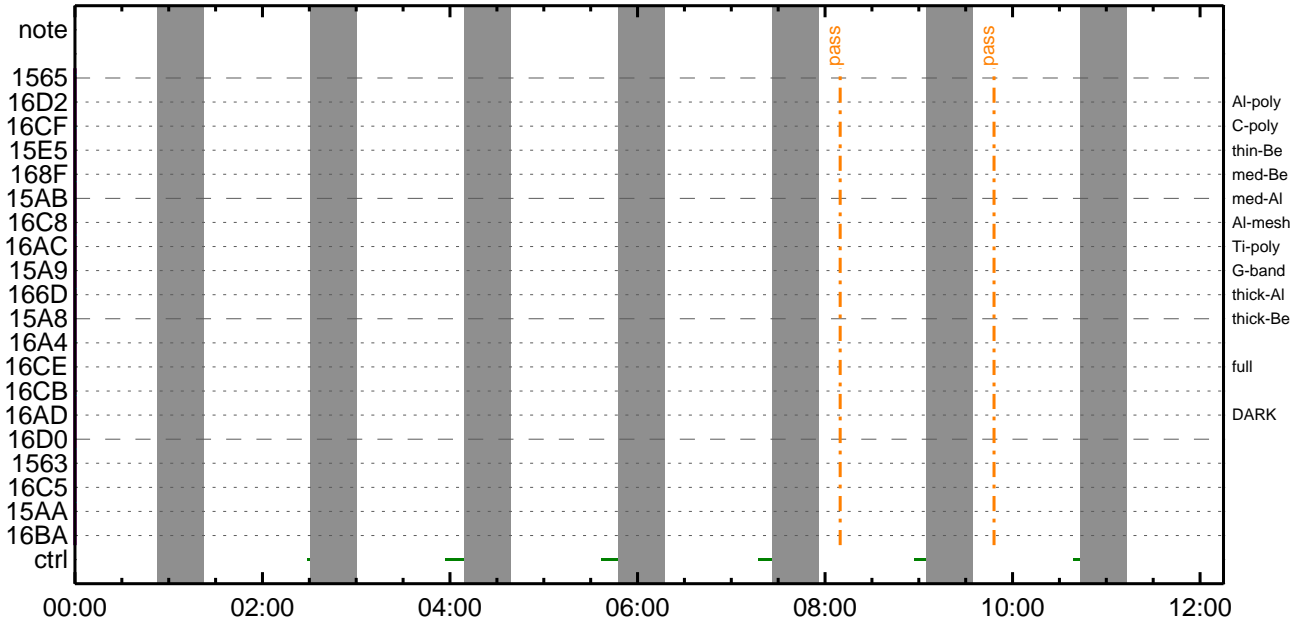
CMDI #0641 2009/07/02



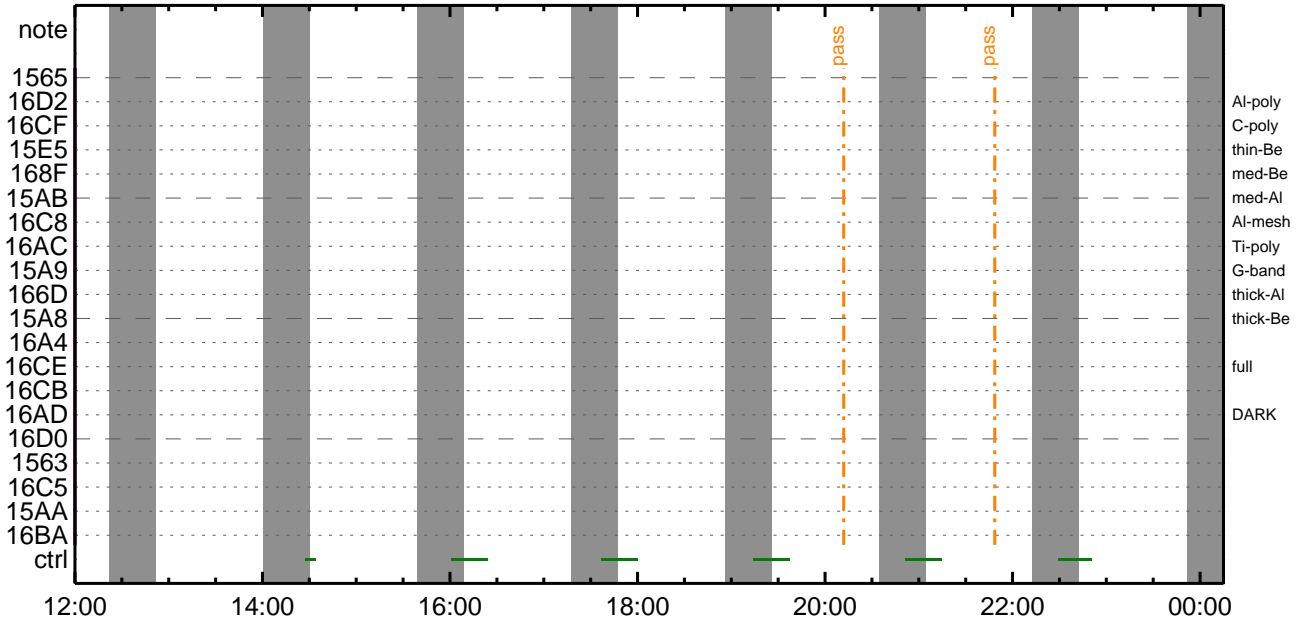
CMDI #0641 2009/07/02



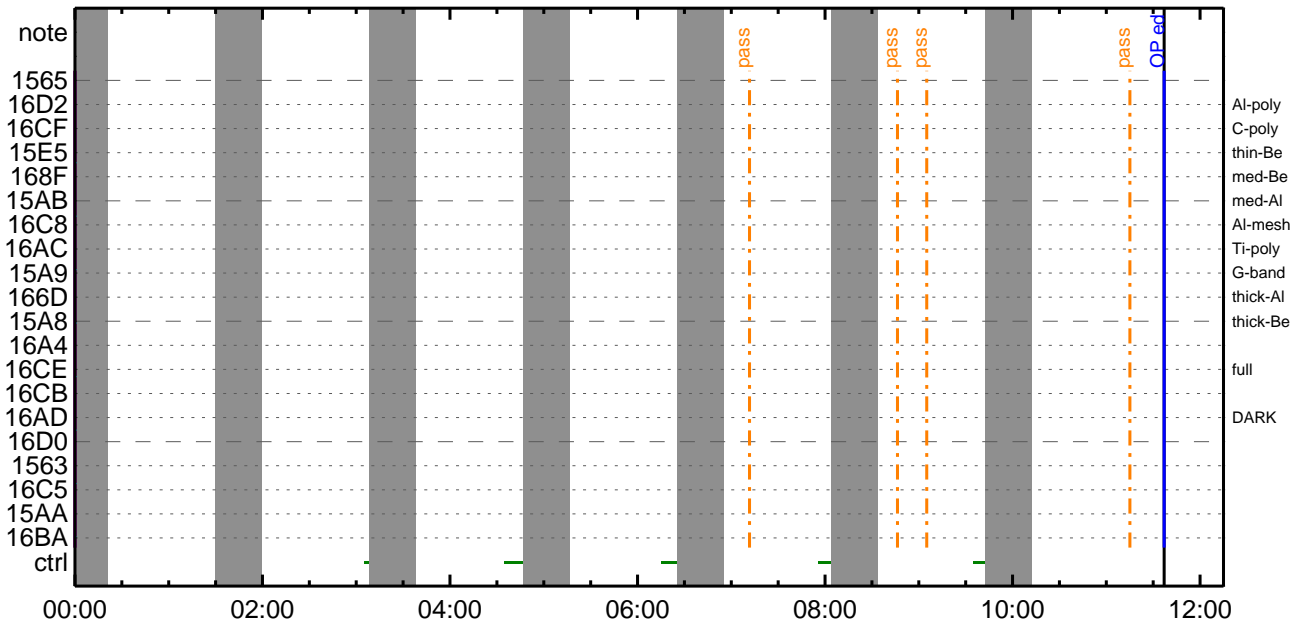
CMDI #0641 2009/07/03



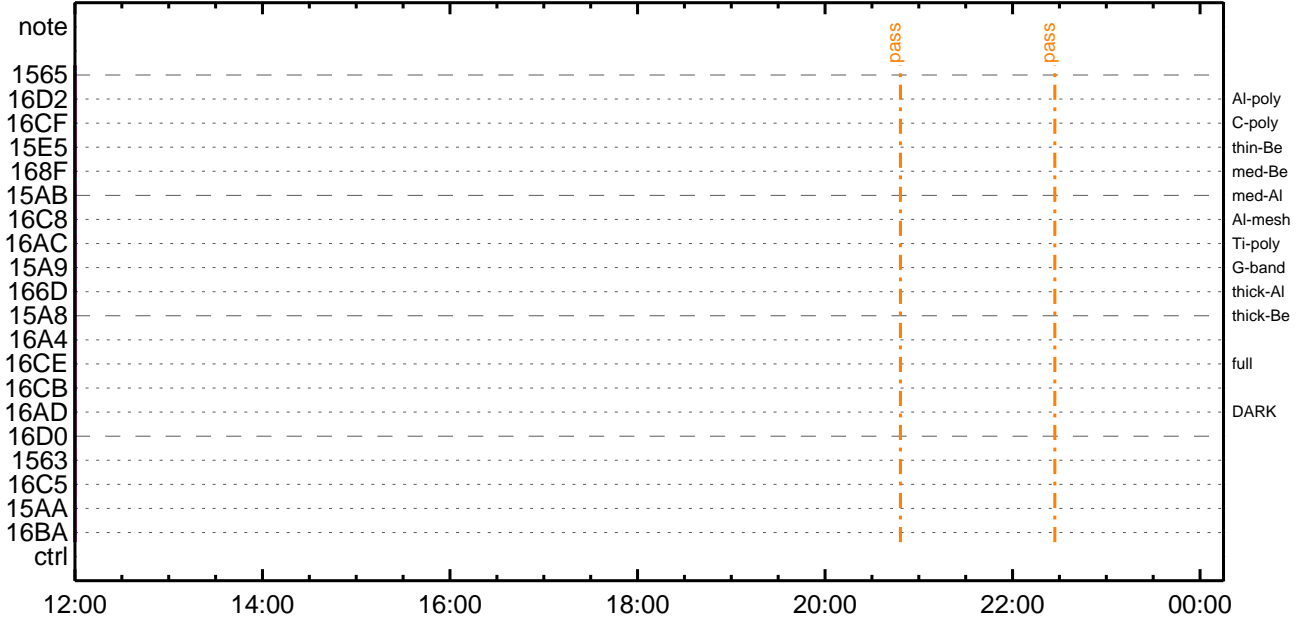
CMDI #0641 2009/07/03



CMDI #0641 2009/07/04



CMDI #0641 2009/07/04




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0096 C.
0097 C.
0098 . C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 . C. ;ãOP/OGY1;4YE;ã
0103 . S. OP op-689:OP
0104 ( )
0105 . S. OG og-689:OG
0106 ( )
0107 C.
0108 . C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;s 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 . C. YAYOXx½ª î»ð³ ÎÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 . C. RAM ID=NMOGñî½Ë¹ç•è² ÎOKð³ ÎÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;s 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 . C. YAYOXx½ª î»ð³ ÎÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 . C. RAM ID=NMOGñî½Ë¹ç•è² ÎOKð³ ÎÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;s 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 . C. YAYOXx½ª î»ð³ ÎÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 . C. RAM ID=NMOG, RAM ID=OPñî½Ë¹ç•è² ÎOKð³ ÎÇ§
0165 C.
0166 . C. ***** òË²¼ð Î½Ã´¶Á°òËË¬ò°Ã÷¿® (¼åµ-YAYOXx½ª ê½çððÃÔÃæðÇ¼ª°¬ðè¼i¹çðçðâ) *****
0167 C. DHUYâ;4YE;Ê½Y½, Yî;4YE;Ëððîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 . C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE ;§ OPOG UPLOADñ-Á÷¿®NGñî½Ë¹ç;ç°Ë²¼ðîTI-CMDÁ÷¿®ñî½Ë¹Ôñ•ñËññ³ðË;f
0180 C. ðËñ¿;çSETñEDUMPNñ±°iñY¹ñç¹Ôñ|ñ³ðË;f
0181 C.
0182 . C. TIY³Y½YOXËðððÃî¿¿(UT)
0183 +. TI 2009-06-30 10:43:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2009-06-30 10:43:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2009-06-30 10:43:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2009-06-30 10:47:59.5
0196 DC 01-B2 DHU_OP_START
0197 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0198 C.
0199 C. °Ê²¼αİÄē%İİñαİŷÄŷ§ŷÄŷ⁻¹âİŪ
0200 C. çç[HK1_TI_CMD_ENA/DIS] EQ ENA
0201 C. çç[HK1_TI_CMD_NUM] EQ 4
0202 C. çç[HK1_NEXT_EXEC_PIM] EQ DHU
0203 C. çç[HK1_NEXT_EXEC_DC] EQ 0xB3
0204 C.
0205 . C. *****
0206 C. Tİİİ°ēŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C. çç[HK1_DMP_TOP_ADRS_1] EQ 07
0213 C. çç[HK1_DMP_TOP_ADRS_0] EQ 2B
0214 C. çç[HK1_DMP_BLOCK_NUM] EQ 3
0215 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0216 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C. çç[HK1_PKT_FORM_NO] EQ 7
0220 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0221 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0222 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0223 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0224 C.
0225 . C. ŷÄŷÖŷ×½ªİ»αð³İÇ§
0226 C. çç[HK1_DMP_CHK_FLG] EQ NON
0227 C.
0228 . C. RAM ID=TI_TBLαİŷÈ¹ç•ē²İOKαð³İÇ§
0229 C.
0230 . C. DHUŷâ;¼ŷÈ;È¼ŷ½. ŷİ;¼ŷÈ;Èαðİâα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C. çç[HK1_PKT_FORM_NO] EQ 2
0234 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0235 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0236 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0237 C.
0238 C.
0239 C. ***** XRT START *****
0240 C. Execute, after the success of OP upload.
0241 +. TI 2009-06-30 10:47:00.0
0242 DC 07-F0 MDP_XRT_MODE_STBY
0243 BC (c3)
0244 . C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0245 C.
0246 C. ***** XRT END *****
0247 . C. Stop EIS observation and temporarily disable EIS mode changes
0248 C.
0249 C.
0250 C. ***** Start EIS operation (TI set) *****
0251 C. Execute, after the success of OP upload.
0252 C. Set EIS TI-commands
0253 +. TI 2009-06-30 10:47:30.0
0254 DC 07-FC EIS_MODE_MANU
0255 BC (21 02)
0256 +. TI 2009-06-30 10:47:40.0
0257 DC 07-FC EIS_MODE_CHG_DIS
0258 BC (22)
0259 . C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0260 C. ***** End EIS operation (TI set) *****
0261 C.
0262 C.
0263 C. *****
0264 C. SOT TI command set
0265 C. *****
0266 C. Execute, after the success of OP upload.
0267 +. TI 2009-06-30 10:47:16.0
0268 DC 07-F0 MDP_SOT_MODE_STBY
0269 BC (41)
0270 . C. -----
0271 C. HK1_TI_CMD_NUM = 1 CNTUP []
0272 C. -----
0273 C. ***** SOT END *****
0274 C.
0275 . C. ***** MDP `úÃİαİ»ö¼ŷαÈÄα¹αēDCBC•x²è *****
0276 C. (¼ª°İŷÖŷÄŷÈŷŷŷÄŷçŷèè¼αα¼Ä»Ūα¹αè)
0277 . S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 . C. ***** ŷĐŷ¹•İ Daily±çİñαÈ'Øα¹αēDCBC•x²è *****
0282 . S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 . C. ;ãLOŷÄŷ§ŷÄŷ⁻¼Ä»Ū;ã
0287 C.
0288 . C. ***** LOS *****
0289 C.

(a) Spacecraft Operation Procedure (real-commands)

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main-690 2009-06-30 13:51:27 175 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÅYŞYÅY-¼Å»Û;ã
0005 C.
0006 C. YÀYŞ;¼Y³YÞYÓYÉÀ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C.   Āī;Ēǝ¿ǝĀǝ•μ°Ē»Ī×ĀÇǝĪYÇYĀY×Yİ;¼YŞÉ;ĒĒĒ%μ•ĪĪĒ;ĒǝĒ%Çǝǝ•ǝ¿¼Ī¹ÇǝĪ;ÇĀ®, ùǝ¹ǝĒǝǝÇĀ+¿®ǝ•ǝĒǝǝǝĒ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C.   XĀ+¿μ;ON
0016 C.   *****
0017 C.   °ĒĀ,Ī×ĒYǝǝĀǝǝǝǝĪ»P´ǝǝǝĪĪ,ǝ•;ÇĒĪ×ǝĒĒXĀĪONǝĪ¹ǝĒǝĪǝĒǝǝǝĒ;f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C.                       ÇÇ[HK1_XPA_ON/OFF]                    EQ        ON
0025 C.                       ÇÇ[HK1_XPA_PWR_HI/LO]                 EQ        HI
0026 C.                       ÇÇ[HK1_XMOD_ON/OFF]                   EQ        ON
0027 C.                       ÇÇ[HK1_XMOD_QPSK/PM]                    EQ        QPSK
0028 C.
0029 . C. XYDYÓYÉYİYÅY-¾Ōǝ-ǝĀĀĒǝ•ǝ¿ǝĒ;Ç°Ē²¼ǝĪ°ĒĀ,¼Ē%Çǝǝǝ¼Ā¹ǝǝǝĒ;f
0030 C.
0031 . C. *****
0032 C.   DR PT1 ĀĪ¼Īi°ĒĀ,
0033 C.   *****
0034 C.   °ĒĒ RESTART;ĒPT1;Ēǝ•ǝ¿ǝǝ¼Ī¹ÇǝĪ;Ç°Ē²¼ǝĪ°ĒĀ»ǝ•;ÇDCBC-150ǝǝ¿ĒĒǝĒ;f
0035 C.
0036 . C. ;ãPT1°ĒĀ,³«»Ī;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C.                       ÇÇ[HK1_REP_PT_1/2]                    EQ        PT1 (¼Ā¹ǝ,¼Ū)
0043 C.                       ÇÇ[HK1_REP_STA/STP]                   EQ        START (¼Ā¹ǝ,¼Ū)
0044 C.                       ÇÇ[HK1_X_VC4_ON/OFF]                  EQ        ON (¼Ā¹ǝ,¼Ū)
0045 C.
0046 . C. ;ãYÇYÓYÉYŞYĒĀŪŌ;ĒĀ•Ā°²ŌĒǝ;Ē,ãǝĪ°ĒĀ,°Ē³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C.                       ÇÇ[HK1_REP_PT_1/2]                    EQ        PT1 (¼Ā¹ǝ,¼Ū)
0050 C.                       ÇÇ[HK1_REP_STA/STP]                   EQ        START (¼Ā¹ǝ,¼Ū)
0051 C.                       ÇÇ[HK1_X_VC4_ON/OFF]                  EQ        ON (¼Ā¹ǝ,¼Ū)
0052 C.
0053 C.
0054 . C. PT1°ĒĀ,ǝ-¼«Ē°ĀĀ»ǝǝǝǝǝ,ã;Ç°Ē²¼ǝǝ¼Ā¹ǝǝǝĒ;f
0055 C.   YÇYÓYÉYŞYĒĀŪŌǝĀĀ•Ā°²ŌĒǝ-ŪĪǝǝ¼Ī¹ÇǝĪ´ǝĪ»ǝ¹ǝĒǝǝǝÇĀŌǝĀ;f
0056 C.
0057 . C. *****
0058 C.   DR PT2 ĀĪ¼Īi°ĒĀ,
0059 C.   *****
0060 C.   °ĒĒ RESTART;ĒPT2;Ēǝ•ǝ¿ǝǝ¼Ī¹ÇǝĪ;Ç°Ē²¼ǝĪ°ĒĀ»ǝ•;ÇDCBC-151ǝǝ¿ĒĒǝĒ;f
0061 C.
0062 . C. ;ãPT2°ĒĀ,³«»Ī;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C.                       ÇÇ[HK1_REP_PT_1/2]                    EQ        PT2 (¼Ā¹ǝ,¼Ū)
0069 C.                       ÇÇ[HK1_REP_STA/STP]                   EQ        START (¼Ā¹ǝ,¼Ū)
0070 C.                       ÇÇ[HK1_X_VC4_ON/OFF]                  EQ        ON (¼Ā¹ǝ,¼Ū)
0071 C.
0072 . C. ;ãYÇYÓYÉYŞYĒĀŪŌ;ĒĀ•Ā°²ŌĒǝ;Ē,ãǝĪ°ĒĀ,°Ē³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C.                       ÇÇ[HK1_REP_PT_1/2]                    EQ        PT2 (¼Ā¹ǝ,¼Ū)
0076 C.                       ÇÇ[HK1_REP_STA/STP]                   EQ        START (¼Ā¹ǝ,¼Ū)
0077 C.                       ÇÇ[HK1_X_VC4_ON/OFF]                  EQ        ON (¼Ā¹ǝ,¼Ū)
0078 C.
0079 . C. *****
0080 C.   DR°ĒĀ,ĀĀ»Ş;ÇXĀ+¿μ;OFF
0081 C.   *****
0082 C.
0083 . C. ;ãDR°ĒĀ,ĀĀ»Ş;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C.                       ÇÇ[HK1_REP_STA/STP]                    EQ        STOP
0087 C.                       ÇÇ[HK1_S_VC4_ON/OFF]                  EQ        OFF
0088 C.                       ÇÇ[HK1_X_VC4_ON/OFF]                  EQ        OFF
0089 C.
0090 C.   ;ãXĀ+¿μ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C.                       ÇÇ[HK1_XMOD_ON/OFF]                    EQ        OFF
0095 C.                       ÇÇ[HK1_XPA_ON/OFF]                     EQ        OFF

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(a) Spacecraft Operation Procedure (real-commands)

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main-691 2009-06-30 13:51:27 127 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÃYŞYÃY-¼Ã»Û;ã
0005 C.
0006 C. YÃYB;¼Y³YFÏYÉÃ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āí;È¿µ°È»Í×ÁÇŃÍYÇYÃY×YÍ;¼YÉ;ÈÈµ•íÉ;ÈÈ¼°ÇÓ••¿¼í¹ÇŃÍ;çÀ®, ù¹ñÈÈÈçÁ+¿®••ÈÈÈÈ³È;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Upload DPL table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 C. YÇYÃY×YÍ;¼YÉŃĀ°ñESTS_CHKñðOFFñÈñ¹ñē
0025 C.
0026 . S. RAM ram-271:MDP_DPL
0027 ( )
0028 C.
0029 . C. < Dump RAMID=MDP_DPL >
0030 +. DC 07-F0 MDP_DUMP_FGTBL
0031 BC (82 07 00 38 b8 00 40)
0032 C. -----
0033 C. MDP_DPL verify = OK [ ]
0034 C. -----
0035 C.
0036 C. STS_CHKñðONñÈñ¹ñē
0037 C.
0038 . C. < Update MDP DSC PAR1 >
0039 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0040 BC (4c)
0041 C. MDP_CMD_CODE = F04C0700[ ]
0042 C. MDP_CMD_CNT (count-up 1) [ ]
0043 C. -----
0044 C.
0045 C.
0046 C. *****
0047 C. SOT TI command set
0048 C. *****
0049 C. Execute, after the success of TBL upload.
0050 +. TI 2009-06-30 10:47:18.0
0051 DC 07-F0 MDP_SOT_MODE_OBSV
0052 BC (40)
0053 . C. -----
0054 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0055 C. -----
0056 C.
0057 C.
0058 C. ***** XRT START *****
0059 C.
0060 +. DC 07-F0 MDP_XRT_CTRL_MANU
0061 BC (c1)
0062 + DC 07-F0 MDP_XRT_MODE_STBY
0063 BC (c3)
0064 . C. ----- Success Verify ? OK / NG_____
0065 C.
0066 C. XRT Obs. Table Upload
0067 . S. RAM ram-291:MDP_OBS_X
0068 ( )
0069 C.
0070 +. DC 07-F0 MDP_DUMP_XRTTBL
0071 BC (84 07 00 00 00 3a d4)
0072 . C. ----- Comparison Check ? OK / ERR _____
0073 C.
0074 C.
0075 +. DC 07-F0 MDP_XRT_ROI_SET
0076 BC (cd 01 b1 b1 04 04)
0077 + DC 07-F0 MDP_XRT_ROI_SET
0078 BC (cd 02 b1 b1 08 08)
0079 + DC 07-F0 MDP_XRT_ROI_SET
0080 BC (cd 03 b1 b1 08 08)
0081 + DC 07-F0 MDP_XRT_ROI_SET
0082 BC (cd 04 b1 b1 06 06)
0083 + DC 07-F0 MDP_XRT_ROI_SET
0084 BC (cd 06 80 80 06 06)
0085 + DC 07-F0 MDP_XRT_ROI_SET
0086 BC (cd 07 80 80 0c 0c)
0087 + DC 07-F0 MDP_XRT_ROI_SET
0088 BC (cd 08 80 80 10 10)
0089 + DC 07-F0 MDP_XRT_ROI_SET
0090 BC (cd 09 80 80 20 20)
0091 + DC 07-F0 MDP_XRT_ROI_SET
0092 BC (cd 0a 80 60 20 18)
0093 + DC 07-F0 MDP_XRT_ROI_SET
0094 BC (cd 0b a0 80 18 20)
0095 + DC 07-F0 MDP_XRT_ROI_SET
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0096 BC (cd 0f 80 80 04 04)
0097 + DC 07-F0 MDP_XRT_ROI_SET
0098 BC (cd 10 80 80 10 10)
0099 . C. ----- Success Verify ? OK / NG ____
0100 C.
0101 C.
0102 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0103 C.
0104 +. DC 07-F0 MDP_XRT_MODE_OBSV
0105 BC (c2)
0106 +. TI 2009-06-30 10:47:02.0
0107 DC 07-F0 MDP_XRT_MODE_OBSV
0108 BC (c2)
0109 . C. ----- Success Verify ? OK / NG ____
0110 C.
0111 C. ***** XRT END *****
0112 C.
0113 . C. ***** MDP 'úÃîâî»ö¼ýðÊÂð¹æDCBC•x²è *****
0114 C. (%ã°îÿÓÿÄÿÈÿÞÿËÿÁÿçÿèæ%¼æ%Ä»Û¹æè)
0115 . S. DC-BC dcbc-402:DCBC
0116 (MDP_known_event)
0117 C.
0118 C.
0119 . C. ***** ÿÐÿ¹•Ï Daily±¿ÎÑæË'Ø¹æèDCBC•x²è *****
0120 . S. DC-BC dcbc-153:DCBC
0121 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0122 C.
0123 C.
0124 . C. ;ãLOSÿÁÿ$ÿÄÿÿ-¼Ä»Û;ä
0125 C.
0126 . C. ***** LOS *****
0127 C.
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*** OP Sequence for XRT ***

2009/06/30	10:50:00.0	XRT_CTRL_MANU_418_OG [0x1a2]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/06/30	10:50:10.0	XRT_FOCUS_RECALIBRATE_402_OG [0x192]			
		XRT_FOCUS_RECAL	2	07-F8	78 00
2009/06/30	10:54:10.0	XRT_FOCUS_POSITION_427_OG [0x1ab]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/06/30	10:58:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2009/06/30	11:00:00.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/06/30	11:00:02.0	XRT_FOCUS_POSITION_441_OG [0x1b9]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/06/30	11:00:22.0	XRT_QT_PROG_SET_443_OG [0x1bb]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2009/06/30	11:00:24.0	XRT_AEC_RESET_415_OG [0x19f]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2009/06/30	11:00:26.0	XRT_ARS_DIS_422_OG [0x1a6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/06/30	11:00:28.0	XRT_FLD_DIS_445_OG [0x1bd]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/06/30	11:00:30.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/06/30	11:03:08.0	XRT_CTRL_AUTO_403_OG [0x193]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/06/30	12:08:00.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/06/30	12:38:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/06/30	13:46:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/06/30	14:16:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/06/30	15:25:00.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/06/30	15:47:00.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/06/30	16:10:30.0	XRT_Custom_430_OG [0x1ae]			
2009/06/30	16:11:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/06/30	17:03:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/06/30	17:23:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/06/30	17:47:00.0	XRT_Custom_430_OG [0x1ae]			
2009/06/30	17:48:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/06/30	18:02:54.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/06/30	18:02:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2009/06/30	18:03:00.0	AOCS_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2009/06/30	18:03:16.0	XRT_FLD_DIS_419_OG [0x1a3]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/06/30	18:03:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/06/30	18:03:20.0	XRT_ARS_DIS_410_OG [0x19a]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/06/30	18:05:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2009/06/30	18:06:00.0	XRT_CTRL_AUTO_440_OG [0x1b8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/06/30	18:12:54.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/06/30	18:12:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/06/30	18:13:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2009/06/30	18:13:16.0	XRT_QT_PROG_SET_443_OG [0x1bb]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2009/06/30	18:13:18.0	XRT_AEC_RESET_415_OG [0x19f]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2009/06/30	18:13:20.0	XRT_ARS_DIS_422_OG [0x1a6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/06/30	18:13:22.0	XRT_FLD_DIS_445_OG [0x1bd]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/06/30	18:13:24.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/06/30	18:16:02.0	XRT_CTRL_AUTO_403_OG [0x193]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/06/30	18:42:00.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/06/30	19:00:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/06/30	19:24:00.0	XRT_Custom_430_OG [0x1ae]			
2009/06/30	19:25:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/06/30	20:20:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/06/30	20:38:00.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1

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2009/06/30	21:01:30.0	XRT_Custom_430_OG [0x1ae]							
2009/06/30	21:02:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/30	21:59:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/30	22:15:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/30	22:37:30.0	XRT_Custom_430_OG [0x1ae]							
2009/06/30	22:38:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/06/30	23:37:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/06/30	23:53:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	00:07:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/01	01:16:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	01:20:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	01:46:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/01	02:52:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	02:54:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	03:24:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/01	04:20:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	04:33:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	05:03:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/01	05:33:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	05:33:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/07/01	05:34:00.0	AOCs_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2009/07/01	05:34:16.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/07/01	05:34:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/07/01	05:34:20.0	XRT_ARS_DIS_410_OG [0x19a]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/07/01	05:36:58.0	XRT_QT_PROG_SET_400_OG [0x190]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2009/07/01	05:37:00.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/01	05:43:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	05:43:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2009/07/01	05:44:00.0	AOCs_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2009/07/01	05:44:16.0	XRT_QT_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2009/07/01	05:44:18.0	XRT_AEC_RESET_415_OG [0x19f]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2009/07/01	05:44:20.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/07/01	05:44:22.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/07/01	05:44:24.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/07/01	05:47:02.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/01	06:01:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	06:11:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	06:41:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/01	07:41:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	07:50:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	08:20:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/01	09:21:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	09:28:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	09:58:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/01	10:29:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	10:29:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/07/01	10:30:00.0	AOCs_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2009/07/01	10:30:16.0	XRT_FLD_DIS_419_OG [0x1a3]							

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2009/07/01	10:30:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLD_DIS	1	07-F0	d9				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/07/01	10:30:20.0	XRT_ARS_DIS_410_OG [0x19a]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/07/01	10:32:58.0	XRT_QT_PROG_SET_407_OG [0x197]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	09			
2009/07/01	10:33:00.0	XRT_CTRL_AUTO_403_OG [0x193]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/01	11:04:00.0	XRT_CTRL_MANU_435_OG [0x1b3]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	11:06:00.0	XRT_Custom_430_OG [0x1ae]								
2009/07/01	11:07:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/01	11:07:00.5	XRT_CTRL_MANU_435_OG [0x1b3]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	11:37:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/01	12:45:30.0	XRT_CTRL_MANU_435_OG [0x1b3]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	13:15:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/01	13:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	13:59:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]								
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2009/07/01	14:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]								
			AOCU_NM	5	02-76	00	ac	00	00	00
2009/07/01	14:00:16.0	XRT_QT_PROG_SET_405_OG [0x195]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2009/07/01	14:00:18.0	XRT_ARS_DIS_422_OG [0x1a6]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/07/01	14:00:20.0	XRT_FLD_DIS_445_OG [0x1bd]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/07/01	14:00:22.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/07/01	14:03:00.0	XRT_CTRL_AUTO_403_OG [0x193]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/01	14:24:00.0	XRT_CTRL_MANU_435_OG [0x1b3]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	14:49:00.0	XRT_CTRL_MANU_435_OG [0x1b3]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	14:54:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/01	15:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	15:59:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]								
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2009/07/01	16:00:00.0	AOCS_ORe-point_Start_5_OG [0x09b]								
			AOCU_NM	5	02-76	00	00	00	54	00
2009/07/01	16:00:16.0	XRT_QT_PROG_SET_431_OG [0x1af]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2009/07/01	16:00:18.0	XRT_ARS_DIS_422_OG [0x1a6]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/07/01	16:00:20.0	XRT_FLD_DIS_445_OG [0x1bd]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/07/01	16:00:22.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/07/01	16:02:30.0	XRT_CTRL_MANU_435_OG [0x1b3]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	16:23:30.0	XRT_CTRL_MANU_435_OG [0x1b3]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	16:32:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/01	17:41:00.0	XRT_CTRL_MANU_435_OG [0x1b3]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	18:00:30.0	XRT_CTRL_MANU_435_OG [0x1b3]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	18:11:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/01	18:23:54.0	XRT_CTRL_MANU_428_OG [0x1ac]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	18:23:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]								
			XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2009/07/01	18:24:00.5	AOCS_ORe-point_Start_2_OG [0x098]								
			AOCU_NM	5	02-76	00	00	00	00	00
2009/07/01	18:24:16.0	XRT_FLD_DIS_419_OG [0x1a3]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/07/01	18:24:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/07/01	18:24:20.0	XRT_ARS_DIS_410_OG [0x19a]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/07/01	18:26:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2009/07/01	18:27:00.0	XRT_CTRL_AUTO_440_OG [0x1b8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/01	18:33:54.0	XRT_CTRL_MANU_428_OG [0x1ac]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/01	18:33:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]								
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2009/07/01	18:34:00.0	AOCS_ORe-point_Start_1_OG [0x097]								
			AOCU_NM	5	02-76	03	00	00	00	00
2009/07/01	18:34:16.0	XRT_QT_PROG_SET_443_OG [0x1bb]								

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2009/07/01	18:34:18.0	XRT_AEC_RESET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2009/07/01	18:34:20.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/07/01	18:34:22.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/07/01	18:34:24.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/07/01	18:37:02.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/07/01	19:19:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/07/01	19:37:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/07/01	20:00:31.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/07/01	20:01:31.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/07/01	20:58:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/07/01	21:15:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/07/01	21:38:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/07/01	21:39:00.5	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/07/01	22:36:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/07/01	22:52:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/07/01	23:13:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/07/01	23:14:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/07/02	00:15:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/07/02	00:29:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/07/02	00:44:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/07/02	01:53:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/07/02	01:53:30.5	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/07/02	02:23:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/07/02	03:28:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/07/02	03:32:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/07/02	04:01:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/07/02	04:59:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/07/02	05:10:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/07/02	05:40:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/07/02	05:55:24.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/07/02	05:55:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2009/07/02	05:55:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00	00
2009/07/02	05:55:46.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/07/02	05:55:48.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/07/02	05:55:50.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/07/02	05:58:28.0	XRT_QT_PROG_SET_400_OG [0x190]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2009/07/02	05:58:30.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/07/02	06:05:24.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/07/02	06:05:30.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00	00
2009/07/02	10:34:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00	00