

XRT Timeline to be uploaded on 2009/07/04

Period: 2009/07/04 11:37:00 - 2009/07/09 10:08:00

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Normal mode

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XOB #16D3: AR monitor - 1FOV core_9filter - draft													
Term		Pointing (x, y)					Comment						
07/04 11:50:30 - 07/04 12:30:00		Track (-107.6, -488.6) ^{Ⓢ 07/04 11:47:00}					# OP start + 10min, observe AR 11024 in S.						
PROG= 07 Inf.-time(s)													
└─ Subr= 1 15-time(s) 2.0sec													
└─ Seqn= 48 4-time(s) 60.0sec													
┌	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=85	3	0	2.0sec
┌	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	707ms	Obs	1x1	384x384 (1024, 1024)	Q=85	3	0	2.0sec
┌	Al-poly/Open	med-Al/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=85	3	0	2.0sec
┌	C-poly/Open	C-poly/Open	close	Safe	Norm	707ms	Obs	1x1	384x384 (1024, 1024)	Q=85	3	0	2.0sec
┌	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1024, 1024)	Q=85	3	0	2.0sec
┌	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=85	3	0	2.0sec
┌	med-Al/Open	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1024, 1024)	Q=85	3	0	2.0sec
┌	Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs	1x1	384x384 (1024, 1024)	Q=85	3	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16C5: AR monitor - 3FOV core_Q85 around_outre_Q92 - draft													
Term		Pointing (x, y)					Comment						
07/04 12:33:08 - 07/04 17:39:54		Track (-107.6, -488.6) ^{Ⓢ 07/04 11:47:00}					# OP start + 10min, observe AR 11024 in S.						
07/04 17:53:02 - 07/05 05:59:54		Track (-60.7, -489.2) ^{Ⓢ 07/04 17:50:00}					# Cont.						
07/05 06:13:02 - 07/05 17:54:24		Track (35.4, -490.1) ^{Ⓢ 07/05 06:10:00}					# Cont.						
07/05 18:07:32 - 07/06 05:23:54		Track (127.7, -490.3) ^{Ⓢ 07/05 18:04:30}					# Cont., HOP 121 due from 6 July.						
07/06 05:37:02 - 07/06 07:31:30		Track (215.4, -489.9) ^{Ⓢ 07/06 05:34:00}					# Cont.						
PROG= 03 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 81 1-time(s) 4.0sec													
┌	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
┌	Open/Ti-poly	Open/Ti-poly	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
┌	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	768x768 (1024, 1024)	Q=98	0	0	2.0sec
┌	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	1024x1024 (1024, 1024)	Q=98	0	0	2.0sec
└─ Subr= 2 15-time(s) 2.0sec													
└─ Seqn= 70 4-time(s) 60.0sec													
┌	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	707ms	Obs	1x1	384x384 (1024, 1024)	Q=85	3	0	2.0sec
┌	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=85	3	0	2.0sec
└─ Seqn= 79 1-time(s) 4.0sec													
┌	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	768x768 (1024, 1024)	Q=92	2	0	2.0sec
┌	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	2x2	768x768 (1024, 1024)	Q=92	2	0	2.0sec
┌	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	768x768 (1024, 1024)	Q=92	2	0	2.0sec
└─ Seqn= 32 1-time(s) 4.0sec													
┌	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	1024x1024 (1024, 1024)	Q=92	2	0	2.0sec
┌	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	177ms	Obs	4x4	1024x1024 (1024, 1024)	Q=92	2	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #166D: Synoptic Q95 2x2 - Al/poly(181/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Ti-poly(256/4096) + G-band(16)													
Term		Pointing (x, y)					Comment						
07/04 17:43:00 - 07/04 17:49:54		Fixed (0.0, 0.0)					synoptic, shifted -20.0 min						
07/05 17:57:30 - 07/05 18:04:24		Fixed (0.0, 0.0)					synoptic, shifted -5.5 min						
07/06 18:17:00 - 07/07 05:59:54		Fixed (0.0, 0.0)					synoptic, shifted 14.0 min						
PROG= 11 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 14 1-time(s) 4.0sec													
┌	Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
┌	Al-poly/Open	Al-poly/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 7 1-time(s) 2.0sec													
┌	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
┌	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
┌	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 21 1-time(s) 4.0sec													
┌	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
┌	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 92 1-time(s) 2.0sec													
┌	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16BA: Synoptic Q95 2x2 - Al/mesh(256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Ti-poly(181/2048) + G-band(16)												
Term		Pointing (x, y)					Comment					
07/05 06:03:00 - 07/05 06:09:54		Fixed (0.0, 0.0)					synoptic					
07/06 05:27:00 - 07/06 05:33:54		Fixed (0.0, 0.0)					synoptic, shifted -36.0 min					
07/07 06:03:00 - 07/09 10:08:00		Fixed (0.0, 0.0)					synoptic					
PROG= 01 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												

Seqn	1-time(s)	2-time(s)	Filter	Mode	Image	Exp.	CCD	Bin	ROI: size (center)	Q	Comp.	AEC Buffer	Interval
Seqn= 18													
	1-time(s)	4.0sec											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 7													
	1-time(s)	2.0sec											
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 68													
	1-time(s)	4.0sec											
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 92													
	1-time(s)	2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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NOT USED

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Active Region Search

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NOT USED

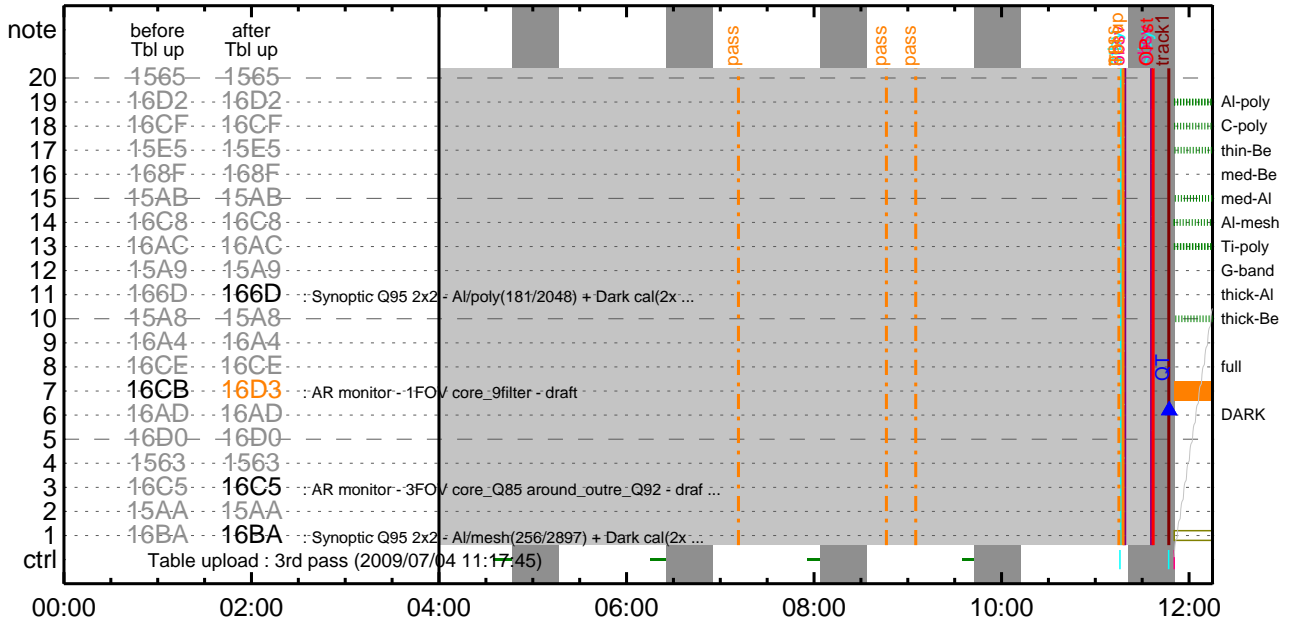
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Flare Detection

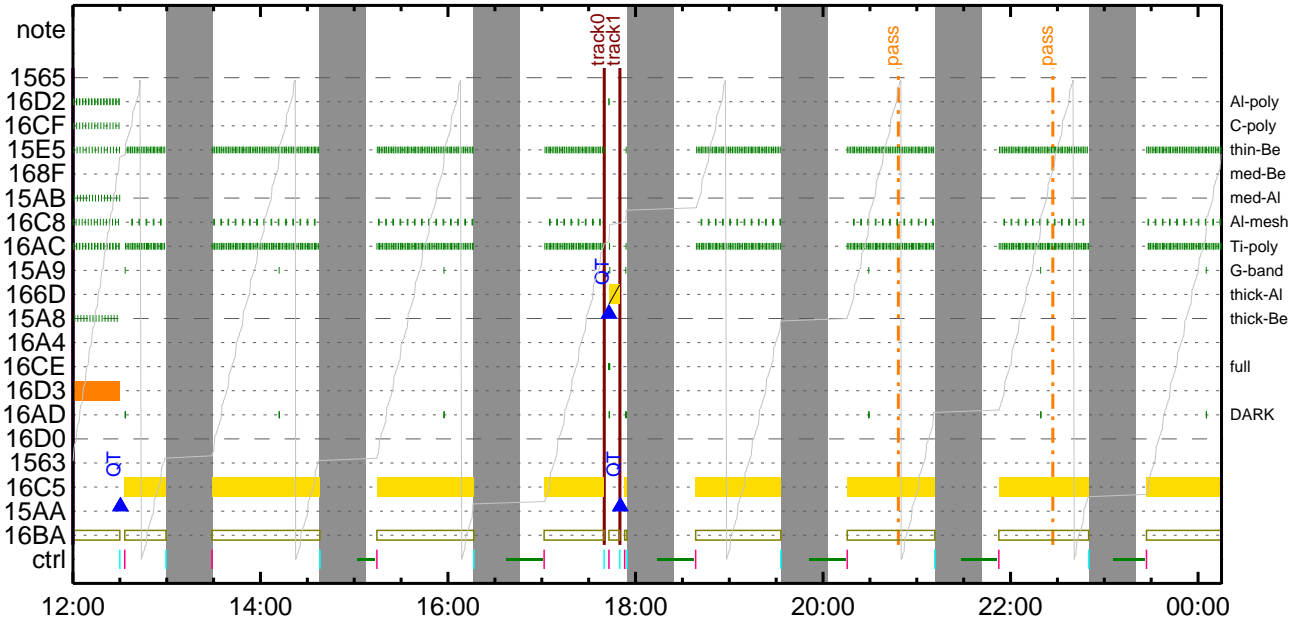
* * * * *

NOT USED

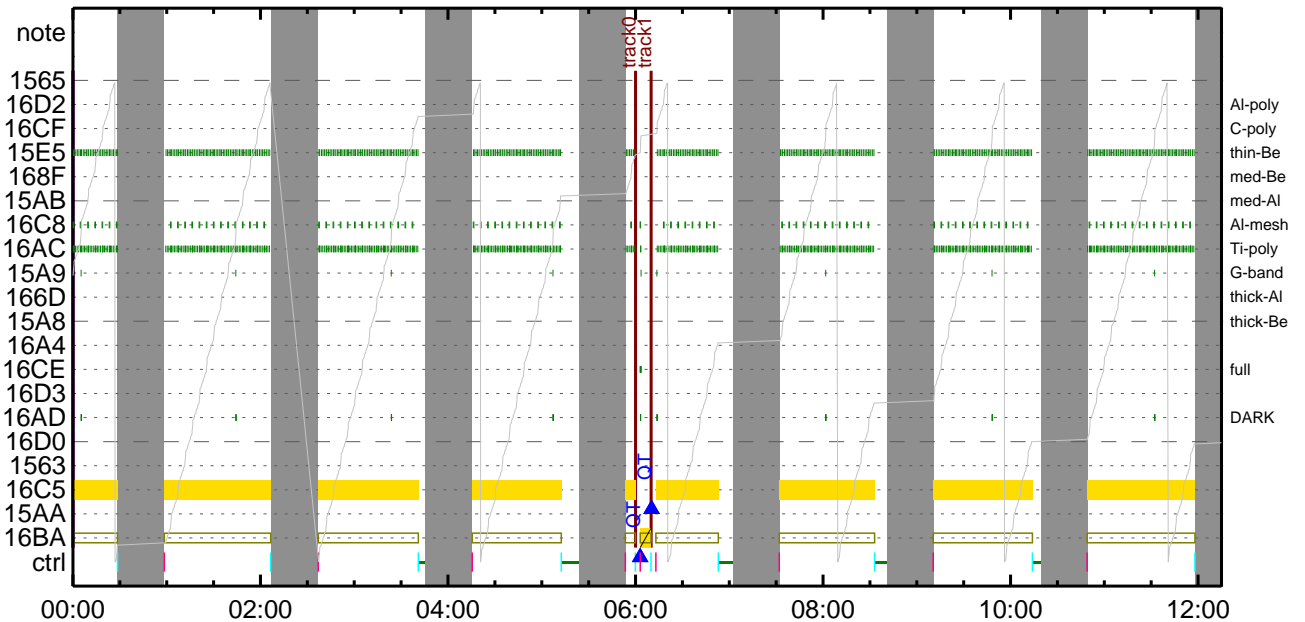
CMDI #0650 2009/07/04



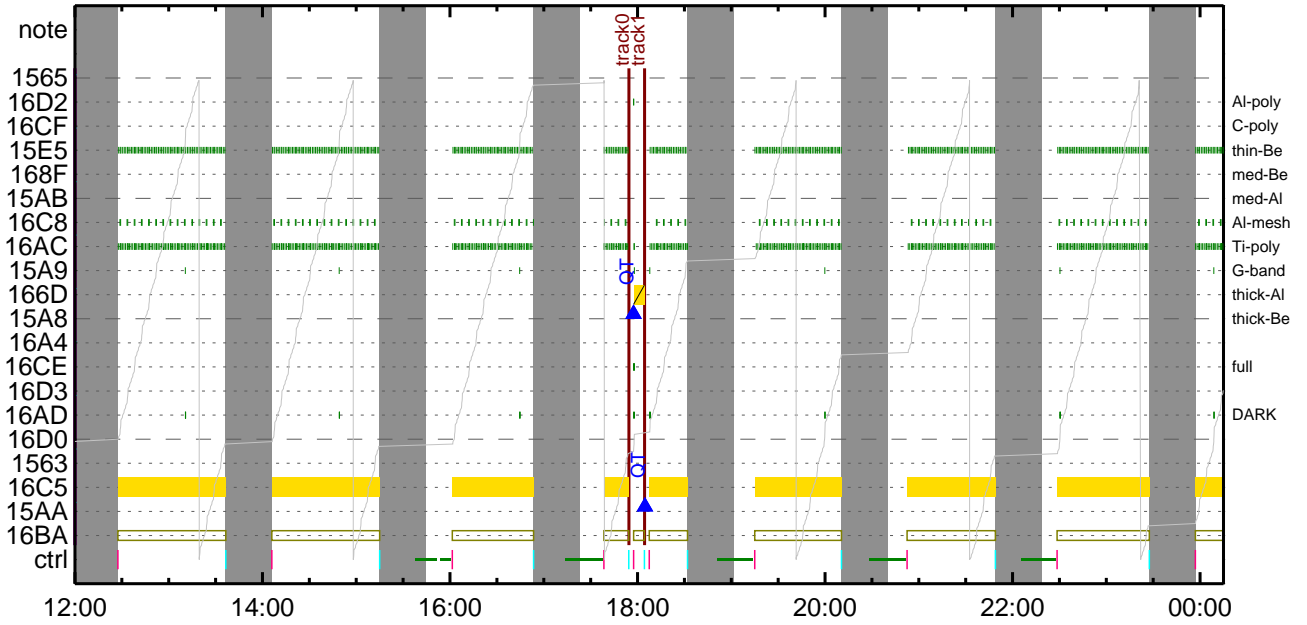
CMDI #0650 2009/07/04



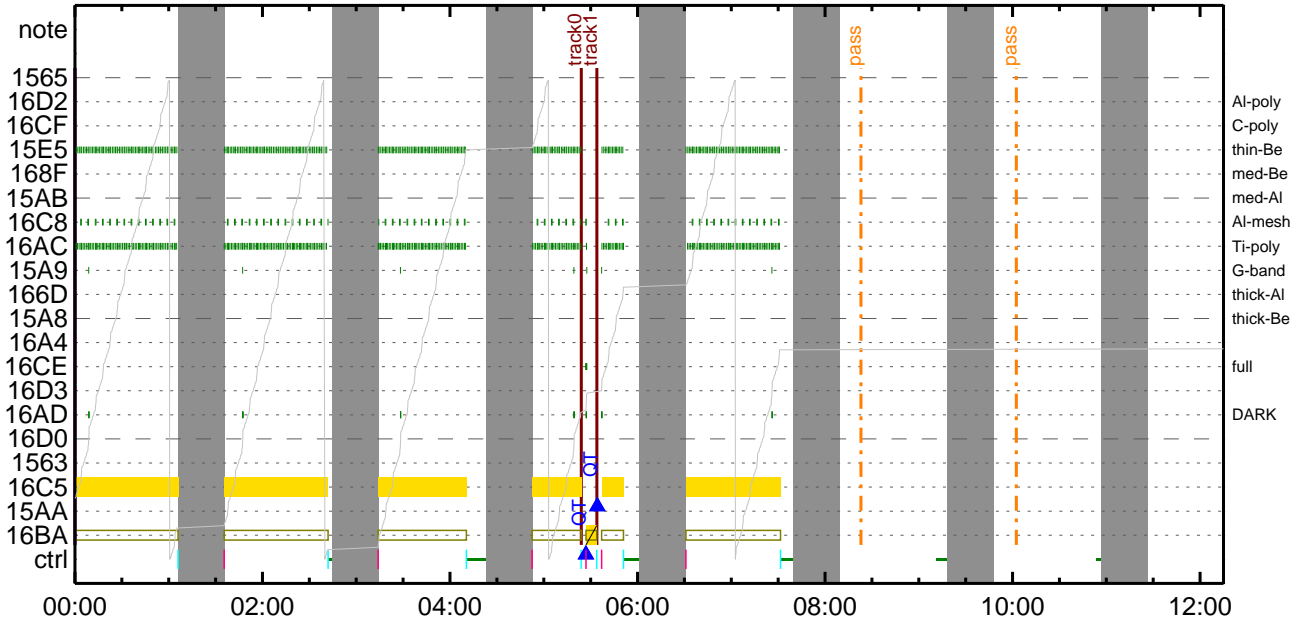
CMDI #0650 2009/07/05



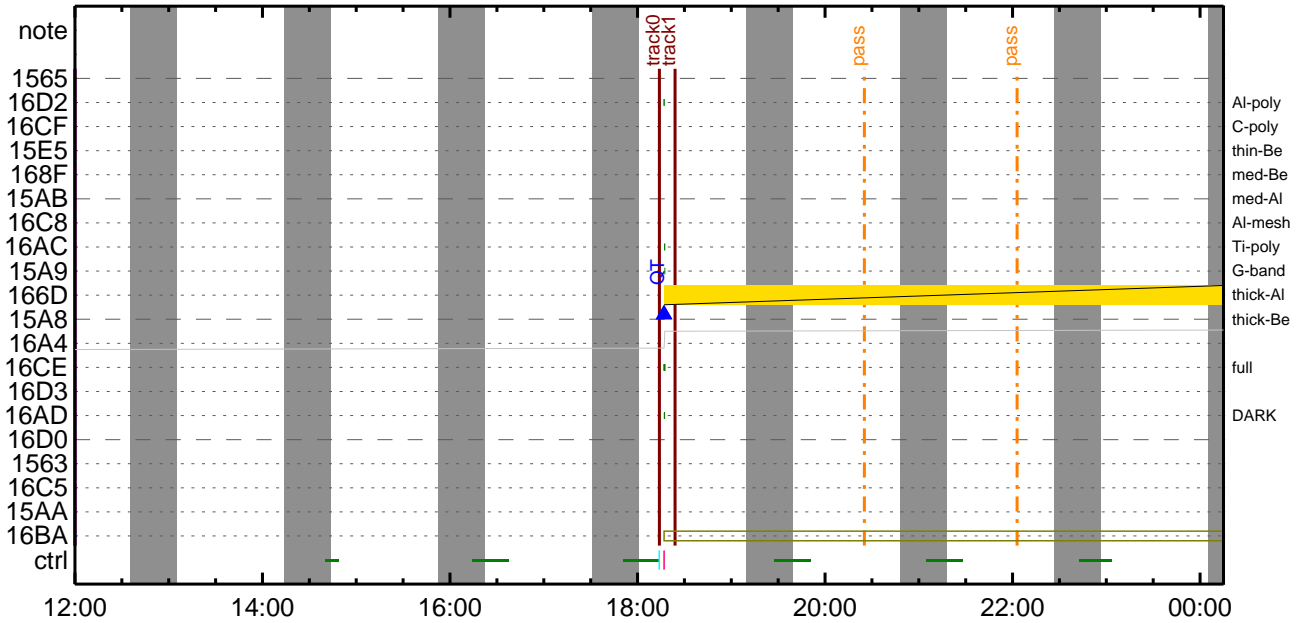
CMDI #0650 2009/07/05



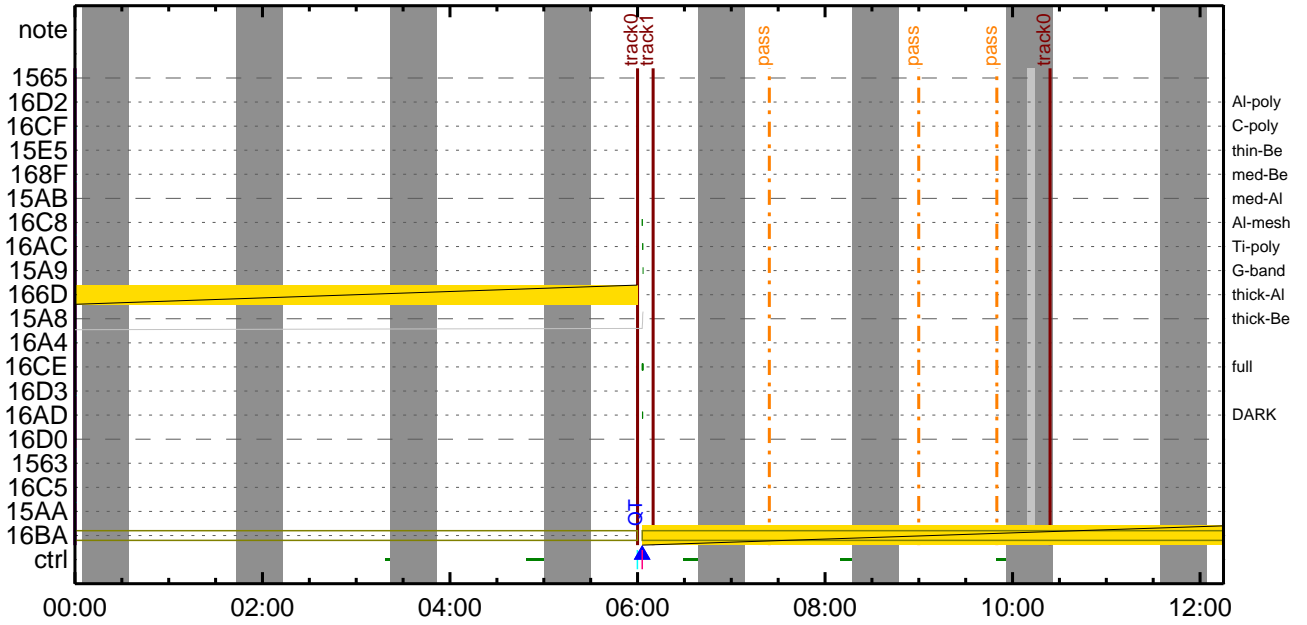
CMDI #0650 2009/07/06



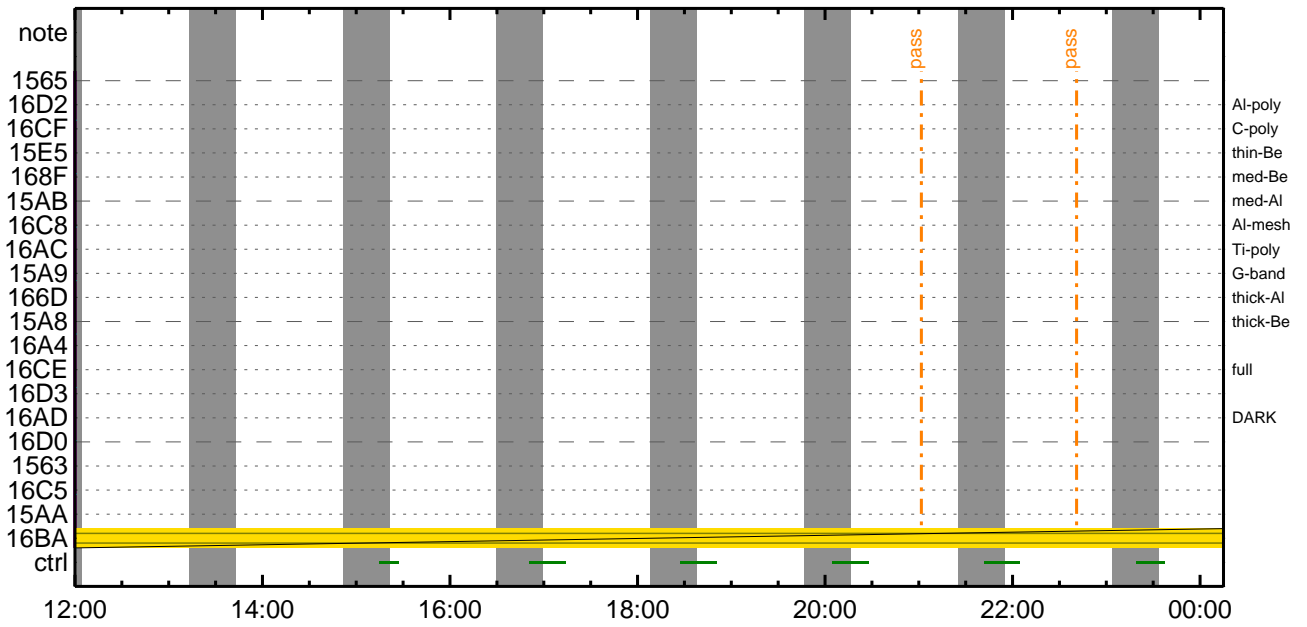
CMDI #0650 2009/07/06



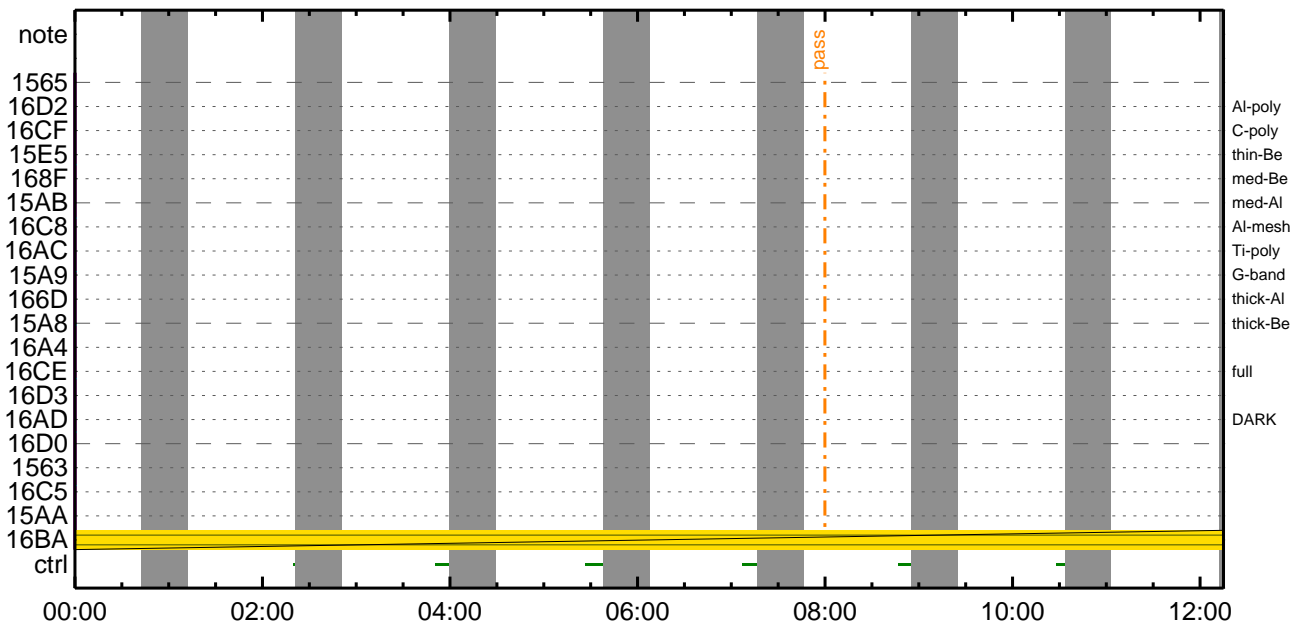
CMDI #0650 2009/07/07



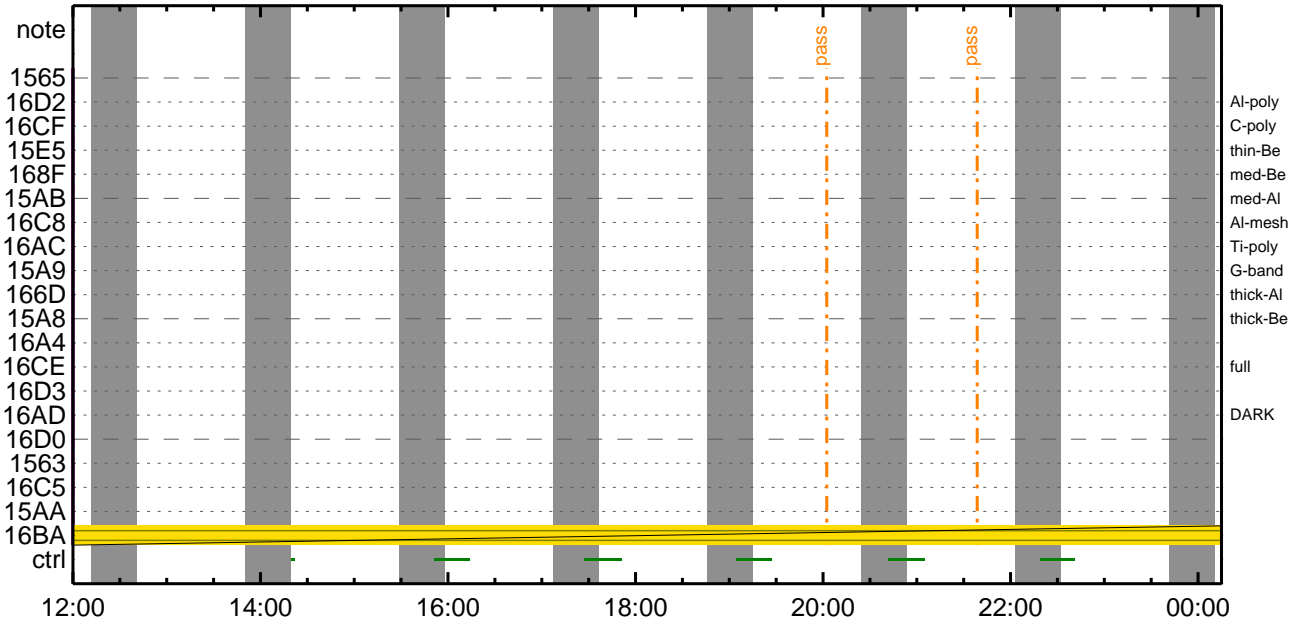
CMDI #0650 2009/07/07



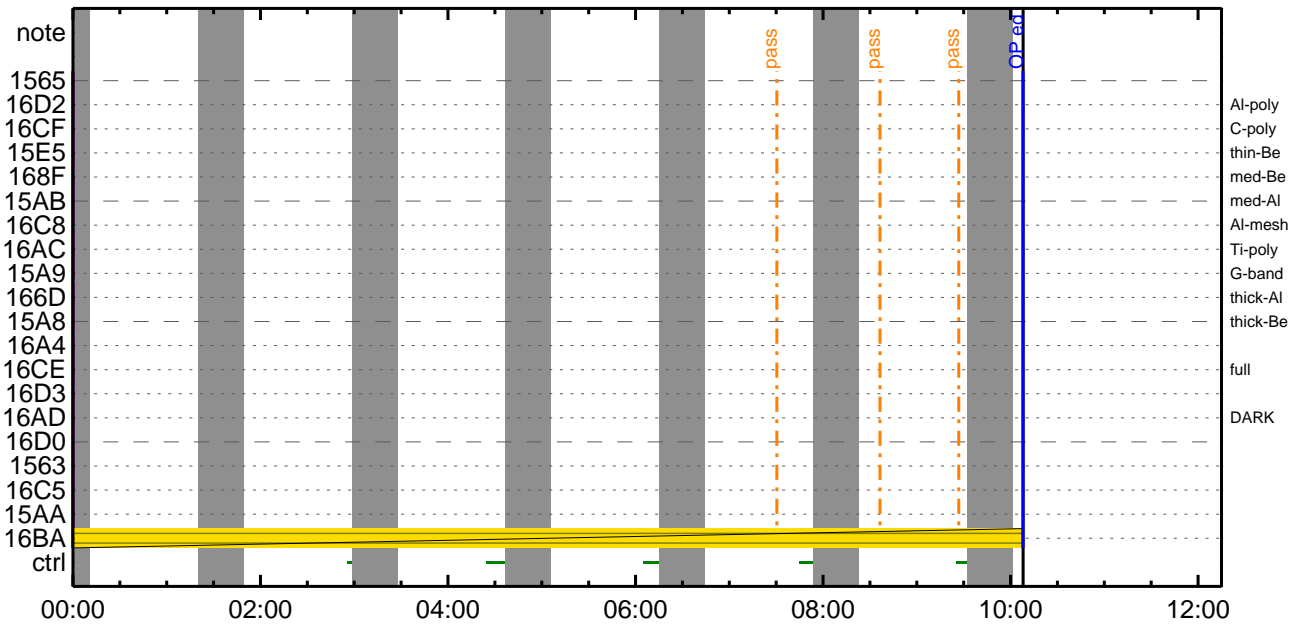
CMDI #0650 2009/07/08



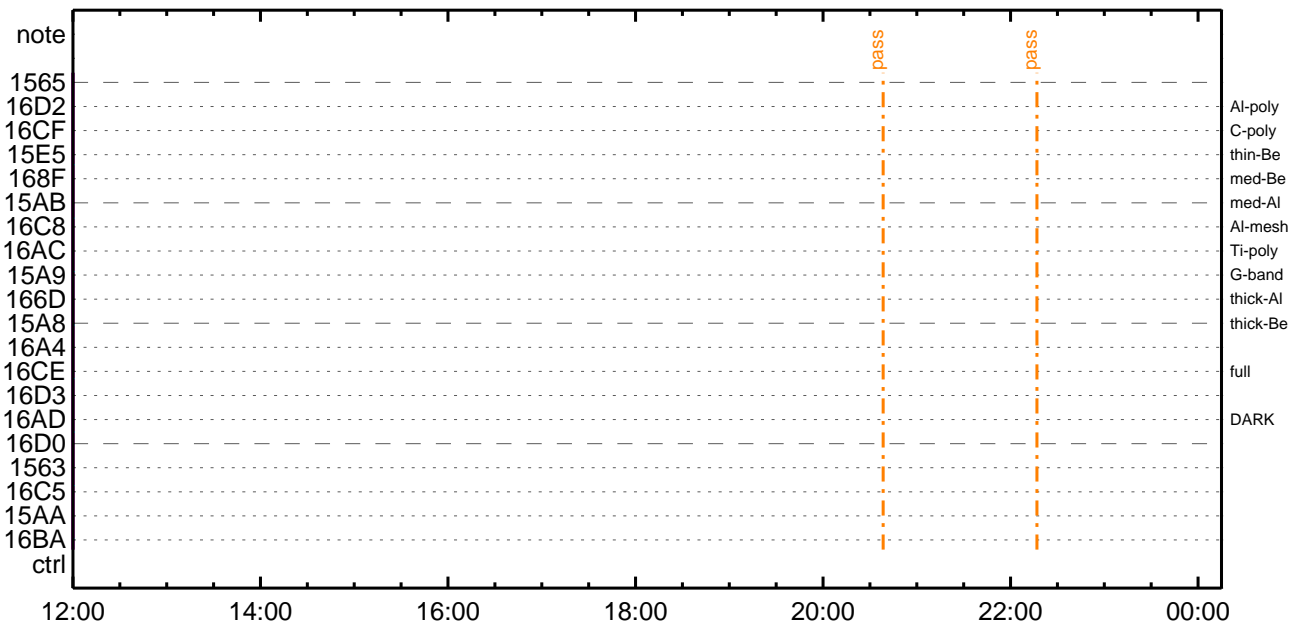
CMDI #0650 2009/07/08



CMDI #0650 2009/07/09



CMDI #0650 2009/07/09




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-698:OP
0104 ( )
0105 S. OG og-698:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¼E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¼E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î¼E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî¼Ã´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOYx½ê¼çòðÁÔÃæç¼ª°¬²è¼î¹çòçðâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î¼î¹ç;ç°E²¼òî¼TI-CMDÁ÷¿@²î¼Á¹Ô²•²E²²²³²E;f
0180 C. ²²²¿;çSET²E²DUMP²î¼E²±²îY²¹²ç¹Ô²|²³²E;f
0181 C.
0182 C. TIY³Y²Y²Y²E²òðÁDî¿(UT)
0183 +. TI 2009-07-04 11:32:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2009-07-04 11:32:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2009-07-04 11:32:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2009-07-04 11:36:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.         çç[HK1_TI_CMD_NUM]                    EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼õİÄë%îíñõîŷÄŷ§ŷÄŷ¹àîü
0200 C.         çç[HK1_TI_CMD_ENA/DIS]                EQ      ENA
0201 C.         çç[HK1_TI_CMD_NUM]                    EQ      4
0202 C.         çç[HK1_NEXT_EXEC_PIM]                 EQ      DHU
0203 C.         çç[HK1_NEXT_EXEC_DC]                  EQ      0xB3
0204 C.
0205 . C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.         çç[HK1_DMP_TOP_ADRS_1]                EQ      07
0213 C.         çç[HK1_DMP_TOP_ADRS_0]                EQ      2B
0214 C.         çç[HK1_DMP_BLOCK_NUM]                 EQ      3
0215 C.         çç[HK1_DMP_REPEAT_NUM]               EQ      0
0216 C.         çç[HK1_DMA_DMP_PIM]                  EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.         çç[HK1_PKT_FORM_NO]                   EQ      7
0220 C.         çç[HK1_PKT_GEN_TIME]                   EQ      0.25 s
0221 C.         çç[HK1_S_TLM_BIT_RATE]                EQ      32k
0222 C.         çç[HK1_X_TLM_BIT_RATE]                EQ      4M
0223 C.         çç[HK1_DMP_CHK_FLG]                   EQ      EXEC
0224 C.
0225 . C. ŷÄŷÖŷ×½ªî»õ³îç§
0226 C.         çç[HK1_DMP_CHK_FLG]                   EQ      NON
0227 C.
0228 . C. RAM ID=TI_TBLñî¼È¹ç•è²îOKõ³îç§
0229 C.
0230 . C. DHUŷâ;¼ŷÉ;È¼ŷ¼.ŷî;¼ŷÉ;Èõðİãñ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.         çç[HK1_PKT_FORM_NO]                   EQ      2
0234 C.         çç[HK1_PKT_GEN_TIME]                   EQ      0.5S
0235 C.         çç[HK1_S_TLM_BIT_RATE]                EQ      32K
0236 C.         çç[HK1_X_TLM_BIT_RATE]                EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2009-07-04 11:36:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 . C. -----
0246 C.   HK1_TI_CMD_NUM           = 1 CNTUP [ ]
0247 C.   -----
0248 C. ***** SOT END *****
0249 . C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2009-07-04 11:36:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2009-07-04 11:36:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 . C.         [ ] [HK1_TI_CMD_NUM]                EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2009-07-04 11:36:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 . C.         [ ] [HK1_TI_CMD_NUM]                EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 . C. ***** MDP ´ûÃîñİ»ö¼ŷõÈÄðñ¹ñèDCBC•x²è *****
0276 C. (%ã°îŷÖŷÄŷÈŷŷŷÈŷáŷçŷèèÈ¼õ¼Ä»Ûñ¹è)
0277 . S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 . C. ***** ŷĐŷ¹•İ Daily±çİñõÈ¹õñ¹ñèDCBC•x²è *****
0282 . S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 . C. ;ãLOSŷÄŷ§ŷÄŷ¹¼Ä»Û;ã
0287 C.
0288 . C. ***** LOS *****
0289 C.

```



```

0096  C. *****
0097  C. EIS END OBSTBL LOAD
0098  C. *****
0099  C.
0100  . C. ***** MDP 'uAiIi'»0Y«EAd»eDCBC•x²è *****
0101  C. (%â°iYÔYÃYÈYpYÈYáYçYé»E¼q»¼Å»Û»e)
0102  . S. DC-BC dcbc-402:DCBC
0103  (MDP_known_event)
0104  C.
0105  C.
0106  . C. ***** YDY¹·İ Daily±;İÑ»E'Ø»eDCBC•x²è *****
0107  . S. DC-BC dcbc-153:DCBC
0108  (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0109  C.
0110  C.
0111  . C. ;ãLOSÝÁÝSÝÃÝ-¼Å»Û;ã
0112  C.
0113  . C. ***** LOS *****
0114  C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-700 2009-07-04 14:12:37 80 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿áB•µ°E»ÍxÁÇóÍYçYÁY×Yí;¼YÉ;ÈÈ¿µ•ÍÉ;ÈBÈ¼°ÇÓB•B¿¼Í¹çBÍ;çÀ®, ùB¹BÈBßBçÁ+¿®B•BÈBDB³BÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG ____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 06 80 80 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 07 80 80 0c 0c)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 08 80 80 10 10)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 09 80 80 20 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 0f 80 80 04 04)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 10 80 80 10 10)
0052 . C. ----- Success Verify ? OK / NG ____
0053 C.
0054 C.
0055 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0056 C.
0057 +. DC 07-F0 MDP_XRT_MODE_OBSV
0058 BC (c2)
0059 +. TI 2009-07-04 11:36:02.0
0060 DC 07-F0 MDP_XRT_MODE_OBSV
0061 BC (c2)
0062 . C. ----- Success Verify ? OK / NG ____
0063 C.
0064 C. ***** XRT END *****
0065 C.
0066 . C. ***** MDP `úÁÍBÍ»ò¼YóÈÁÐB¹BÈDCBC•x²è *****
0067 C. (¼á°ÍYÓYÁYÈY¥YÉYÁYçYÈBÈ¾¼B»ÛB¹BÈ)
0068 . S. DC-BC dcbc-402:DCBC
0069 (MDP_known_event)
0070 C.
0071 C.
0072 . C. ***** YDY¹•Í Daily±¿ÍÑBÉ'ØB¹BÈDCBC•x²è *****
0073 . S. DC-BC dcbc-153:DCBC
0074 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0075 C.
0076 C.
0077 . C. ;ãLOSÁY$YÁY-¼Á»Û;ã
0078 C.
0079 . C. ***** LOS *****
0080 C.
```

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*** OP Sequence for XRT ***

2009/07/04	11:46:54.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/07/04	11:46:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/07/04	11:47:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2009/07/04	11:47:16.0	XRT_QT_PROG_SET_408_OG [0x198]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2009/07/04	11:47:18.0	XRT_AEC_RESET_415_OG [0x19f]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2009/07/04	11:47:20.0	XRT_ARS_DIS_422_OG [0x1a6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/07/04	11:47:22.0	XRT_FLD_DIS_445_OG [0x1bd]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/07/04	11:47:24.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/07/04	11:50:30.0	XRT_CTRL_AUTO_403_OG [0x193]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/07/04	12:30:00.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/07/04	12:30:02.0	XRT_FOCUS_POSITION_441_OG [0x1b9]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/07/04	12:30:22.0	XRT_QT_PROG_SET_443_OG [0x1bb]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2009/07/04	12:30:24.0	XRT_AEC_RESET_415_OG [0x19f]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2009/07/04	12:30:26.0	XRT_ARS_DIS_422_OG [0x1a6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/07/04	12:30:28.0	XRT_FLD_DIS_445_OG [0x1bd]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/07/04	12:30:30.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/07/04	12:33:08.0	XRT_CTRL_AUTO_403_OG [0x193]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/07/04	12:59:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/07/04	13:29:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/07/04	14:38:00.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/07/04	15:13:30.0	XRT_Custom_430_OG [0x1ae]			
2009/07/04	15:14:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/07/04	16:16:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/07/04	17:00:30.5	XRT_Custom_430_OG [0x1ae]			
2009/07/04	17:01:30.5	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/07/04	17:39:54.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/07/04	17:39:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2009/07/04	17:40:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2009/07/04	17:40:16.0	XRT_FLD_DIS_419_OG [0x1a3]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/07/04	17:40:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/07/04	17:40:20.0	XRT_ARS_DIS_410_OG [0x19a]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/07/04	17:42:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2009/07/04	17:43:00.0	XRT_CTRL_AUTO_440_OG [0x1b8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/07/04	17:49:54.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/07/04	17:49:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/07/04	17:50:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2009/07/04	17:50:16.0	XRT_QT_PROG_SET_443_OG [0x1bb]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2009/07/04	17:50:18.0	XRT_AEC_RESET_415_OG [0x19f]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2009/07/04	17:50:20.0	XRT_ARS_DIS_422_OG [0x1a6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/07/04	17:50:22.0	XRT_FLD_DIS_445_OG [0x1bd]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/07/04	17:50:24.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/07/04	17:53:02.0	XRT_CTRL_AUTO_403_OG [0x193]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/07/04	17:54:30.5	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/07/04	18:37:30.5	XRT_Custom_430_OG [0x1ae]			
2009/07/04	18:38:30.5	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/07/04	19:33:00.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/07/04	20:14:30.0	XRT_Custom_430_OG [0x1ae]			
2009/07/04	20:15:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]			

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2009/07/04	21:11:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/07/04	21:51:30.0	XRT_Custom_430_OG [0x1ae]				
2009/07/04	21:52:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/07/04	22:50:00.0	XRT_CTRL_MANU_435_OG [0x1b3]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/07/04	23:26:00.0	XRT_Custom_430_OG [0x1ae]				
2009/07/04	23:27:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/07/05	00:28:30.0	XRT_CTRL_MANU_435_OG [0x1b3]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/07/05	00:58:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/07/05	02:06:30.0	XRT_CTRL_MANU_435_OG [0x1b3]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/07/05	02:37:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/07/05	03:41:00.0	XRT_CTRL_MANU_435_OG [0x1b3]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/07/05	04:15:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/07/05	05:12:30.0	XRT_CTRL_MANU_435_OG [0x1b3]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/07/05	05:53:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/07/05	05:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/07/05	05:59:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]				
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2009/07/05	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]				
			AOCU_NM	5	02-76	00 00 00 00 00
2009/07/05	06:00:16.0	XRT_FLD_DIS_419_OG [0x1a3]				
			MDP_XRT_FLD_DIS	1	07-F0	d9
2009/07/05	06:00:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/07/05	06:00:20.0	XRT_ARS_DIS_410_OG [0x19a]				
			MDP_XRT_ARS_DIS	1	07-F0	d5
2009/07/05	06:02:58.0	XRT_QT_PROG_SET_400_OG [0x190]				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2009/07/05	06:03:00.0	XRT_CTRL_AUTO_403_OG [0x193]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/07/05	06:09:54.0	XRT_CTRL_MANU_428_OG [0x1ac]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/07/05	06:09:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]				
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/07/05	06:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]				
			AOCU_NM	5	02-76	01 00 00 00 00
2009/07/05	06:10:16.0	XRT_QT_PROG_SET_443_OG [0x1bb]				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2009/07/05	06:10:18.0	XRT_AEC_RESET_415_OG [0x19f]				
			MDP_XRT_AEC_RESET	1	07-F0	d0
2009/07/05	06:10:20.0	XRT_ARS_DIS_422_OG [0x1a6]				
			MDP_XRT_ARS_DIS	1	07-F0	d5
2009/07/05	06:10:22.0	XRT_FLD_DIS_445_OG [0x1bd]				
			MDP_XRT_FLD_DIS	1	07-F0	d9
2009/07/05	06:10:24.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/07/05	06:13:02.0	XRT_CTRL_AUTO_403_OG [0x193]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/07/05	06:53:00.0	XRT_CTRL_MANU_435_OG [0x1b3]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/07/05	07:32:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/07/05	08:33:00.0	XRT_CTRL_MANU_435_OG [0x1b3]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/07/05	09:10:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/07/05	10:14:00.0	XRT_CTRL_MANU_435_OG [0x1b3]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/07/05	10:49:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/07/05	11:58:00.0	XRT_CTRL_MANU_435_OG [0x1b3]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/07/05	12:27:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/07/05	13:36:30.0	XRT_CTRL_MANU_435_OG [0x1b3]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/07/05	14:06:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/07/05	15:15:00.0	XRT_CTRL_MANU_435_OG [0x1b3]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/07/05	16:00:30.0	XRT_Custom_430_OG [0x1ae]				
2009/07/05	16:01:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/07/05	16:53:30.0	XRT_CTRL_MANU_435_OG [0x1b3]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/07/05	17:37:30.0	XRT_Custom_430_OG [0x1ae]				
2009/07/05	17:38:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/07/05	17:54:24.0	XRT_CTRL_MANU_428_OG [0x1ac]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1

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2009/07/05	17:54:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2009/07/05	17:54:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2009/07/05	17:54:46.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/07/05	17:54:48.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/07/05	17:54:50.0	XRT_ARS_DIS_410_OG [0x19a]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/07/05	17:57:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2009/07/05	17:57:30.0	XRT_CTRL_AUTO_440_OG [0x1b8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/05	18:04:24.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/05	18:04:26.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2009/07/05	18:04:30.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01	00	00	00	00
2009/07/05	18:04:46.0	XRT_QT_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2009/07/05	18:04:48.0	XRT_AEC_RESET_415_OG [0x19f]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2009/07/05	18:04:50.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/07/05	18:04:52.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/07/05	18:04:54.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/07/05	18:07:32.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/05	18:32:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/05	19:14:00.0	XRT_Custom_430_OG [0x1ae]							
2009/07/05	19:15:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/05	20:10:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/05	20:51:30.0	XRT_Custom_430_OG [0x1ae]							
2009/07/05	20:52:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/05	21:49:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/05	22:27:30.0	XRT_Custom_430_OG [0x1ae]							
2009/07/05	22:28:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/05	23:27:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/05	23:57:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/06	01:06:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/06	01:35:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/06	02:42:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/06	03:14:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/06	04:10:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/06	04:52:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/06	05:23:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/06	05:23:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2009/07/06	05:24:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2009/07/06	05:24:16.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/07/06	05:24:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/07/06	05:24:20.0	XRT_ARS_DIS_410_OG [0x19a]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/07/06	05:26:58.0	XRT_QT_PROG_SET_400_OG [0x190]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	01			
2009/07/06	05:27:00.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/06	05:33:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/06	05:33:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2009/07/06	05:34:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01	00	00	00	00
2009/07/06	05:34:16.0	XRT_QT_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2009/07/06	05:34:18.0	XRT_AEC_RESET_415_OG [0x19f]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2009/07/06	05:34:20.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/07/06	05:34:22.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				

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2009/07/06	05:34:24.0	XRT_FLRCTRL_DIS_425_OG [0x1a9] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/07/06	05:37:02.0	XRT_CTRL_AUTO_403_OG [0x193] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/06	05:51:00.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/06	06:31:00.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/06	07:31:30.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/06	18:13:54.0	XRT_CTRL_MANU_428_OG [0x1ac] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/06	18:13:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba] XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00			
2009/07/06	18:14:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00 00 00 00	00 00			
2009/07/06	18:14:16.0	XRT_FLD_DIS_419_OG [0x1a3] MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/07/06	18:14:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/07/06	18:14:20.0	XRT_ARS_DIS_410_OG [0x19a] MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/07/06	18:16:58.0	XRT_QT_PROG_SET_421_OG [0x1a5] MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2009/07/06	18:17:00.0	XRT_CTRL_AUTO_440_OG [0x1b8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/06	18:24:00.0	AOCS_ORe-point_Start_1_OG [0x097] AOCU_NM	5	02-76	01 00 00 00	00 00			
2009/07/07	05:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/07/07	05:59:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba] XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00			
2009/07/07	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00 00 00 00	00 00			
2009/07/07	06:00:16.0	XRT_FLD_DIS_419_OG [0x1a3] MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/07/07	06:00:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/07/07	06:00:20.0	XRT_ARS_DIS_410_OG [0x19a] MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/07/07	06:02:58.0	XRT_QT_PROG_SET_400_OG [0x190] MDP_XRT_QT_PROG_SET	2	07-F0	c4	01			
2009/07/07	06:03:00.0	XRT_CTRL_AUTO_403_OG [0x193] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/07/07	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097] AOCU_NM	5	02-76	01 00 00 00	00 00			
2009/07/07	10:24:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00 00 00 00	00 00			