

XRT Timeline to be uploaded on 2009/08/22

Period: 2009/08/22 11:30:00 - 2009/08/27 10:49:00

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Normal mode

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XOB #16FA: CH Jets - C/poly - 23s exp - FOV384-Offset												
Term	Pointing (x, y)	Comment										
08/22 11:43:00 - 08/22 17:21:30	Fixed (0.0, 910.0)	# OP start + 10min, Poler jet obs. in N-CH										
08/22 18:13:00 - 08/23 05:45:24	Fixed (0.0, 910.0)	Poler jet obs. in N-CH										
08/23 10:03:00 - 08/23 17:48:24	Fixed (0.0, 910.0)	Poler jet obs. in N-CH										
PROG= 18 Inf.-time(s)												
└─ Subr= 1 1-time(s) 32.0sec												
└─ Seqn= 22 1-time(s) 2.0sec												
	C-poly/Open	C-poly/Open	close	Safe	Norm	22.6s	Obs 1x1	384x384 (1024, 952)	DPCM 0 0	2.0sec		
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16D9: Synoptic Q95 2x2 - Al/poly(512/5795) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 2048x512 -1x1 512x2048) +Ti-poly(723/11571) + G-band(1												
Term	Pointing (x, y)	Comment										
08/22 18:03:00 - 08/22 18:09:54	Fixed (0.0, 0.0)	synoptic										
08/23 17:51:30 - 08/23 17:58:24	Fixed (0.0, 0.0)	synoptic, shifted -11.5 min										
08/24 18:09:30 - 08/24 22:22:54	Fixed (0.0, 0.0)	synoptic, shifted 6.5 min										
PROG= 01 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 36 1-time(s) 4.0sec												
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95 0 0	2.0sec		
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	5.66s	Obs 2x2	2048x2048 (1024, 1024)	Q=95 0 0	2.0sec		
└─ Seqn= 85 1-time(s) 2.0sec												
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98 0 0	2.0sec		
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 4x4	2048x2048 (1024, 1024)	Q=98 0 0	2.0sec		
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 8x8	2048x2048 (1024, 1024)	Q=98 0 0	2.0sec		
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	2048x512 (1024, 1024)	DPCM 0 0	2.0sec		
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	512x2048 (1024, 1024)	DPCM 0 0	2.0sec		
└─ Seqn= 88 1-time(s) 4.0sec												
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	707ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95 0 0	2.0sec		
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs 2x2	2048x2048 (1024, 1024)	Q=95 0 0	2.0sec		
└─ Seqn= 92 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95 0 0	2.0sec		
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16D8: Synoptic Q95 2x2 - Al/mesh(512/5795) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 2048x512 -1x1 512x2048) +Ti-poly(723/11571) + G-band(1												
Term	Pointing (x, y)	Comment										
08/23 05:48:30 - 08/23 07:26:54	Fixed (0.0, 0.0)	synoptic, shifted -14.5 min										
08/24 06:03:00 - 08/24 07:26:54	Fixed (0.0, 0.0)	synoptic										
08/25 06:28:30 - 08/25 07:54:24	Fixed (0.0, 0.0)	synoptic, shifted 25.5 min										
PROG= 19 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 47 1-time(s) 4.0sec												
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95 0 0	2.0sec		
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	5.66s	Obs 2x2	2048x2048 (1024, 1024)	Q=95 0 0	2.0sec		
└─ Seqn= 85 1-time(s) 2.0sec												
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98 0 0	2.0sec		
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 4x4	2048x2048 (1024, 1024)	Q=98 0 0	2.0sec		
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 8x8	2048x2048 (1024, 1024)	Q=98 0 0	2.0sec		
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	2048x512 (1024, 1024)	DPCM 0 0	2.0sec		
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	512x2048 (1024, 1024)	DPCM 0 0	2.0sec		
└─ Seqn= 88 1-time(s) 4.0sec												
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	707ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95 0 0	2.0sec		
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs 2x2	2048x2048 (1024, 1024)	Q=95 0 0	2.0sec		
└─ Seqn= 92 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95 0 0	2.0sec		
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16F1: HOP 126 (Ti/poly fov256x256 exp23s 60s-cadence)												
Term	Pointing (x, y)	Comment										
08/23 07:30:00 - 08/23 09:33:30	Track (-19.0, 0.0) @ 08/23 05:55:30	HOP126 (8-10UT)										
08/24 07:30:00 - 08/24 18:06:24	Track (-16.8, -0.0) @ 08/24 06:10:00	HOP126 (8-10UT)										
08/25 07:57:30 - 08/25 11:02:30	Track (-12.9, -0.0) @ 08/25 06:35:30	HOP126 (8-10UT)										
PROG= 20 Inf.-time(s)												
└─ Subr= 1 1-time(s) 52.5sec												
└─ Seqn= 74 1-time(s) 4.0sec												
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	22.6s	Obs 1x1	256x256 (1024, 1024)	Q=90 0 0	2.0sec		
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16AC: G-Band Alignment with North Pole Q90 2x2(G-band only) - 5min cadence - Partial Sun-wNGT												
Term	Pointing (x, y)	Comment										

08/23 18:13:30 - 08/23 19:59:54 Fixed (0.0, 945.0) Co-alignment at N (18-20UT)

PROG= 04 1-time(s)

└ Subr= 1 1-time(s) 360.0sec

└ Seqn= 10 24-time(s) 300.0sec

Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16AD: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8 min cadence-wNGT

Term Pointing (x, y) Comment

08/23 20:15:00 - 08/23 21:55:54 Fixed (-945.0, 0.0) Co-alignment at E (20-22UT)

PROG= 17 1-time(s)

└ Subr= 1 1-time(s) 360.0sec

└ Seqn= 13 15-time(s) 480.0sec

Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16F9: XBP Al/mesh - Ti/Poly - FOV384 - Q90 - AEC4 - 1min cadence

Term Pointing (x, y) Comment

08/23 22:03:02 - 08/24 05:59:54 Track (-18.3, -0.0) @ 08/23 22:00:00 HOP124 XBP (22-4UT)

08/24 22:26:02 - 08/25 06:25:24 Track (166.5, 1.3) @ 08/24 18:16:30 HOP124 XBP (22-4UT)

PROG= 09 Inf.-time(s)

└ Subr= 1 1-time(s) 2.0sec

└ Seqn= 76 60-time(s) 60.0sec

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=90	4	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1024, 1024)	Q=90	4	0	2.0sec

└ Subr= 2 1-time(s) 2.0sec

└ Seqn= 66 1-time(s) 4.0sec

Open/Al-mesh	Open/G-band	close	Safe	Dark	4.00s	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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NOT USED

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Active Region Search

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NOT USED

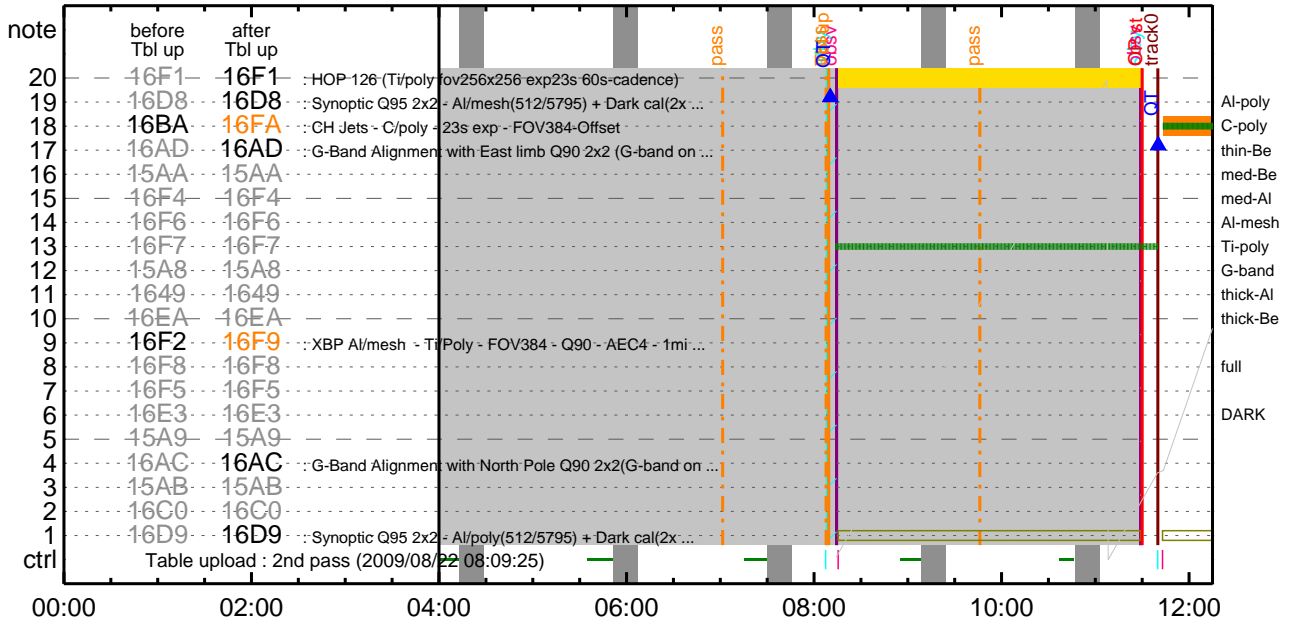
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Flare Detection

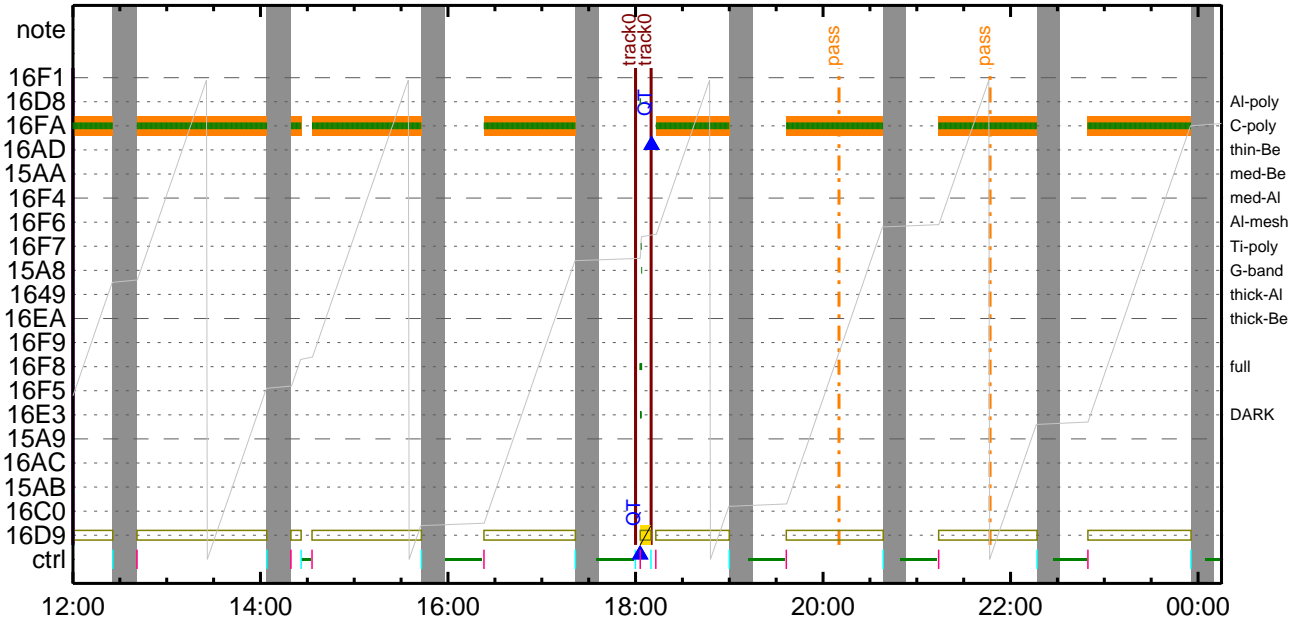
* * * * *

NOT USED

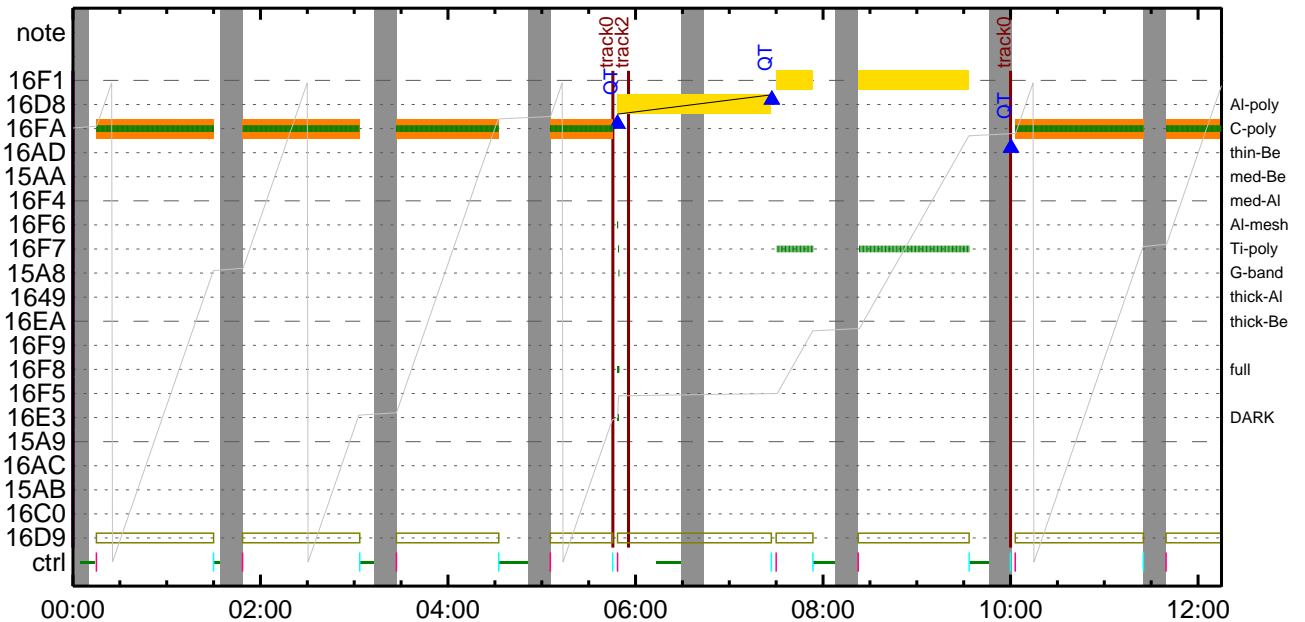
CMDI #0750 2009/08/22



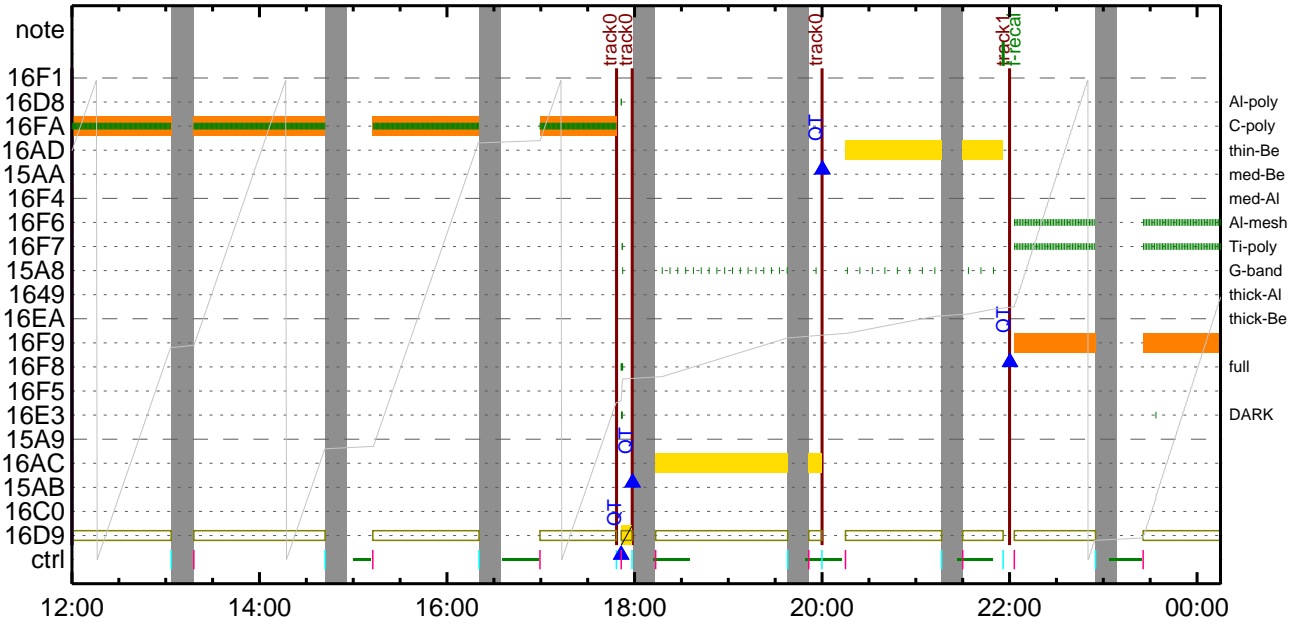
CMDI #0750 2009/08/22



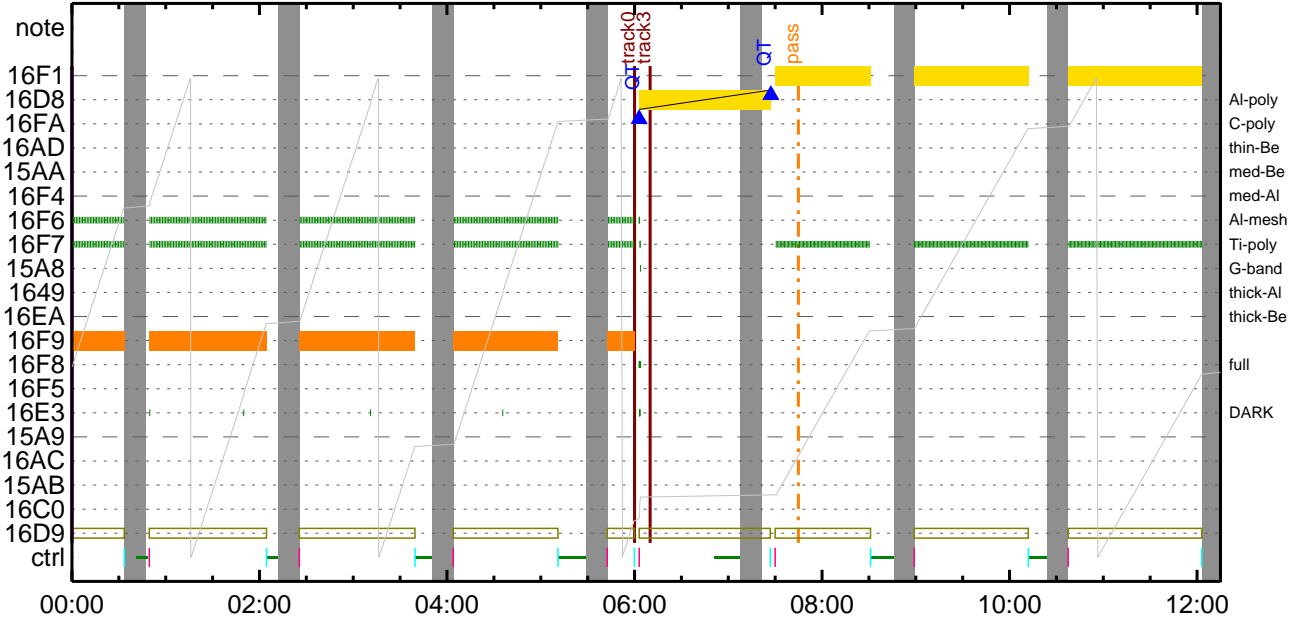
CMDI #0750 2009/08/23



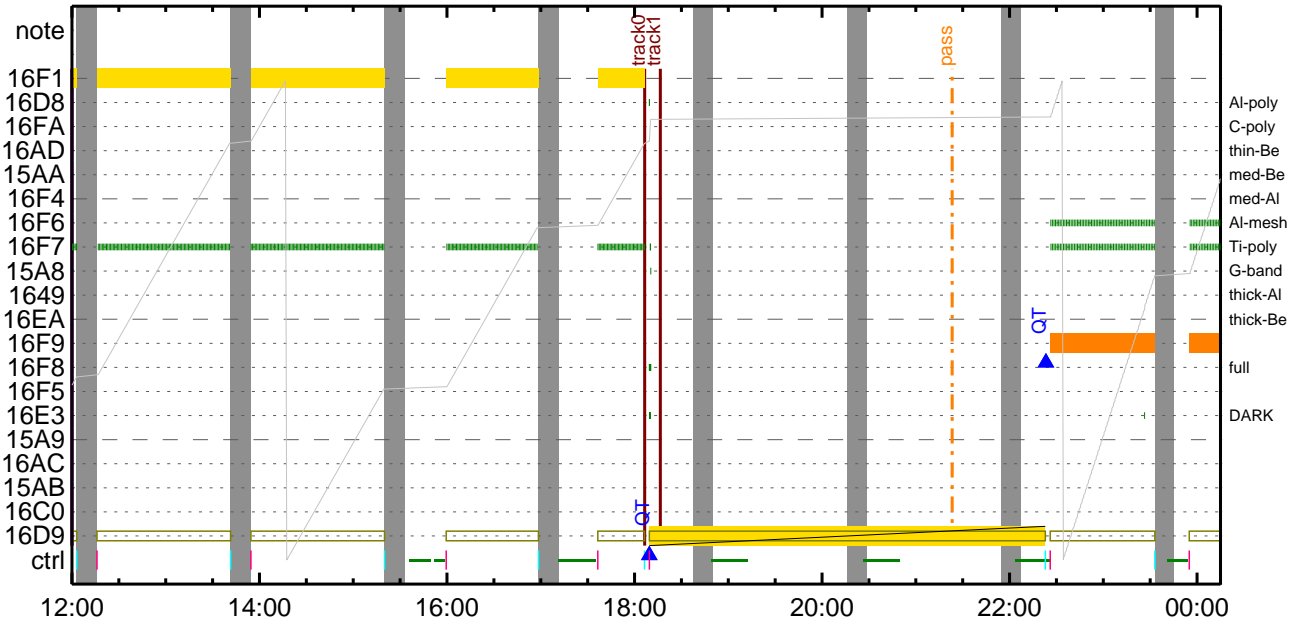
CMDI #0750 2009/08/23



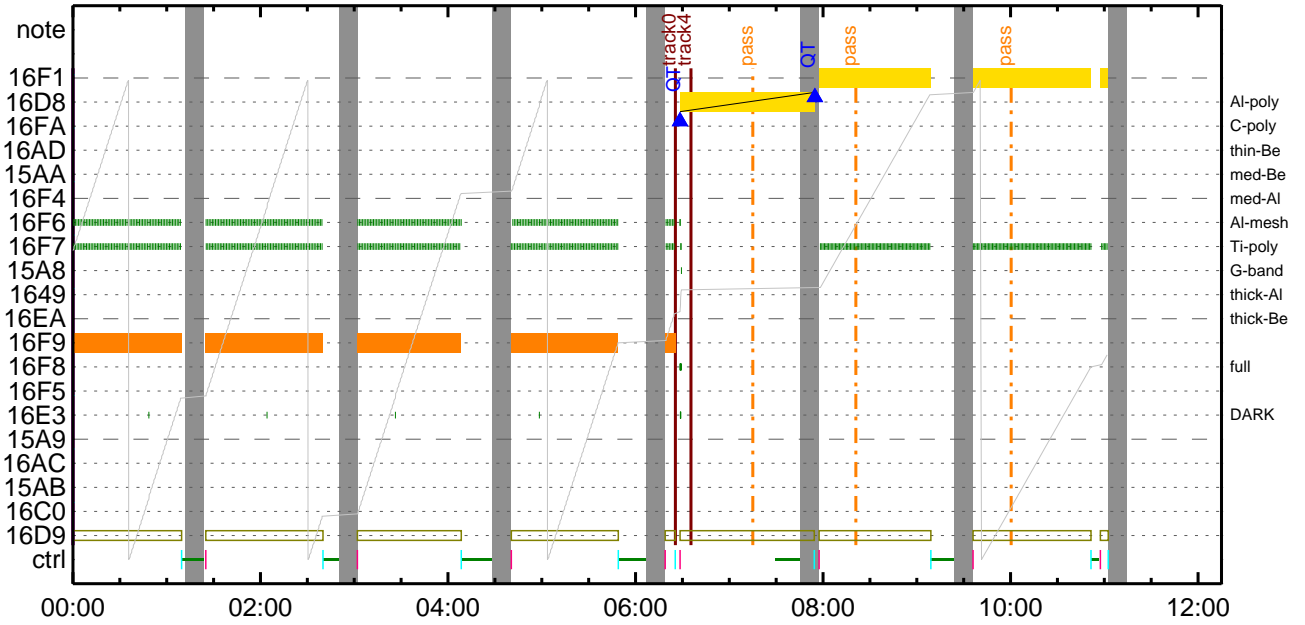
CMDI #0750 2009/08/24



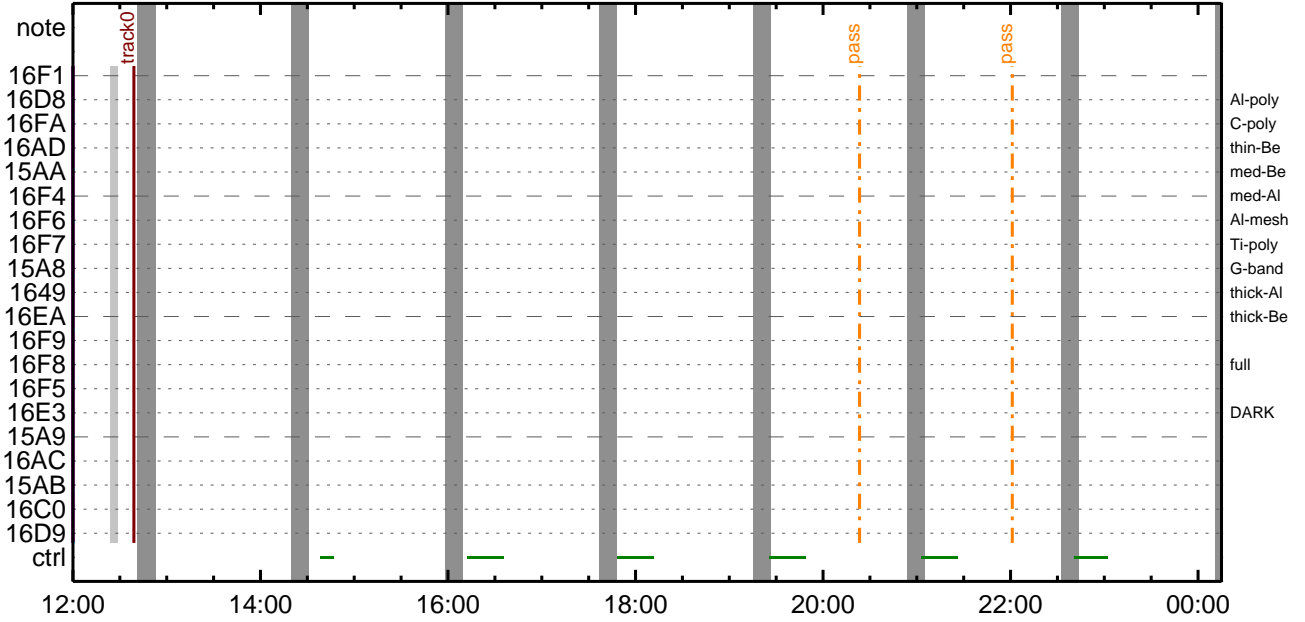
CMDI #0750 2009/08/24



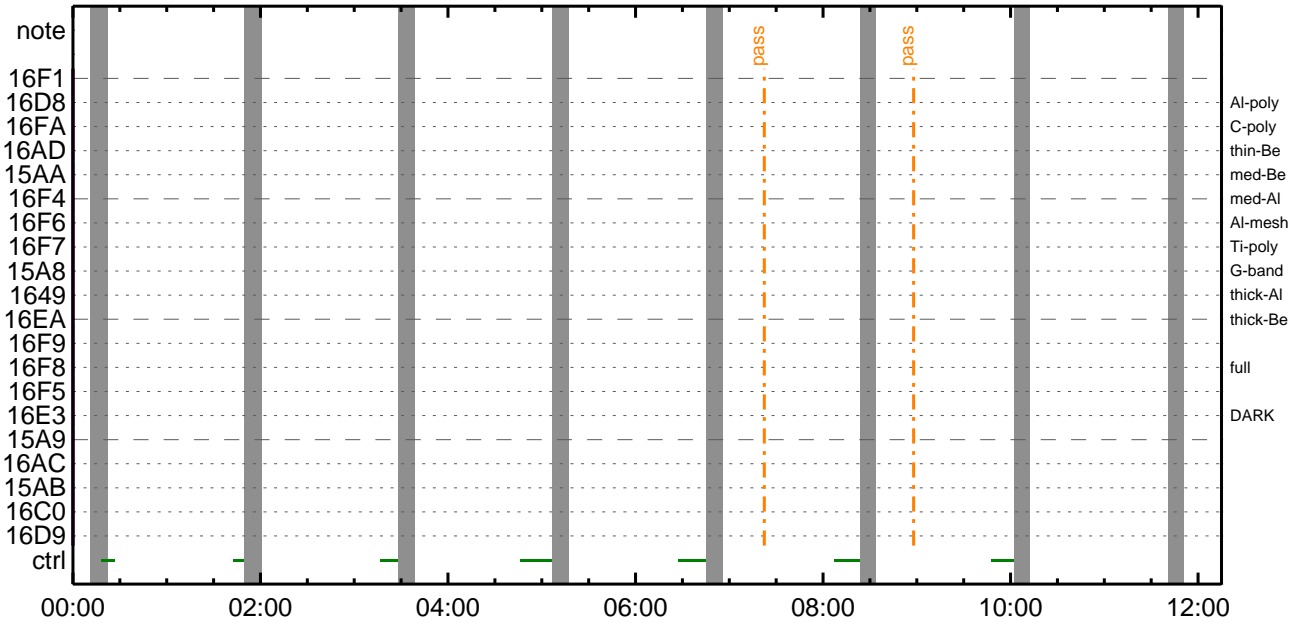
CMDI #0750 2009/08/25



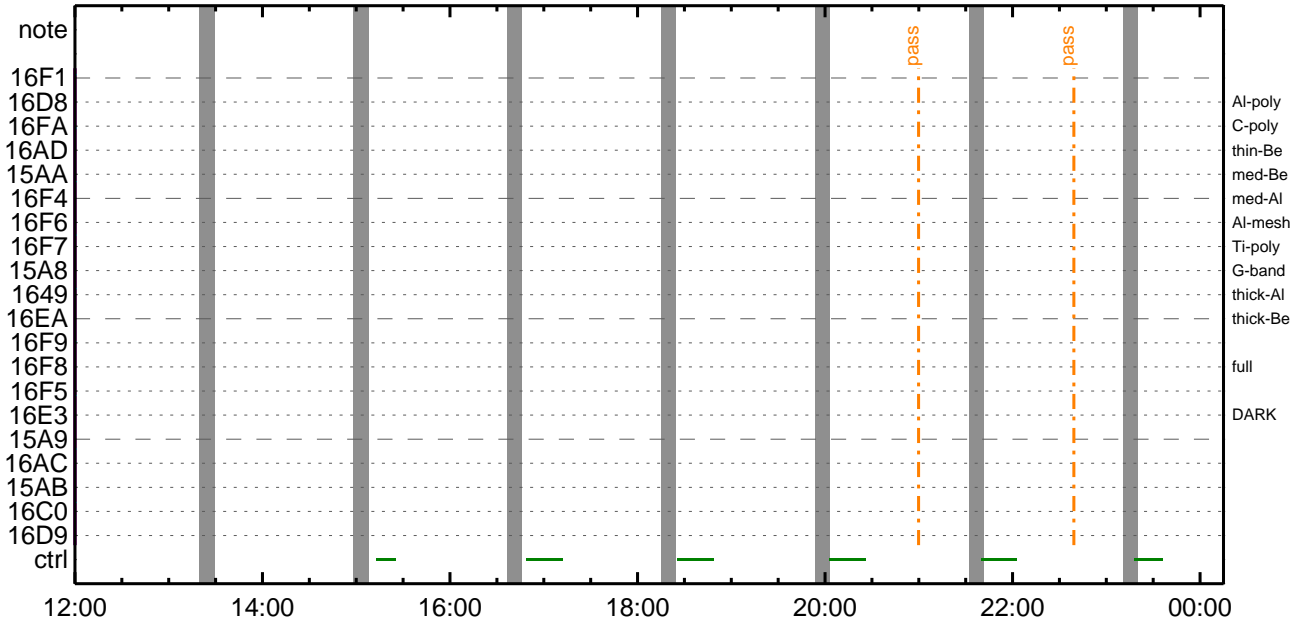
CMDI #0750 2009/08/25



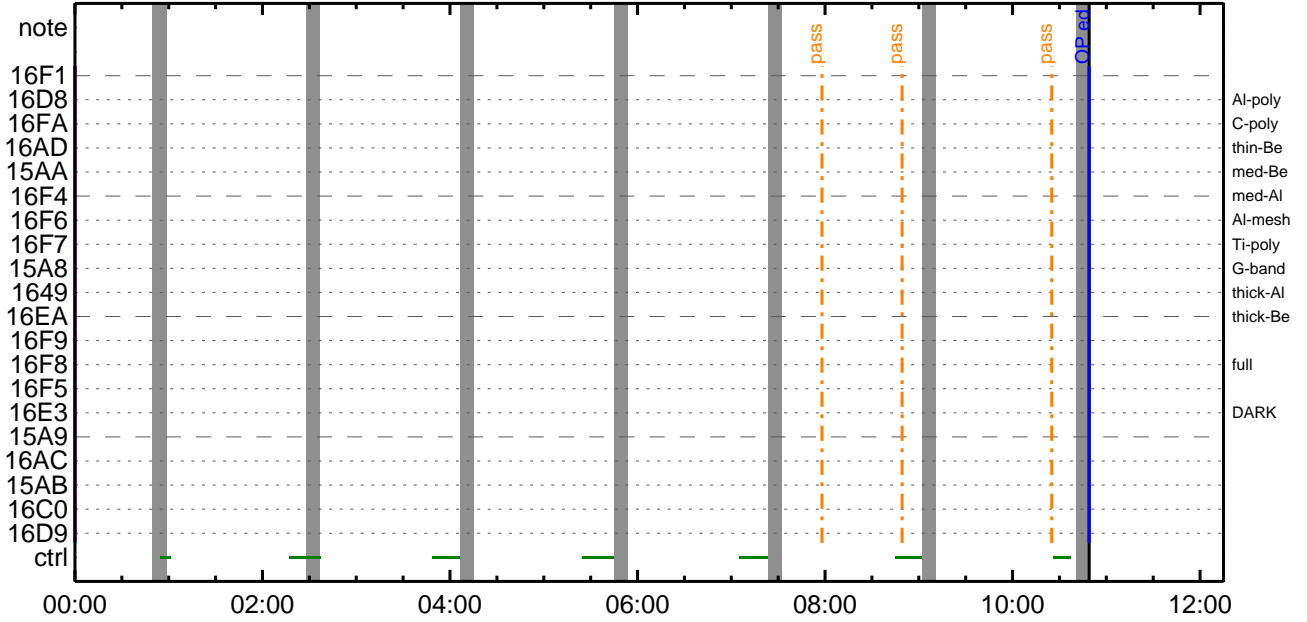
CMDI #0750 2009/08/26



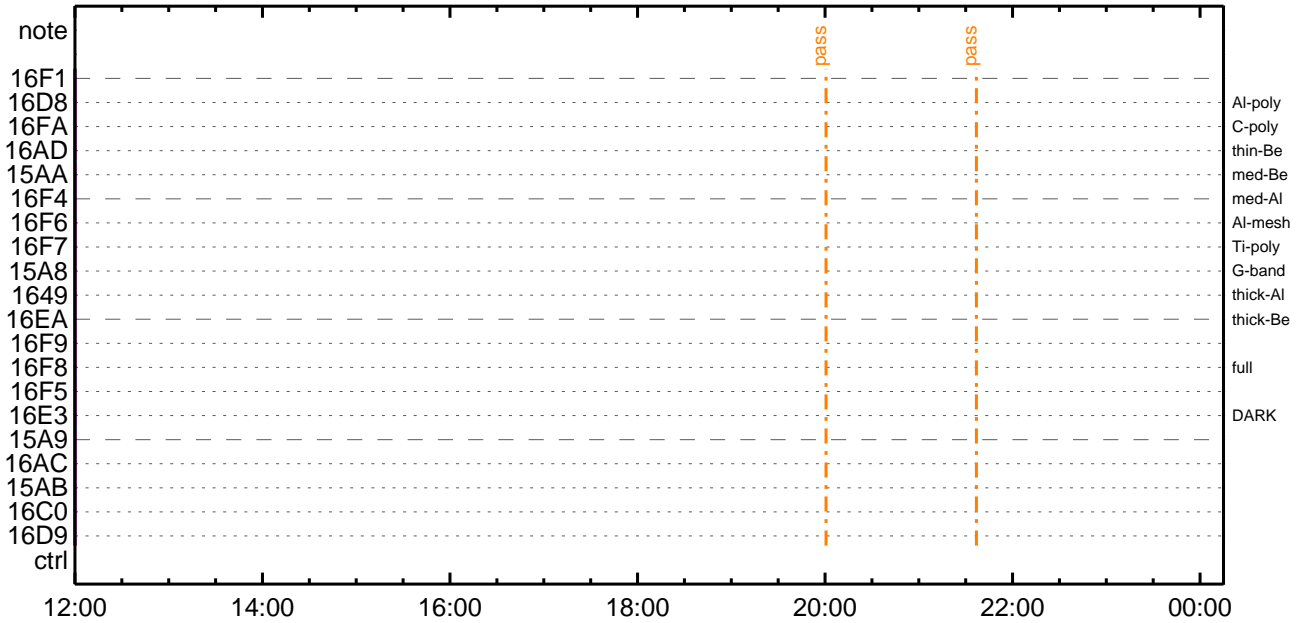
CMDI #0750 2009/08/26



CMDI #0750 2009/08/27



CMDI #0750 2009/08/27



(a) Spacecraft Operation Procedure (real-commands)

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main-798 2009-08-22 12:21:30 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YFÿ6YÉÁ+¿©
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿Àß•µ°Æ»Í×ÁÇ¿ÍYçYÁY×Yí;¼YÉ;ÈÈèµ•íÉ;ÈÈÈ¼°ÇÒα•α¿¼l¹ç¿Í;çÁ®, ùα¹αÈßαÇÁ+¿©α•αÈααα³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ;|YÁY6Y×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ä
0019 . S. OP op-798:OP
0020 ()
0021 . S. OG og-798:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPÍ°èYÁY6Y×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÁY6Y×½ª î»ðð³ÍÇ§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGαÍ¼È¹ç•è²ÍOKαð³ÍÇ§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÁY6Y×½ª î»ðð³ÍÇ§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGαÍ¼È¹ç•è²ÍOKαð³ÍÇ§
0062 C.
0063 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÁY6Y×½ª î»ðð³ÍÇ§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OPαÍ¼È¹ç•è²ÍOKαð³ÍÇ§
0081 C.
0082 . C. ***** øÈ²¼αÍ¼Á´¶¼°αÈÈ-α°Á+¿© (¼åµ-YÁY6Y×½ª¼È¿çαðÁÓÁæαÇª°α°¼¼¿l¹çαÇαâ) *****
0083 C. DHUYá;¼YÉ;È¼Y¼; Yí;¼YÉ;ÈαðÍáα¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;§ OPOG UPLOADα-Á+¿©NGαÍ¼¿l¹ç;ç°È²¼αÍTI-CMDÁ+¿©αÍ¼Á¹Ôα•αÈααα³αÈ;f
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0096 C.                0x0000; SET 0x0000 DUMP 0x0000 0x0000 0x0000 0x0000; E
0097 C.
0098 C. TI 2009-08-22 11:25:00.0
0099 +. TI 2009-08-22 11:25:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                0x0000 [HK1_TI_CMD_NUM] EQ 1COUNTUP
0102 C.
0103 +. TI 2009-08-22 11:25:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                0x0000 [HK1_TI_CMD_NUM] EQ 1COUNTUP
0106 C.
0107 +. TI 2009-08-22 11:25:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                0x0000 [HK1_TI_CMD_NUM] EQ 1COUNTUP
0110 C.
0111 +. TI 2009-08-22 11:29:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                0x0000 [HK1_TI_CMD_NUM] EQ 1COUNTUP
0114 C.
0115 C.                0x0000 [HK1_TI_CMD_ENA/DIS] EQ ENA
0116 C.                0x0000 [HK1_TI_CMD_NUM] EQ 4
0117 C.                0x0000 [HK1_NEXT_EXEC_PIM] EQ DHU
0118 C.                0x0000 [HK1_NEXT_EXEC_DC] EQ 0xB3
0119 C.
0120 C.
0121 C. *****
0122 C. TI 2009-08-22 11:29:59.5
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                0x0000 [HK1_DMP_TOP_ADRS_1] EQ 07
0129 C.                0x0000 [HK1_DMP_TOP_ADRS_0] EQ 2B
0130 C.                0x0000 [HK1_DMP_BLOCK_NUM] EQ 3
0131 C.                0x0000 [HK1_DMP_REPEAT_NUM] EQ 0
0132 C.                0x0000 [HK1_DMA_DMP_PIM] EQ DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                0x0000 [HK1_PKT_FORM_NO] EQ 7
0136 C.                0x0000 [HK1_PKT_GEN_TIME] EQ 0.25 s
0137 C.                0x0000 [HK1_S_TLM_BIT_RATE] EQ 32k
0138 C.                0x0000 [HK1_X_TLM_BIT_RATE] EQ 4M
0139 C.                0x0000 [HK1_DMP_CHK_FLG] EQ EXEC
0140 C.
0141 C.                0x0000 [HK1_DMP_CHK_FLG] EQ NON
0142 C.
0143 C.
0144 C. RAM ID=TI_TBL 0x03AB00-0x03AEFF
0145 C.
0146 C. DHU 2009-08-22 11:29:59.5
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                0x0000 [HK1_PKT_FORM_NO] EQ 2
0150 C.                0x0000 [HK1_PKT_GEN_TIME] EQ 0.5S
0151 C.                0x0000 [HK1_S_TLM_BIT_RATE] EQ 32K
0152 C.                0x0000 [HK1_X_TLM_BIT_RATE] EQ 4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2009-08-22 11:29:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 C.
0162 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C.
0164 C. ***** SOT END *****
0165 C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2009-08-22 11:29:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2009-08-22 11:29:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 C.                [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2009-08-22 11:29:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 C.                [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 C. ***** MDP 2009-08-22 11:29:00.0 *****
0192 C. (0x0000 [HK1_TI_CMD_NUM] EQ 1COUNTUP)
0193 S. DC-BC dcbc-402:DCBC
```



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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 C.
0130 C. ***** XRT START *****
0131 C.
0132 +. DC 07-F0 MDP_XRT_CTRL_MANU
0133 BC (c1)
0134 + DC 07-F0 MDP_XRT_MODE_STBY
0135 BC (c3)
0136 . C. ----- Success Verify ? OK / NG____
0137 C.
0138 C. XRT Obs. Table Upload
0139 . S. RAM ram-291:MDP_OBS_X
0140 ( )
0141 C.
0142 +. DC 07-F0 MDP_DUMP_XRTTBL
0143 BC (84 07 00 00 00 3a d4)
0144 . C. ----- Comparison Check ? OK / ERR ____
0145 C.
0146 C.
0147 +. DC 07-F0 MDP_XRT_ROI_SET
0148 BC (cd 01 b1 b1 04 04)
0149 + DC 07-F0 MDP_XRT_ROI_SET
0150 BC (cd 02 b1 b1 08 08)
0151 + DC 07-F0 MDP_XRT_ROI_SET
0152 BC (cd 03 b1 b1 08 08)
0153 + DC 07-F0 MDP_XRT_ROI_SET
0154 BC (cd 04 b1 b1 06 06)
0155 + DC 07-F0 MDP_XRT_ROI_SET
0156 BC (cd 06 80 80 04 04)
0157 + DC 07-F0 MDP_XRT_ROI_SET
0158 BC (cd 07 80 77 06 06)
0159 + DC 07-F0 MDP_XRT_ROI_SET
0160 BC (cd 08 80 80 20 20)
0161 + DC 07-F0 MDP_XRT_ROI_SET
0162 BC (cd 09 80 80 20 08)
0163 + DC 07-F0 MDP_XRT_ROI_SET
0164 BC (cd 0a 80 80 08 20)
0165 + DC 07-F0 MDP_XRT_ROI_SET
0166 BC (cd 0b 80 60 20 18)
0167 + DC 07-F0 MDP_XRT_ROI_SET
0168 BC (cd 0c a0 80 18 20)
0169 + DC 07-F0 MDP_XRT_ROI_SET
0170 BC (cd 0d 80 80 06 06)
0171 + DC 07-F0 MDP_XRT_ROI_SET
0172 BC (cd 0f 80 80 04 04)
0173 + DC 07-F0 MDP_XRT_ROI_SET
0174 BC (cd 10 80 80 10 10)
0175 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0176 BC (c4 14)
0177 +. DC 07-F0 MDP_XRT_ARS_DIS
0178 BC (d5)
0179 +. DC 07-F0 MDP_XRT_FLD_DIS
0180 BC (d9)
0181 +. DC 07-F0 MDP_XRT_FLRCTRL_DIS
0182 BC (c9)
0183 . C. ----- Success Verify ? OK / NG ____
0184 C.
0185 C.
0186 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0187 C.
0188 +. DC 07-F0 MDP_XRT_MODE_OBSV
0189 BC (c2)
0190 +. DC 07-F0 MDP_XRT_CTRL_AUTO
0191 BC (c0)
0192 +. TI 2009-08-22 11:29:02.0
0193 DC 07-F0 MDP_XRT_MODE_OBSV

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0194 BC (c2)
0195 . C. ----- Success Verify ? OK / NG ____
0196 C.
0197 C. ***** XRT END *****
0198 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0199 +. DC 07-FC EIS_MODE_MANU
0200 BC (21 02)
0201 . C. Verify EIS in MANUAL mode
0202 . C. Estimated OBSTBL upload time is 42s
0203 C. *****
0204 C. EIS START OBSTBL LOAD
0205 C. *****
0206 . S. RAM ram-820:EIS_OBSTBL
0207 ( )
0208 +. DC 07-FC EIS_DUMP_OBSTBL
0209 BC (07 07 07 00 00 70 00)
0210 C.
0211 C. Execute, after the success of OBSTBL upload.
0212 C. Set EIS TI-commands
0213 +. TI 2009-08-22 11:29:50.0
0214 DC 07-FC EIS_MODE_CHG_ENA
0215 BC (20)
0216 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0217 C. *****
0218 C. EIS END OBSTBL LOAD
0219 C. *****
0220 C.
0221 . C. ***** MDP `úÃîñî»ö%ÝðÊÃð¹ñèDCBC•x²è *****
0222 C. (%Á°îÝÓÝÃÝÈÝÞÝËÝáÝçÝèñÊ½¼ñ¼Ã»Û¹ñè)
0223 . S. DC-BC dcbc-402:DCBC
0224 (MDP_known_event)
0225 C.
0226 C.
0227 . C. ***** ¥DÝ¹•İ Daily±¿İññÈ´Ø¹ñèDCBC•x²è *****
0228 . S. DC-BC dcbc-153:DCBC
0229 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0230 C.
0231 C.
0232 . C. ;ãLOS¥Á¥$¥Ã¥¬¼Ã»Û;ã
0233 C.
0234 . C. ***** LOS *****
0235 C.

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*** OP Sequence for XRT ***

2009/08/22	11:39:54.0	XRT_CTRL_MANU_428_OG [0x1ac]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/08/22	11:39:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2009/08/22	11:40:00.0	AOCS_OrE-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 af 1b 00 00		
2009/08/22	11:40:16.0	XRT_QT_PROG_SET_401_OG [0x191]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12		
2009/08/22	11:42:54.0	XRT_ARS_DIS_422_OG [0x1a6]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2009/08/22	11:42:56.0	XRT_FLD_DIS_445_OG [0x1bd]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2009/08/22	11:42:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2009/08/22	11:43:00.0	XRT_CTRL_AUTO_403_OG [0x193]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/08/22	12:25:30.0	XRT_CTRL_MANU_435_OG [0x1b3]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/08/22	12:41:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/08/22	14:04:00.0	XRT_CTRL_MANU_435_OG [0x1b3]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/08/22	14:19:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/08/22	14:26:00.0	XRT_CTRL_MANU_435_OG [0x1b3]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/08/22	14:32:00.0	XRT_Custom_430_OG [0x1ae]					
2009/08/22	14:33:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/08/22	15:43:00.0	XRT_CTRL_MANU_435_OG [0x1b3]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/08/22	16:22:00.0	XRT_Custom_430_OG [0x1ae]					
2009/08/22	16:23:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/08/22	17:21:30.0	XRT_CTRL_MANU_435_OG [0x1b3]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/08/22	17:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/08/22	17:59:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2009/08/22	18:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2009/08/22	18:00:16.0	XRT_FLD_DIS_419_OG [0x1a3]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2009/08/22	18:00:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2009/08/22	18:00:20.0	XRT_ARS_DIS_410_OG [0x19a]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2009/08/22	18:02:58.0	XRT_QT_PROG_SET_400_OG [0x190]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01		
2009/08/22	18:03:00.0	XRT_CTRL_AUTO_403_OG [0x193]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/08/22	18:09:54.0	XRT_CTRL_MANU_428_OG [0x1ac]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/08/22	18:09:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2009/08/22	18:10:00.0	AOCS_OrE-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 af 1b 00 00		
2009/08/22	18:10:16.0	XRT_QT_PROG_SET_401_OG [0x191]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12		
2009/08/22	18:12:54.0	XRT_ARS_DIS_422_OG [0x1a6]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2009/08/22	18:12:56.0	XRT_FLD_DIS_445_OG [0x1bd]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2009/08/22	18:12:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2009/08/22	18:13:00.0	XRT_CTRL_AUTO_403_OG [0x193]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/08/22	19:00:00.0	XRT_CTRL_MANU_435_OG [0x1b3]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/08/22	19:35:30.0	XRT_Custom_430_OG [0x1ae]					
2009/08/22	19:36:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/08/22	20:38:30.0	XRT_CTRL_MANU_435_OG [0x1b3]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/08/22	21:13:00.0	XRT_Custom_430_OG [0x1ae]					
2009/08/22	21:14:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/08/22	22:17:00.0	XRT_CTRL_MANU_435_OG [0x1b3]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/08/22	22:48:30.0	XRT_Custom_430_OG [0x1ae]					
2009/08/22	22:49:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/08/22	23:55:30.0	XRT_CTRL_MANU_435_OG [0x1b3]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/08/23	00:14:00.0	XRT_Custom_430_OG [0x1ae]					
2009/08/23	00:15:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/08/23	01:30:00.0	XRT_CTRL_MANU_435_OG [0x1b3]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/08/23	01:48:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]					

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2009/08/23	03:03:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/08/23	03:27:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/08/23	04:32:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/08/23	05:05:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/08/23	05:45:24.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/08/23	05:45:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2009/08/23	05:45:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2009/08/23	05:45:46.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/08/23	05:45:48.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/08/23	05:45:50.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2009/08/23	05:48:28.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13			
2009/08/23	05:48:30.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/08/23	05:55:30.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02 00 00 00 00			
2009/08/23	07:26:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/08/23	07:26:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2009/08/23	07:27:16.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14			
2009/08/23	07:27:18.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2009/08/23	07:27:20.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/08/23	07:27:22.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/08/23	07:30:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/08/23	07:53:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/08/23	08:22:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/08/23	09:33:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/08/23	09:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/08/23	09:59:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2009/08/23	10:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 af 1b 00 00			
2009/08/23	10:00:16.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12			
2009/08/23	10:02:54.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2009/08/23	10:02:56.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/08/23	10:02:58.0	XRT_FLRCTRL_DIS_416_OG [0x1a0]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/08/23	10:03:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/08/23	11:25:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/08/23	11:39:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/08/23	13:03:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/08/23	13:18:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/08/23	14:42:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/08/23	15:11:30.0	XRT_Custom_430_OG [0x1ae]							
2009/08/23	15:12:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/08/23	16:20:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/08/23	16:58:30.0	XRT_Custom_430_OG [0x1ae]							
2009/08/23	16:59:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/08/23	17:48:24.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/08/23	17:48:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2009/08/23	17:48:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2009/08/23	17:48:46.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/08/23	17:48:48.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/08/23	17:48:50.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5			

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2009/08/23	17:51:28.0	XRT_QT_PROG_SET_400_OG [0x190]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	01			
2009/08/23	17:51:30.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/08/23	17:58:24.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/08/23	17:58:26.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2009/08/23	17:58:30.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	ac	00	00	00
2009/08/23	17:58:46.0	XRT_QT_PROG_SET_413_OG [0x19d]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2009/08/23	17:58:48.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/08/23	17:58:50.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/08/23	17:58:52.0	XRT_FLRCTRL_DIS_418_OG [0x1a2]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/08/23	18:13:30.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/08/23	19:38:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/08/23	19:51:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/08/23	19:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/08/23	19:59:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2009/08/23	20:00:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	00	00	54	00
2009/08/23	20:00:16.0	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2009/08/23	20:00:18.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/08/23	20:00:20.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/08/23	20:00:22.0	XRT_FLRCTRL_DIS_418_OG [0x1a2]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/08/23	20:15:00.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/08/23	21:16:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/08/23	21:30:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/08/23	21:55:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/08/23	21:55:56.0	XRT_FOCUS_RECALIBRATE_402_OG [0x192]							
		XRT_FOCUS_RECAL	2	07-F8	78	00			
2009/08/23	21:59:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2009/08/23	22:00:00.0	AOCS_Ore-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	01	00	00	00	00
2009/08/23	22:00:16.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	09			
2009/08/23	22:00:18.0	XRT_AEC_RESET_415_OG [0x19f]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2009/08/23	22:00:20.0	XRT_ARS_DIS_422_OG [0x1a6]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/08/23	22:00:22.0	XRT_FLD_DIS_445_OG [0x1bd]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/08/23	22:00:24.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/08/23	22:03:02.0	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/08/23	22:55:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/08/23	23:24:30.0	XRT_Custom_430_OG [0x1ae]							
2009/08/23	23:25:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/08/24	00:33:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/08/24	00:48:30.0	XRT_Custom_430_OG [0x1ae]							
2009/08/24	00:49:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/08/24	02:04:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/08/24	02:25:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/08/24	03:39:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/08/24	04:04:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/08/24	05:11:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/08/24	05:42:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/08/24	05:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/08/24	05:59:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2009/08/24	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2009/08/24	06:00:16.0	XRT_FLD_DIS_419_OG [0x1a3]							

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2009/08/24	06:00:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/08/24	06:00:20.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/08/24	06:02:58.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2009/08/24	06:03:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13		
2009/08/24	06:10:00.0	AOCS_OrE-point_Start_7_OG [0x09d]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/08/24	07:26:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	AOCU_NM	5	02-76	03	00	00	00
2009/08/24	07:26:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/08/24	07:27:16.0	XRT_QT_PROG_SET_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2009/08/24	07:27:18.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14		
2009/08/24	07:27:20.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2009/08/24	07:27:22.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/08/24	07:30:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/08/24	08:31:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/08/24	08:59:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/08/24	10:12:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/08/24	10:37:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/08/24	12:03:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/08/24	12:16:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/08/24	13:41:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/08/24	13:54:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/08/24	15:20:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/08/24	15:58:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/08/24	15:59:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CUSTOM_430_OG [0x1ae]	1	07-F0	c0			
2009/08/24	16:58:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/08/24	17:35:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/08/24	17:36:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CUSTOM_430_OG [0x1ae]	1	07-F0	c0			
2009/08/24	18:06:24.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/08/24	18:06:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/08/24	18:06:30.0	AOCS_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2009/08/24	18:06:46.0	XRT_FLD_DIS_419_OG [0x1a3]	AOCU_NM	5	02-76	00	00	00	00
2009/08/24	18:06:48.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/08/24	18:06:50.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/08/24	18:09:28.0	XRT_QT_PROG_SET_400_OG [0x190]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2009/08/24	18:09:30.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01		
2009/08/24	18:16:30.0	AOCS_OrE-point_Start_6_OG [0x09c]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/08/24	22:22:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	AOCU_NM	5	02-76	01	00	00	00
2009/08/24	22:22:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/08/24	22:23:16.0	XRT_QT_PROG_SET_407_OG [0x197]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2009/08/24	22:23:18.0	XRT_AEC_RESET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09		
2009/08/24	22:23:20.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2009/08/24	22:23:22.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2009/08/24	22:23:24.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/08/24	22:26:02.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/08/24	23:33:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/08/24	23:54:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/08/24	23:55:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CUSTOM_430_OG [0x1ae]	1	07-F0	c0			
2009/08/25	01:09:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/08/25	01:24:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1			

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2009/08/25	01:25:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/08/25	02:40:00.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/08/25	03:02:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/08/25	04:08:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/08/25	04:40:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/08/25	05:49:00.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/08/25	06:19:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/08/25	06:25:24.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/08/25	06:25:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2009/08/25	06:25:30.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2009/08/25	06:25:46.0	XRT_FLD_DIS_419_OG [0x1a3]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/08/25	06:25:48.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/08/25	06:25:50.0	XRT_ARS_DIS_410_OG [0x19a]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/08/25	06:28:28.0	XRT_QT_PROG_SET_409_OG [0x199]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2009/08/25	06:28:30.0	XRT_CTRL_AUTO_403_OG [0x193]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/08/25	06:35:30.0	AOCS_ORe-point_Start_8_OG [0x09e]			
		AOCU_NM	5	02-76	04 00 00 00 00
2009/08/25	07:54:24.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/08/25	07:54:26.0	XRT_FOCUS_POSITION_441_OG [0x1b9]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/08/25	07:54:46.0	XRT_QT_PROG_SET_420_OG [0x1a4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2009/08/25	07:54:48.0	XRT_ARS_DIS_422_OG [0x1a6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/08/25	07:54:50.0	XRT_FLD_DIS_445_OG [0x1bd]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/08/25	07:54:52.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/08/25	07:57:30.0	XRT_CTRL_AUTO_403_OG [0x193]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/08/25	09:09:00.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/08/25	09:36:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/08/25	10:51:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/08/25	10:56:30.0	XRT_Custom_430_OG [0x1ae]			
2009/08/25	10:57:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/08/25	11:02:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/08/25	12:39:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00