XRT Timeline to be uploaded on 2009/08/27

Period: 2009/08/27 10:49:00 - 2009/08/31 09:29:00

* * * * * **Normal mode** * * * * *

XOB #1563: CCD Monitor During Bakeout - G-band + dark - wide FOV

Term	Pointing (x, y)	Comment
08/27 11:00:11 - 08/27 13:00:00	Fixed (0.0, 910.0)	# OP start + 10min, N pole. EIS looking for jet in slit.
08/27 21:04:40 - 08/28 01:51:54	Fixed (0.0, 910.0)	# OP start + 10min, N pole. EIS looking for jet in slit.

PROG= 08 Inf.-time(s)
Subr= 1 1-time(s) 600.0sec
Seqn= 98 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec

XOB #1565: CCD Monitor During Bakeout - G-band + dark - wide FOV - lower cadence (30min)

Term	Pointing (x, y)	Comment
08/27 13:00:10 - 08/27 20:39:30	Fixed (0.0, 910.0)	# OP start + 10min, N pole. EIS looking for jet in slit.

PROG= 06 Inf.-time(s)
Subr= 1 1-time(s) 1800.0sec
Seqn= 98 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec

XOB #15A8: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant

Term	Pointing (x, y)	Comment
08/28 01:55:30 - 08/28 01:57:00	Fixed (-528.4, -528.4)	* Post-XRT-bakeout four-quadrant pointings; four in total.

PROG= 12 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 28 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec

XOB #16FB: Synoptic Q95 2x2 - Al/mesh (5795) + Dark cal(5795 Q98) + Ti-poly (11571)

Term	Pointing (x, y)	Comment
08/28 01:57:10 - 08/28 01:58:54	Fixed (-528.4, -528.4)	* Post-XRT-bakeout four-quadrant pointings; four in total.
08/28 02:04:10 - 08/28 02:05:54	Fixed (528.4, -528.4)	
08/28 02:11:10 - 08/28 02:18:54	Fixed (528.4, 528.4)	
08/28 02:24:10 - 08/28 02:29:54	Fixed (-528.4, 528.4)	* Final four-quadrant pointing.

PROG= 10 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 9 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 64 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 71 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

XOB #15A9: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant

Term	Pointing (x, y)	Comment
08/28 02:02:30 - 08/28 02:04:00	Fixed (528.4, -528.4)	

PROG= 05 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 41 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec

XOB #15AA: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant

Term	Pointing (x, y)	Comment
08/28 02:09:30 - 08/28 02:11:00	Fixed (528.4, 528.4)	

PROG= 16 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 8 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec

Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #15AB: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant

Term	Pointing (x, y)	Comment
08/28 02:22:30 - 08/28 02:24:00	Fixed (-528.4, 528.4)	* Final four-quadrant pointing.

PROG= 03 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 40 1-time(s) 12.0sec

Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #16F9: XBP Al/mesh - Ti/Poly - FOV384 - Q90 - AEC4 - 1min cadence

Term	Pointing (x, y)	Comment
08/28 02:33:02 - 08/28 08:26:24	Track (-50.4, 0.2) @ 08/28 02:30:00	# HOP 124, XBPs near disk center; XRT synoptics to be done near 8:00 UT.

PROG= 09 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 76 60-time(s) 60.0sec

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=90	4	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1024, 1024)	Q=90	4	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 66 1-time(s) 4.0sec

Open/Al-mesh	Open/G-band	close	Safe	Dark	4.00s	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #16D8: Synoptic Q95 2x2 - Al/mesh(512/5795) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 2048x512 -1x1 512x2048) +Ti-poly(723/11571) + G-band(

Term	Pointing (x, y)	Comment
08/28 08:26:54 - 08/28 08:29:54	Track (-50.4, 0.2) @ 08/28 02:30:00	# HOP 124, XBPs near disk center; XRT synoptics to be done near 8:00 UT.

PROG= 19 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 47 1-time(s) 4.0sec

Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 85 1-time(s) 2.0sec

Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 88 1-time(s) 4.0sec

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 92 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #16ED: HOP 126 (Ti/poly fov384x384 exp16s 60s-cadence)

Term	Pointing (x, y)	Comment
08/28 08:33:00 - 08/28 10:29:54	Track (4.6, -0.0) @ 08/28 08:30:00	# HOP 126, with La Palma, disc-center tracking.

PROG= 13 Inf.-time(s)

Subr= 1 1-time(s) 52.5sec

Seqn= 79 1-time(s) 4.0sec

Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1024, 1024)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

* * * * * **Flare mode** * * * * *

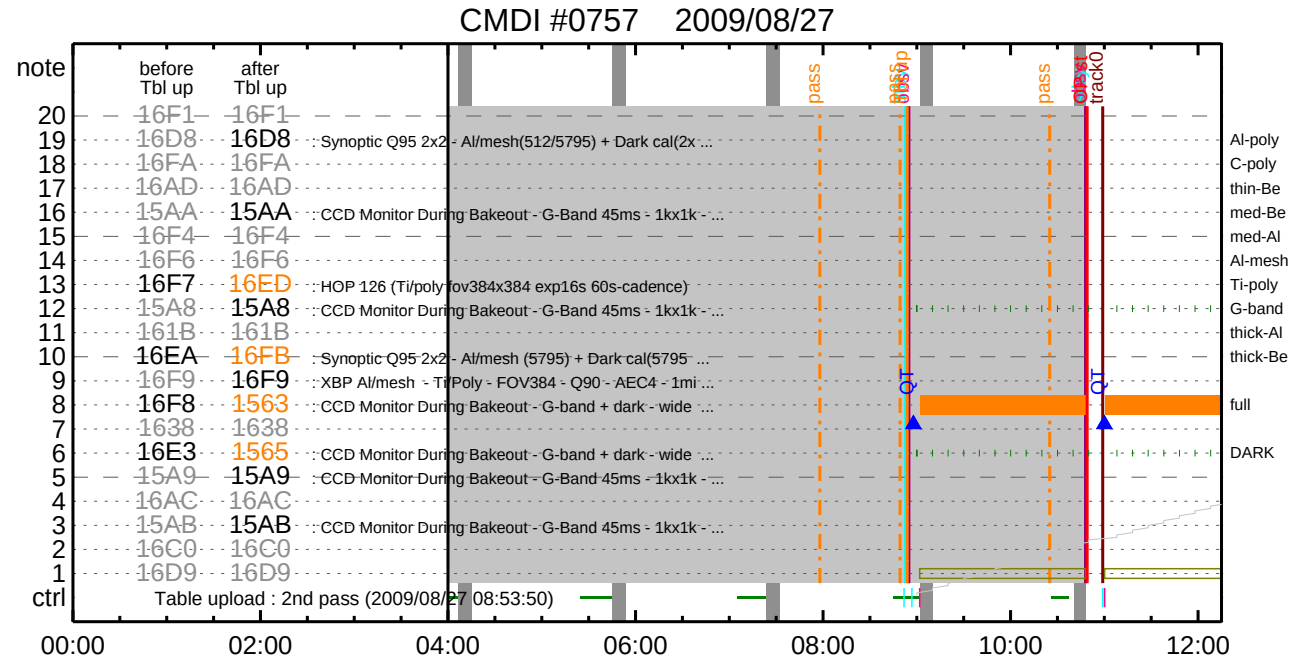
NOT USED

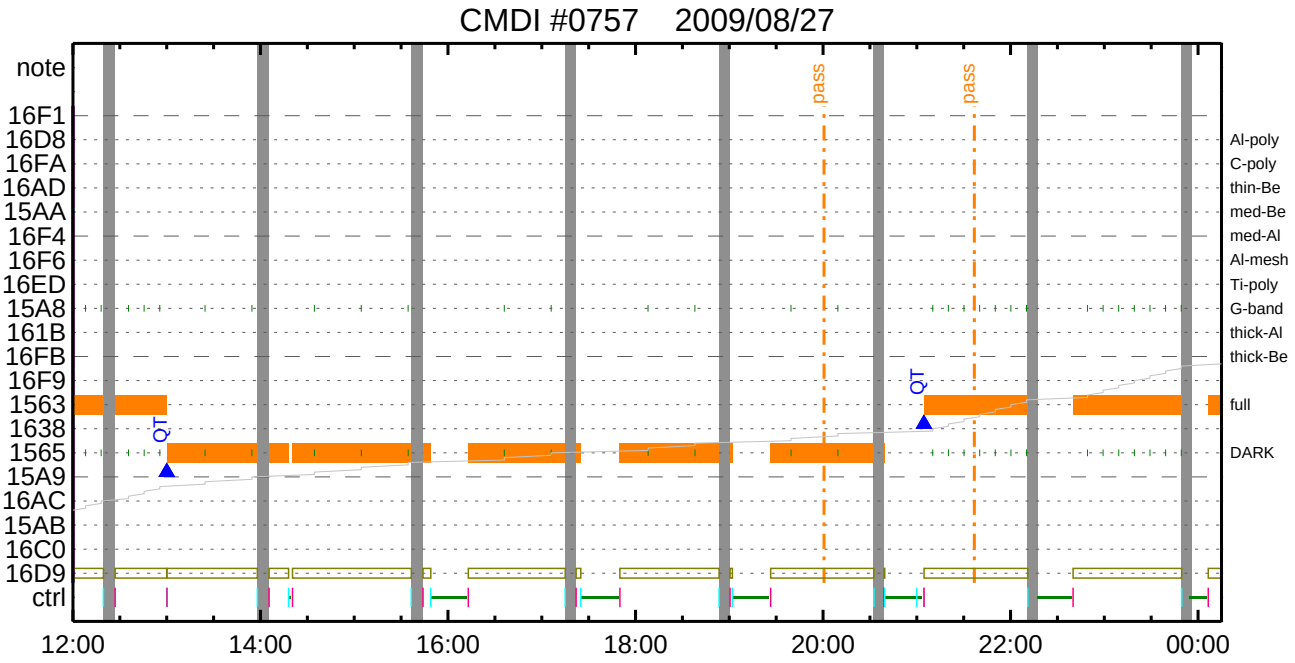
* * * * * **Active Region Search** * * * * *

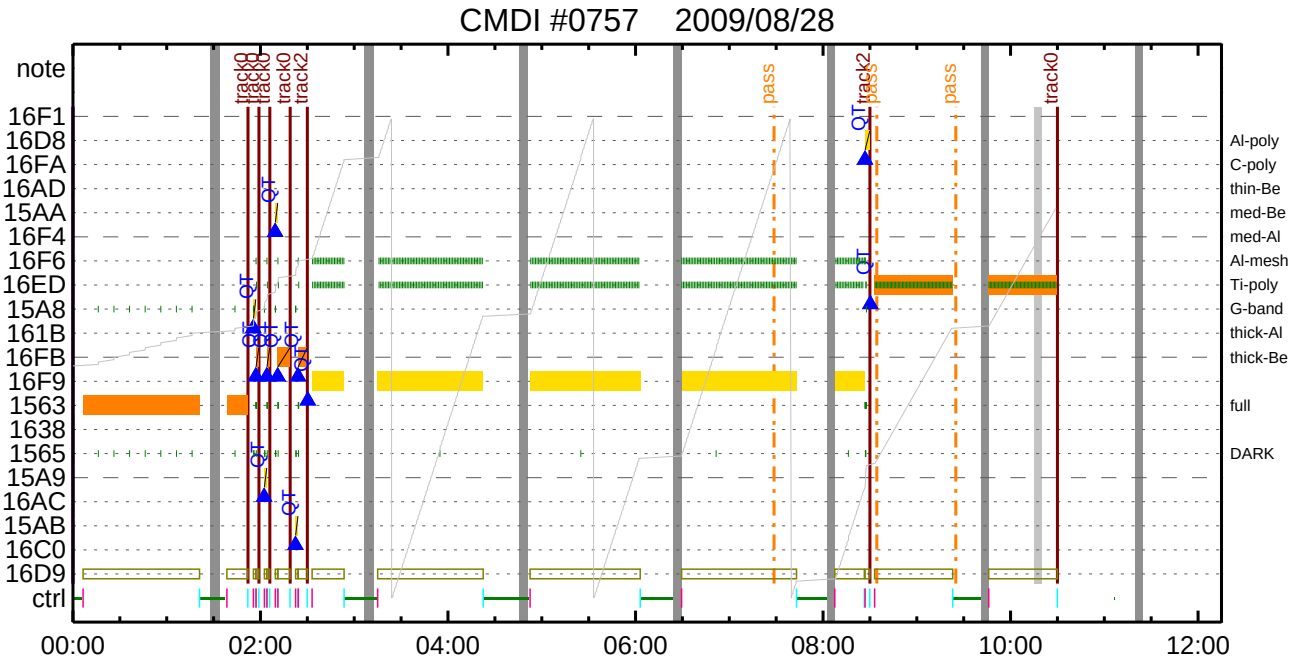
NOT USED

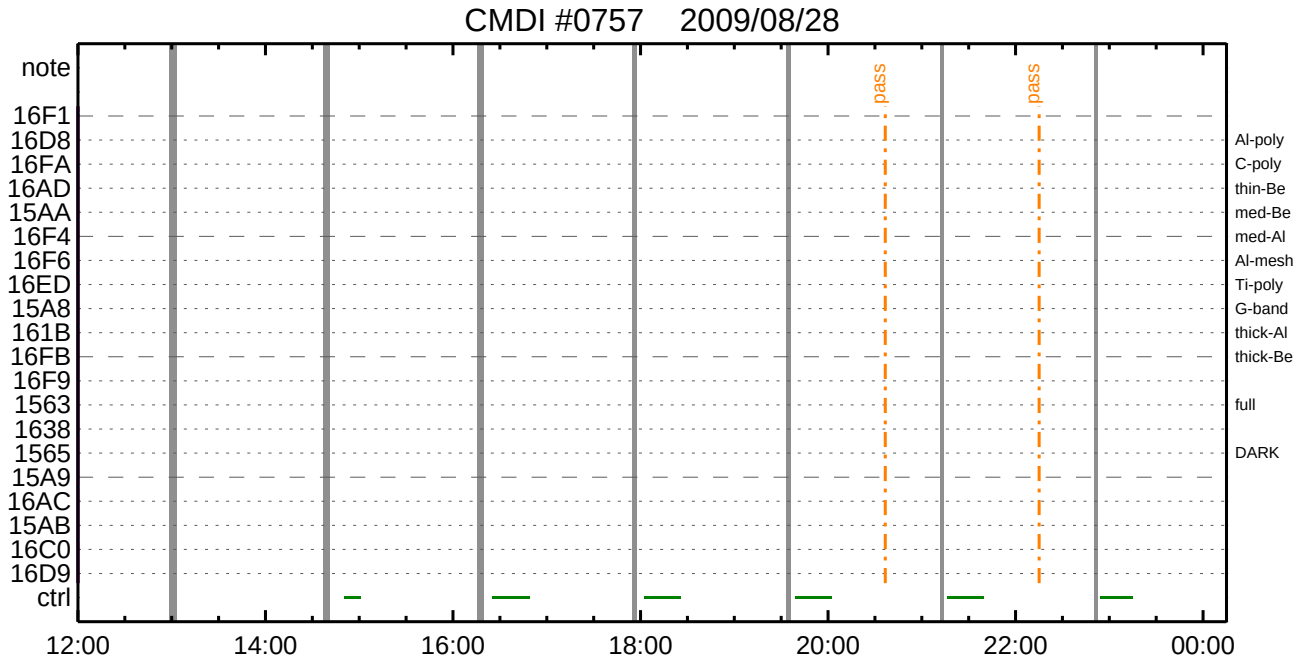
* * * * * **Flare Detection** * * * * *

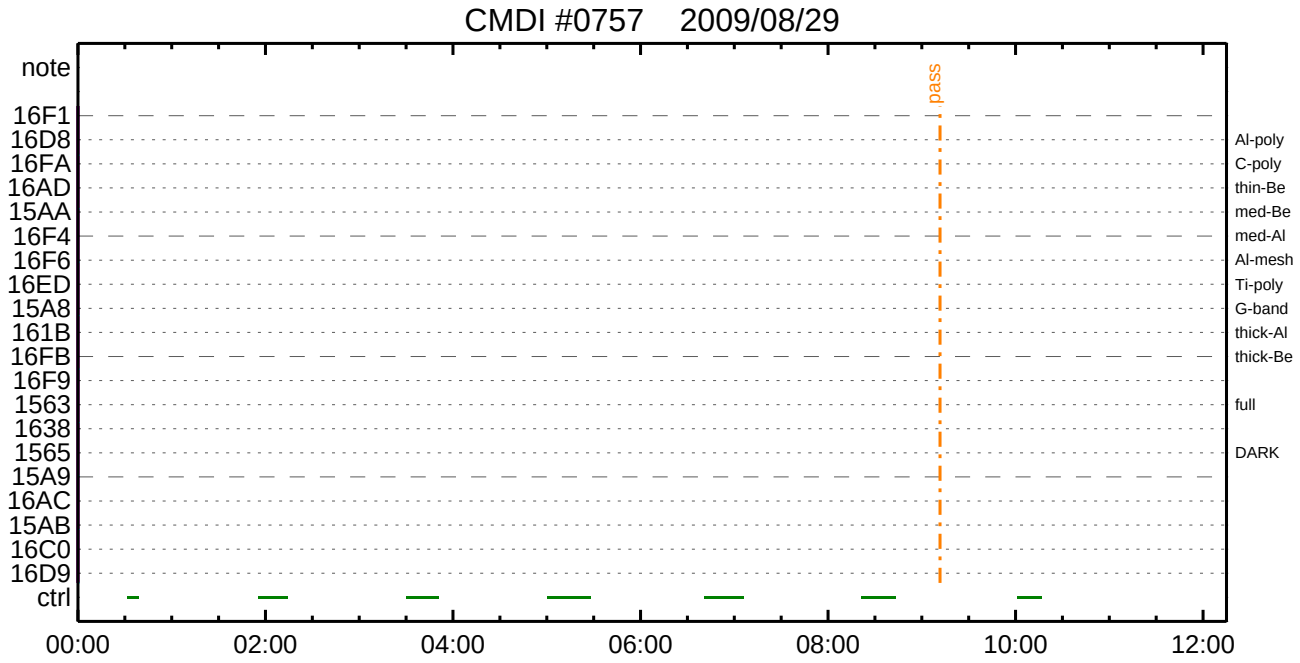
NOT USED


CMDI #0757 2009/08/27
note
before Tbl up
after Tbl up
20 16F1 16F1
19 16D8 16D8 : Synoptic Q95 2x2 - Al/mesh(512/5795) + Dark cal(2x ...
18 16FA 16FA
17 16AD 16AD
16 15AA 15AA : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
15 16F4 16F4
14 16F6 16F6
13 16F7 16ED : HOP 126 (Ti/poly fov384x384 exp16s 60s-cadence)
12 15A8 15A8 : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
11 161B 161B
10 16EA 16FB : Synoptic Q95 2x2 - Al/mesh (5795) + Dark cal(5795 ...
9 16F9 16F9 : XBP Al/mesh - Ti/Poly - FOV384 - Q90 - AEC4 - 1mi ...
8 16F8 1563 : CCD Monitor During Bakeout - G-band + dark - wide ...
7 1638 1638
6 16E3 1565 : CCD Monitor During Bakeout - G-band + dark - wide ...
5 15A9 15A9 : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
4 16AC 16AC
3 15AB 15AB : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
2 16C0 16C0
1 16D9 16D9
ctrl Table upload : 2nd pass (2009/08/27 08:53:50)
pass
track0
QT
00:00
02:00
04:00
06:00
08:00
10:00
12:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0757 2009/08/27
note
16F1
16D8
16FA
16AD
15AA
16F4
16F6
16ED
15A8
161B
16FB
16F9
1563
1638
1565
15A9
16AC
15AB
16C0
16D9
ctrl
pass
QT
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0757 2009/08/28
note
16F1
16D8
16FA
16AD
15AA
16F4
16F6
16ED
15A8
161B
16FB
16F9
1563
1638
1565
15A9
16AC
15AB
16C0
16D9
ctrl
track0
track2
pass
QT
00:00
02:00
04:00
06:00
08:00
10:00
12:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0757 2009/08/28
note
16F1
16D8
16FA
16AD
15AA
16F4
16F6
16ED
15A8
161B
16FB
16F9
1563
1638
1565
15A9
16AC
15AB
16C0
16D9
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0757 2009/08/29
note
16F1
16D8
16FA
16AD
15AA
16F4
16F6
16ED
15A8
161B
16FB
16F9
1563
1638
1565
15A9
16AC
15AB
16C0
16D9
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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04:00
06:00
08:00
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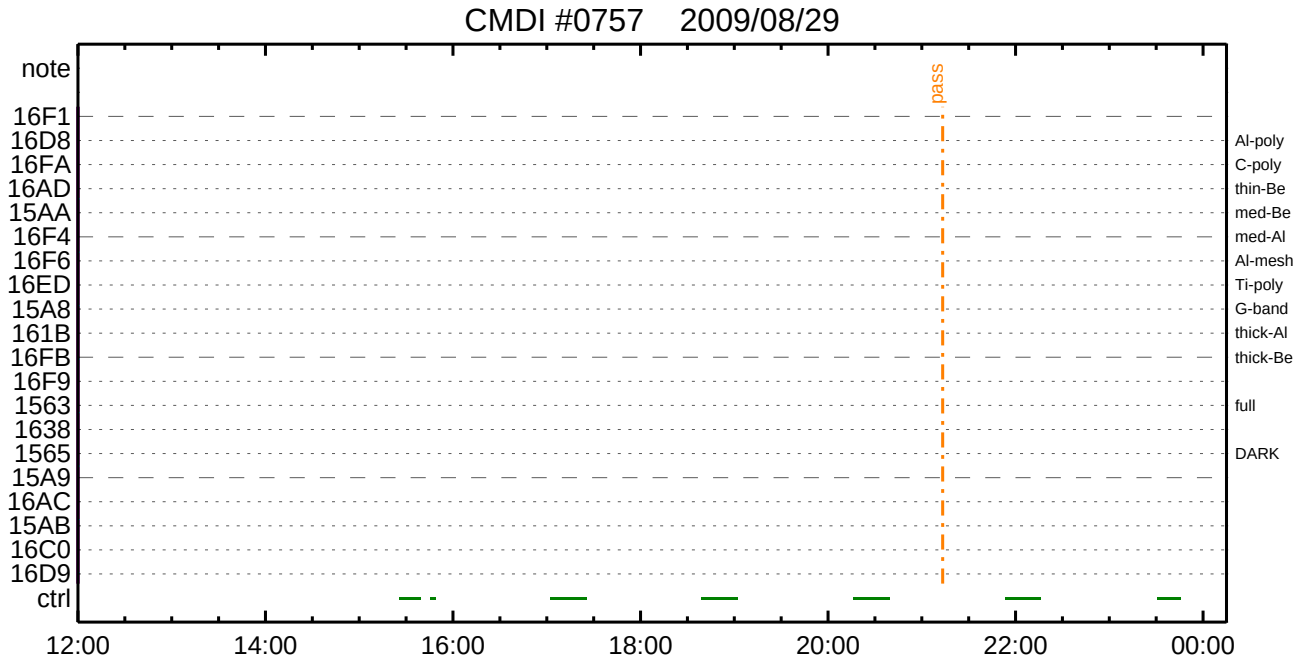
12:00


CMDI #0757 2009/08/29
note
16F1
16D8
16FA
16AD
15AA
16F4
16F6
16ED
15A8
161B
16FB
16F9
1563
1638
1565
15A9
16AC
15AB
16C0
16D9
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
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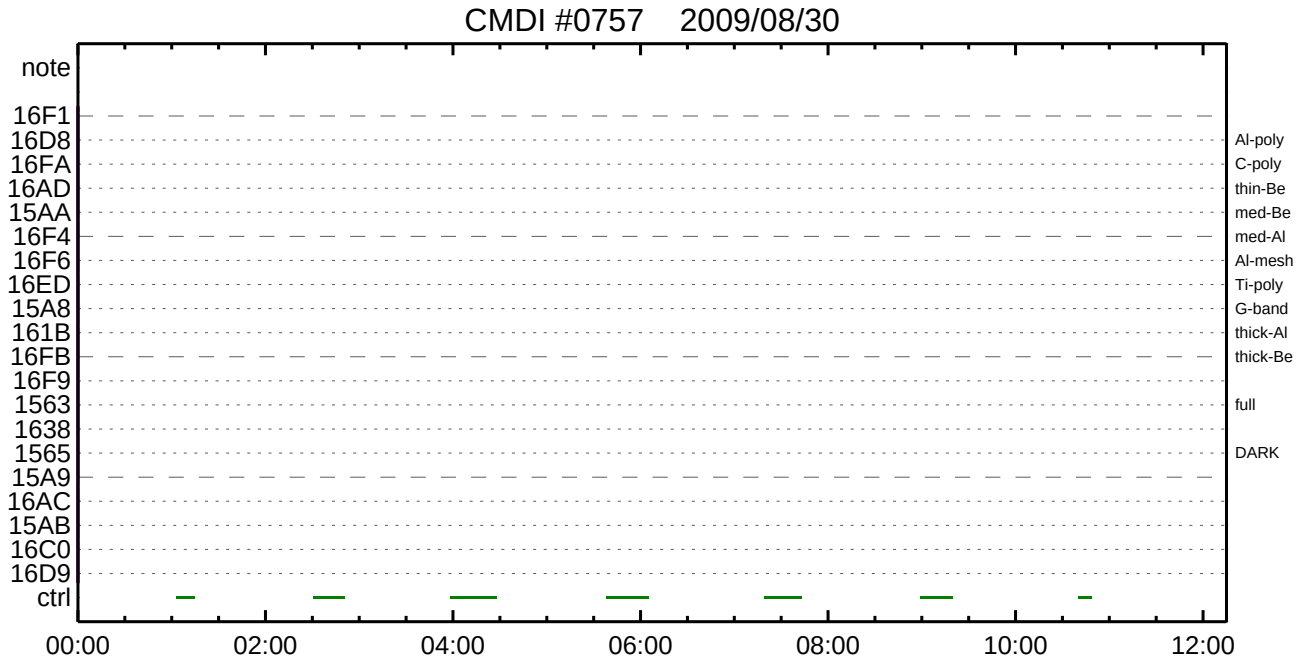
CMDI #0757 2009/08/30
note
16F1
16D8
16FA
16AD
15AA
16F4
16F6
16ED
15A8
161B
16FB
16F9
1563
1638
1565
15A9
16AC
15AB
16C0
16D9
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
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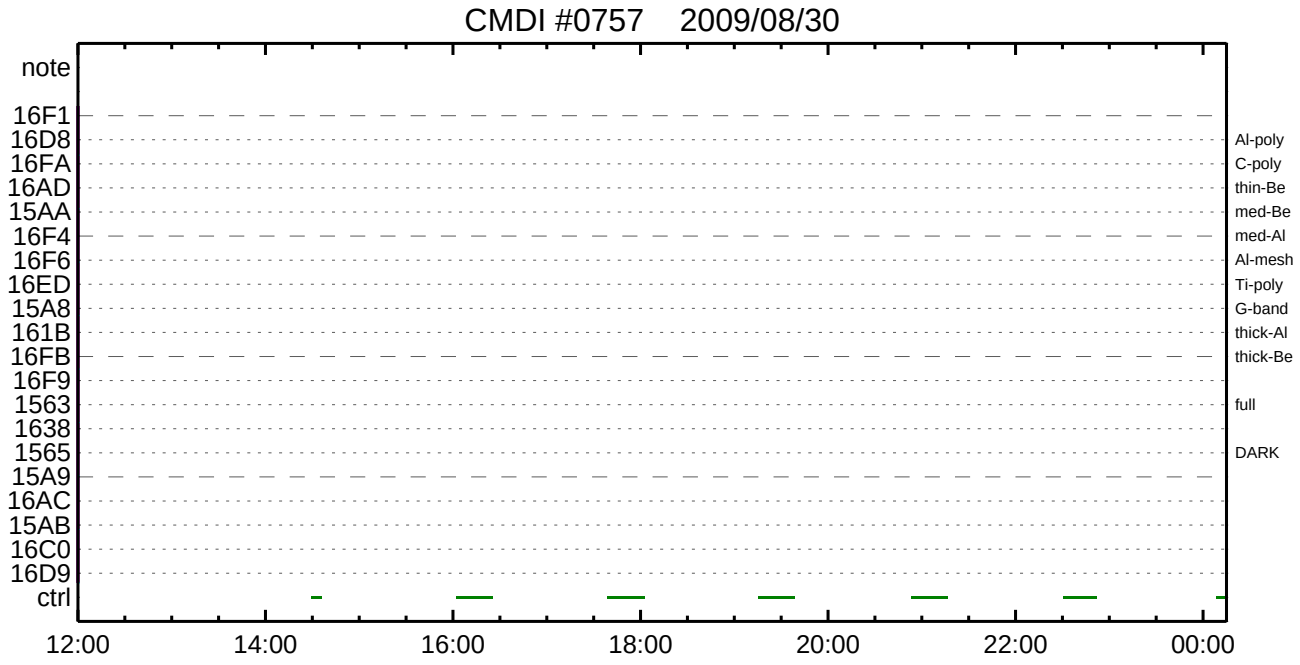
06:00
08:00
10:00
12:00


CMDI #0757 2009/08/30
note
16F1
16D8
16FA
16AD
15AA
16F4
16F6
16ED
15A8
161B
16FB
16F9
1563
1638
1565
15A9
16AC
15AB
16C0
16D9
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
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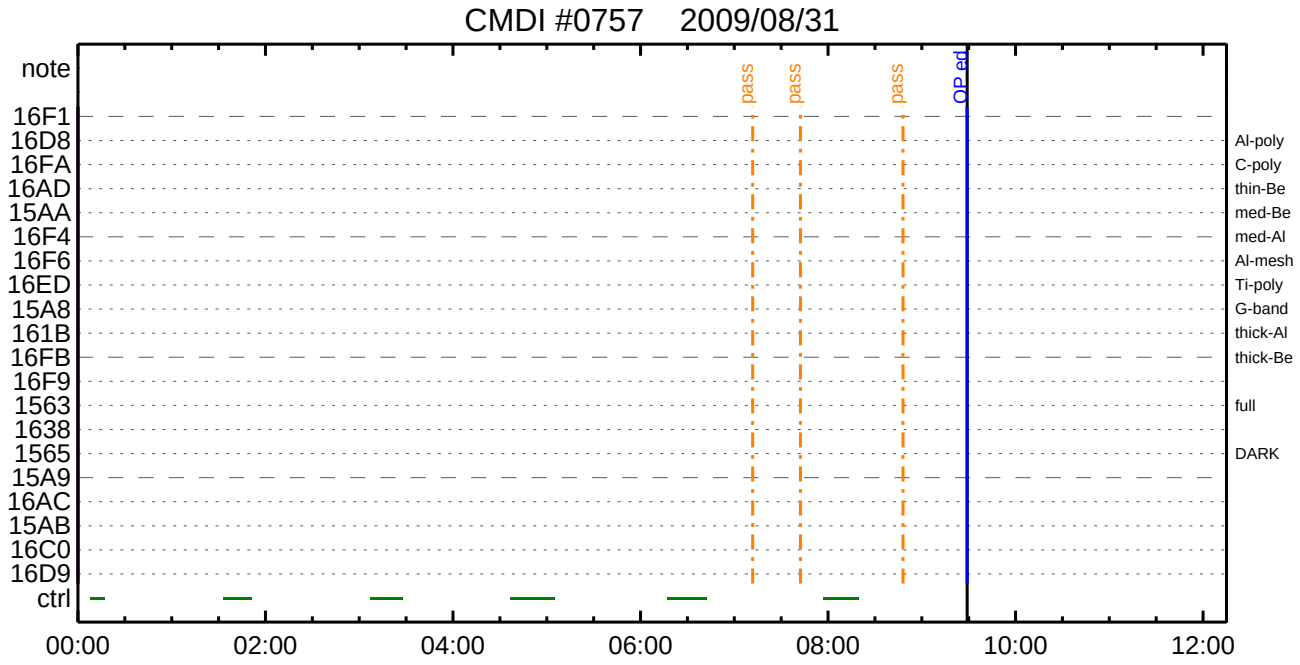
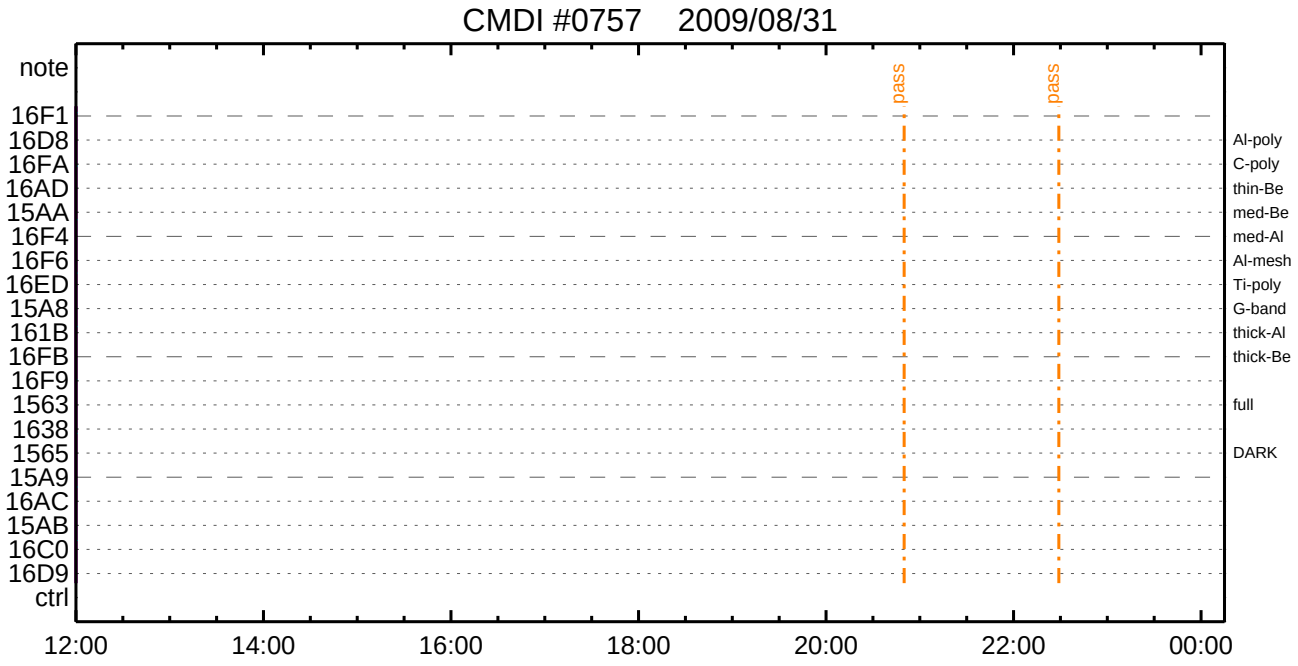

CMDI #0757 2009/08/31
note
pass
pass
pass
OP ed
16F1
16D8
16FA
16AD
15AA
16F4
16F6
16ED
15A8
161B
16FB
16F9
1563
1638
1565
15A9
16AC
15AB
16C0
16D9
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0757 2009/08/31
note
16F1
16D8
16FA
16AD
15AA
16F4
16F6
16ED
15A8
161B
16FB
16F9
1563
1638
1565
15A9
16AC
15AB
16C0
16D9
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
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```
(a) Spacecraft Operation Procedure (real-commands)

main-807 2009-08-27 12:48:43 278 33 SOLAR-B MAIN //
0001     C.
0002   . C. ********** AOS **********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇÎ»þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
     C.
     C.
   . C. ****************************
     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
     C. ****************************
     C.
   . C. ¡ãOP/OG¥í¡¼¥É¡ä
   . S. OP    op-807:OP
     ()
   . S. OG    og-807:OG
     ()
     C.
   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
  +. DC 01-23 DHU_DMA_DMP_PRM_SET
     BC       (20 00 7f 01 02)
     C.                   ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
     C.                   ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
     C.                   ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
     C.                   ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
     C.                   ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
  +. DC 01-22 DHU_MODE_CHNG
     BC       (07 0b f8)
     C.                   ¢¢[HK1_PKT_FORM_NO]             EQ     7
     C.                   ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
     C.                   ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
     C.                   ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
     C.                   ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
     C.                   ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
     C.
     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
  +. DC 01-23 DHU_DMA_DMP_PRM_SET
     BC       (20 80 7f 01 02)
     C.                   ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
     C.                   ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
     C.                   ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
     C.                   ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
     C.                   ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
  +. DC 01-22 DHU_MODE_CHNG
     BC       (07 0b f8)
     C.                   ¢¢[HK1_PKT_FORM_NO]             EQ     7
     C.                   ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
     C.                   ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
     C.                   ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
     C.                   ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
     C.                   ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
     C.
     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
  +. DC 01-23 DHU_DMA_DMP_PRM_SET
     BC       (21 00 41 01 02)
     C.                   ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
     C.                   ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
     C.                   ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
     C.                   ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
     C.                   ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
  +. DC 01-22 DHU_MODE_CHNG
     BC       (07 0b f8)
     C.                   ¢¢[HK1_PKT_FORM_NO]             EQ     7
     C.                   ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
     C.                   ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
     C.                   ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
     C.                   ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
     C.                   ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
     C.
   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
     C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤ÎÌá¤¹
  +. DC 01-22 DHU_MODE_CHNG
     BC       (02 0a f8)
     C.                   ¢¢[HK1_PKT_FORM_NO]             EQ     2
     C.                   ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
     C.                   ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
     C.                   ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
     C.
   . C. *********************************
     C. TI-CMD SET (OPOG STOP/COPY/START)
     C. *********************************
     C.
   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
     C.
   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
  +. TI 2009-08-27 10:44:00.0
     DC 01-B3 DHU_OP_STOP
     C.                   ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
     C.
  +. TI 2009-08-27 10:44:01.0
     DC 01-B4 DHU_OP_COPY
     C.                   ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
     C.
  +. TI 2009-08-27 10:44:01.0
     DC 01-B5 DHU_OPOG_COPY
     C.                   ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195   +. TI 2009-08-27 10:48:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238     C.
0239     C. ************ XRT START ***********
0240     C. Execute, after the success of OP upload.
0241   +. TI 2009-08-27 10:48:00.0
0242     DC 07-F0 MDP_XRT_MODE_STBY
0243     BC       (c3)
0244    . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0245     C.
0246     C. ************ XRT END ***********
0247    . C. Stop EIS observation and temporarily disable EIS mode changes
0248     C.
0249     C.
0250     C. ********** Start EIS operation (TI set) **********
0251     C. Execute, after the success of OP upload.
0252     C. Set EIS TI-commands
0253   +. TI 2009-08-27 10:48:30.0
0254     DC 07-FC EIS_MODE_MANU
0255     BC       (21 02)
0256   +. TI 2009-08-27 10:48:40.0
0257     DC 07-FC EIS_MODE_CHG_DIS
0258     BC       (22)
0259    . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0260     C. *********** End EIS operation (TI set) ***********
0261     C.
0262     C.
0263     C.
0264    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0265     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0266    . S. DC-BC dcbc-402:DCBC
0267     (MDP_known_event)
0268     C.
0269     C.
0270    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0271    . S. DC-BC dcbc-153:DCBC
0272     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273     C.
0274     C.
0275    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0276     C.
0277    . C. *********** LOS ***********
0278     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-808 2009-08-27 12:48:43 179 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0015     C. Upload the Orbit Element and the Target Attitude
0016     C. RAM-ID:TARGET_ATT
0017   . S. RAM   ram-150:TARGET_ATT
0018     ()
0019     C.
0020     C.
0021     C. Set the dump memory area of TARGET_ATT
0022   +. DC 02-48 AOCU_DUMP_SET
0023     BC       (07 00 00 00 18 00)
0024     C.
0025     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026     C.
0027     C.
0028     C. Change the TLMFormatNo for the AOCS Dump Format
0029   +. DC 01-22 DHU_MODE_CHNG
0030     BC       (04 0b f8)
0031     C.
0032     C. Wait for AOCSDUMP to end
0033     C.
0034   . C. Check the dump memory
0035     C.
0036     C.                                      Result = OK [ ]
0037     C.
0038   +. DC 01-22 DHU_MODE_CHNG
0039     BC       (02 0a f8)
0040     C.
0041     C.                          <A_***>[TLM STS] FMT = 2 [ ]
0042     C.
0043   +. DC 02-8E AOCU_ORB_UPD
0044    . C.
0045     C.
0046     C. ************ XRT START ************
0047     C.
0048   +. DC 07-F0 MDP_XRT_CTRL_MANU
0049     BC       (c1)
0050   +  DC 07-F0 MDP_XRT_MODE_STBY
0051     BC       (c3)
0052    . C. ----------- Success Verify ?             OK / NG____
0053     C.
0054     C. XRT Obs. Table Upload
0055   . S. RAM   ram-291:MDP_OBS_X
0056     ()
0057     C.
0058   +. DC 07-F0 MDP_DUMP_XRTTBL
0059     BC       (84 07 00 00 00 3a d4)
0060    . C. ----------- Comparison Check ?                       OK / ERR ____
0061     C.
0062     C.
0063   +. DC 07-F0 MDP_XRT_ROI_SET
0064     BC       (cd 01 b1 b1 04 04)
0065   +  DC 07-F0 MDP_XRT_ROI_SET
0066     BC       (cd 02 b1 b1 08 08)
0067   +  DC 07-F0 MDP_XRT_ROI_SET
0068     BC       (cd 03 b1 b1 08 08)
0069   +  DC 07-F0 MDP_XRT_ROI_SET
0070     BC       (cd 04 b1 b1 06 06)
0071   +  DC 07-F0 MDP_XRT_ROI_SET
0072     BC       (cd 06 80 80 20 04)
0073   +  DC 07-F0 MDP_XRT_ROI_SET
0074     BC       (cd 07 c0 c0 10 10)
0075   +  DC 07-F0 MDP_XRT_ROI_SET
0076     BC       (cd 08 80 80 20 20)
0077   +  DC 07-F0 MDP_XRT_ROI_SET
0078     BC       (cd 09 40 c0 10 10)
0079   +  DC 07-F0 MDP_XRT_ROI_SET
0080     BC       (cd 0a 40 40 10 10)
0081   +  DC 07-F0 MDP_XRT_ROI_SET
0082     BC       (cd 0b c0 40 10 10)
0083   +  DC 07-F0 MDP_XRT_ROI_SET
0084     BC       (cd 0c 80 80 06 06)
0085   +  DC 07-F0 MDP_XRT_ROI_SET
0086     BC       (cd 0d 80 80 20 08)
0087   +  DC 07-F0 MDP_XRT_ROI_SET
0088     BC       (cd 0e 80 80 08 20)
0089   +  DC 07-F0 MDP_XRT_ROI_SET
0090     BC       (cd 0f 80 80 04 04)
0091   +  DC 07-F0 MDP_XRT_ROI_SET
0092     BC       (cd 10 80 80 10 10)
0093    . C. ---------- Success Verify ?           OK / NG ____
0094     C.
0095     C.
```

```
  . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
    C.
 +. DC 07-F0 MDP_XRT_MODE_OBSV
    BC       (c2)
 +. TI 2009-08-27 10:48:02.0
    DC 07-F0 MDP_XRT_MODE_OBSV
    BC       (c2)
  . C. ---------- Success Verify ?            OK / NG ____
    C.
    C. ************ XRT END ***********
  . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
 +. DC 07-FC EIS_MODE_MANU
    BC       (21 02)
  . C. Verify EIS in MANUAL mode
  . C. Estimated OBSTBL upload time is 2s
    C. *********************************
    C. EIS START OBSTBL LOAD
    C. *********************************
  . S. RAM   ram-820:EIS_OBSTBL
    ()
 +. DC 07-FC EIS_DUMP_OBSTBL
    BC       (07 07 07 00 00 70 00)
    C.
    C. Execute, after the success of OBSTBL upload.
    C. Set EIS TI-commands
 +. TI 2009-08-27 10:48:50.0
    DC 07-FC EIS_MODE_CHG_ENA
    BC       (20)
  . C.                    [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
    C. *********************************
    C. EIS END OBSTBL LOAD
    C. *********************************
    C. ************************************************
    C.  START of XRT_CCD_HEATER_ON operation
    C. ************************************************
    C.
 +. DC 07-F0 MDP_XRT_CTRL_MANU
    BC       (c1)
    C. ---------- Success Verify ?           OK / NG;
    C.
 +. DC 07-F0 MDP_XRT_QT_PROG_SET
    BC       (c4 08)
 +  DC 07-F0 MDP_XRT_FLD_DIS
    BC       (d9)
 +  DC 07-F0 MDP_XRT_FLRCTRL_DIS
    BC       (c9)
 +  DC 07-F0 MDP_XRT_ARS_DIS
    BC       (d5)
    C. ---------- Success Verify ?            OK / NG ____
    C.
    C.
    C. All OK?   Yes--> Please Proceed. / No --> Stop here.
    C.
 +. DC 07-F0 MDP_XRT_CTRL_AUTO
    BC       (c0)
    C. ---------- Success Verify ?           OK / NG;
    C.
 +. DC 04-BC TCIB_XRT_S_HTR_A_ENA
    C. ---------- Success Verify ?           OK / NG;
    C.
    C. ----------------------------------------
    C. If anomalous situation appeared, execute TCIB_XRT_S_HTR_A_DIS using DCBC-441 (line 24)
    C. ----------------------------------------
    C. ************************************************
    C.  END of XRT_CCD_HEATER_ON operation
    C. ************************************************
    C.
    C.
    C.
  . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
    C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
  . S. DC-BC dcbc-402:DCBC
    (MDP_known_event)
    C.
    C.
  . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
  . S. DC-BC dcbc-153:DCBC
    (SPECIAL-CMD_DAILY_OPERATIN_DCB)
    C.
    C.
  . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
    C.
  . C. ********** LOS **********
    C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-809 2009-08-27 12:48:43 29 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011  +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014     C.
0015   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0016     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0017   . S. DC-BC dcbc-402:DCBC
0018     (MDP_known_event)
0019     C.
0020     C.
0021   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0022   . S. DC-BC dcbc-153:DCBC
0023     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0024     C.
0025     C.
0026   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0027     C.
0028   . C. *********** LOS ***********
0029     C.
```

```
*** OP Sequence for XRT ***

2009/08/27  10:58:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/27  10:59:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 af 1b 00 00
2009/08/27  11:00:01.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/27  11:00:03.0  XRT_QT_PROG_SET_412_OG [0x19c]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 08
2009/08/27  11:00:05.0  XRT_ARS_DIS_422_OG [0x1a6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2009/08/27  11:00:07.0  XRT_FLD_DIS_445_OG [0x1bd]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2009/08/27  11:00:09.0  XRT_FLRCTRL_DIS_416_OG [0x1a0]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/08/27  11:00:11.0  XRT_CTRL_AUTO_403_OG [0x193]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/27  12:19:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/27  12:27:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/27  13:00:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/27  13:00:02.0  XRT_QT_PROG_SET_431_OG [0x1af]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 06
2009/08/27  13:00:04.0  XRT_ARS_DIS_422_OG [0x1a6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2009/08/27  13:00:06.0  XRT_FLD_DIS_445_OG [0x1bd]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2009/08/27  13:00:08.0  XRT_FLRCTRL_DIS_416_OG [0x1a0]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/08/27  13:00:10.0  XRT_CTRL_AUTO_403_OG [0x193]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/27  13:58:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/27  14:05:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/27  14:18:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/27  14:19:30.0  XRT_Custom_430_OG [0x1ae]
2009/08/27  14:20:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/27  15:36:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/27  15:44:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/27  15:49:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/27  16:12:00.0  XRT_Custom_430_OG [0x1ae]
2009/08/27  16:13:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/27  17:15:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/27  17:22:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/27  17:25:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/27  17:49:00.0  XRT_Custom_430_OG [0x1ae]
2009/08/27  17:50:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/27  18:53:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/27  19:00:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/27  19:02:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/27  19:25:30.0  XRT_Custom_430_OG [0x1ae]
2009/08/27  19:26:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/27  20:32:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/27  20:39:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/27  20:39:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/27  21:00:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/27  21:00:02.0  XRT_TCIB_XRT_S_HTR_A_DIS_427_OG [0x1ab]
                                TCIB_XRT_S_HTR_A_DIS 0 04-C0
2009/08/27  21:04:30.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/27  21:04:32.0  XRT_QT_PROG_SET_412_OG [0x19c]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 08
2009/08/27  21:04:34.0  XRT_ARS_DIS_422_OG [0x1a6]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2009/08/27  21:04:36.0  XRT_FLD_DIS_445_OG [0x1bd]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2009/08/27  21:04:38.0  XRT_FLRCTRL_DIS_416_OG [0x1a0]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/08/27  21:04:40.0  XRT_CTRL_AUTO_403_OG [0x193]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/27  22:11:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
```

```
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/27  22:39:00.0  XRT_Custom_430_OG [0x1ae]
2009/08/27  22:40:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/27  23:49:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/28  00:05:30.5  XRT_Custom_430_OG [0x1ae]
2009/08/28  00:06:30.5  XRT_CTRL_AUTO_432_OG [0x1b0]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/28  01:21:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/28  01:37:30.0  XRT_Custom_430_OG [0x1ae]
2009/08/28  01:38:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/28  01:51:54.0  XRT_CTRL_MANU_444_OG [0x1bc]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/28  01:52:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 2e f9 2e f9
2009/08/28  01:55:02.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2009/08/28  01:55:22.0  XRT_QT_PROG_SET_438_OG [0x1b6]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2009/08/28  01:55:24.0  XRT_FLD_DIS_419_OG [0x1a3]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2009/08/28  01:55:26.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/08/28  01:55:28.0  XRT_ARS_DIS_446_OG [0x1be]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2009/08/28  01:55:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/28  01:57:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/28  01:57:02.0  XRT_QT_PROG_SET_406_OG [0x196]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0a
2009/08/28  01:57:04.0  XRT_FLD_DIS_419_OG [0x1a3]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2009/08/28  01:57:06.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/08/28  01:57:08.0  XRT_ARS_DIS_446_OG [0x1be]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2009/08/28  01:57:10.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/28  01:58:54.0  XRT_CTRL_MANU_444_OG [0x1bc]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/28  01:59:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 00 2e f9 d1 07
2009/08/28  02:02:02.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2009/08/28  02:02:22.0  XRT_QT_PROG_SET_424_OG [0x1a8]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 05
2009/08/28  02:02:24.0  XRT_FLD_DIS_419_OG [0x1a3]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2009/08/28  02:02:26.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/08/28  02:02:28.0  XRT_ARS_DIS_446_OG [0x1be]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2009/08/28  02:02:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/28  02:04:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/28  02:04:02.0  XRT_QT_PROG_SET_406_OG [0x196]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0a
2009/08/28  02:04:04.0  XRT_FLD_DIS_419_OG [0x1a3]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2009/08/28  02:04:06.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/08/28  02:04:08.0  XRT_ARS_DIS_446_OG [0x1be]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2009/08/28  02:04:10.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/28  02:05:54.0  XRT_CTRL_MANU_444_OG [0x1bc]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/28  02:06:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                        AOCU_NM              5 02-76 00 d1 07 d1 07
2009/08/28  02:09:02.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2009/08/28  02:09:22.0  XRT_QT_PROG_SET_404_OG [0x194]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 10
2009/08/28  02:09:24.0  XRT_FLD_DIS_419_OG [0x1a3]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2009/08/28  02:09:26.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/08/28  02:09:28.0  XRT_ARS_DIS_446_OG [0x1be]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2009/08/28  02:09:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/28  02:11:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/28  02:11:02.0  XRT_QT_PROG_SET_406_OG [0x196]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0a
2009/08/28  02:11:04.0  XRT_FLD_DIS_419_OG [0x1a3]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2009/08/28  02:11:06.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
```

```
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/08/28  02:11:08.0  XRT_ARS_DIS_446_OG [0x1be]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2009/08/28  02:11:10.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/28  02:18:54.0  XRT_CTRL_MANU_444_OG [0x1bc]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/28  02:19:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                    AOCU_NM              5 02-76 00 d1 07 2e f9
2009/08/28  02:22:02.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2009/08/28  02:22:22.0  XRT_QT_PROG_SET_443_OG [0x1bb]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2009/08/28  02:22:24.0  XRT_FLD_DIS_419_OG [0x1a3]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2009/08/28  02:22:26.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/08/28  02:22:28.0  XRT_ARS_DIS_446_OG [0x1be]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2009/08/28  02:22:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/28  02:24:00.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/28  02:24:02.0  XRT_QT_PROG_SET_406_OG [0x196]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0a
2009/08/28  02:24:04.0  XRT_FLD_DIS_419_OG [0x1a3]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2009/08/28  02:24:06.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/08/28  02:24:08.0  XRT_ARS_DIS_446_OG [0x1be]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2009/08/28  02:24:10.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/28  02:29:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/28  02:29:56.0  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2009/08/28  02:30:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                    AOCU_NM              5 02-76 02 00 00 00 00
2009/08/28  02:30:16.0  XRT_QT_PROG_SET_407_OG [0x197]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 09
2009/08/28  02:30:18.0  XRT_AEC_RESET_415_OG [0x19f]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2009/08/28  02:30:20.0  XRT_ARS_DIS_422_OG [0x1a6]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2009/08/28  02:30:22.0  XRT_FLD_DIS_445_OG [0x1bd]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2009/08/28  02:30:24.0  XRT_FLRCTRL_DIS_425_OG [0x1a9]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/08/28  02:33:02.0  XRT_CTRL_AUTO_403_OG [0x193]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/28  02:53:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/28  03:14:00.5  XRT_Custom_430_OG [0x1ae]
2009/08/28  03:15:00.5  XRT_CTRL_AUTO_432_OG [0x1b0]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/28  04:22:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/28  04:51:30.5  XRT_Custom_430_OG [0x1ae]
2009/08/28  04:52:30.5  XRT_CTRL_AUTO_432_OG [0x1b0]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/28  06:03:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/28  06:29:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/28  07:43:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/28  08:07:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/28  08:26:24.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/28  08:26:26.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2009/08/28  08:26:46.0  XRT_FLD_DIS_419_OG [0x1a3]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2009/08/28  08:26:48.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/08/28  08:26:50.0  XRT_ARS_DIS_434_OG [0x1b2]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2009/08/28  08:26:52.0  XRT_QT_PROG_SET_409_OG [0x199]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2009/08/28  08:26:54.0  XRT_CTRL_AUTO_403_OG [0x193]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/28  08:29:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/28  08:29:56.0  XRT_FOCUS_POSITION_441_OG [0x1b9]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2009/08/28  08:30:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                    AOCU_NM              5 02-76 02 00 00 00 00
2009/08/28  08:30:16.0  XRT_QT_PROG_SET_405_OG [0x195]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2009/08/28  08:30:18.0  XRT_ARS_DIS_422_OG [0x1a6]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
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2009/08/28  08:30:20.0  XRT_FLD_DIS_445_OG [0x1bd]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2009/08/28  08:30:22.0  XRT_FLRCTRL_DIS_425_OG [0x1a9]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/08/28  08:33:00.0  XRT_CTRL_AUTO_403_OG [0x193]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/28  09:23:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/28  09:46:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/08/28  10:29:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/08/28  10:30:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                AOCU_NM              5 02-76 00 00 00 00 00
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