

# XRT Timeline to be uploaded on 2009/08/31

Period: 2009/08/31 09:29:00 - 2009/09/03 10:33:00

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Normal mode

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## XOB #16FC: QS - C/Poly (Thick-Al) - FOV512 - 1min

Term	Pointing (x, y)	Comment
08/31 09:56:00 - 08/31 13:49:54	Track ( -236.0, 456.6) <sup>Ⓜ 08/31 09:39:00</sup>	# OP start + 10min, On-disk CH boundary obs, for EIS interchange reconnection study.
08/31 15:53:00 - 08/31 17:51:54	Track ( -194.3, 455.1) <sup>Ⓜ 08/31 15:50:00</sup>	* Resume on-disk CH boundary observations.
08/31 18:05:00 - 09/01 05:59:54	Track ( -179.3, 454.6) <sup>Ⓜ 08/31 18:02:00</sup>	# Cont.
09/01 06:13:00 - 09/01 10:48:54	Track ( -95.5, 452.7) <sup>Ⓜ 09/01 06:10:00</sup>	# Cont.

### PROG= 15 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
└─ Seqn= 4	120-time(s)	60.0sec										
└─ C-poly/Open	thin-Be/Open	close	Safe	Norm	22.6s	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
└─ Seqn= 24	1-time(s)	4.0sec										
└─ C-poly/Open	C-poly/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

## XOB #161B: High Temperature Plasma Study - Med-Be (12s) + Med-Al (32s) - Darks - no AEC - low cadence

Term	Pointing (x, y)	Comment
08/31 13:53:00 - 08/31 14:45:00	Track ( 490.0, -507.3) <sup>Ⓜ 08/31 13:50:00</sup>	* Track AR 11024 remnant.
08/31 15:01:06 - 08/31 15:49:54	Track ( 490.0, -507.3) <sup>Ⓜ 08/31 13:50:00</sup>	* Track AR 11024 remnant.

### PROG= 11 Inf.-time(s)

Subr= 1	1-time(s)	10.0sec										
└─ Seqn= 43	20-time(s)	30.0sec										
└─ med-Be/Open	med-Be/Open	close	Safe	Norm	11.3s	Obs	8x8	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 34	12-time(s)	60.0sec										
└─ med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	8x8	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Subr= 2	1-time(s)	4.0sec										
└─ Seqn= 80	1-time(s)	4.0sec										
└─ med-Be/Open	med-Be/Open	close	Safe	Dark	11.3s	Obs	8x8	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ med-Be/Open	med-Be/Open	close	Safe	Dark	32.0s	Obs	8x8	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

## XOB #168F: twilight monitor 384x384

Term	Pointing (x, y)	Comment
08/31 14:48:06 - 08/31 14:58:00	Track ( 490.0, -507.3) <sup>Ⓜ 08/31 13:50:00</sup>	* Track AR 11024 remnant.

### PROG= 14 Inf.-time(s)

Subr= 1	1-time(s)	30.0sec										
└─ Seqn= 56	1-time(s)	2.0sec										
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1024, 1024)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

## XOB #16D9: Synoptic Q95 2x2 - Al/poly(512/5795) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 2048x512 -1x1 512x2048) +Ti-poly(723/11571) + G-band(1

Term	Pointing (x, y)	Comment
08/31 17:55:00 - 08/31 18:01:54	Fixed ( 0.0, 0.0)	synoptic, shifted -8.0 min

### PROG= 01 1-time(s)

Subr= 1	1-time(s)	12.0sec										
└─ Seqn= 36	1-time(s)	4.0sec										
└─ Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 85	1-time(s)	2.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 88	1-time(s)	4.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 92	1-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

## XOB #16D8: Synoptic Q95 2x2 - Al/mesh(512/5795) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 2048x512 -1x1 512x2048) +Ti-poly(723/11571) + G-band(1

Term	Pointing (x, y)	Comment
09/01 06:03:00 - 09/01 06:09:54	Fixed ( 0.0, 0.0)	synoptic

### PROG= 19 1-time(s)

Subr= 1	1-time(s)	12.0sec										
└─ Seqn= 47	1-time(s)	4.0sec										
└─ Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 85	1-time(s)	2.0sec										

Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
<b>Seqn= 88</b>			<b>1-time(s)</b>			<b>4.0sec</b>						
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 92</b>			<b>1-time(s)</b>			<b>2.0sec</b>						
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \*

### Flare mode

\* \* \* \* \*

NOT USED

\* \* \* \* \*

### Active Region Search

\* \* \* \* \*

NOT USED

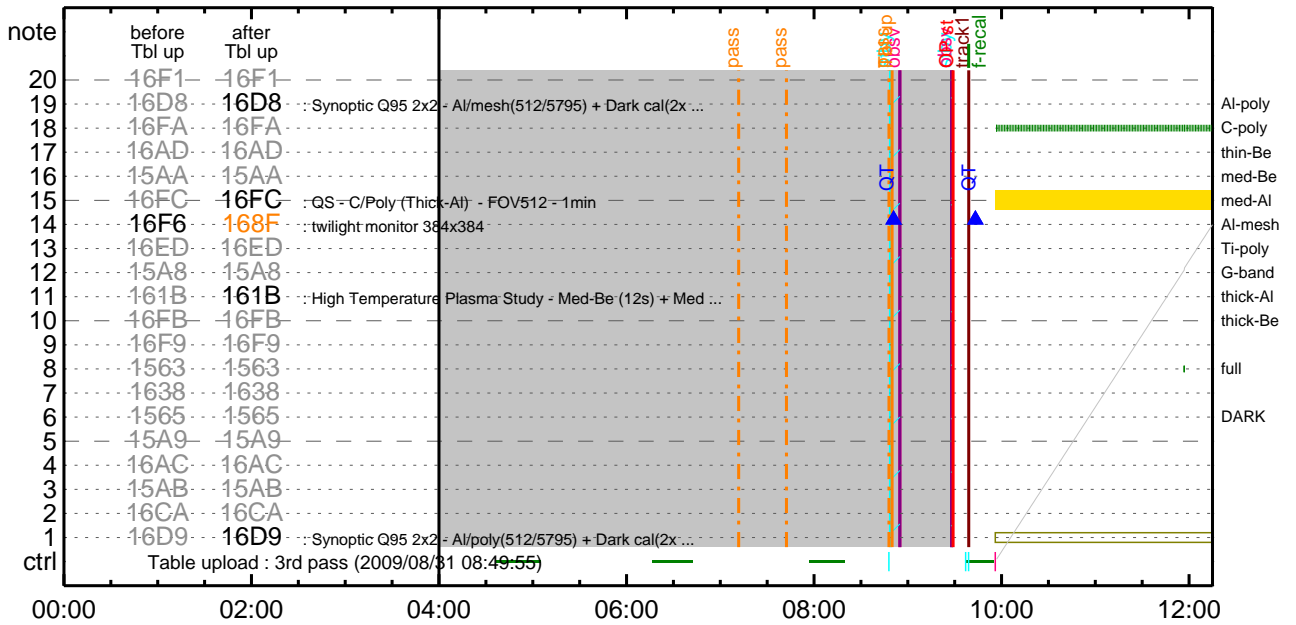
\* \* \* \* \*

### Flare Detection

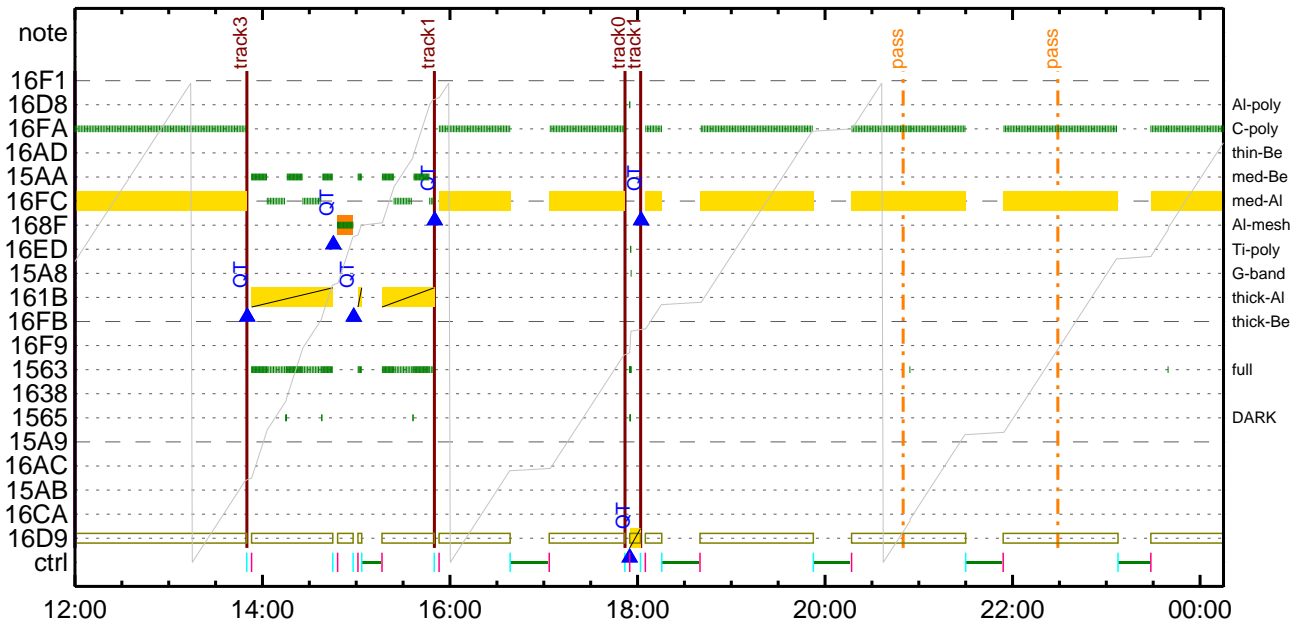
\* \* \* \* \*

NOT USED

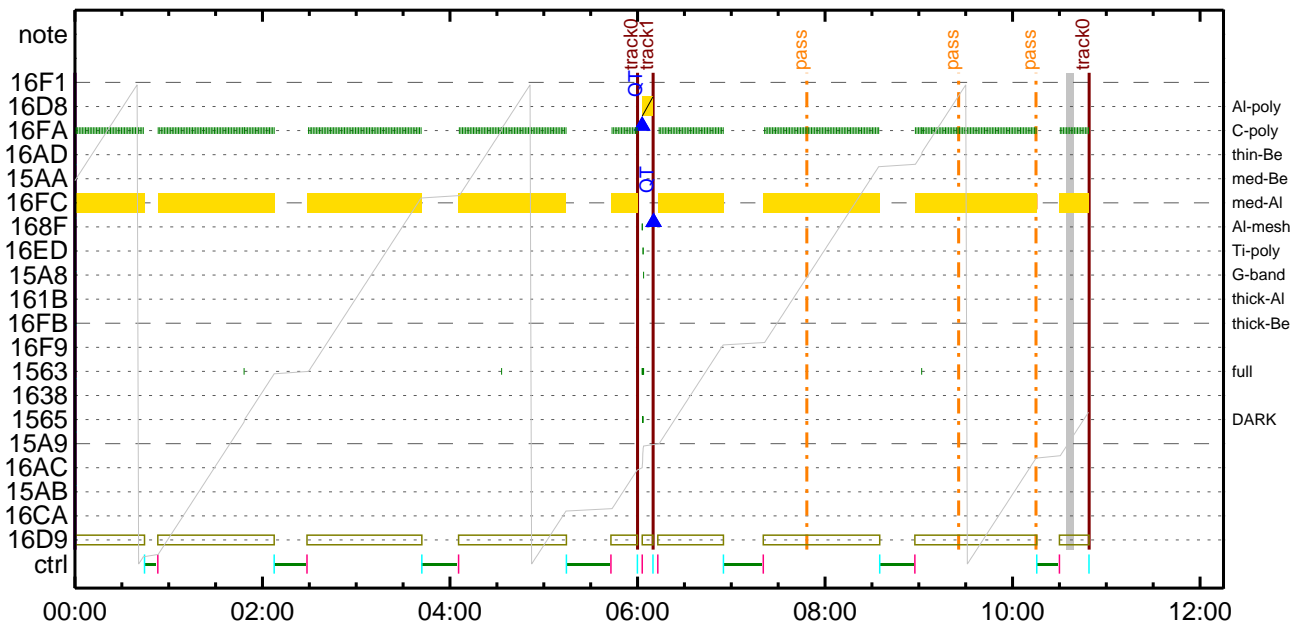
### CMDI #0765 2009/08/31



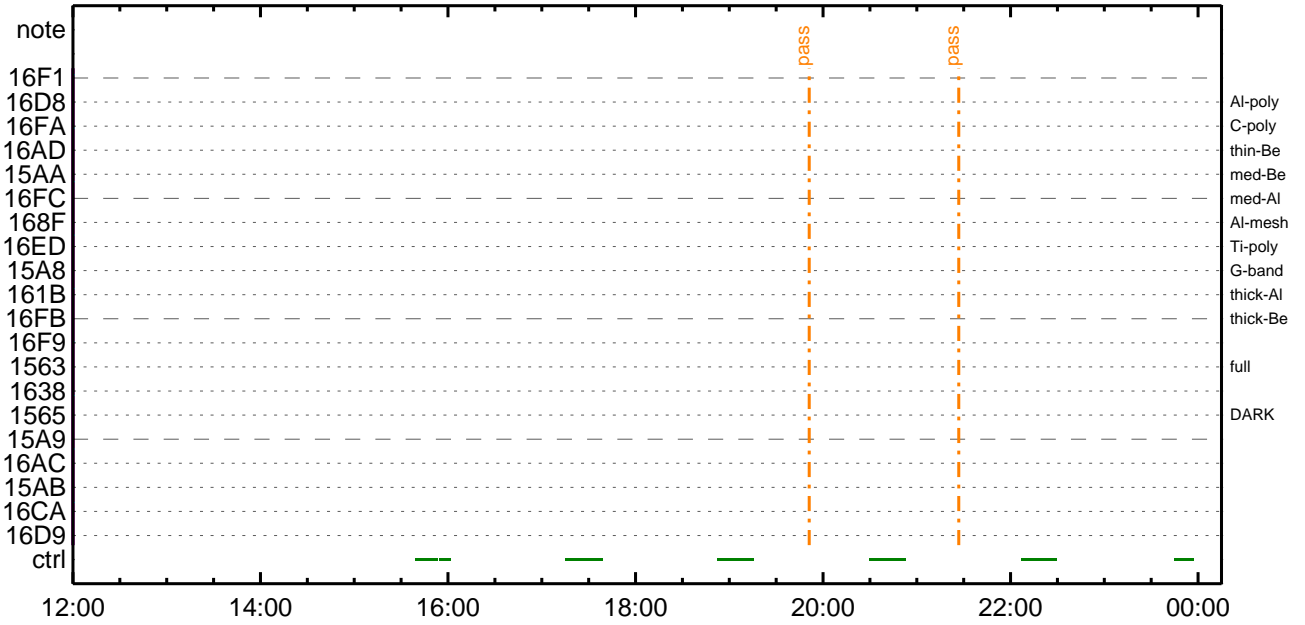
### CMDI #0765 2009/08/31



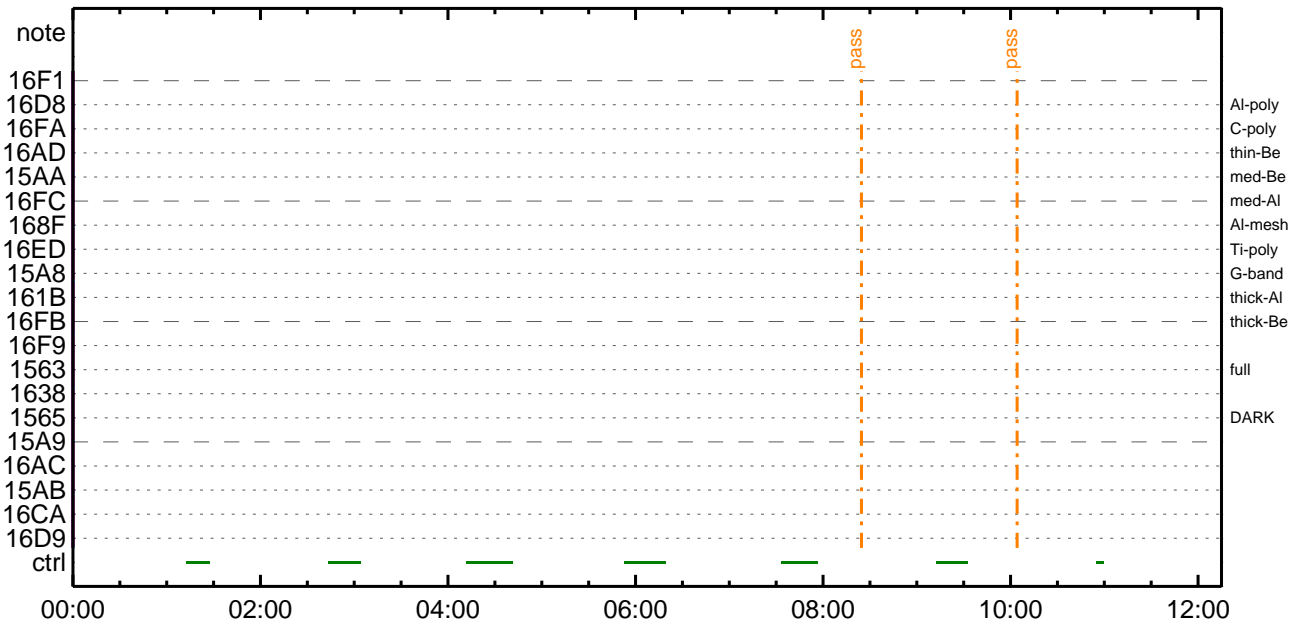
### CMDI #0765 2009/09/01



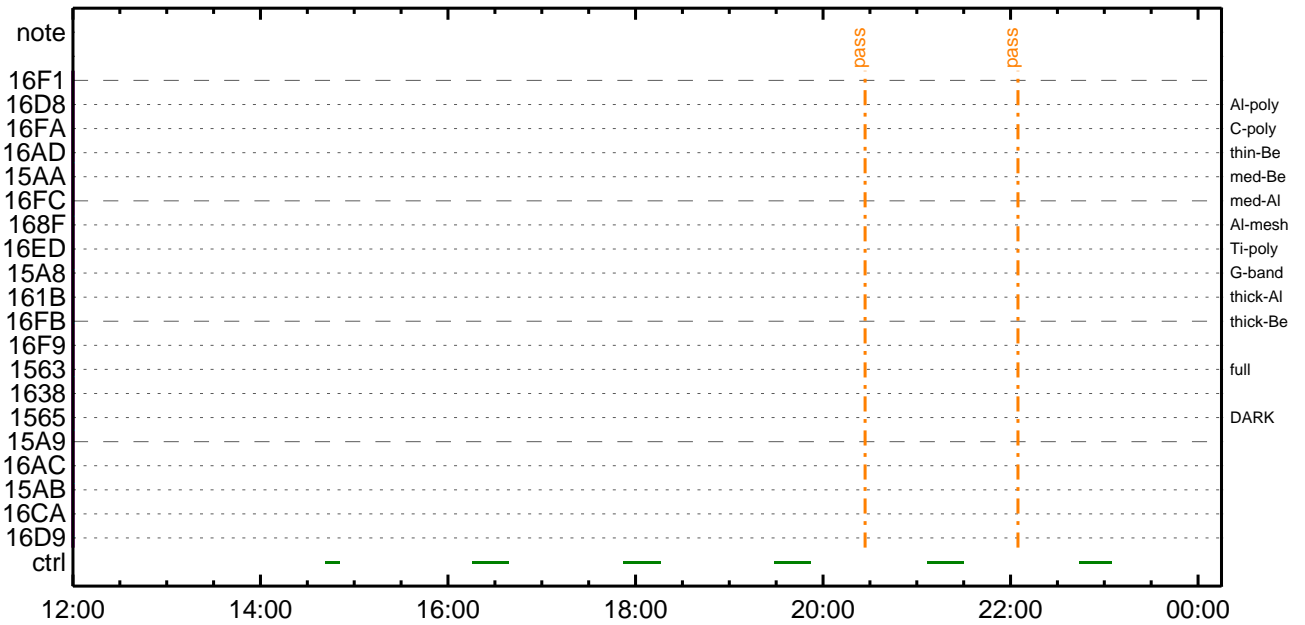
CMDI #0765 2009/09/01



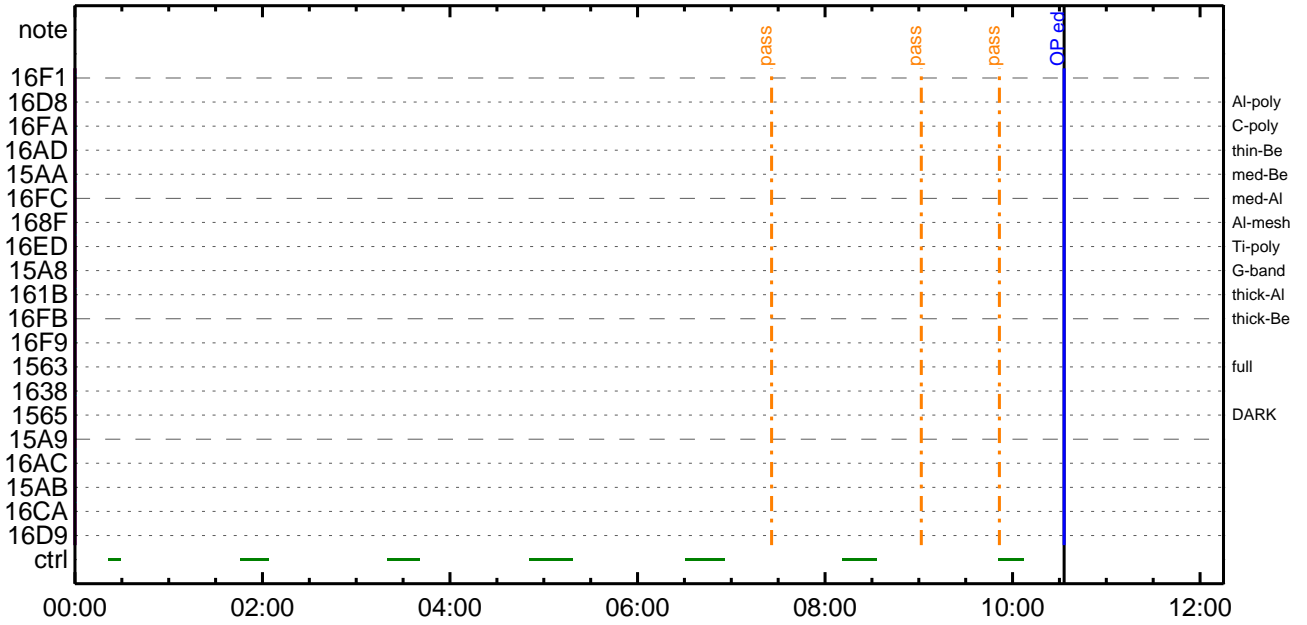
CMDI #0765 2009/09/02



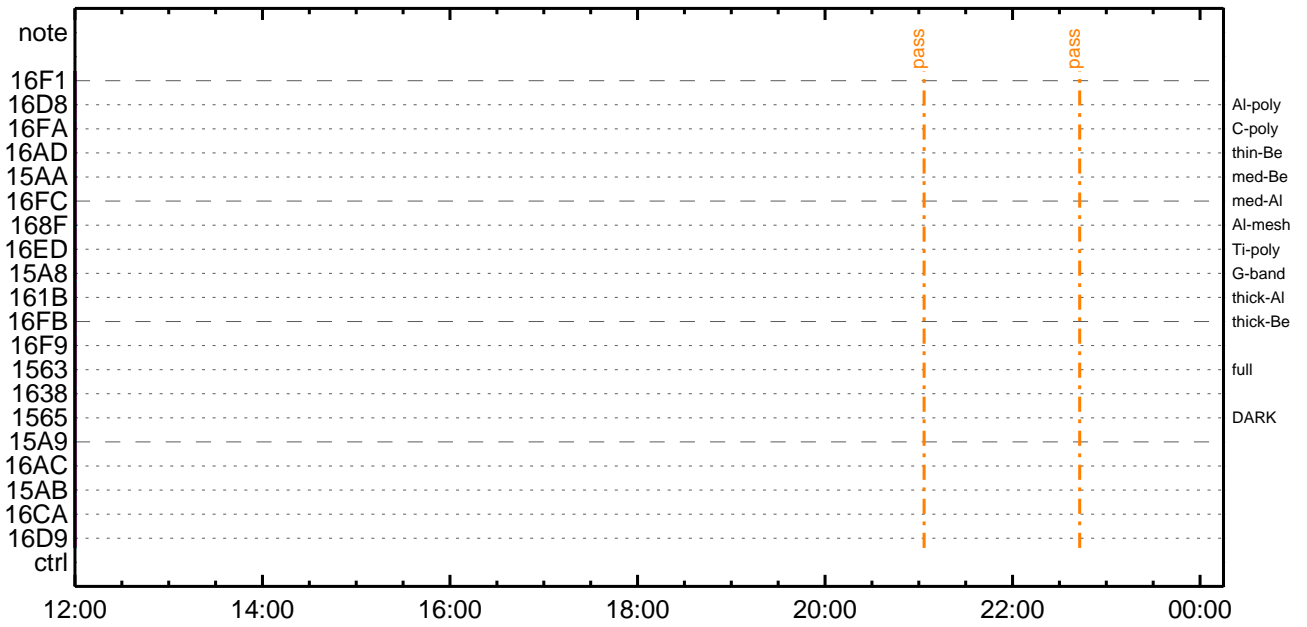
CMDI #0765 2009/09/02



CMDI #0765 2009/09/03



CMDI #0765 2009/09/03



(a) Spacecraft Operation Procedure (real-commands)

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main-814 2009-08-31 14:24:09 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY~¼Á»Ü;ä
0005 C.
0006 C. YÀYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èò¿òÁò•μ°Æ»Í×ÁÇòÍYçYÁY×Yí;¼YÉ;ÈÈèμ•íÍÉ;ÈòÈ¼°ÇÒò•ò¿¼í¹çòÍ;çÀ®, ùò¹òÈòòòÇÁ+¿®ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ;|YÁYóY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ä
0019 . S. OP op-814:OP
0020 ()
0021 . S. OG og-814:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPÍ°èYÁYóY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÀYóY×¼ªª î»òð³îç§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGòí¼È¹ç•è²îOKòð³îç§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÀYóY×¼ªª î»òð³îç§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGòí¼È¹ç•è²îOKòð³îç§
0062 C.
0063 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÀYóY×¼ªª î»òð³îç§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG,RAM ID=OPòí¼È¹ç•è²îOKòð³îç§
0081 C.
0082 . C. ***** òÈ²¼òí¼Á´¶¼òÈÈ-òòÁ+¿® (¼áμ-YÀYóY×¼è¼çòðÁÓÁæòçªªò²òÈ¼í¹çòçòâ) *****
0083 C. DHUYâ;¼YÉ;È¼Y¼;Yí;¼YÉ;Èòòíáò¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE |§ OPOG UPLOADò-Á+¿®NGòí¼í¹ç;ç°È²¼òí¼TI-CMDÁ+¿®òí¼Á¹Òò•òÈòòò³òÈ;f

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0096 C.                0300; SET0EDUMPAI0iYNY1aC10a|a30E;E
0097 C.
0098 . C. TIY3YBY0YH00dADi (UT)
0099 +. TI 2009-08-31 09:24:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                c[Hk1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2009-08-31 09:24:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                c[Hk1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2009-08-31 09:24:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                c[Hk1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2009-08-31 09:28:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                c[Hk1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C.    0E2%0IAE%iIn0iVA¥$YA¥-1aiU
0116 C.                c[Hk1_TI_CMD_ENA/DIS]              EQ        ENA
0117 C.                c[Hk1_TI_CMD_NUM]                EQ        4
0118 C.                c[Hk1_NEXT_EXEC_PIM]              EQ        DHU
0119 C.                c[Hk1_NEXT_EXEC_DC]              EQ        0xB3
0120 C.
0121 . C. *****
0122 C. TIiI0eYAYOYx
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; $ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                c[Hk1_DMP_TOP_ADRS_1]              EQ        07
0129 C.                c[Hk1_DMP_TOP_ADRS_0]              EQ        2B
0130 C.                c[Hk1_DMP_BLOCK_NUM]              EQ        3
0131 C.                c[Hk1_DMP_REPEAT_NUM]             EQ        0
0132 C.                c[Hk1_DMA_DMP_PIM]               EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                c[Hk1_PKT_FORM_NO]                EQ        7
0136 C.                c[Hk1_PKT_GEN_TIME]              EQ        0.25 s
0137 C.                c[Hk1_S_TLM_BIT_RATE]            EQ        32k
0138 C.                c[Hk1_X_TLM_BIT_RATE]            EQ        4M
0139 C.                c[Hk1_DMP_CHK_FLG]               EQ        EXEC
0140 C.
0141 . C. YAYOYx%aI»0d3iC$
0142 C.                c[Hk1_DMP_CHK_FLG]              EQ        NON
0143 C.
0144 . C. RAM ID=TI_TBL0I%E1q.e2IOK0d3iC$
0145 C.
0146 . C. DHU¥â;¥¥É;E¥%¥,¥i;¥¥E;E0dIâ01
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                c[Hk1_PKT_FORM_NO]                EQ        2
0150 C.                c[Hk1_PKT_GEN_TIME]              EQ        0.5S
0151 C.                c[Hk1_S_TLM_BIT_RATE]            EQ        32K
0152 C.                c[Hk1_X_TLM_BIT_RATE]            EQ        4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2009-08-31 09:28:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 . C. -----
0162 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 . C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2009-08-31 09:28:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2009-08-31 09:28:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 . C.                [ ] [Hk1_TI_CMD_NUM]            EQ        2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2009-08-31 09:28:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 . C.                [ ] [Hk1_TI_CMD_NUM]            EQ        1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP `uAI0i»0Y0EâD010eDCBC•x2e *****
0192 C. (%â0iYAYE¥P¥E¥â¥c¥E0E%¥00¥A»U010e)
0193 . S. DC-BC dcbc-402:DCBC

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```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```







```

0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 06 80 80 08 08)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 07 80 80 20 20)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 08 80 80 06 06)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 09 80 80 20 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 0a 80 80 08 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0f 80 80 04 04)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 10 80 80 10 10)
0138 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0139 BC (c4 0f)
0140 +. DC 07-F0 MDP_XRT_ARS_DIS
0141 BC (d5)
0142 +. DC 07-F0 MDP_XRT_FLD_DIS
0143 BC (d9)
0144 +. DC 07-F0 MDP_XRT_FLRCTRL_DIS
0145 BC (c9)
0146 . C. ----- Success Verify ? OK / NG ____
0147 C.
0148 C.
0149 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0150 C.
0151 +. DC 07-F0 MDP_XRT_MODE_OBSV
0152 BC (c2)
0153 +. TI 2009-08-31 09:28:02.0
0154 DC 07-F0 MDP_XRT_MODE_OBSV
0155 BC (c2)
0156 . C. ----- Success Verify ? OK / NG ____
0157 C.
0158 C. ***** XRT END *****
0159 C. *****
0160 C. SOT table upload
0161 C. *****
0162 . C. < Stop FG table >
0163 +. DC 07-F0 MDP_FG_CTRL_MANU
0164 BC (51)
0165 . C. -----
0166 C. MDP_FG_CTRL_MODE = MANU [ ]
0167 C. -----
0168 C.
0169 . C. <Upload FG Observation Table>
0170 . S. RAM ram-268:MDP_OBS_F
0171 ( )
0172 C.
0173 . C. < Dump RAMID=MDP_OBS_F >
0174 +. DC 07-F0 MDP_DUMP_FGTBL
0175 BC (82 07 00 00 00 38 b8)
0176 C. -----
0177 C. MDP_OBS_F verify = OK/NG [ ]
0178 C. -----
0179 C.
0180 . C. < Upload DPL table >
0181 C.
0182 C. ¥ç¥Ã¥×¥í;¼¥É°îÁ°¤ESTS_CHK¤òOFF¤È¤¹¤è
0183 C.
0184 . S. RAM ram-271:MDP_DPL
0185 ( )
0186 C.
0187 . C. < Dump RAMID=MDP_DPL >
0188 +. DC 07-F0 MDP_DUMP_FGTBL
0189 BC (82 07 00 38 b8 00 40)
0190 C. -----
0191 C. MDP_DPL verify = OK [ ]
0192 C. -----
0193 C.

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0194 C. STS_CHKαδONαÈα¹αè
0195 C.
0196 . C. < Update MDP DSC PAR1 >
0197 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0198 BC (4c)
0199 C. MDP_CMD_CODE = F04C0700[ ]
0200 C. MDP_CMD_CNT (count-up 1) [ ]
0201 C. -----
0202 C.
0203 . C.
0204 . C. < Resume FG table (auto mode) >
0205 +. DC 07-F0 MDP_FG_CTRL_AUTO
0206 BC (50)
0207 . C. -----
0208 C. MDP_FG_CTRL_MODE = AUTO [ ]
0209 C. -----
0210 C.
0211 C. *****
0212 C. SOT TI command set
0213 C. *****
0214 C. Execute, after the success of TBL upload.
0215 +. TI 2009-08-31 09:28:18.0
0216 DC 07-F0 MDP_SOT_MODE_OBSV
0217 BC (40)
0218 . C. -----
0219 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0220 C. -----
0221 C.
0222 C. Only when FG_CTRL_AUTO is used in RT.
0223 +. TI 2009-08-31 09:28:20.0
0224 DC 07-F0 MDP_FG_CTRL_AUTO
0225 BC (50)
0226 . C. -----
0227 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0228 C. -----
0229 C. ***** SOT END *****
0230 C.
0231 . C. ***** MDP `ûÃîαî»ò%ÿαÈÃα¹αéDCBC•x²è *****
0232 C. (%ã°îÿÓÿÃÿÈÿPÿÈÿãÿçÿèαÈ%¼αα¼Ã»Ûα¹αè)
0233 . S. DC-BC dcbc-402:DCBC
0234 (MDP_known_event)
0235 C.
0236 C.
0237 . C. ***** ÿDÿ¹.Ï Daily±;îÑαÈ´Øα¹αèDCBC•x²è *****
0238 . S. DC-BC dcbc-153:DCBC
0239 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0240 C.
0241 C.
0242 . C. ;ãLOSÿÃÿSÿÿÃÿ`¼Ã»Û;ã
0243 C.
0244 . C. ***** LOS *****
0245 C.

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Aug 31, 09 14:24

## XRT\_OGLIST\_0765.chk

Page 1/3

\*\*\* OP Sequence for XRT \*\*\*

2009/08/31	09:37:00.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/08/31	09:38:54.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/08/31	09:38:56.0	XRT_FOCUS_RECALIBRATE_402_OG [0x192]			
		XRT_FOCUS_RECAL	2	07-F8	78 00
2009/08/31	09:39:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2009/08/31	09:42:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/08/31	09:43:16.0	XRT_QT_PROG_SET_411_OG [0x19b]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2009/08/31	09:43:18.0	XRT_ARS_DIS_422_OG [0x1a6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/08/31	09:43:20.0	XRT_FLD_DIS_445_OG [0x1bd]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/08/31	09:43:22.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/08/31	09:55:00.0	XRT_Custom_430_OG [0x1ae]			
2009/08/31	09:56:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/08/31	13:49:54.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/08/31	13:49:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2009/08/31	13:50:00.0	AOCS_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	03 00 00 00 00
2009/08/31	13:50:16.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2009/08/31	13:52:54.0	XRT_FLD_DIS_419_OG [0x1a3]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/08/31	13:52:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/08/31	13:52:58.0	XRT_ARS_DIS_446_OG [0x1be]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/08/31	13:53:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/08/31	14:45:00.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/08/31	14:45:02.0	XRT_FOCUS_POSITION_441_OG [0x1b9]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/08/31	14:45:22.0	XRT_QT_PROG_SET_426_OG [0x1aa]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2009/08/31	14:45:24.0	XRT_ARS_DIS_422_OG [0x1a6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/08/31	14:45:26.0	XRT_FLD_DIS_445_OG [0x1bd]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/08/31	14:45:28.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/08/31	14:48:06.0	XRT_CTRL_AUTO_403_OG [0x193]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/08/31	14:58:00.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/08/31	14:58:02.0	XRT_FOCUS_POSITION_442_OG [0x1ba]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2009/08/31	14:58:22.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2009/08/31	15:01:00.5	XRT_FLD_DIS_419_OG [0x1a3]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/08/31	15:01:02.5	XRT_FLRCTRL_DIS_447_OG [0x1bf]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/08/31	15:01:04.5	XRT_ARS_DIS_446_OG [0x1be]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/08/31	15:01:06.5	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/08/31	15:03:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/08/31	15:15:30.0	XRT_Custom_430_OG [0x1ae]			
2009/08/31	15:16:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/08/31	15:49:54.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/08/31	15:49:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/08/31	15:50:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2009/08/31	15:50:16.0	XRT_QT_PROG_SET_411_OG [0x19b]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2009/08/31	15:50:18.0	XRT_ARS_DIS_422_OG [0x1a6]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/08/31	15:50:20.0	XRT_FLD_DIS_445_OG [0x1bd]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/08/31	15:50:22.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/08/31	15:53:00.0	XRT_CTRL_AUTO_403_OG [0x193]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/08/31	16:38:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/08/31	17:02:30.0	XRT_Custom_430_OG [0x1ae]			
2009/08/31	17:03:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0

Aug 31, 09 14:24

## XRT\_OGLIST\_0765.chk

Page 2/3

2009/08/31	17:51:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/08/31	17:51:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2009/08/31	17:52:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2009/08/31	17:52:16.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9
2009/08/31	17:52:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/08/31	17:52:20.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5
2009/08/31	17:54:58.0	XRT_QT_PROG_SET_400_OG [0x190]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2009/08/31	17:55:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/08/31	18:01:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/08/31	18:01:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/08/31	18:02:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00
2009/08/31	18:02:16.0	XRT_QT_PROG_SET_411_OG [0x19b]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2009/08/31	18:02:18.0	XRT_ARS_DIS_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2009/08/31	18:02:20.0	XRT_FLD_DIS_445_OG [0x1bd]	MDP_XRT_FLD_DIS	1	07-F0	d9
2009/08/31	18:02:22.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/08/31	18:05:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/08/31	18:15:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/08/31	18:39:00.0	XRT_Custom_430_OG [0x1ae]				
2009/08/31	18:40:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/08/31	19:52:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/08/31	20:16:00.0	XRT_Custom_430_OG [0x1ae]				
2009/08/31	20:17:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/08/31	21:30:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/08/31	21:53:00.0	XRT_Custom_430_OG [0x1ae]				
2009/08/31	21:54:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/08/31	23:07:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/08/31	23:27:30.0	XRT_Custom_430_OG [0x1ae]				
2009/08/31	23:28:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/09/01	00:44:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/09/01	00:52:00.0	XRT_Custom_430_OG [0x1ae]				
2009/09/01	00:53:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/09/01	02:07:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/09/01	02:27:30.0	XRT_Custom_430_OG [0x1ae]				
2009/09/01	02:28:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/09/01	03:42:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/09/01	04:04:30.0	XRT_Custom_430_OG [0x1ae]				
2009/09/01	04:05:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/09/01	05:14:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/09/01	05:42:00.0	XRT_Custom_430_OG [0x1ae]				
2009/09/01	05:43:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/09/01	05:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/09/01	05:59:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2009/09/01	06:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2009/09/01	06:00:16.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9
2009/09/01	06:00:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/09/01	06:00:20.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5
2009/09/01	06:02:58.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2009/09/01	06:03:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/09/01	06:09:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/09/01	06:09:56.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/09/01	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]				

Aug 31, 09 14:24

## XRT\_OGLIST\_0765.chk

Page 3/3

2009/09/01	06:10:16.0	XRT_QT_PROG_SET_411_OG [0x19b]	AOCU_NM	5	02-76	01	00	00	00	00
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2009/09/01	06:10:18.0	XRT_ARS_DIS_422_OG [0x1a6]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/09/01	06:10:20.0	XRT_FLD_DIS_445_OG [0x1bd]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/09/01	06:10:22.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/09/01	06:13:00.0	XRT_CTRL_AUTO_403_OG [0x193]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/09/01	06:55:00.0	XRT_CTRL_MANU_435_OG [0x1b3]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/09/01	07:19:30.0	XRT_Custom_430_OG [0x1ae]								
2009/09/01	07:20:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/09/01	08:35:00.0	XRT_CTRL_MANU_435_OG [0x1b3]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/09/01	08:56:30.0	XRT_Custom_430_OG [0x1ae]								
2009/09/01	08:57:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/09/01	10:15:30.0	XRT_CTRL_MANU_435_OG [0x1b3]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/09/01	10:29:00.0	XRT_Custom_430_OG [0x1ae]								
2009/09/01	10:30:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/09/01	10:48:54.0	XRT_CTRL_MANU_428_OG [0x1ac]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/09/01	10:49:00.0	AOCS_Or-point_Start_3_OG [0x099]								
			AOCU_NM	5	02-76	00	00	00	00	00