XRT Timeline to be uploaded on 2009/09/08

Period: 2009/09/08 09:33:00 - 2009/09/12 10:56:00

* * * * * **Normal mode** * * * * *

XOB #16FC: QS - C/Poly (Thick-Al) - FOV512 - 1min

Term	Pointing (x, y)	Comment
09/08 10:03:00 - 09/08 17:55:24	Fixed (-12.0, 865.0)	# OP start + 10min, HOP 80, polar CH observation.
09/08 18:08:30 - 09/08 23:11:00	Fixed (-12.0, 865.0)	# Cont.

PROG= 15 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 4 120-time(s) 60.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
C-poly/Open	thin-Be/Open	close	Safe	Norm	22.6s	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 24 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
C-poly/Open	C-poly/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #16E7: Synoptic Q95 2x2 - Al/poly(181/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 2048x512 -1x1 512x2048) + Ti-poly(256/4096) + G-band(16

Term	Pointing (x, y)	Comment
09/08 17:58:30 - 09/08 18:05:24	Fixed (0.0, 0.0)	synoptic, shifted -4.5 min
09/09 18:02:30 - 09/09 18:09:24	Fixed (0.0, 0.0)	synoptic, shifted -0.5 min

PROG= 12 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 30 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 85 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 60 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 92 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #16E8: Synoptic Q95 2x2 - Al/mesh(256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(181/2048) + G-band(1

Term	Pointing (x, y)	Comment
09/09 06:03:00 - 09/09 06:09:54	Fixed (0.0, 0.0)	synoptic
09/10 05:32:00 - 09/12 10:56:00	Fixed (0.0, 0.0)	synoptic, shifted -31.0 min

PROG= 08 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 18 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 85 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 68 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 92 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1702: AR dynamics - C/poly - 384x384 - DPCM - 4s - AEC2

Term	Pointing (x, y)	Comment
09/09 06:13:00 - 09/09 10:19:30	Track (191.8, -206.5) @ 09/09 06:10:00	# HOP 119, flux emergence with CRISP/SST, track bright point near disk center.
09/09 18:12:30 - 09/09 20:33:00	Fixed (888.0, 250.0)	# AR 11025 at W limb.

PROG= 13 Inf.-time(s)

Subr= 1 50-time(s) 18.0sec

Seqn= 87 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
C-poly/Open	C-poly/Open	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1024, 1024)	DPCM	2	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 54 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec

XOB #1567: AR multifilter Q95 - hot plasma - FOV384 - AEC1 thin filters

Term	Pointing (x, y)	Comment
09/09 11:00:00 - 09/09 15:42:30	Track (191.8, -206.5) @ 09/09 06:10:00	# HOP 119, flux emergence with CRISP/SST, track bright point near disk center.
09/09 21:30:00 - 10/10 02:47:00	Fixed (888.0, 250.0)	# AR 11025 at W limb.

PROG= 02 Inf.-time(s)

Subr= 1 1-time(s) 300.0sec

Seqn= 25 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	30.0sec
C-poly/Open	thin-Be/Open	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	30.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	30.0sec
med-Be/Open	Open/thick-Be	close	Safe	Norm	32.0s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	30.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	45.2s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	30.0sec

Seqn= 73 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	20.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	20.0sec

* * * * * **Flare mode** * * * * *

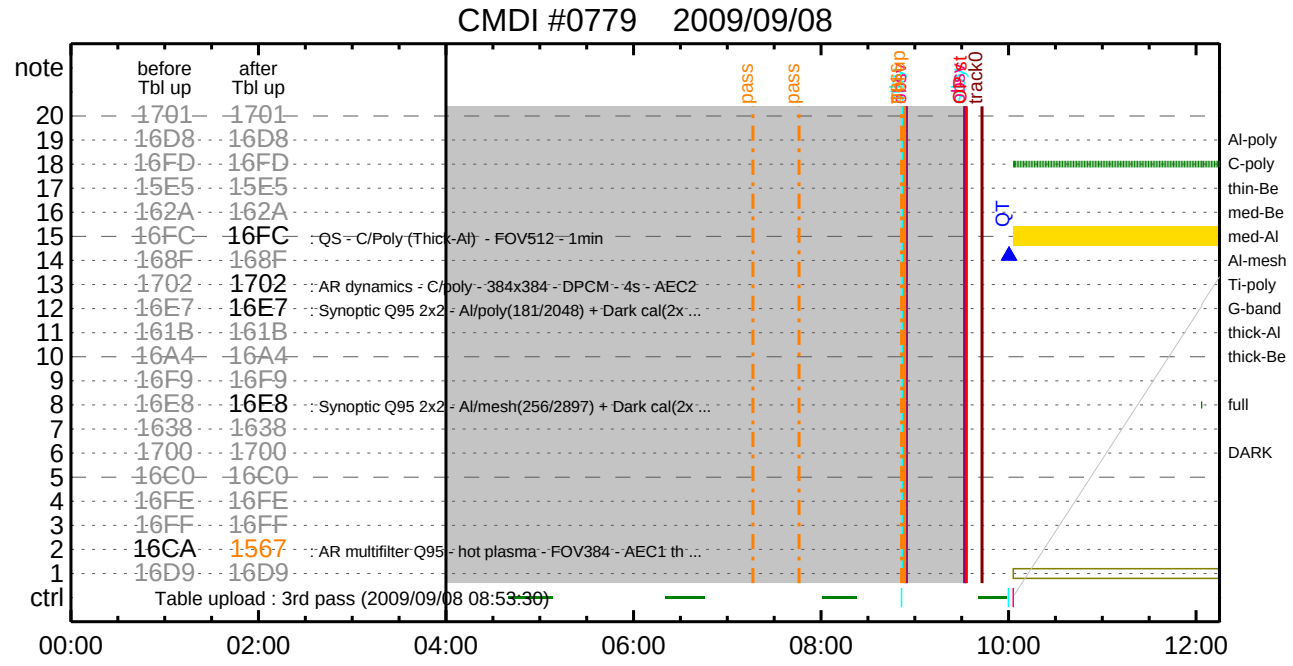
NOT USED

* * * * * **Active Region Search** * * * * *

NOT USED

* * * * * **Flare Detection** * * * * *

NOT USED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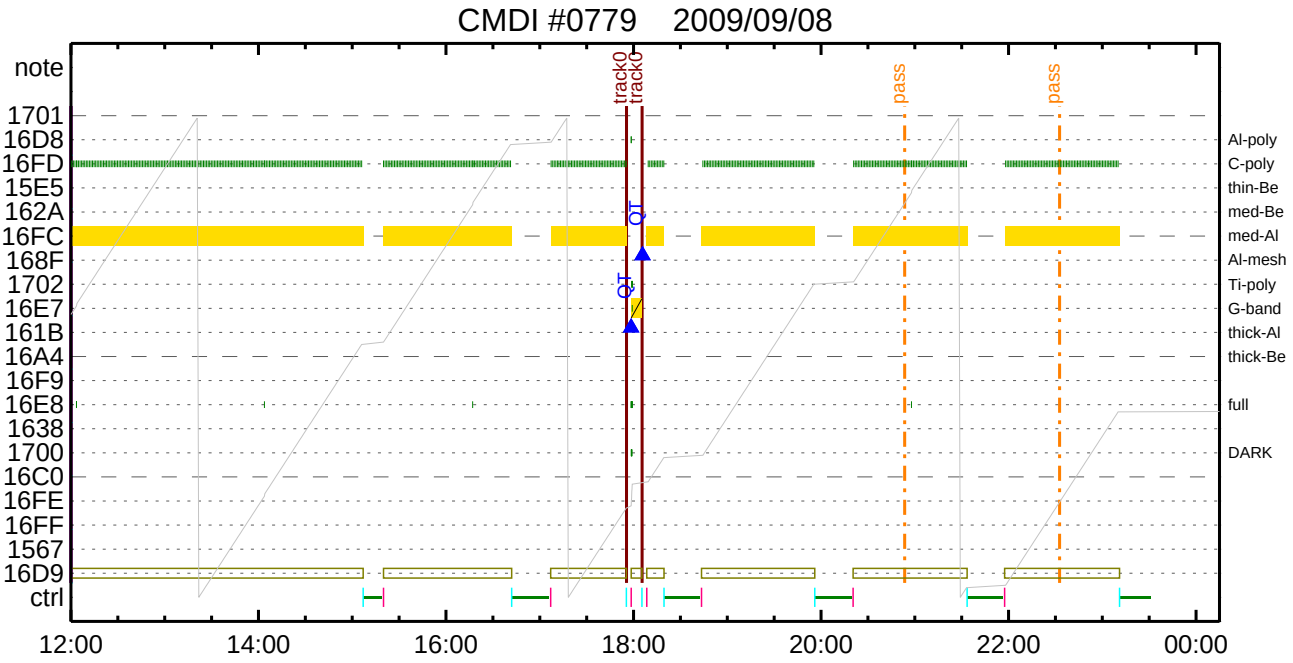
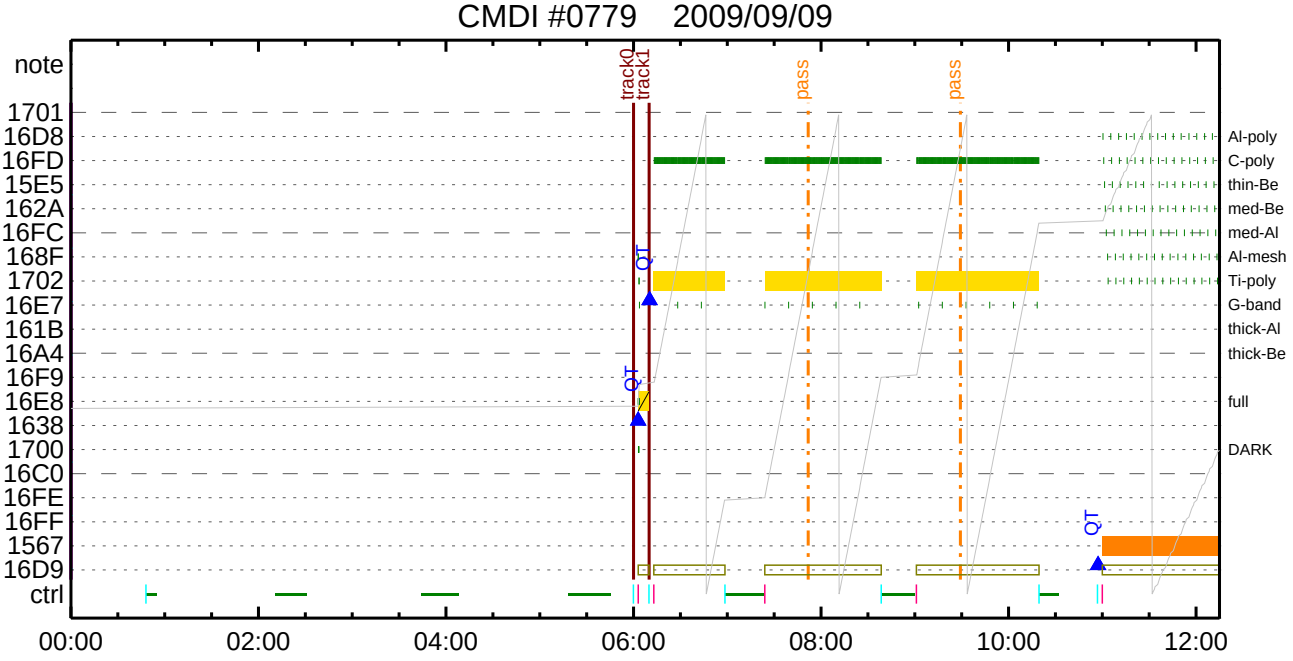
before Tbl up
after Tbl up
20 1701 1701
19 16D8 16D8
18 16FD 16FD
17 15E5 15E5
16 162A 162A
15 16FC 16FC
14 168F 168F
13 1702 1702
12 16E7 16E7
11 161B 161B
10 16A4 16A4
9 16F9 16F9
8 16E8 16E8
7 1638 1638
6 1700 1700
5 16C0 16C0
4 16FE 16FE
3 16FF 16FF
2 16CA 1567
1 16D9 16D9
ctrl
: QS - C/Poly (Thick-Al) - FOV512 - 1min
: AR dynamics - C/poly - 384x384 - DPCM - 4s - AEC2
: Synoptic Q95 2x2 - Al/poly(181/2048) + Dark cal(2x ...
: Synoptic Q95 2x2 - Al/mesh(256/2897) + Dark cal(2x ...
: AR multifilter Q95 - hot plasma - FOV384 - AEC1 th ...
Table upload : 3rd pass (2009/09/08 08:53:30)
pass
pass
track0
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0779 2009/09/08
note
1701
16D8
16FD
15E5
162A
16FC
168F
1702
16E7
161B
16A4
16F9
16E8
1638
1700
16C0
16FE
16FF
1567
16D9
ctrl
track0
track0
pass
pass
QT
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0779 2009/09/09
note
1701
16D8
16FD
15E5
162A
16FC
168F
1702
16E7
161B
16A4
16F9
16E8
1638
1700
16C0
16FE
16FF
1567
16D9
ctrl
track0
track1
pass
pass
QT
QT
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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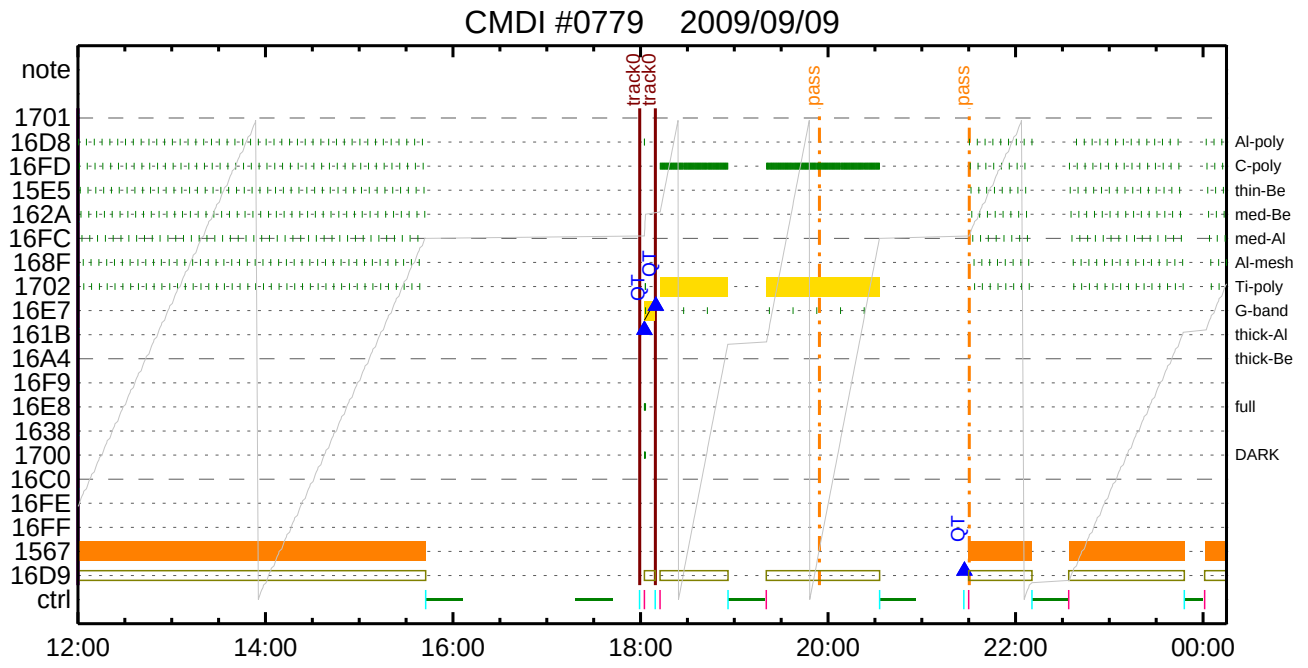
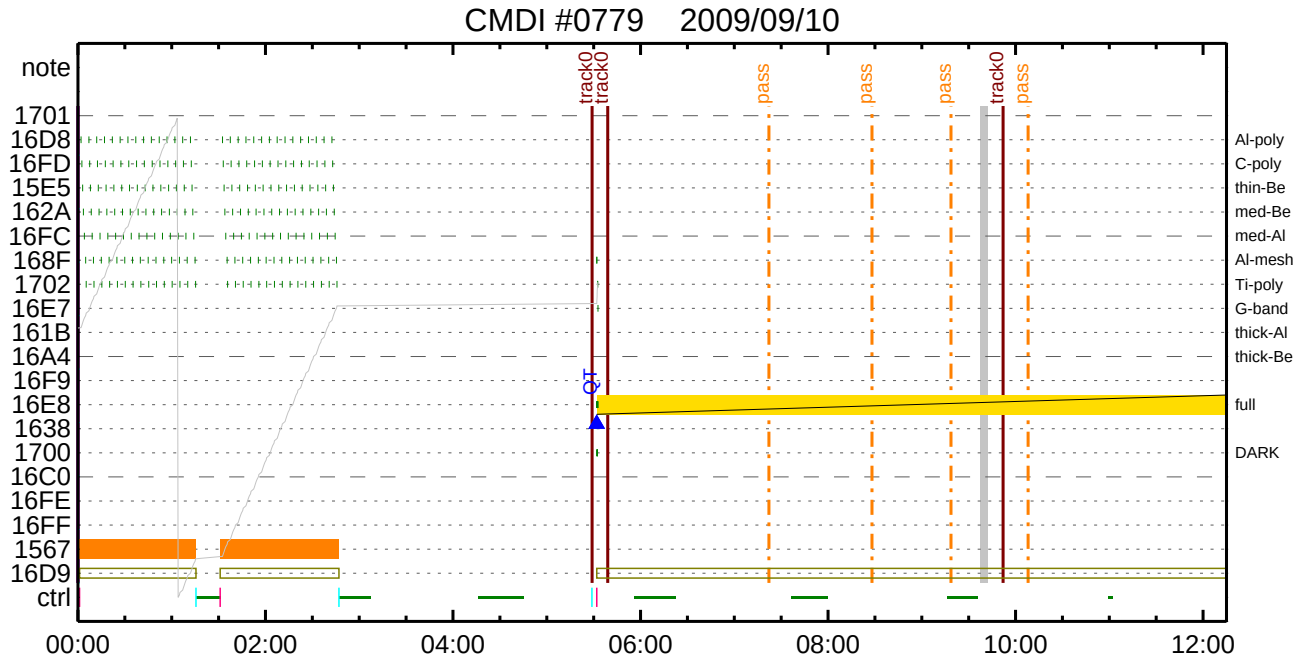
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0779 2009/09/09
note
1701
16D8
16FD
15E5
162A
16FC
168F
1702
16E7
161B
16A4
16F9
16E8
1638
1700
16C0
16FE
16FF
1567
16D9
ctrl
track0
track0
pass
pass
QT
QT
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00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16FD
15E5
162A
16FC
168F
1702
16E7
161B
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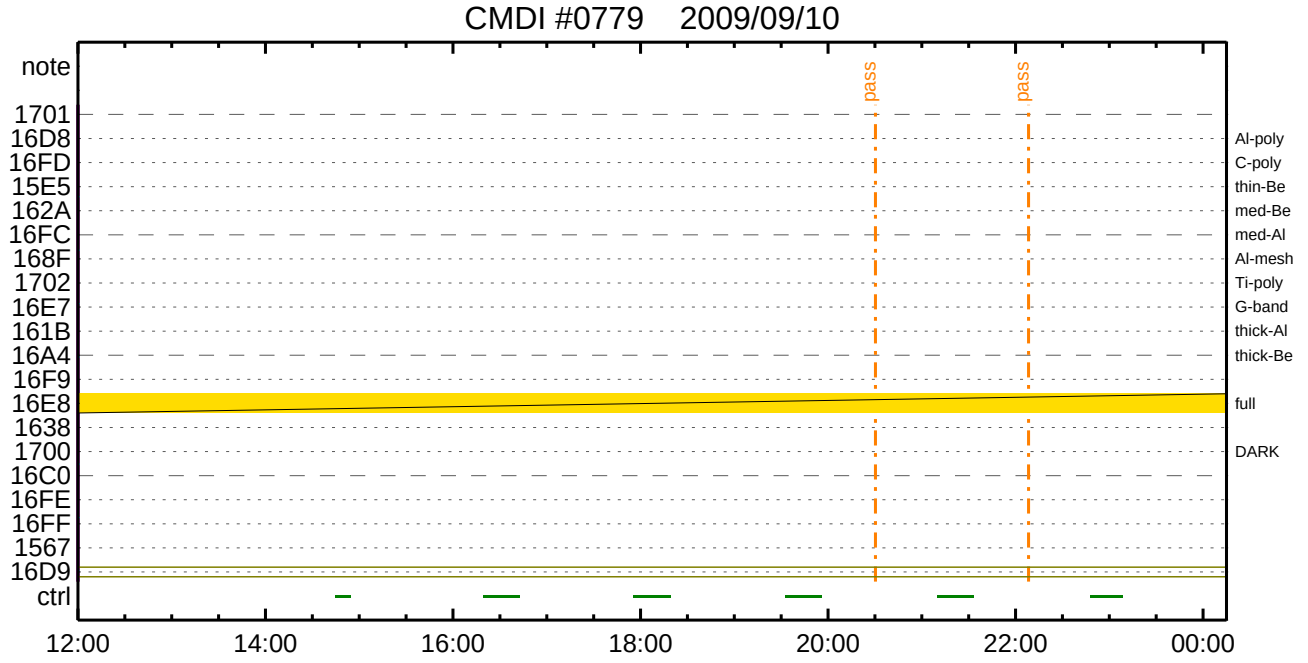
16F9
16E8
1638
1700
16C0
16FE
16FF
1567
16D9
ctrl
track0
track0
pass
pass
pass
track0
pass
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00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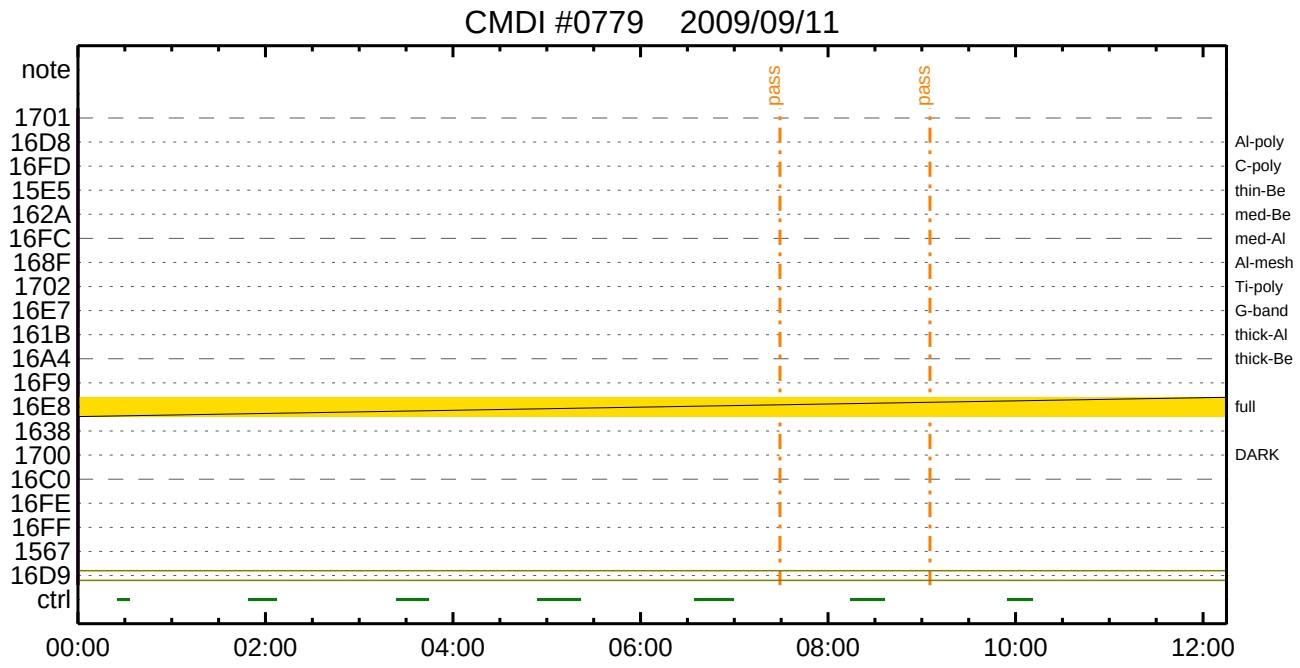
1701
16D8
16FD
15E5
162A
16FC
168F
1702
16E7
161B
16A4
16F9
16E8
1638
1700
16C0
16FE
16FF
1567
16D9
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00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16D8
16FD
15E5
162A
16FC
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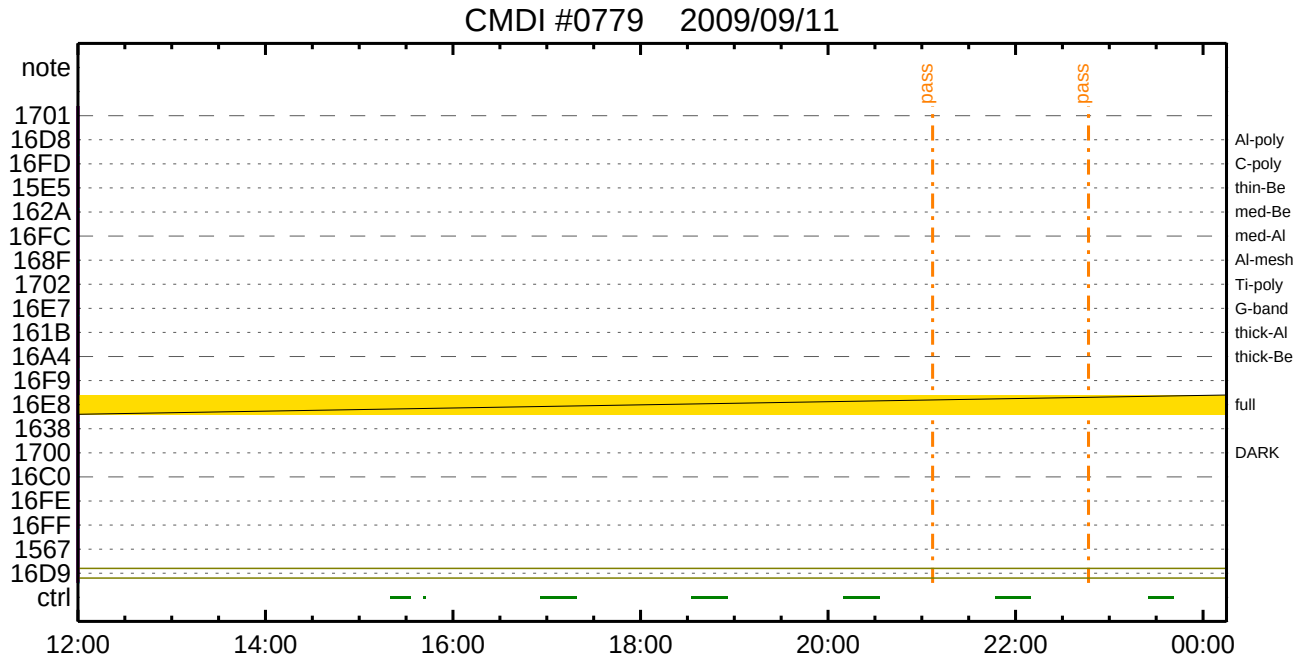
1702
16E7
161B
16A4
16F9
16E8
1638
1700
16C0
16FE
16FF
1567
16D9
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00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16D8
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15E5
162A
16FC
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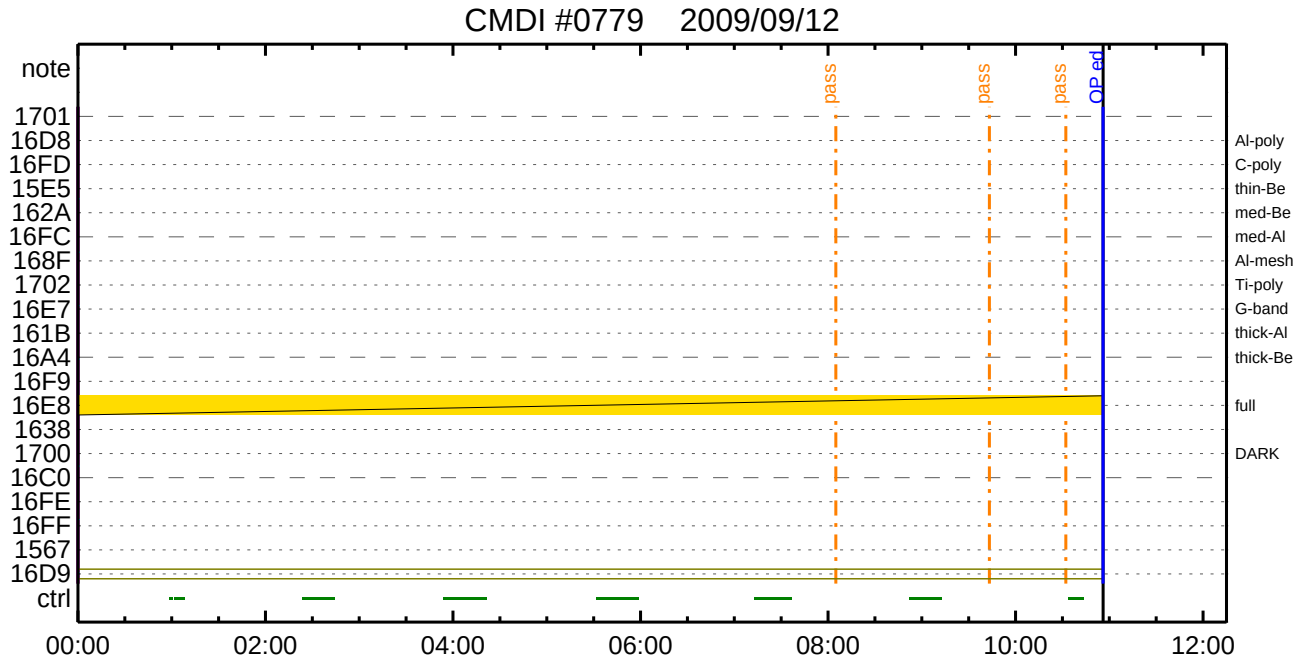
1702
16E7
161B
16A4
16F9
16E8
1638
1700
16C0
16FE
16FF
1567
16D9
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00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note
1701
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15E5
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1702
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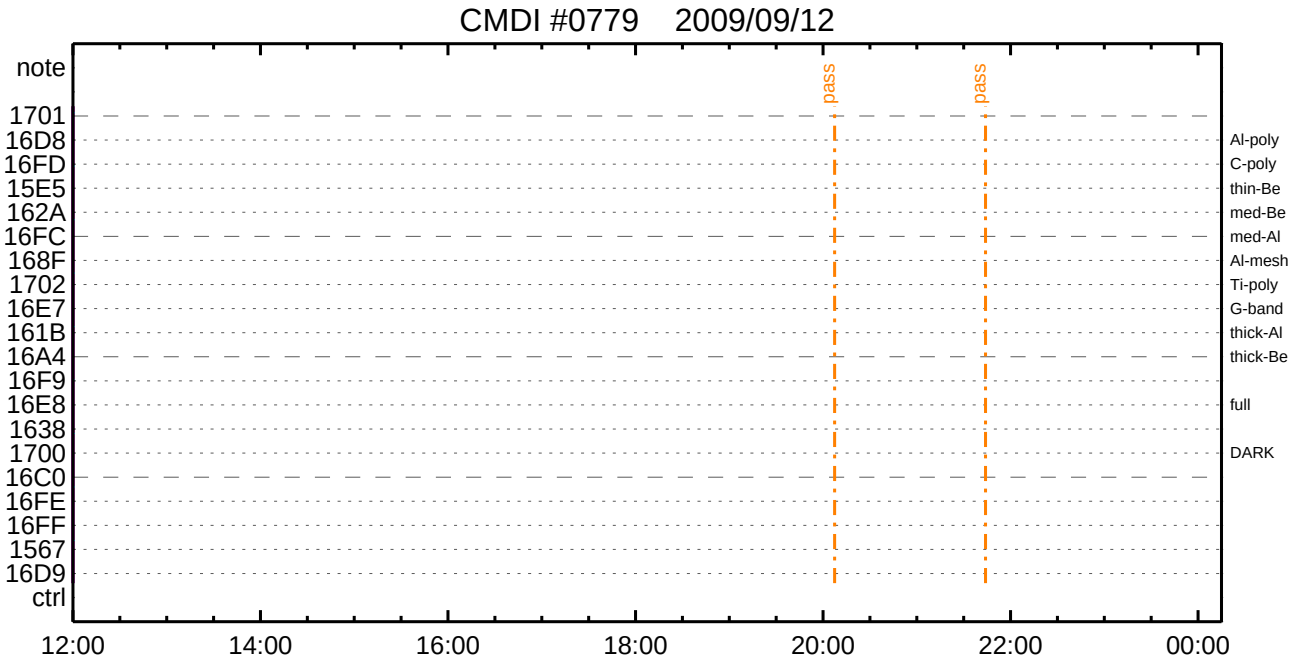
161B
16A4
16F9
16E8
1638
1700
16C0
16FE
16FF
1567
16D9
ctrl
pass
pass
pass
OP end
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0779 2009/09/12
note
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16D8
16FD
15E5
162A
16FC
168F
1702
16E7
161B
16A4
16F9
16E8
1638
1700
16C0
16FE
16FF
1567
16D9
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-831 2009-09-08 11:51:05 289 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
    C.
    C.
  . C. ****************************
    C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
    C. ****************************
    C.
  . C. ¡ãOP/OG¥í¡¼¥É¡ä
  . S. OP    op-831:OP
    ()
  . S. OG    og-831:OG
    ()
    C.
  . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
    C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
 +. DC 01-23 DHU_DMA_DMP_PRM_SET
    BC       (20 00 7f 01 02)
    C.                     ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
    C.                     ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
    C.                     ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
    C.                     ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
    C.                     ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
 +. DC 01-22 DHU_MODE_CHNG
    BC       (07 0b f8)
    C.                     ¢¢[HK1_PKT_FORM_NO]             EQ     7
    C.                     ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
    C.                     ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
    C.                     ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
    C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
  . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
    C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
  . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
    C.
    C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
 +. DC 01-23 DHU_DMA_DMP_PRM_SET
    BC       (20 80 7f 01 02)
    C.                     ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
    C.                     ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
    C.                     ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
    C.                     ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
    C.                     ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
 +. DC 01-22 DHU_MODE_CHNG
    BC       (07 0b f8)
    C.                     ¢¢[HK1_PKT_FORM_NO]             EQ     7
    C.                     ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
    C.                     ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
    C.                     ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
    C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
  . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
    C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
  . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
    C.
    C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
 +. DC 01-23 DHU_DMA_DMP_PRM_SET
    BC       (21 00 41 01 02)
    C.                     ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
    C.                     ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
    C.                     ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
    C.                     ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
    C.                     ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
 +. DC 01-22 DHU_MODE_CHNG
    BC       (07 0b f8)
    C.                     ¢¢[HK1_PKT_FORM_NO]             EQ     7
    C.                     ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
    C.                     ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
    C.                     ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
    C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
  . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
    C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
  . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
    C.
  . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
    C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
 +. DC 01-22 DHU_MODE_CHNG
    BC       (02 0a f8)
    C.                     ¢¢[HK1_PKT_FORM_NO]             EQ     2
    C.                     ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
    C.                     ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
    C.                     ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
    C.
  . C. *********************************
    C. TI-CMD SET (OPOG STOP/COPY/START)
    C. *********************************
    C.
  . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
    C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
    C.
  . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
 +. TI 2009-09-08 09:28:00.0
    DC 01-B3 DHU_OP_STOP
    C.                     ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
    C.
 +. TI 2009-09-08 09:28:01.0
    DC 01-B4 DHU_OP_COPY
    C.                     ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
    C.
 +. TI 2009-09-08 09:28:01.0
    DC 01-B5 DHU_OPOG_COPY
    C.                     ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195   +. TI 2009-09-08 09:32:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                        ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                        ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                        ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                        ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                        ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                        ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                        ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                        ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                        ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                        ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238     C. ********************************
0239     C.  SOT TI command set
0240     C. ********************************
0241     C. Execute, after the success of OP upload.
0242   +. TI 2009-09-08 09:32:16.0
0243     DC 07-F0 MDP_SOT_MODE_STBY
0244     BC       (41)
0245    . C. ------------------------------------
0246     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0247     C. ------------------------------------
0248     C. ************ SOT END ***********
0249    . C. Stop EIS observation and temporarily disable EIS mode changes
0250     C.
0251     C.
0252     C. ********** Start EIS operation (TI set) **********
0253     C. Execute, after the success of OP upload.
0254     C. Set EIS TI-commands
0255   +. TI 2009-09-08 09:32:30.0
0256     DC 07-FC EIS_MODE_MANU
0257     BC       (21 02)
0258   +. TI 2009-09-08 09:32:40.0
0259     DC 07-FC EIS_MODE_CHG_DIS
0260     BC       (22)
0261    . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0262     C. ************ End EIS operation (TI set) ***********
0263     C.
0264     C.
0265     C.
0266     C. ************ XRT START ***********
0267     C. Execute, after the success of OP upload.
0268   +. TI 2009-09-08 09:32:00.0
0269     DC 07-F0 MDP_XRT_MODE_STBY
0270     BC       (c3)
0271    . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0272     C.
0273     C. ************ XRT END ***********
0274     C.
0275    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0276     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0277    . S. DC-BC dcbc-402:DCBC
0278     (MDP_known_event)
0279     C.
0280     C.
0281    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0282    . S. DC-BC dcbc-153:DCBC
0283     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284     C.
0285     C.
0286    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0287     C.
0288    . C. ********** LOS **********
0289     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-832 2009-09-08 11:51:06 91 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0015     C. Upload the Orbit Element and the Target Attitude
0016     C. RAM-ID:TARGET_ATT
0017   . S. RAM   ram-150:TARGET_ATT
0018     ()
0019     C.
0020     C.
0021     C. Set the dump memory area of TARGET_ATT
0022   +. DC 02-48 AOCU_DUMP_SET
0023     BC       (07 00 00 00 18 00)
0024     C.
0025     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026     C.
0027     C.
0028     C. Change the TLMFormatNo for the AOCS Dump Format
0029   +. DC 01-22 DHU_MODE_CHNG
0030     BC       (04 0b f8)
0031     C.
0032     C. Wait for AOCSDUMP to end
0033     C.
0034   . C. Check the dump memory
0035     C.
0036     C.                                      Result = OK [ ]
0037     C.
0038   +. DC 01-22 DHU_MODE_CHNG
0039     BC       (02 0a f8)
0040     C.
0041     C.                          <A_***>[TLM STS] FMT = 2 [ ]
0042     C.
0043   +. DC 02-8E AOCU_ORB_UPD
0044   . C.
0045   . C. ***** AOCS Commands (Orbital Element Update) *****
0046     C. Update the orbital element
0047   +. DC 02-50 AOCU_ORB_PRPGT_START
0048     BC       (16)
0049   +  DC 02-8E AOCU_ORB_UPD
0050     C.
0051     C.       <A_ORB>[ORBIT] EPC =  797430.0 +- 1.0 (s) [ ]
0052     C.
0053   . C.
0054   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055   +. DC 07-FC EIS_MODE_MANU
0056     BC       (21 02)
0057   . C. Verify EIS in MANUAL mode
0058   . C. Estimated OBSTBL upload time is 18s
0059     C. *********************************
0060     C. EIS START OBSTBL LOAD
0061     C. *********************************
0062   . S. RAM   ram-820:EIS_OBSTBL
0063     ()
0064   +. DC 07-FC EIS_DUMP_OBSTBL
0065     BC       (07 07 07 00 00 70 00)
0066     C.
0067     C. Execute, after the success of OBSTBL upload.
0068     C. Set EIS TI-commands
0069   +. TI 2009-09-08 09:32:50.0
0070     DC 07-FC EIS_MODE_CHG_ENA
0071     BC       (20)
0072   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0073     C. *********************************
0074     C. EIS END OBSTBL LOAD
0075     C. *********************************
0076     C.
0077   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0078     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0079   . S. DC-BC dcbc-402:DCBC
0080     (MDP_known_event)
0081     C.
0082     C.
0083   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0084   . S. DC-BC dcbc-153:DCBC
0085     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0086     C.
0087     C.
0088   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0089     C.
0090   . C. *********** LOS ***********
0091     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-833 2009-09-08 11:51:06 240 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Äº²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098     C.
0099     C. ************ XRT START ************
0100     C.
0101  +. DC 07-F0 MDP_XRT_CTRL_MANU
0102     BC       (c1)
0103  +  DC 07-F0 MDP_XRT_MODE_STBY
0104     BC       (c3)
0105   . C. ----------- Success Verify ?              OK / NG____
0106     C.
0107     C. XRT Obs. Table Upload
0108   . S. RAM   ram-291:MDP_OBS_X
0109     ()
0110     C.
0111  +. DC 07-F0 MDP_DUMP_XRTTBL
0112     BC       (84 07 00 00 00 3a d4)
0113   . C. ----------- Comparison Check ?                      OK / ERR ____
0114     C.
0115     C.
0116  +. DC 07-F0 MDP_XRT_ROI_SET
0117     BC       (cd 01 b1 b1 04 04)
0118  +  DC 07-F0 MDP_XRT_ROI_SET
0119     BC       (cd 02 b1 b1 08 08)
0120  +  DC 07-F0 MDP_XRT_ROI_SET
0121     BC       (cd 03 b1 b1 08 08)
0122  +  DC 07-F0 MDP_XRT_ROI_SET
0123     BC       (cd 04 b1 b1 06 06)
0124  +  DC 07-F0 MDP_XRT_ROI_SET
0125     BC       (cd 06 80 80 08 08)
0126  +  DC 07-F0 MDP_XRT_ROI_SET
0127     BC       (cd 07 80 80 20 20)
0128  +  DC 07-F0 MDP_XRT_ROI_SET
0129     BC       (cd 08 80 80 20 08)
0130  +  DC 07-F0 MDP_XRT_ROI_SET
0131     BC       (cd 09 80 80 08 20)
0132  +  DC 07-F0 MDP_XRT_ROI_SET
0133     BC       (cd 0a 80 80 06 06)
0134  +  DC 07-F0 MDP_XRT_ROI_SET
0135     BC       (cd 0f 80 80 04 04)
0136  +  DC 07-F0 MDP_XRT_ROI_SET
0137     BC       (cd 10 80 80 10 10)
0138   . C. ---------- Success Verify ?            OK / NG ____
0139     C.
0140     C.
0141   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0142     C.
0143  +. DC 07-F0 MDP_XRT_MODE_OBSV
0144     BC       (c2)
0145  +. TI 2009-09-08 09:32:02.0
0146     DC 07-F0 MDP_XRT_MODE_OBSV
0147     BC       (c2)
0148   . C. ---------- Success Verify ?            OK / NG ____
0149     C.
0150     C. ************ XRT END ***********
0151   . C. ********************************
0152     C.  SOT table upload
0153     C. ********************************
0154   . C. < Stop FG table >
0155  +. DC 07-F0 MDP_FG_CTRL_MANU
0156     BC       (51)
0157   . C. ------------------------------------
0158     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0159     C. ------------------------------------
0160     C.
0161   . C. <Upload FG Observation Table>
0162   . S. RAM   ram-262:MDP_OBS_F
0163     ()
0164     C.
0165   . C. < Dump RAMID=MDP_OBS_F >
0166  +. DC 07-F0 MDP_DUMP_FGTBL
0167     BC       (82 07 00 00 00 38 b8)
0168     C. ------------------------------------
0169     C.  MDP_OBS_F verify = OK/NG        [ ]
0170     C. ------------------------------------
0171     C.
0172   . C. < Stop SP table >
0173  +. DC 07-F0 MDP_SP_CTRL_MANU
0174     BC       (61)
0175     C. ------------------------------------
0176     C.  MDP_SP_CTRL_MODE      = MANU    [ ]
0177     C. ------------------------------------
0178     C.
0179   . C. <Upload SP Observation Table>
0180   . S. RAM   ram-286:MDP_OBS_S
0181     ()
0182     C.
0183   . C. < Dump RAMID=MDP_OBS_S >
0184  +. DC 07-F0 MDP_DUMP_SPTBL
0185     BC       (83 07 00 00 00 38 b8)
0186     C. ------------------------------------
0187     C.  MDP_OBS_S verify = OK/NG        [ ]
0188     C. ------------------------------------
0189     C.
0190   . C. < Upload DPL table >
0191     C.
0192     C. ¥¢¥Ã¥×¥í¡¼¥É¤ÎÁ°¤ËSTS_CHK¤òOFF¤Ë¤¹¤ë
0193     C.
```

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0194   . S. RAM   ram-271:MDP_DPL
0195     ()
0196     C.
0197   . C. < Dump RAMID=MDP_DPL >
0198  +. DC 07-F0 MDP_DUMP_FGTBL
0199     BC       (82 07 00 38 b8 00 40)
0200     C. ------------------------------------
0201     C.  MDP_DPL verify          = OK    [ ]
0202     C. ------------------------------------
0203     C.
0204     C. STS_CHK¤òON¤Ë¤¹¤ë
0205     C.
0206   . C. < Update MDP DSC PAR1 >
0207  +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0208     BC       (4c)
0209     C.  MDP_CMD_CODE          = F04C0700[ ]
0210     C.  MDP_CMD_CNT        (count-up 1) [ ]
0211     C. ------------------------------------
0212     C.
0213   . C.
0214     C. ******************************
0215     C.  SOT TI command set
0216     C. ******************************
0217     C. Execute, after the success of TBL upload.
0218  +. TI 2009-09-08 09:32:18.0
0219     DC 07-F0 MDP_SOT_MODE_OBSV
0220     BC       (40)
0221   . C. ------------------------------------
0222     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0223     C. ------------------------------------
0224     C.
0225     C.
0226   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0227     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0228   . S. DC-BC dcbc-402:DCBC
0229     (MDP_known_event)
0230     C.
0231     C.
0232   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0233   . S. DC-BC dcbc-153:DCBC
0234     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0235     C.
0236     C.
0237   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0238     C.
0239   . C. ********** LOS **********
0240     C.
```

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*** OP Sequence for XRT ***

2009/09/08  09:43:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM               5 02-76 00 b3 1b 01 0e
2009/09/08  09:59:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2009/09/08  09:59:56.0  XRT_FOCUS_POSITION_402_OG [0x192]
                                XRT_FOCUS_POSITION    4 07-F8 22 fe 97 00
2009/09/08  10:00:16.0  XRT_QT_PROG_SET_411_OG [0x19b]
                                MDP_XRT_QT_PROG_SET   2 07-F0 c4 0f
2009/09/08  10:02:54.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS       1 07-F0 d9
2009/09/08  10:02:56.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS   1 07-F0 c9
2009/09/08  10:02:58.0  XRT_ARS_DIS_446_OG [0x1be]
                                MDP_XRT_ARS_DIS       1 07-F0 d5
2009/09/08  10:03:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2009/09/08  15:07:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2009/09/08  15:19:00.0  XRT_Custom_430_OG [0x1ae]
2009/09/08  15:20:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2009/09/08  16:42:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2009/09/08  17:06:00.0  XRT_Custom_430_OG [0x1ae]
2009/09/08  17:07:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2009/09/08  17:55:24.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2009/09/08  17:55:26.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                XRT_FOCUS_POSITION    4 07-F8 22 ff aa 00
2009/09/08  17:55:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM               5 02-76 00 00 00 00 00
2009/09/08  17:55:46.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS       1 07-F0 d9
2009/09/08  17:55:48.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS   1 07-F0 c9
2009/09/08  17:55:50.0  XRT_ARS_DIS_410_OG [0x19a]
                                MDP_XRT_ARS_DIS       1 07-F0 d5
2009/09/08  17:58:28.0  XRT_QT_PROG_SET_438_OG [0x1b6]
                                MDP_XRT_QT_PROG_SET   2 07-F0 c4 0c
2009/09/08  17:58:30.0  XRT_CTRL_AUTO_403_OG [0x193]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2009/09/08  18:05:24.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2009/09/08  18:05:26.0  XRT_FOCUS_POSITION_402_OG [0x192]
                                XRT_FOCUS_POSITION    4 07-F8 22 fe 97 00
2009/09/08  18:05:30.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM               5 02-76 00 b3 1b 01 0e
2009/09/08  18:05:46.0  XRT_QT_PROG_SET_411_OG [0x19b]
                                MDP_XRT_QT_PROG_SET   2 07-F0 c4 0f
2009/09/08  18:08:24.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS       1 07-F0 d9
2009/09/08  18:08:26.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS   1 07-F0 c9
2009/09/08  18:08:28.0  XRT_ARS_DIS_446_OG [0x1be]
                                MDP_XRT_ARS_DIS       1 07-F0 d5
2009/09/08  18:08:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2009/09/08  18:19:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2009/09/08  18:42:30.0  XRT_Custom_430_OG [0x1ae]
2009/09/08  18:43:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2009/09/08  19:56:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2009/09/08  20:19:30.0  XRT_Custom_430_OG [0x1ae]
2009/09/08  20:20:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2009/09/08  21:33:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2009/09/08  21:56:30.0  XRT_Custom_430_OG [0x1ae]
2009/09/08  21:57:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2009/09/08  23:11:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2009/09/09  00:48:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2009/09/09  05:59:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2009/09/09  05:59:56.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                XRT_FOCUS_POSITION    4 07-F8 22 ff aa 00
2009/09/09  06:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM               5 02-76 00 00 00 00 00
2009/09/09  06:00:16.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS       1 07-F0 d9
2009/09/09  06:00:18.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS   1 07-F0 c9
2009/09/09  06:00:20.0  XRT_ARS_DIS_410_OG [0x19a]
                                MDP_XRT_ARS_DIS       1 07-F0 d5
2009/09/09  06:02:58.0  XRT_QT_PROG_SET_412_OG [0x19c]
                                MDP_XRT_QT_PROG_SET   2 07-F0 c4 08
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2009/09/09  06:03:00.0  XRT_CTRL_AUTO_403_OG [0x193]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/09/09  06:09:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/09/09  06:09:56.0  XRT_FOCUS_POSITION_402_OG [0x192]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2009/09/09  06:10:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 01 00 00 00 00
2009/09/09  06:10:16.0  XRT_QT_PROG_SET_405_OG [0x195]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2009/09/09  06:12:52.0  XRT_AEC_RESET_415_OG [0x19f]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2009/09/09  06:12:54.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2009/09/09  06:12:56.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/09/09  06:12:58.0  XRT_ARS_DIS_446_OG [0x1be]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2009/09/09  06:13:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/09/09  06:58:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/09/09  07:23:00.0  XRT_Custom_430_OG [0x1ae]
2009/09/09  07:24:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/09/09  08:38:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/09/09  09:00:00.0  XRT_Custom_430_OG [0x1ae]
2009/09/09  09:01:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/09/09  10:19:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/09/09  10:56:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/09/09  10:56:56.0  XRT_FOCUS_POSITION_402_OG [0x192]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2009/09/09  10:57:16.0  XRT_QT_PROG_SET_414_OG [0x19e]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 02
2009/09/09  10:59:52.0  XRT_AEC_RESET_415_OG [0x19f]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2009/09/09  10:59:54.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2009/09/09  10:59:56.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/09/09  10:59:58.0  XRT_ARS_DIS_446_OG [0x1be]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2009/09/09  11:00:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/09/09  15:42:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/09/09  17:59:24.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/09/09  17:59:26.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2009/09/09  17:59:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2009/09/09  17:59:46.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2009/09/09  17:59:48.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/09/09  17:59:50.0  XRT_ARS_DIS_410_OG [0x19a]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2009/09/09  18:02:28.0  XRT_QT_PROG_SET_438_OG [0x1b6]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2009/09/09  18:02:30.0  XRT_CTRL_AUTO_403_OG [0x193]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/09/09  18:09:24.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/09/09  18:09:26.0  XRT_FOCUS_POSITION_402_OG [0x192]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2009/09/09  18:09:30.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 e9 cb b1 0f
2009/09/09  18:09:46.0  XRT_QT_PROG_SET_405_OG [0x195]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2009/09/09  18:12:22.0  XRT_AEC_RESET_415_OG [0x19f]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2009/09/09  18:12:24.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2009/09/09  18:12:26.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/09/09  18:12:28.0  XRT_ARS_DIS_446_OG [0x1be]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2009/09/09  18:12:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/09/09  18:56:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/09/09  19:19:30.0  XRT_Custom_430_OG [0x1ae]
2009/09/09  19:20:30.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/09/09  20:33:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/09/09  21:26:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
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2009/09/09  21:26:56.0  XRT_FOCUS_POSITION_402_OG [0x192]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2009/09/09  21:27:16.0  XRT_QT_PROG_SET_414_OG [0x19e]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 02
2009/09/09  21:29:52.0  XRT_AEC_RESET_415_OG [0x19f]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2009/09/09  21:29:54.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2009/09/09  21:29:56.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/09/09  21:29:58.0  XRT_ARS_DIS_446_OG [0x1be]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2009/09/09  21:30:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/09/09  22:10:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/09/09  22:33:00.0  XRT_Custom_430_OG [0x1ae]
2009/09/09  22:34:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/09/09  23:48:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/09/10  00:00:00.0  XRT_Custom_430_OG [0x1ae]
2009/09/10  00:01:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/09/10  01:15:30.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/09/10  01:31:00.0  XRT_CTRL_AUTO_432_OG [0x1b0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/09/10  02:47:00.0  XRT_CTRL_MANU_435_OG [0x1b3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/09/10  05:28:54.0  XRT_CTRL_MANU_428_OG [0x1ac]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/09/10  05:28:56.0  XRT_FOCUS_POSITION_442_OG [0x1ba]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2009/09/10  05:29:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2009/09/10  05:29:16.0  XRT_FLD_DIS_419_OG [0x1a3]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2009/09/10  05:29:18.0  XRT_FLRCTRL_DIS_447_OG [0x1bf]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/09/10  05:29:20.0  XRT_ARS_DIS_410_OG [0x19a]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2009/09/10  05:31:58.0  XRT_QT_PROG_SET_412_OG [0x19c]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 08
2009/09/10  05:32:00.0  XRT_CTRL_AUTO_403_OG [0x193]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/09/10  05:39:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 e9 cb b1 0f
2009/09/10  09:52:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
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