

# XRT Timeline to be uploaded on 2009/09/12

Period: 2009/09/12 10:56:00 - 2009/09/17 11:04:00

\* \* \* \* \*

Normal mode

\* \* \* \* \*

XOB #16C8: HOP81 2-filter - Al/poly 16s, Al/mesh 12s, G-band - 384x384												
Term	Pointing (x, y)							Comment				
09/12 11:06:26 - 09/12 17:57:24	Fixed ( 0.0, 910.0)	# OP start + 10min: HOP81 N-pole obs.										
09/13 10:36:26 - 09/13 17:45:24	Fixed ( 0.0, 910.0)	HOP81: N-pole obs										
09/14 11:30:26 - 09/14 18:03:24	Fixed ( 0.0, 910.0)	HOP81: N-pole										
<b>PROG= 04 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─┬─ Seqn= 1 1-time(s) 2.0sec												
└─└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─┬─ Seqn= 5 60-time(s) 30.0sec												
└─└─ Open/Al-mesh Open/Al-mesh close Safe Norm 11.3s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
└─└─ Al-poly/Open Al-poly/Open close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16E7: Synoptic Q95 2x2 - Al/poly(181/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 2048x512 -1x1 512x2048) + Ti-poly(256/4096) + G-band(16												
Term	Pointing (x, y)							Comment				
09/12 18:00:30 - 09/12 18:07:24	Fixed ( 0.0, 0.0)	synoptic, shifted -2.5 min										
09/13 17:48:30 - 09/13 17:55:24	Fixed ( 0.0, 0.0)	synoptic, shifted -14.5 min										
09/14 18:06:30 - 09/14 18:13:24	Fixed ( 0.0, 0.0)	synoptic, shifted 3.5 min										
<b>PROG= 12 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─┬─ Seqn= 30 1-time(s) 4.0sec												
└─└─ Al-poly/Open Al-poly/Open close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─└─ Al-poly/Open Al-poly/Open close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─ Seqn= 85 1-time(s) 2.0sec												
└─└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─┬─ Seqn= 60 1-time(s) 4.0sec												
└─└─ Open/Ti-poly Open/Ti-poly close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─└─ Open/Ti-poly Open/Ti-poly close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─ Seqn= 92 1-time(s) 2.0sec												
└─└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1703: QS Dynamics 2-filter - Al/poly 16s, Al/mesh 12s, G-band - 384x384												
Term	Pointing (x, y)							Comment				
09/12 18:07:56 - 09/13 04:00:00	Track ( 299.8, -53.2) @ 09/12 18:07:30	# EIS bright point obs.										
09/13 17:55:56 - 09/14 05:00:00	Track ( 499.4, -41.4) @ 09/13 17:55:30	# EIS bright point obs.										
09/14 18:13:56 - 09/15 02:37:30	Track ( 675.5, -23.8) @ 09/14 18:13:30	# EIS bright point obs.										
<b>PROG= 19 Inf.-time(s)</b>												
└─ Subr= 2 1-time(s) 2.0sec												
└─┬─ Seqn= 5 1-time(s) 30.0sec												
└─└─ Open/Al-mesh Open/Al-mesh close Safe Norm 11.3s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
└─└─ Al-poly/Open Al-poly/Open close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16E8: Synoptic Q95 2x2 - Al/mesh(256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(181/2048) + G-band(16												
Term	Pointing (x, y)							Comment				
09/13 05:45:40 - 09/13 10:35:54	Fixed ( 0.0, 0.0)	synoptic, shifted -17.5 min										
09/14 06:03:00 - 09/14 11:29:54	Fixed ( 0.0, 0.0)	synoptic										
09/15 06:25:30 - 09/17 11:04:00	Fixed ( 0.0, 0.0)	synoptic, shifted 22.5 min										
<b>PROG= 08 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─┬─ Seqn= 18 1-time(s) 4.0sec												
└─└─ Open/Al-mesh Open/Al-mesh close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─└─ Open/Al-mesh Open/Al-mesh close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─ Seqn= 85 1-time(s) 2.0sec												
└─└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─┬─ Seqn= 68 1-time(s) 4.0sec												
└─└─ Open/Ti-poly Open/thick-Al close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─└─ Open/Ti-poly Open/thick-Al close Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ Seqn= 92 1-time(s) 2.0sec												
└─└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												

\* \* \* \* \*

**Flare mode**

\* \* \* \* \*

NOT USED

\* \* \* \* \*

**Active Region Search**

\* \* \* \* \*

NOT USED

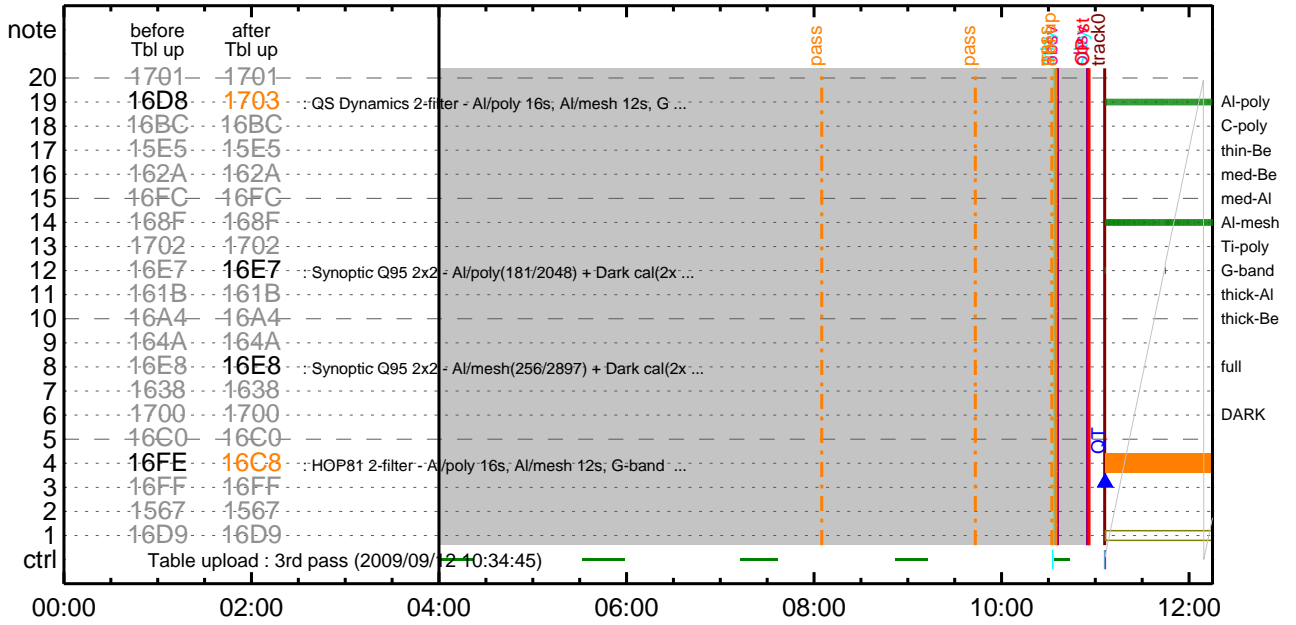
\* \* \* \* \*

**Flare Detection**

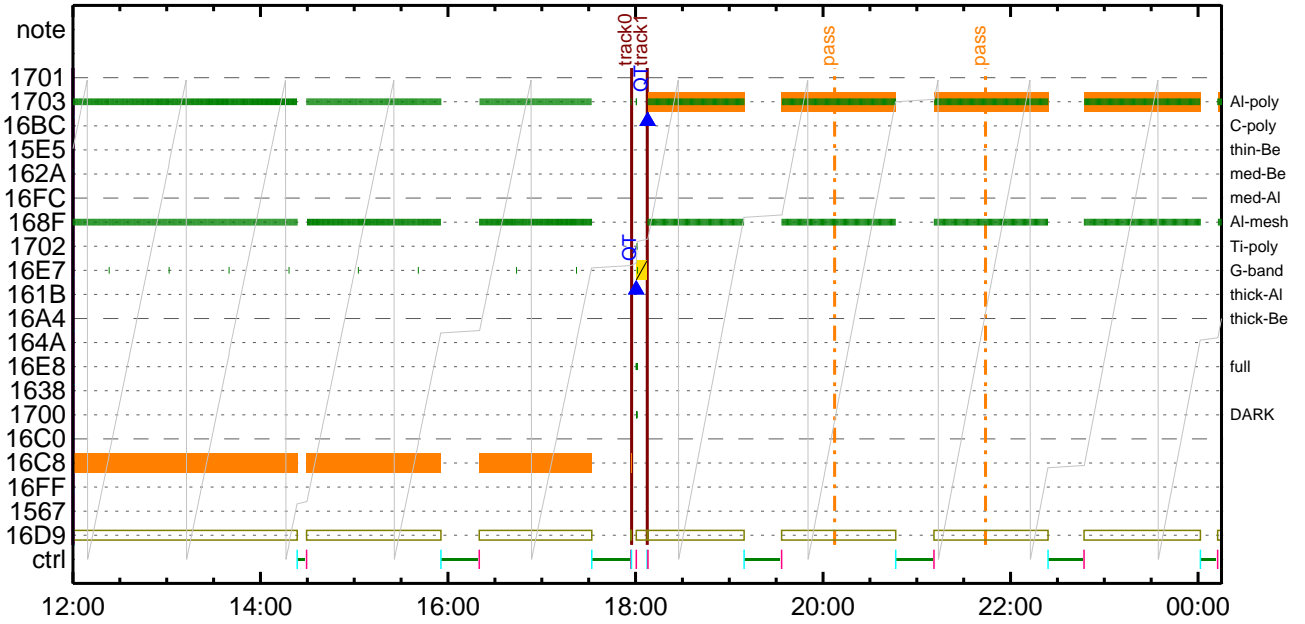
\* \* \* \* \*

NOT USED

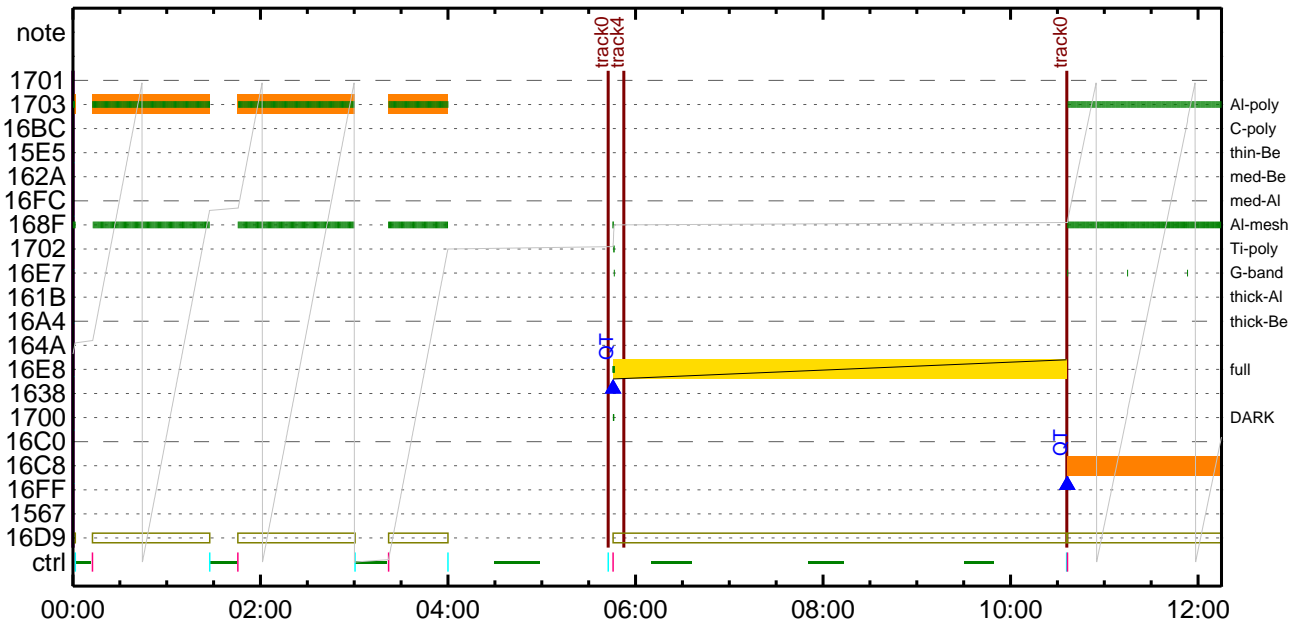
### CMDI #0792 2009/09/12



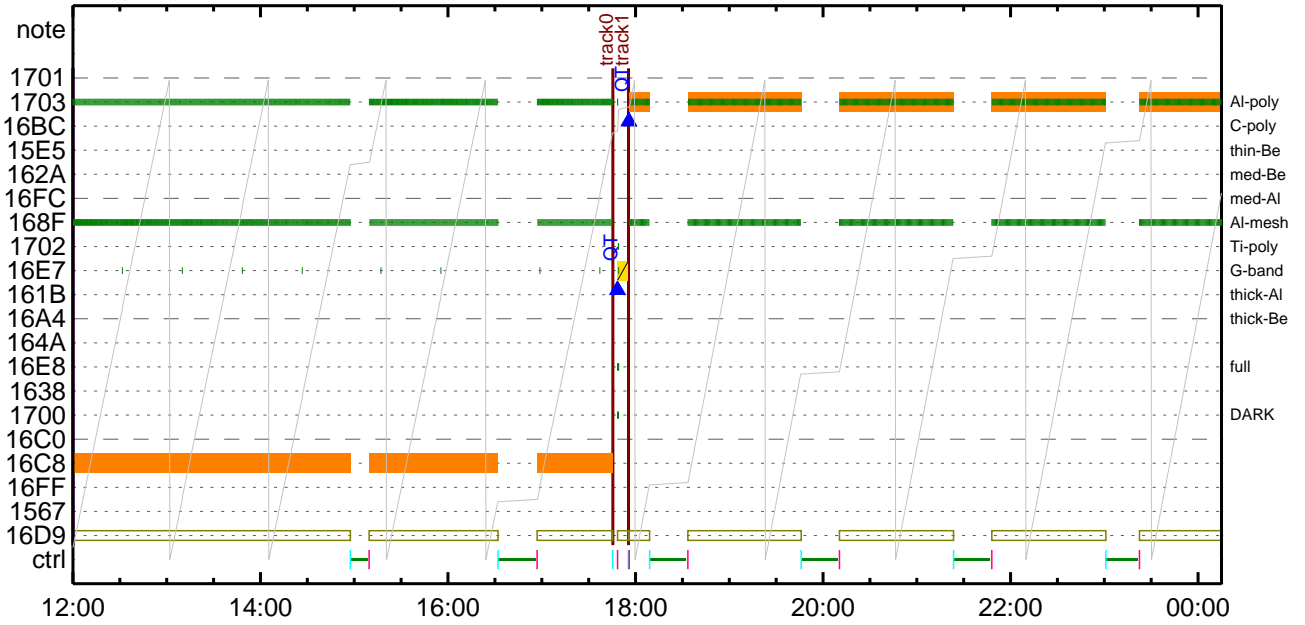
### CMDI #0792 2009/09/12



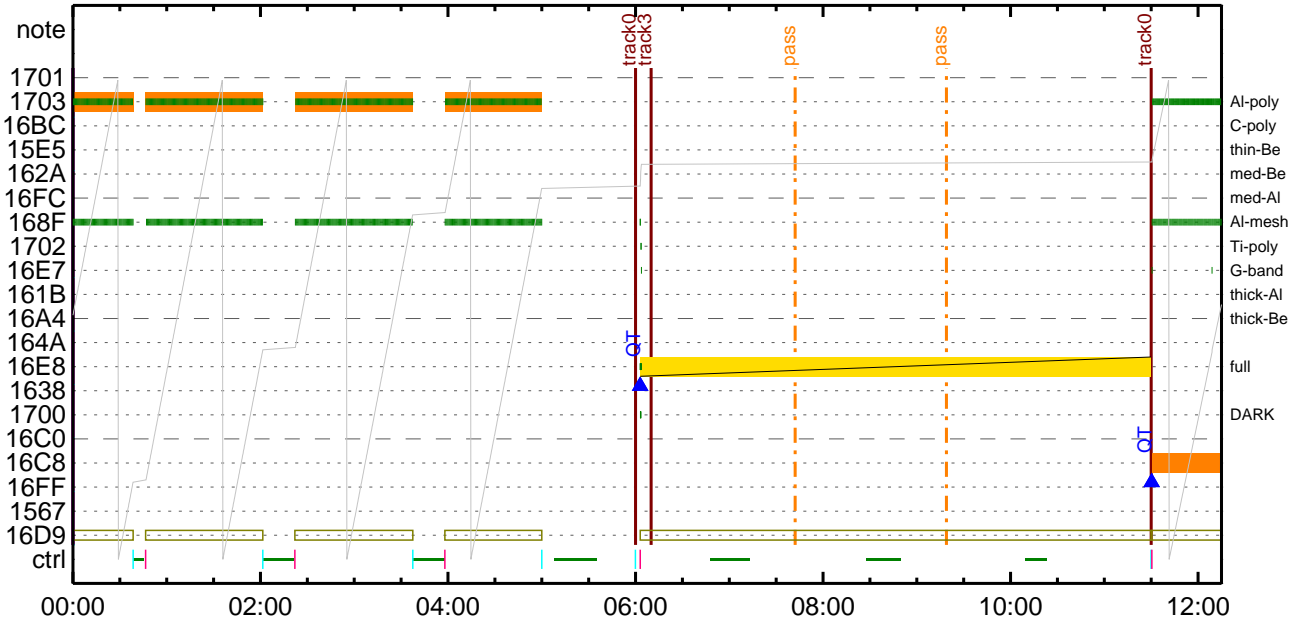
### CMDI #0792 2009/09/13



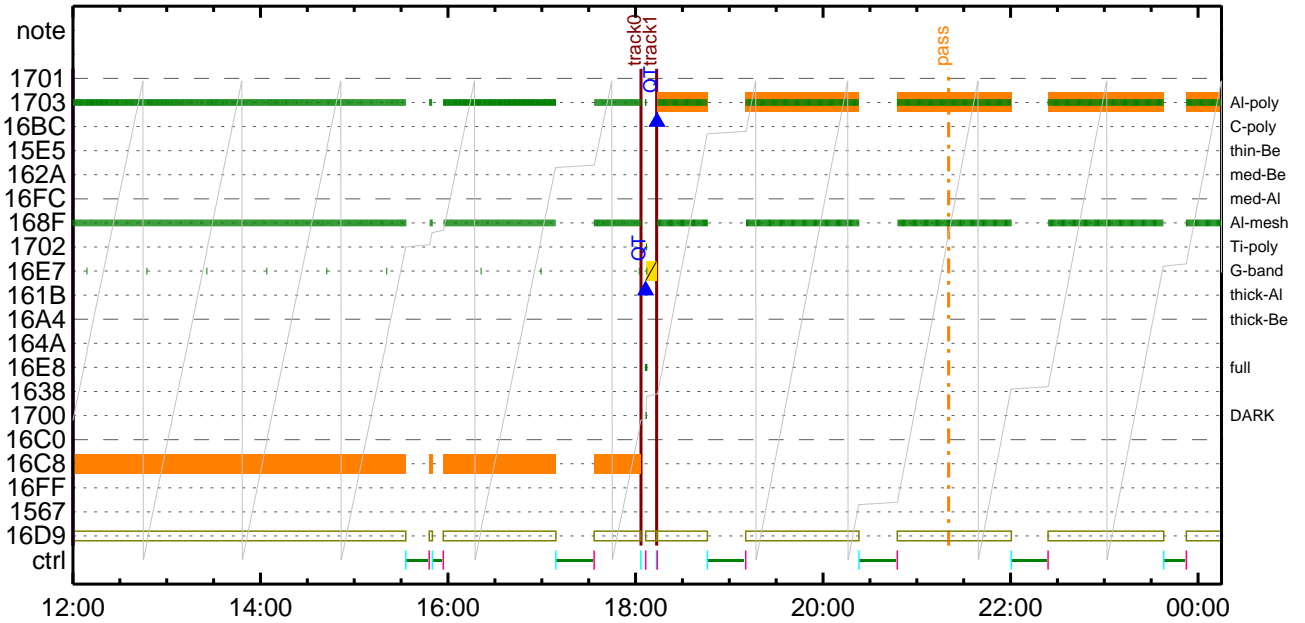
CMDI #0792 2009/09/13



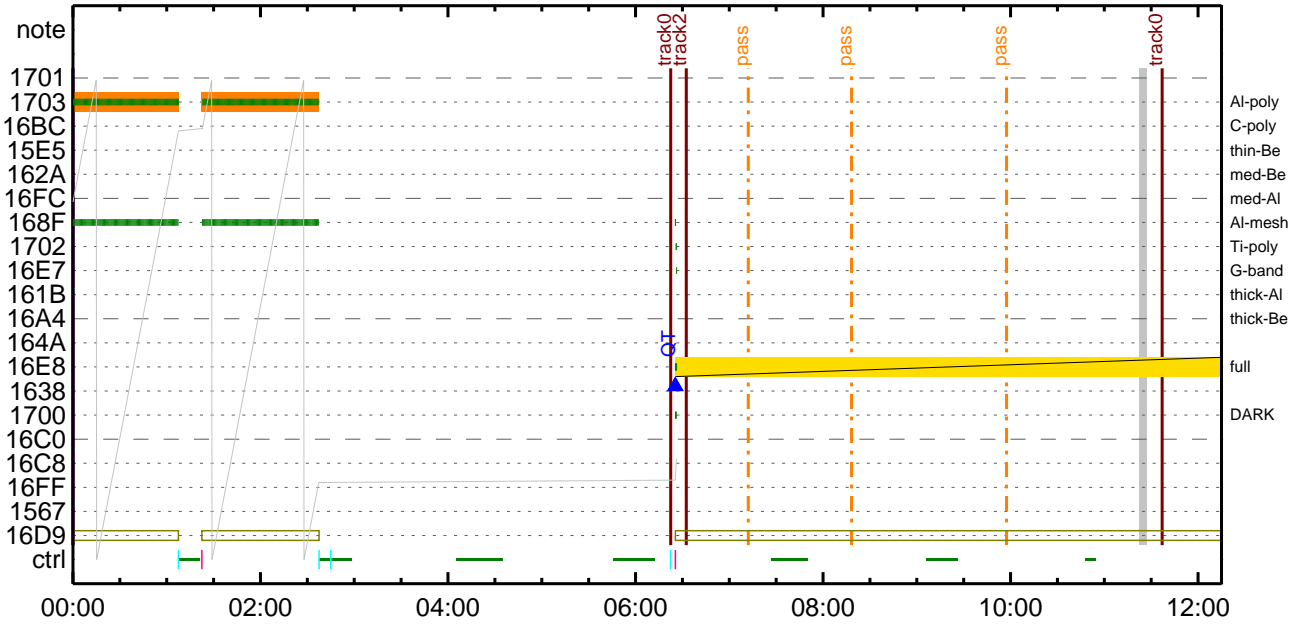
CMDI #0792 2009/09/14



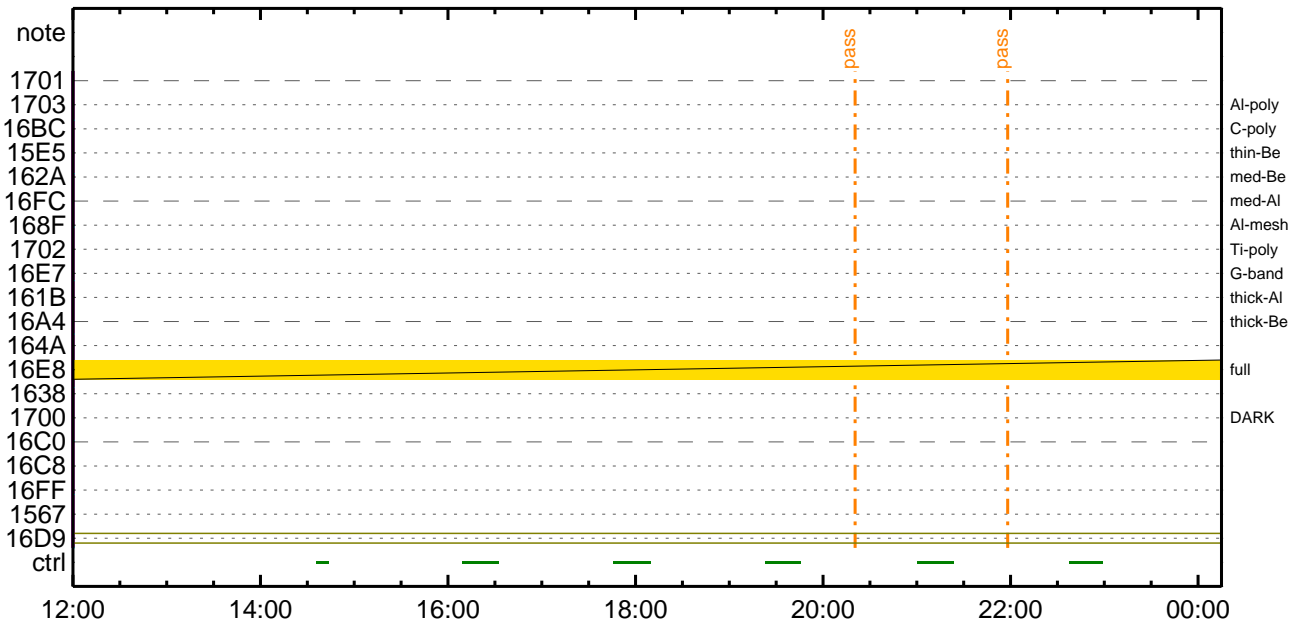
CMDI #0792 2009/09/14



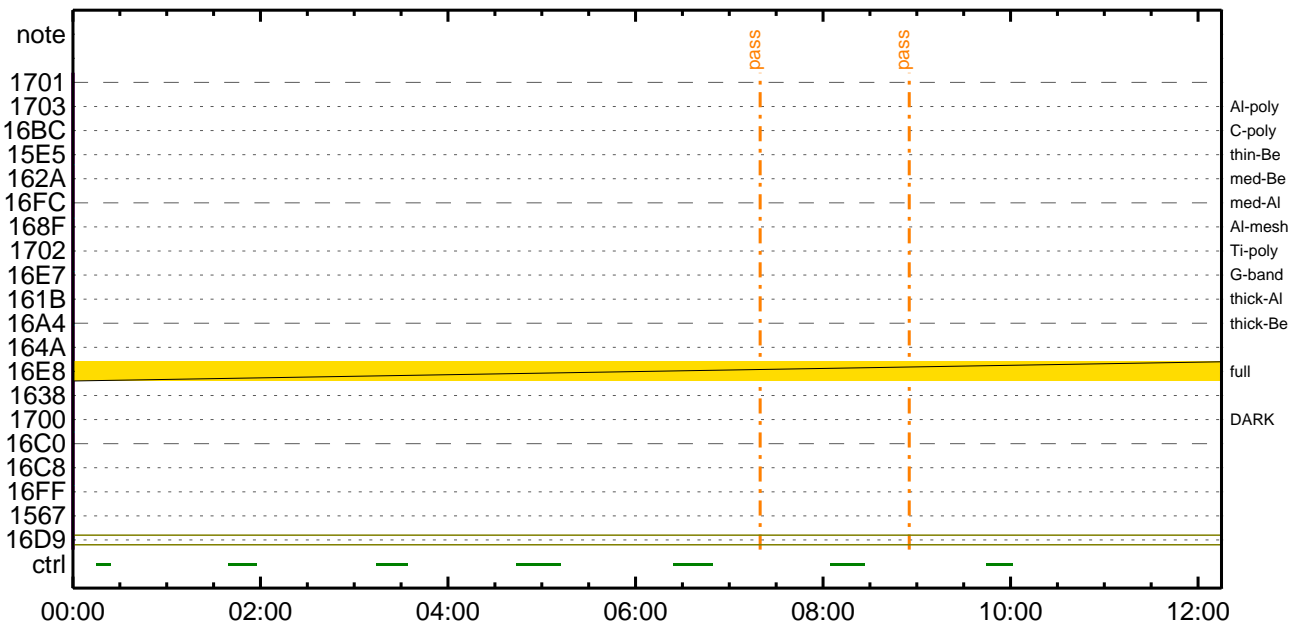
CMDI #0792 2009/09/15



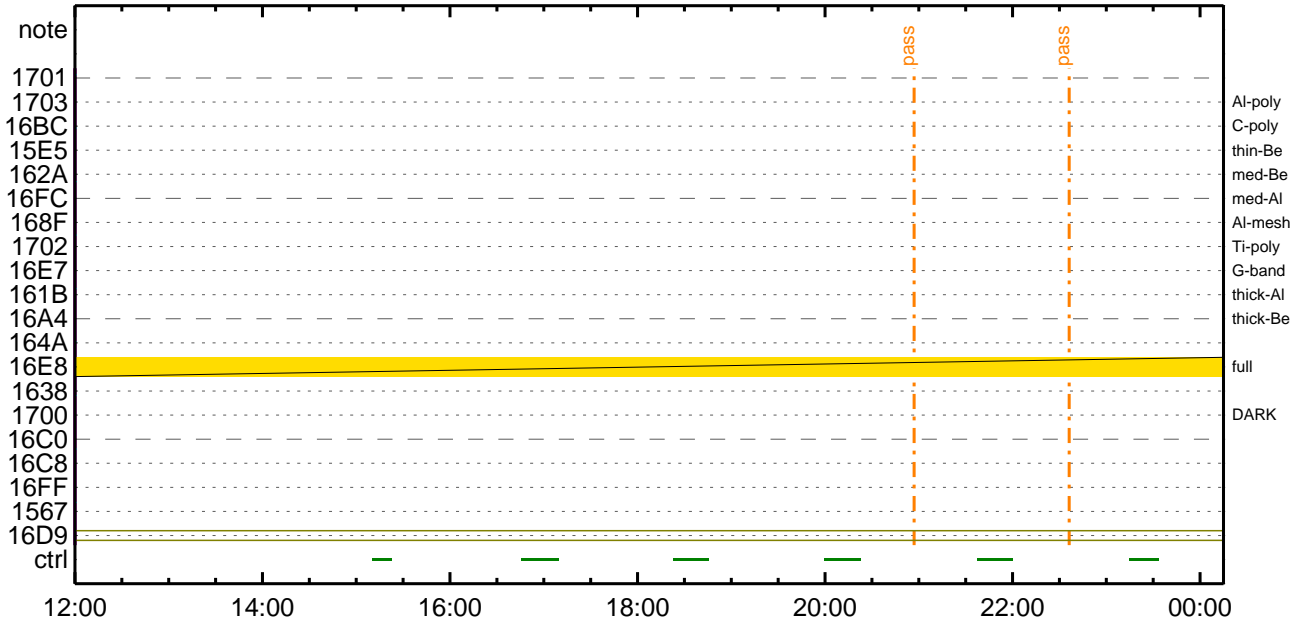
CMDI #0792 2009/09/15



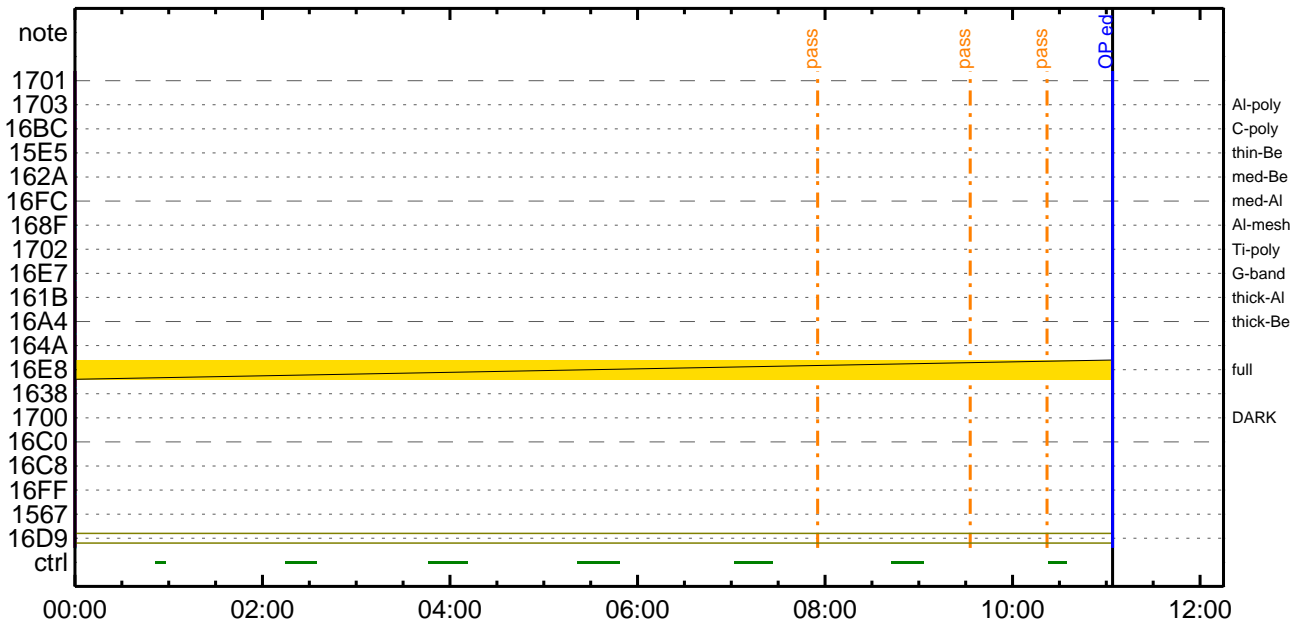
CMDI #0792 2009/09/16



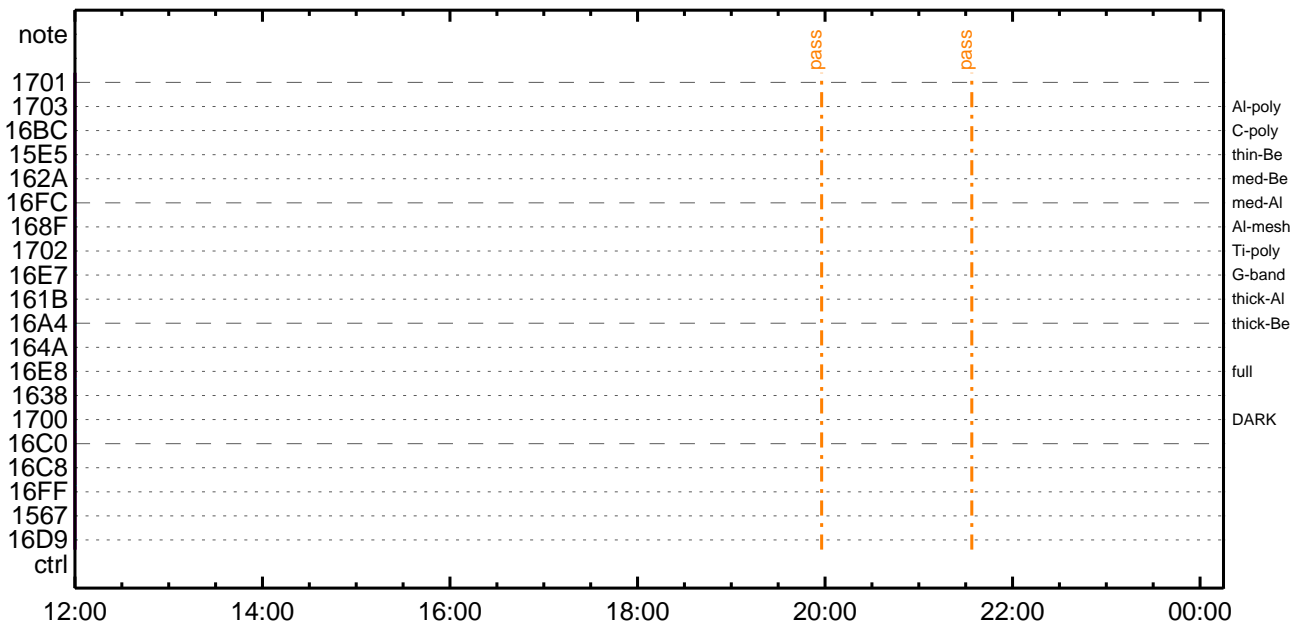
CMDI #0792 2009/09/16



CMDI #0792 2009/09/17



CMDI #0792 2009/09/17





```
0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;aOP/OGY1;4YE;a
0103 S. OP op-843:OP
0104 ( )
0105 S. OG og-843:OG
0106 ( )
0107 C.
0108 C. ;aNMOG&OPf^°eYAYOX;a
0109 C. NMOG(0x200000-0x207FFF;s 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C.         c{[HK1_DMP_TOP_ADRS_1]          EQ      40
0113 C.         c{[HK1_DMP_TOP_ADRS_0]          EQ      0
0114 C.         c{[HK1_DMP_BLOCK_NUM]           EQ     127
0115 C.         c{[HK1_DMP_REPEAT_NUM]          EQ      0
0116 C.         c{[HK1_DMA_DMP_PIM]             EQ     DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C.         c{[HK1_PKT_FORM_NO]             EQ      7
0120 C.         c{[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121 C.         c{[HK1_S_TLM_BIT_RATE]          EQ     32k
0122 C.         c{[HK1_X_TLM_BIT_RATE]          EQ      4M
0123 C.         c{[HK1_DMP_CHK_FLG]            EQ     EXEC
0124 C. YAYOXx1/2^a^I»oð³ÍÇ§
0125 C.         c{[HK1_DMP_CHK_FLG]             EQ     NON
0126 C. RAM ID=NMOGqf^E¹ç•ë²ÏOKoð³ÍÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;s 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C.         c{[HK1_DMP_TOP_ADRS_1]          EQ     41
0132 C.         c{[HK1_DMP_TOP_ADRS_0]          EQ      0
0133 C.         c{[HK1_DMP_BLOCK_NUM]           EQ     127
0134 C.         c{[HK1_DMP_REPEAT_NUM]          EQ      0
0135 C.         c{[HK1_DMA_DMP_PIM]             EQ     DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C.         c{[HK1_PKT_FORM_NO]             EQ      7
0139 C.         c{[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140 C.         c{[HK1_S_TLM_BIT_RATE]          EQ     32k
0141 C.         c{[HK1_X_TLM_BIT_RATE]          EQ      4M
0142 C.         c{[HK1_DMP_CHK_FLG]            EQ     EXEC
0143 C. YAYOXx1/2^a^I»oð³ÍÇ§
0144 C.         c{[HK1_DMP_CHK_FLG]             EQ     NON
0145 C. RAM ID=NMOGqf^E¹ç•ë²ÏOKoð³ÍÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;s 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C.         c{[HK1_DMP_TOP_ADRS_1]          EQ     42
0151 C.         c{[HK1_DMP_TOP_ADRS_0]          EQ      0
0152 C.         c{[HK1_DMP_BLOCK_NUM]           EQ     65
0153 C.         c{[HK1_DMP_REPEAT_NUM]          EQ      0
0154 C.         c{[HK1_DMA_DMP_PIM]             EQ     DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C.         c{[HK1_PKT_FORM_NO]             EQ      7
0158 C.         c{[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159 C.         c{[HK1_S_TLM_BIT_RATE]          EQ     32k
0160 C.         c{[HK1_X_TLM_BIT_RATE]          EQ      4M
0161 C.         c{[HK1_DMP_CHK_FLG]            EQ     EXEC
0162 C. YAYOXx1/2^a^I»oð³ÍÇ§
0163 C.         c{[HK1_DMP_CHK_FLG]             EQ     NON
0164 C. RAM ID=NMOG, RAM ID=OPqf^E¹ç•ë²ÏOKoð³ÍÇ§
0165 C.
0166 C. ***** oE²¼oI³Ä´¶Á°oEÉ¬oÁ÷¿@ (¼áµ-YAYOXx1/2^e¼çoðÁÓÆoÇ¼^a¬°oE¼i¹çoÇoâ) *****
0167 C. DHUYâ;4YE;E¼Y¼, Y¼;4YE;EoðIâ¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C.         c{[HK1_PKT_FORM_NO]             EQ      2
0171 C.         c{[HK1_PKT_GEN_TIME]            EQ     0.5S
0172 C.         c{[HK1_S_TLM_BIT_RATE]          EQ     32K
0173 C.         c{[HK1_X_TLM_BIT_RATE]          EQ      4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE | s OPOG UPLOADo-Á÷¿@NGuI¼i¹ç;ç°E²¼oI³TI-CMDÁ÷¿@oI¼Á¹Ôo•oEoð³oE; f
0180 C.         oE¼;çSEToEDUMPaIÆ±°iYÑY¹ç¹Ôo|o³oE; f
0181 C.
0182 C. TIY³YpYóYEoðÁDÍ¿(UT)
0183 +. TI 2009-09-12 10:51:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C.         c{[HK1_TI_CMD_NUM]               EQ     1COUNTUP
0186 C.
0187 +. TI 2009-09-12 10:51:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C.         c{[HK1_TI_CMD_NUM]               EQ     1COUNTUP
0190 C.
0191 +. TI 2009-09-12 10:51:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C.         c{[HK1_TI_CMD_NUM]               EQ     1COUNTUP
```









(a) Spacecraft Operation Procedure (real-commands)

```
main-845 2009-09-12 12:31:49 130 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿øÅø•µ°È»ÍxÁÇøíYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÈ;ÈøÈ¼°ÇÓø•ø¿¼í¹çøí;çÀ®, ùø¹øÈøÈøçÁ+¿®ø•øÈøøøøøÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG ____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 06 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 07 80 80 20 20)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 08 80 80 20 08)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 09 80 80 08 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 0f 80 80 04 04)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 10 80 80 10 10)
0052 . C. ----- Success Verify ? OK / NG ____
0053 C.
0054 C.
0055 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0056 C.
0057 +. DC 07-F0 MDP_XRT_MODE_OBSV
0058 BC (c2)
0059 +. TI 2009-09-12 10:55:02.0
0060 DC 07-F0 MDP_XRT_MODE_OBSV
0061 BC (c2)
0062 . C. ----- Success Verify ? OK / NG ____
0063 C.
0064 C. ***** XRT END *****
0065 . C. *****
0066 C. SOT table upload
0067 C. *****
0068 . C. < Stop FG table >
0069 +. DC 07-F0 MDP_FG_CTRL_MANU
0070 BC (51)
0071 . C. -----
0072 C. MDP_FG_CTRL_MODE = MANU [ ]
0073 C. -----
0074 C.
0075 . C. <Upload FG Observation Table>
0076 . S. RAM ram-264:MDP_OBS_F
0077 ( )
0078 C.
0079 . C. < Dump RAMID=MDP_OBS_F >
0080 +. DC 07-F0 MDP_DUMP_FGTBL
0081 BC (82 07 00 00 00 38 b8)
0082 C. -----
0083 C. MDP_OBS_F verify = OK/NG [ ]
0084 C. -----
0085 C.
0086 . C. < Stop SP table >
0087 +. DC 07-F0 MDP_SP_CTRL_MANU
0088 BC (61)
0089 C. -----
0090 C. MDP_SP_CTRL_MODE = MANU [ ]
0091 C. -----
0092 C.
0093 . C. <Upload SP Observation Table>
0094 . S. RAM ram-288:MDP_OBS_S
0095 ( )
```



Sep 12, 09 12:31

XRT\_OGLIST\_0792.chk

Page 1/4

\*\*\* OP Sequence for XRT \*\*\*

2009/09/12	11:05:54.0	XRT_CTRL_MANU_428_OG [0x1ac]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/09/12	11:05:56.0	XRT_FOCUS_POSITION_402_OG [0x192]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2009/09/12	11:06:00.0	AOCS_ORe-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 af 1b 00 00		
2009/09/12	11:06:16.0	XRT_QT_PROG_SET_413_OG [0x19d]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04		
2009/09/12	11:06:18.0	XRT_AEC_RESET_415_OG [0x19f]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2009/09/12	11:06:20.0	XRT_FLD_DIS_419_OG [0x1a3]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2009/09/12	11:06:22.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2009/09/12	11:06:24.0	XRT_ARS_DIS_446_OG [0x1be]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2009/09/12	11:06:26.0	XRT_CTRL_AUTO_432_OG [0x1b0]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/09/12	14:23:30.0	XRT_CTRL_MANU_435_OG [0x1b3]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/09/12	14:28:30.0	XRT_Custom_430_OG [0x1ae]					
2009/09/12	14:29:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/09/12	15:55:30.0	XRT_CTRL_MANU_435_OG [0x1b3]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/09/12	16:19:00.0	XRT_Custom_430_OG [0x1ae]					
2009/09/12	16:20:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/09/12	17:32:00.0	XRT_CTRL_MANU_435_OG [0x1b3]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/09/12	17:56:00.0	XRT_Custom_430_OG [0x1ae]					
2009/09/12	17:57:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/09/12	17:57:24.0	XRT_CTRL_MANU_428_OG [0x1ac]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/09/12	17:57:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2009/09/12	17:57:30.0	AOCS_ORe-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2009/09/12	17:57:46.0	XRT_FLD_DIS_419_OG [0x1a3]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2009/09/12	17:57:48.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2009/09/12	17:57:50.0	XRT_ARS_DIS_410_OG [0x19a]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2009/09/12	18:00:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2009/09/12	18:00:30.0	XRT_CTRL_AUTO_403_OG [0x193]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/09/12	18:07:24.0	XRT_CTRL_MANU_428_OG [0x1ac]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/09/12	18:07:26.0	XRT_FOCUS_POSITION_402_OG [0x192]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2009/09/12	18:07:30.0	AOCS_ORe-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	01 00 00 00 00		
2009/09/12	18:07:46.0	XRT_QT_PROG_SET_409_OG [0x199]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13		
2009/09/12	18:07:48.0	XRT_AEC_RESET_415_OG [0x19f]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2009/09/12	18:07:50.0	XRT_FLD_DIS_419_OG [0x1a3]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2009/09/12	18:07:52.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2009/09/12	18:07:54.0	XRT_ARS_DIS_446_OG [0x1be]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2009/09/12	18:07:56.0	XRT_CTRL_AUTO_432_OG [0x1b0]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/09/12	19:09:30.0	XRT_CTRL_MANU_435_OG [0x1b3]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/09/12	19:32:30.0	XRT_Custom_430_OG [0x1ae]					
2009/09/12	19:33:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/09/12	20:46:30.0	XRT_CTRL_MANU_435_OG [0x1b3]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/09/12	21:10:00.5	XRT_Custom_430_OG [0x1ae]					
2009/09/12	21:11:00.5	XRT_CTRL_AUTO_432_OG [0x1b0]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/09/12	22:24:00.5	XRT_CTRL_MANU_435_OG [0x1b3]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/09/12	22:46:01.0	XRT_Custom_430_OG [0x1ae]					
2009/09/12	22:47:01.0	XRT_CTRL_AUTO_432_OG [0x1b0]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/09/13	00:01:30.0	XRT_CTRL_MANU_435_OG [0x1b3]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/09/13	00:11:30.0	XRT_Custom_430_OG [0x1ae]					
2009/09/13	00:12:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/09/13	01:27:30.0	XRT_CTRL_MANU_435_OG [0x1b3]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/09/13	01:44:30.0	XRT_Custom_430_OG [0x1ae]					
2009/09/13	01:45:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]					

Sep 12, 09 12:31

## XRT\_OGLIST\_0792.chk

Page 2/4

2009/09/13	03:00:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/09/13	03:21:00.0	XRT_Custom_430_OG [0x1ae]								
2009/09/13	03:22:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/09/13	04:00:00.0	XRT_CTRL_MANU_428_OG [0x1ac]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/09/13	05:42:30.0	AOCS_Ore-point_Start_2_OG [0x098]								
			AOCU_NM	5	02-76	00	00	00	00	00
2009/09/13	05:42:34.0	XRT_CTRL_MANU_428_OG [0x1ac]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/09/13	05:42:36.0	XRT_FOCUS_POSITION_442_OG [0x1ba]								
			XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2009/09/13	05:42:56.0	XRT_FLD_DIS_419_OG [0x1a3]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/09/13	05:42:58.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/09/13	05:43:00.0	XRT_ARS_DIS_410_OG [0x19a]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/09/13	05:45:38.0	XRT_QT_PROG_SET_412_OG [0x19c]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	08			
2009/09/13	05:45:40.0	XRT_CTRL_AUTO_403_OG [0x193]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/09/13	05:52:30.0	AOCS_Ore-point_Start_4_OG [0x09a]								
			AOCU_NM	5	02-76	04	00	00	00	00
2009/09/13	10:35:54.0	XRT_CTRL_MANU_428_OG [0x1ac]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/09/13	10:35:56.0	XRT_FOCUS_POSITION_402_OG [0x192]								
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2009/09/13	10:36:00.0	AOCS_Ore-point_Start_1_OG [0x097]								
			AOCU_NM	5	02-76	00	af	1b	00	00
2009/09/13	10:36:16.0	XRT_QT_PROG_SET_413_OG [0x19d]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2009/09/13	10:36:18.0	XRT_AEC_RESET_415_OG [0x19f]								
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2009/09/13	10:36:20.5	XRT_FLD_DIS_419_OG [0x1a3]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/09/13	10:36:22.5	XRT_FLRCTRL_DIS_447_OG [0x1bf]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/09/13	10:36:24.5	XRT_ARS_DIS_446_OG [0x1be]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/09/13	10:36:26.5	XRT_CTRL_AUTO_432_OG [0x1b0]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/09/13	14:57:30.0	XRT_CTRL_MANU_435_OG [0x1b3]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/09/13	15:08:30.0	XRT_Custom_430_OG [0x1ae]								
2009/09/13	15:09:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/09/13	16:32:00.0	XRT_CTRL_MANU_435_OG [0x1b3]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/09/13	16:56:00.0	XRT_Custom_430_OG [0x1ae]								
2009/09/13	16:57:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/09/13	17:45:24.0	XRT_CTRL_MANU_428_OG [0x1ac]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/09/13	17:45:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]								
			XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2009/09/13	17:45:30.0	AOCS_Ore-point_Start_2_OG [0x098]								
			AOCU_NM	5	02-76	00	00	00	00	00
2009/09/13	17:45:46.0	XRT_FLD_DIS_419_OG [0x1a3]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/09/13	17:45:48.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/09/13	17:45:50.0	XRT_ARS_DIS_410_OG [0x19a]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/09/13	17:48:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2009/09/13	17:48:30.0	XRT_CTRL_AUTO_403_OG [0x193]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/09/13	17:55:24.0	XRT_CTRL_MANU_428_OG [0x1ac]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/09/13	17:55:26.0	XRT_FOCUS_POSITION_402_OG [0x192]								
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2009/09/13	17:55:30.0	AOCS_Ore-point_Start_3_OG [0x099]								
			AOCU_NM	5	02-76	01	00	00	00	00
2009/09/13	17:55:46.0	XRT_QT_PROG_SET_409_OG [0x199]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2009/09/13	17:55:48.0	XRT_AEC_RESET_415_OG [0x19f]								
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2009/09/13	17:55:50.0	XRT_FLD_DIS_419_OG [0x1a3]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/09/13	17:55:52.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/09/13	17:55:54.0	XRT_ARS_DIS_446_OG [0x1be]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/09/13	17:55:56.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/09/13	18:09:00.0	XRT_CTRL_MANU_435_OG [0x1b3]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/09/13	18:32:30.0	XRT_Custom_430_OG [0x1ae]								
2009/09/13	18:33:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				

Sep 12, 09 12:31

## XRT\_OGLIST\_0792.chk

Page 3/4

2009/09/13	19:46:00.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/09/13	20:09:30.0	XRT_Custom_430_OG [0x1ae]					
2009/09/13	20:10:30.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/09/13	21:23:30.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/09/13	21:47:00.0	XRT_Custom_430_OG [0x1ae]					
2009/09/13	21:48:00.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/09/13	23:01:00.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/09/13	23:21:30.0	XRT_Custom_430_OG [0x1ae]					
2009/09/13	23:22:30.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/09/14	00:38:30.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/09/14	00:45:30.0	XRT_Custom_430_OG [0x1ae]					
2009/09/14	00:46:30.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/09/14	02:01:30.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/09/14	02:21:00.0	XRT_Custom_430_OG [0x1ae]					
2009/09/14	02:22:00.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/09/14	03:37:30.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/09/14	03:58:00.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/09/14	05:00:00.0	XRT_CTRL_MANU_428_OG [0x1ac] MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/09/14	05:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac] MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/09/14	05:59:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2009/09/14	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00 00 00 00 00		
2009/09/14	06:00:16.0	XRT_FLD_DIS_419_OG [0x1a3] MDP_XRT_FLD_DIS	1	07-F0	d9		
2009/09/14	06:00:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2009/09/14	06:00:20.0	XRT_ARS_DIS_410_OG [0x19a] MDP_XRT_ARS_DIS	1	07-F0	d5		
2009/09/14	06:02:58.0	XRT_QT_PROG_SET_412_OG [0x19c] MDP_XRT_QT_PROG_SET	2	07-F0	c4 08		
2009/09/14	06:03:00.0	XRT_CTRL_AUTO_403_OG [0x193] MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/09/14	06:10:00.0	AOCS_Ore-point_Start_5_OG [0x09b] AOCU_NM	5	02-76	03 00 00 00 00		
2009/09/14	11:29:54.0	XRT_CTRL_MANU_428_OG [0x1ac] MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/09/14	11:29:56.0	XRT_FOCUS_POSITION_402_OG [0x192] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2009/09/14	11:30:00.0	AOCS_Ore-point_Start_1_OG [0x097] AOCU_NM	5	02-76	00 af 1b 00 00		
2009/09/14	11:30:16.0	XRT_QT_PROG_SET_413_OG [0x19d] MDP_XRT_QT_PROG_SET	2	07-F0	c4 04		
2009/09/14	11:30:18.0	XRT_AEC_RESET_415_OG [0x19f] MDP_XRT_AEC_RESET	1	07-F0	d0		
2009/09/14	11:30:20.0	XRT_FLD_DIS_419_OG [0x1a3] MDP_XRT_FLD_DIS	1	07-F0	d9		
2009/09/14	11:30:22.0	XRT_FLRCTRL_DIS_447_OG [0x1bf] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2009/09/14	11:30:24.0	XRT_ARS_DIS_446_OG [0x1be] MDP_XRT_ARS_DIS	1	07-F0	d5		
2009/09/14	11:30:26.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/09/14	15:33:00.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/09/14	15:47:00.0	XRT_Custom_430_OG [0x1ae]					
2009/09/14	15:48:00.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/09/14	15:50:00.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/09/14	15:56:00.0	XRT_Custom_430_OG [0x1ae]					
2009/09/14	15:57:00.0	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/09/14	17:09:00.0	XRT_CTRL_MANU_435_OG [0x1b3] MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/09/14	17:32:30.0	XRT_Custom_430_OG [0x1ae]					
2009/09/14	17:33:30.5	XRT_CTRL_AUTO_432_OG [0x1b0] MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2009/09/14	18:03:24.0	XRT_CTRL_MANU_428_OG [0x1ac] MDP_XRT_CTRL_MANU	1	07-F0	c1		
2009/09/14	18:03:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2009/09/14	18:03:30.0	AOCS_Ore-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00 00 00 00 00		
2009/09/14	18:03:46.0	XRT_FLD_DIS_419_OG [0x1a3] MDP_XRT_FLD_DIS	1	07-F0	d9		
2009/09/14	18:03:48.0	XRT_FLRCTRL_DIS_447_OG [0x1bf] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2009/09/14	18:03:50.0	XRT_ARS_DIS_410_OG [0x19a]					



Sep 12, 09 12:31

## XRT\_OGLIST\_0792.chk

Page 4/4

2009/09/14	18:06:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2009/09/14	18:06:30.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2009/09/14	18:13:24.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/09/14	18:13:26.0	XRT_FOCUS_POSITION_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/09/14	18:13:30.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/09/14	18:13:46.0	XRT_QT_PROG_SET_409_OG [0x199]	AOCU_NM	5	02-76	01 00 00 00 00
2009/09/14	18:13:48.0	XRT_AEC_RESET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2009/09/14	18:13:50.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_AEC_RESET	1	07-F0	d0
2009/09/14	18:13:52.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLD_DIS	1	07-F0	d9
2009/09/14	18:13:54.0	XRT_ARS_DIS_446_OG [0x1be]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/09/14	18:13:56.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_ARS_DIS	1	07-F0	d5
2009/09/14	18:46:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/09/14	19:09:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/09/14	19:10:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/09/14	20:23:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/09/14	20:46:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/09/14	20:47:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/09/14	22:00:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/09/14	22:23:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/09/14	22:24:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/09/14	23:38:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/09/14	23:51:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/09/14	23:52:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/09/15	01:07:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/09/15	01:21:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/09/15	01:22:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/09/15	02:37:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/09/15	02:45:00.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/09/15	06:22:24.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/09/15	06:22:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2009/09/15	06:22:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2009/09/15	06:22:46.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9
2009/09/15	06:22:48.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/09/15	06:22:50.5	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5
2009/09/15	06:25:28.5	XRT_QT_PROG_SET_412_OG [0x19c]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2009/09/15	06:25:30.5	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/09/15	06:32:30.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	02 00 00 00 00
2009/09/15	11:37:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00