

XRT Timeline to be uploaded on 2009/09/17

Period: 2009/09/17 11:04:00 - 2009/09/22 10:36:00

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Normal mode

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XOB #1563: CCD Monitor During Bakeout - G-band + dark - wide FOV													
Term		Pointing (x, y)					Comment						
09/17 11:17:00 - 09/17 17:56:54		Track (77.8, -154.0) @ 09/17 11:14:00					# OP start + 10min, EIS QS dynamics, and HOP 134 (follows Hinode pointing).						
09/17 21:52:50 - 09/18 03:29:54		Track (77.8, -154.0) @ 09/17 11:14:00					# OP start + 10min, EIS QS dynamics, and HOP 134 (follows Hinode pointing).						
PROG= 06 Inf.-time(s)													
└─ Subr= 1 1-time(s) 600.0sec													
└─ Seqn= 98 1-time(s) 4.0sec													
Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Be		Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
XOB #1565: CCD Monitor During Bakeout - G-band + dark - wide FOV - lower cadence (30min)													
Term		Pointing (x, y)					Comment						
09/17 18:00:00 - 09/17 21:49:40		Track (77.8, -154.0) @ 09/17 11:14:00					# OP start + 10min, EIS QS dynamics, and HOP 134 (follows Hinode pointing).						
PROG= 10 Inf.-time(s)													
└─ Subr= 1 1-time(s) 1800.0sec													
└─ Seqn= 98 1-time(s) 4.0sec													
Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Be		Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
XOB #15A8: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant													
Term		Pointing (x, y)					Comment						
09/18 03:33:00 - 09/18 03:34:54		Fixed (-528.4, -528.4)					# XRT post-bakeout four-quadrant pointings.						
PROG= 11 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 28 1-time(s) 12.0sec													
Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/thick-Be		Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Open/thick-Be		Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
XOB #1632: Synoptic Q95 2x2 - Al/mesh (4096) + Dark cal(256 Q98) + Ti-poly (8192)													
Term		Pointing (x, y)					Comment						
09/18 03:38:00 - 09/18 03:39:54		Fixed (-528.4, -528.4)					# XRT post-bakeout four-quadrant pointings.						
09/18 03:48:00 - 09/18 03:49:54		Fixed (528.4, -528.4)											
09/18 03:58:00 - 09/18 03:59:54		Fixed (528.4, 528.4)											
09/18 04:08:00 - 09/18 04:09:54		Fixed (-528.4, 528.4)					* XRT four-quadrant pointing, last of four.						
PROG= 03 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 84 1-time(s) 4.0sec													
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 50 1-time(s) 2.0sec													
Open/Al-mesh		Open/Al-mesh	close	Safe	Dark	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 61 1-time(s) 4.0sec													
Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
XOB #15A9: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant													
Term		Pointing (x, y)					Comment						
09/18 03:43:00 - 09/18 03:44:54		Fixed (528.4, -528.4)											
PROG= 15 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 41 1-time(s) 12.0sec													
Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/thick-Be		Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Open/thick-Be		Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
XOB #15AA: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant													
Term		Pointing (x, y)					Comment						
09/18 03:53:00 - 09/18 03:54:54		Fixed (528.4, 528.4)											
PROG= 02 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 8 1-time(s) 12.0sec													
Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/G-band		Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec

Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #15AB: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant												
Term	Pointing (x, y)							Comment				
09/18 04:03:00 - 09/18 04:04:54	Fixed (-528.4, 528.4)							* XRT four-quadrant pointing, last of four.				
PROG= 20 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 40 1-time(s) 12.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1706: Coronal cavity (6 pairs +Gband)-DPCM 2x2-768X768-FOV offset SE(896,904)-AEC2-6.67min cadence												
Term	Pointing (x, y)							Comment				
09/18 04:13:00 - 09/18 05:32:24	Fixed (-600.0, -700.0)							* Cavity and bright point in SE.				
09/18 05:45:30 - 09/18 07:40:00	Fixed (-600.0, -700.0)							* Cont.				
09/18 18:36:00 - 09/19 03:27:30	Fixed (-600.0, -700.0)							# Cavity and XBP in SE.				
PROG= 09 Inf.-time(s)												
└─ Subr= 1 9-time(s) 400.0sec												
└─ Seqn= 32 1-time(s) 4.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	768x768 (896, 904)	DPCM	2	0	2.0sec
└─ Seqn= 31 1-time(s) 4.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	768x768 (896, 904)	DPCM	2	0	2.0sec
└─ Seqn= 21 1-time(s) 4.0sec												
C-poly/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	768x768 (896, 904)	DPCM	2	0	2.0sec
└─ Seqn= 70 1-time(s) 2.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	768x768 (896, 904)	DPCM	2	0	2.0sec
└─ Seqn= 14 1-time(s) 4.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	768x768 (896, 904)	DPCM	2	0	2.0sec
└─ Seqn= 89 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	64.0s	Obs	2x2	768x768 (896, 904)	DPCM	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 3 1-time(s) 4.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #164F: Synoptic Q95 2x2 - Al/mesh(512/5795) + Dark cal(2x2 4x4 8x8 512 Q98) + Ti-poly(723/11571) + G-band(16)-2												
Term	Pointing (x, y)							Comment				
09/18 05:35:30 - 09/18 05:42:24	Fixed (0.0, 0.0)							synoptic, shifted -27.5 min				
09/19 05:57:30 - 09/22 10:36:00	Fixed (0.0, 0.0)							synoptic, shifted -5.5 min				
PROG= 17 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 47 1-time(s) 4.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 7 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 88 1-time(s) 4.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 92 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #16C8: HOP81 2-filter - Al/poly 16s, Al/mesh 12s, G-band - 384x384												
Term	Pointing (x, y)							Comment				
09/18 10:33:00 - 09/18 17:59:00	Fixed (0.0, 910.0)							* HOP 81, polar monitoring camprn., and HOP 134 (from 14:30; follows Hinode pointing).				
PROG= 04 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 1 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 5 60-time(s) 30.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	11.3s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1650: Synoptic Q95 2x2 - Al/poly(512/5795) + Dark cal(2x2 4x4 8x8 512 Q98) + Ti-poly(723/11571) + G-band(16)-2												
Term	Pointing (x, y)							Comment				

PROG= 05		1-time(s)											
└ Subr= 1		1-time(s)		12.0sec									
└ Seqn= 36		1-time(s)		4.0sec									
└ Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└ Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	5.66s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└ Seqn= 7		1-time(s)		2.0sec									
└ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
└ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
└ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
└ Seqn= 88		1-time(s)		4.0sec									
└ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	707ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	11.3s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└ Seqn= 92		1-time(s)		2.0sec									
└ Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval

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Flare mode

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NOT USED

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Active Region Search

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NOT USED

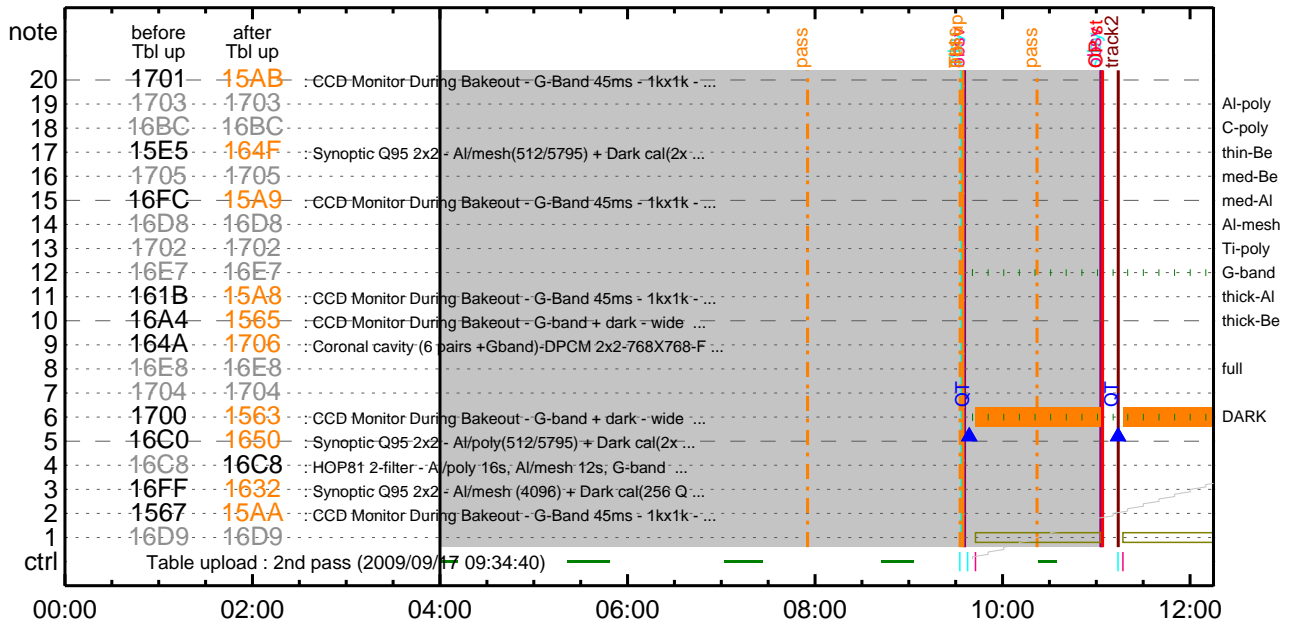
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Flare Detection

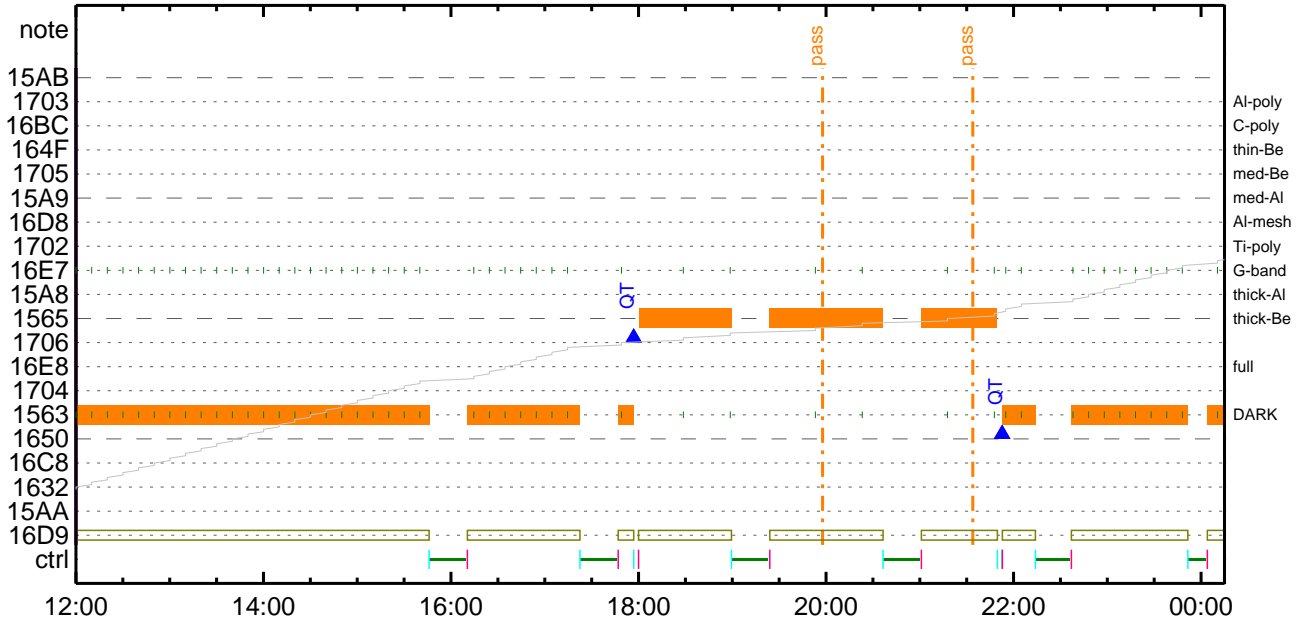
* * * * *

NOT USED

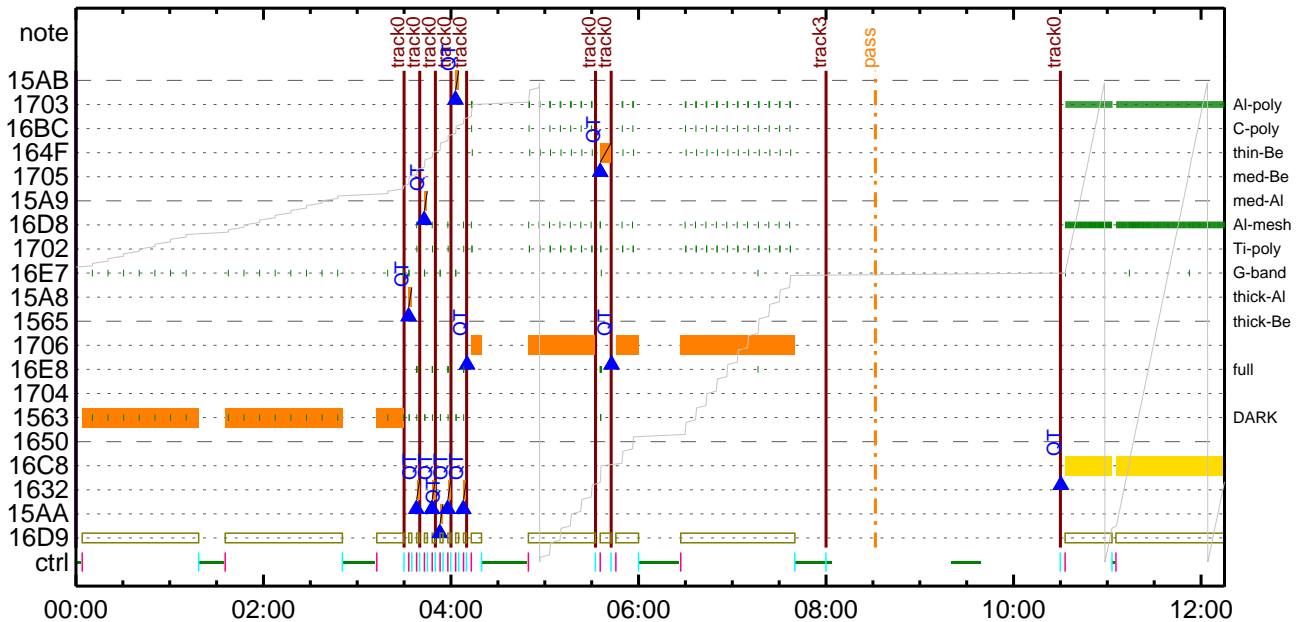
CMDI #0799 2009/09/17



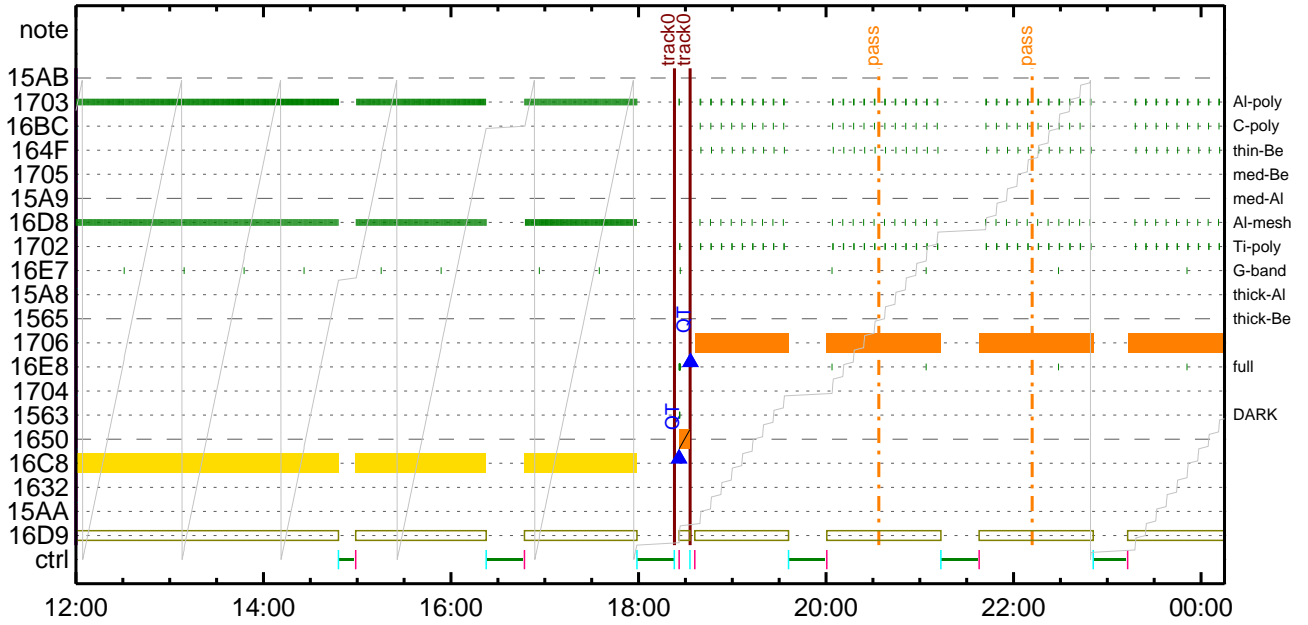
CMDI #0799 2009/09/17



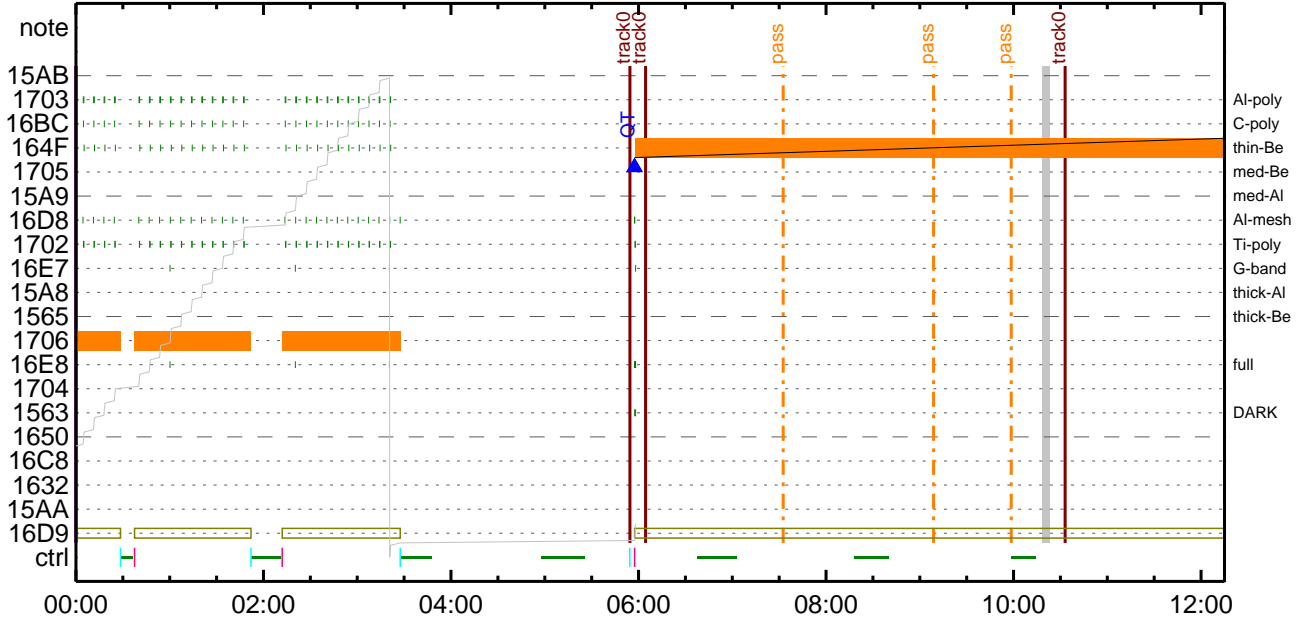
CMDI #0799 2009/09/18



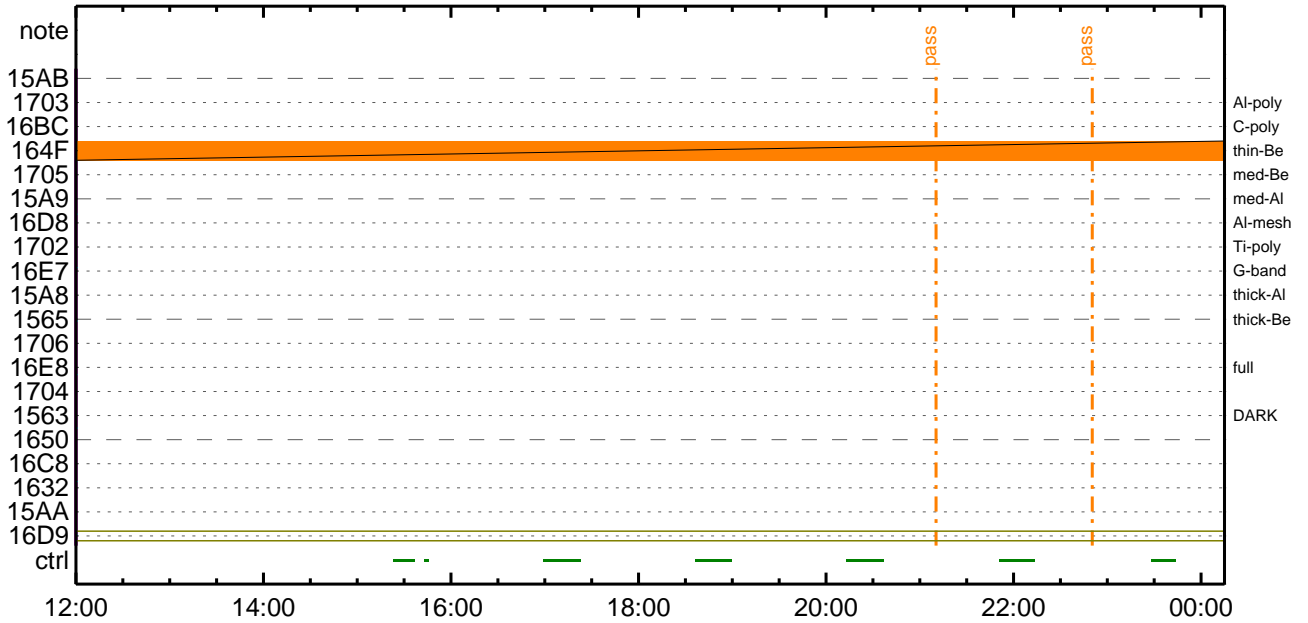
CMDI #0799 2009/09/18



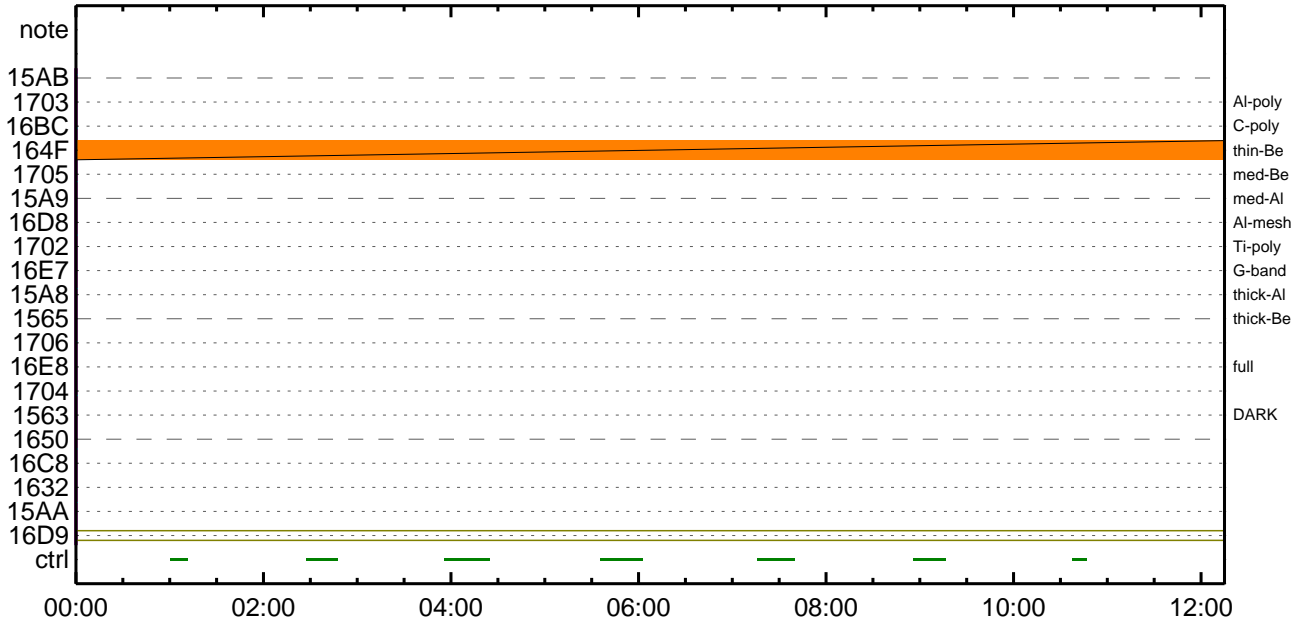
CMDI #0799 2009/09/19



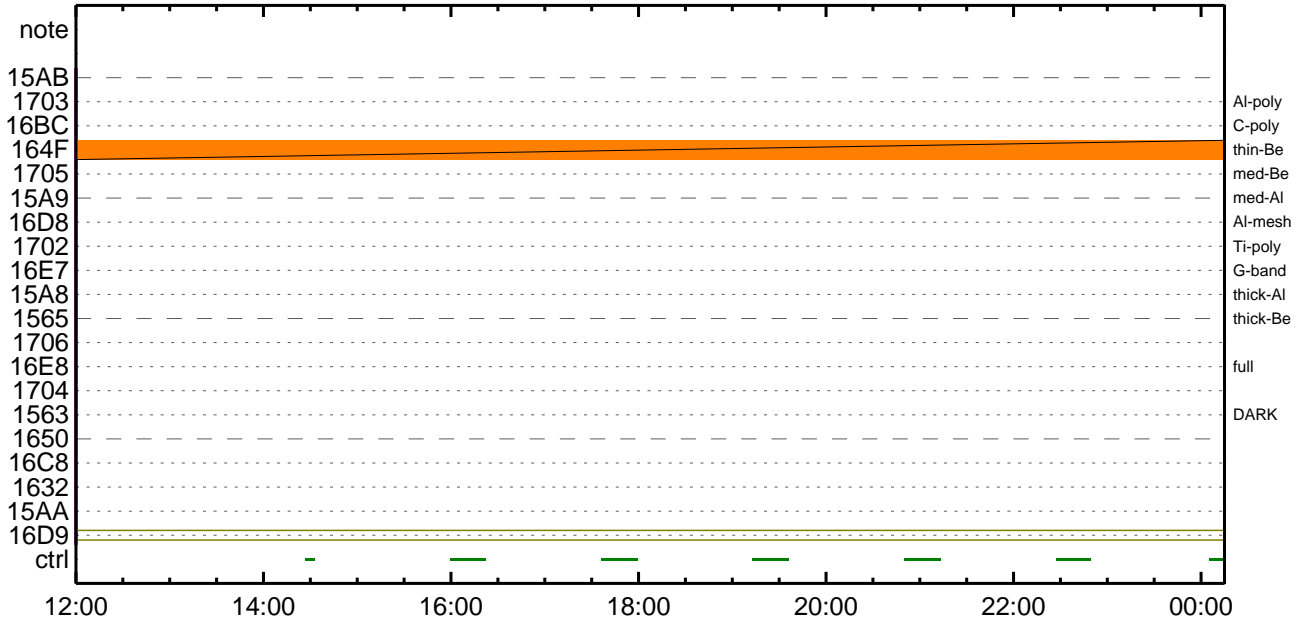
CMDI #0799 2009/09/19



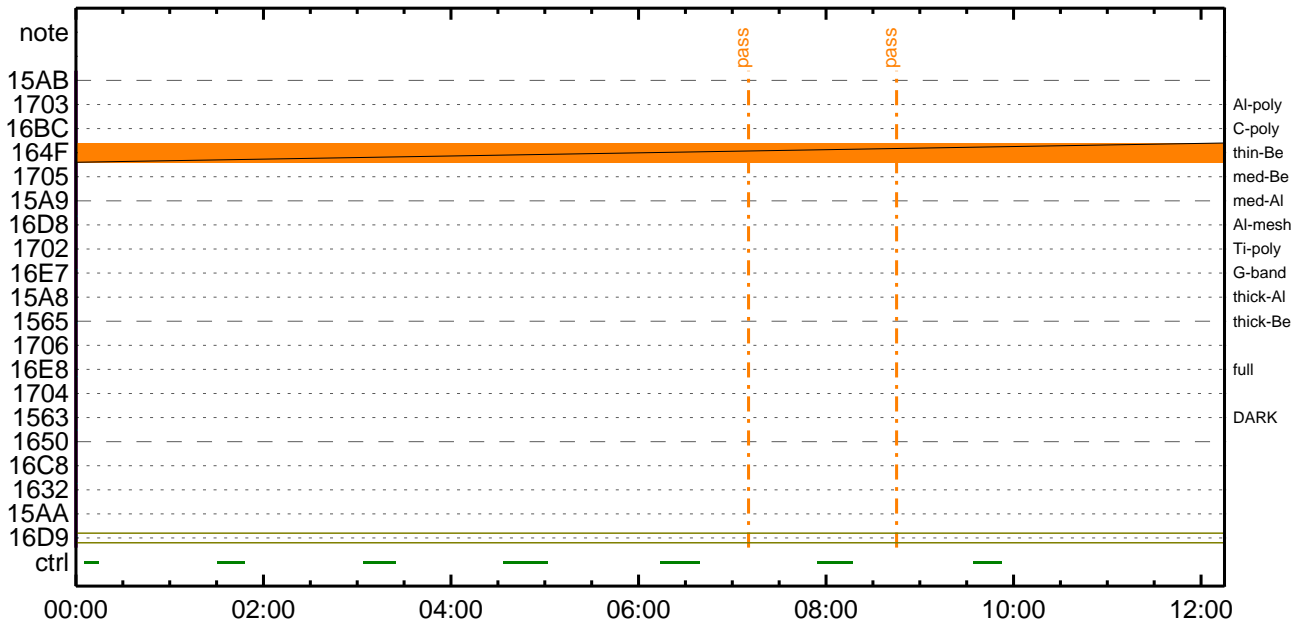
CMDI #0799 2009/09/20



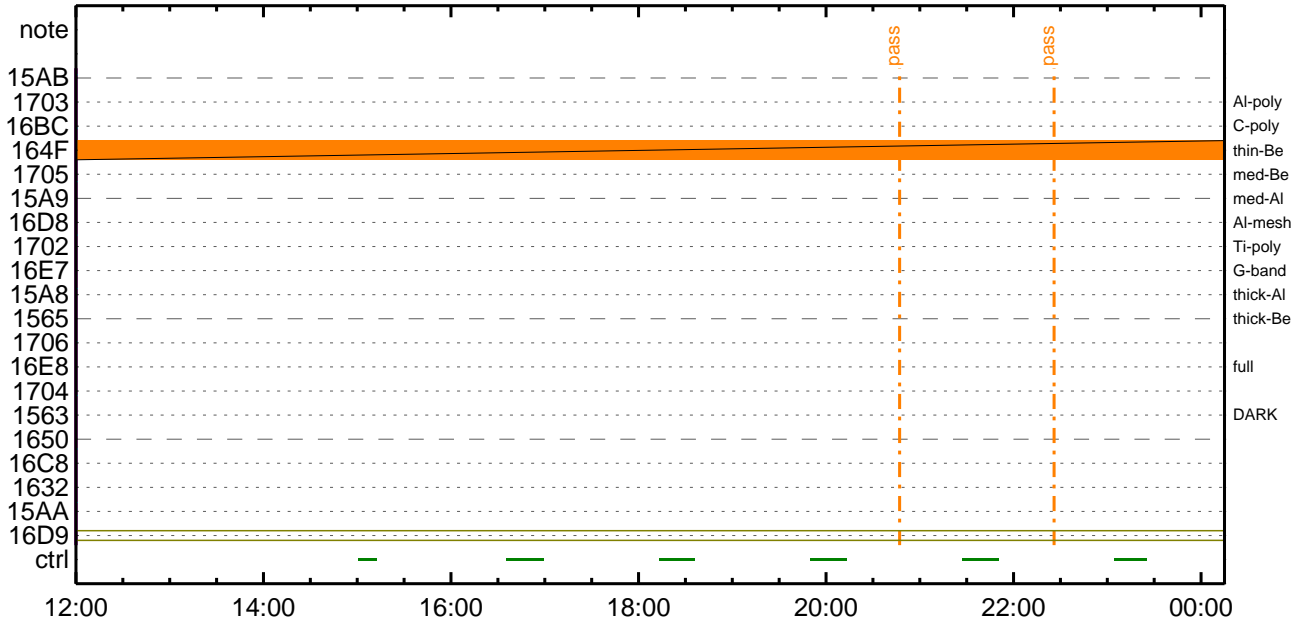
CMDI #0799 2009/09/20



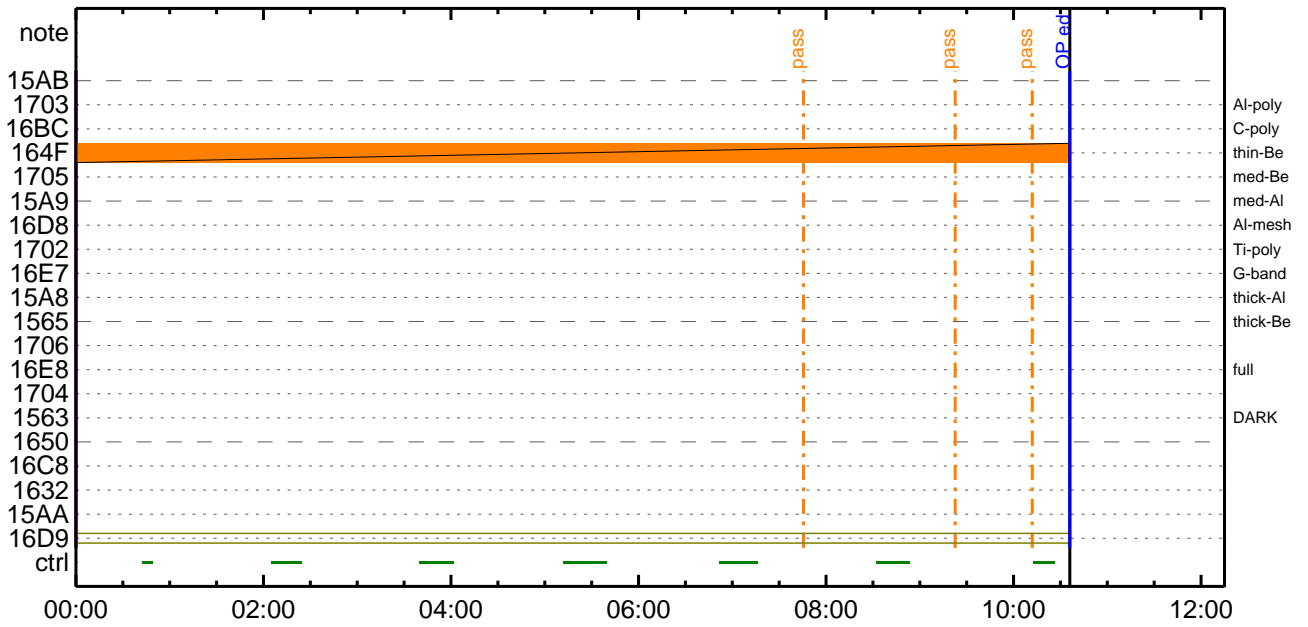
CMDI #0799 2009/09/21



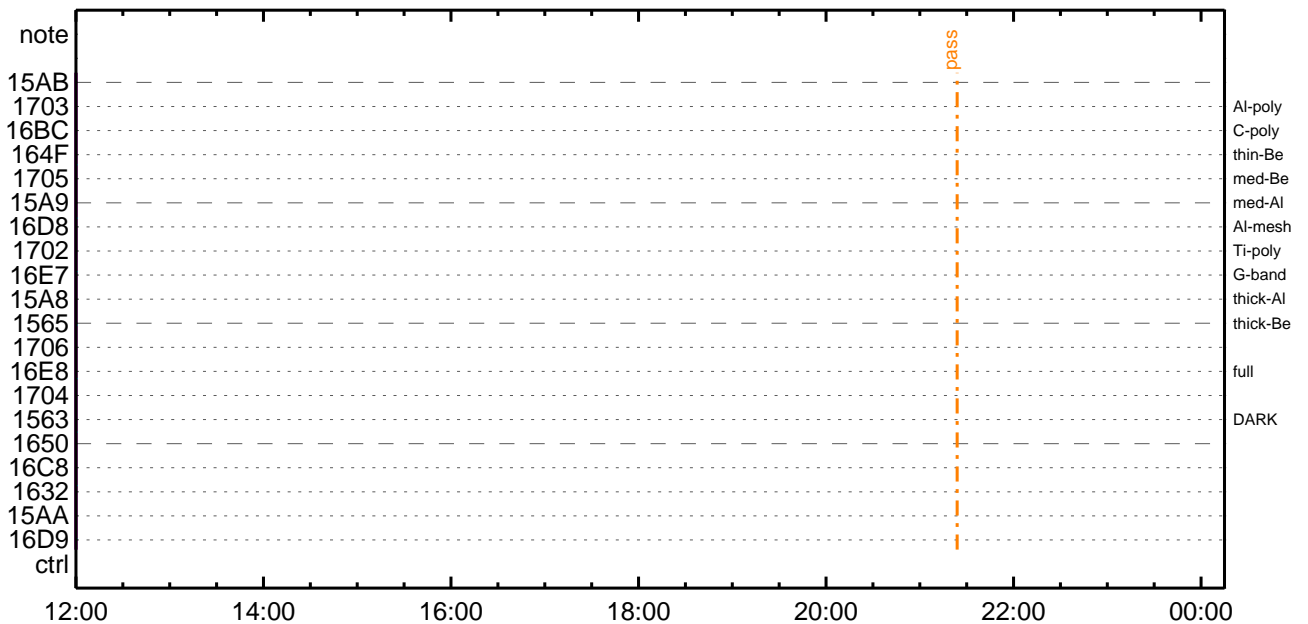
CMDI #0799 2009/09/21



CMDI #0799 2009/09/22



CMDI #0799 2009/09/22




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAY6Yx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-853:OP
0104 ( )
0105 S. OG og-853:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAY6Yx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAY6Yx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¼E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAY6Yx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¼E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAY6Yx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î¼E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî¼Ã´¶Á°òEÉ-ò°Á÷¿@ (¼âµ-YAY6Yx½ê¼çòðÁÔÃæç¼ª°"òè¼i¹çòçðâ) *****
0167 C. DHUYâ;4YE;E¼Y½;Yi;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î¼i¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î¼Á¹Ô²°²E²²²³²E;f
0180 C. ²²²¿;çSET²E²DUMP²îE±²iYNY¹²ç¹Ô²²²³²E;f
0181 C.
0182 C. TIY³Y²Y6YÉ²òðÁDî¿(UT)
0183 +. TI 2009-09-17 10:59:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2009-09-17 10:59:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2009-09-17 10:59:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-854 2009-09-17 12:17:42 261 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSÝÁÝ§ÝÁÝ~¼Á»Û;ã
0005 C.
0006 C. ÝÀÝß;¼Ý³ÝÞÝóÝÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ÈççðÀá•µ°È»Í×ÁÇçÁÝçÝÁÝ×Ýí;¼ÝÉ;ÈÈÈ¼µ•ííÈ;ÈáÈ¼°ÇÓá•çç¼í¹ççí;çÀ®, ùá¹áÈáßáçÁ+ç®á•áÈááááááÈ; f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ççµ;ON
0016 C. *****
0017 C. ç“ °ÈÀ, Í×ÈÝðáLOSáßáççí»p´ Öáá¹íí, á•; çÉÖÍ×áÈXÁÓONáí¹ÓáÈáíáÈáááááÈ; f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XÝÐÝóÝÉÝíÝÁÝ~¼óÁÖá-°ÁÁÈá•çç; ç°È²¼áí°ÈÀ, ¼È¼çááá¼Á¹Óá¹áÈ; f
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÈÀ,
0033 C. *****
0034 C. ç“ RESTART; ÈPT1; Èá•ççáá¼í¹ççí; ç°È²¼áí°ÈÀ¹Óáá°; çDCBC-150ááááÈáÈ; f
0035 C.
0036 . C. ;ãPT1°ÈÀ, ³«»í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãÝçÝóÝÉÝÈÁÛÁ; ÈÁ•Á°²óÈð; È, ááí°ÈÀ, °È³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÈÀ, á-¼«È°Áá»ßá•çç; á; ç°È²¼ááá¼Á¹Óá¹áÈ; f
0055 C. ÝçÝóÝÉÝÈÁÛÁ; ÈÁ•Á°²óÈðá-¼ááá¼í¹ççí´°í»á¹ááááçÁÓáá; f
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÈÀ,
0059 C. *****
0060 C. ç“ RESTART; ÈPT2; Èá•ççáá¼í¹ççí; ç°È²¼áí°ÈÀ¹Óáá°; çDCBC-151ááááÈáÈ; f
0061 C.
0062 . C. ;ãPT2°ÈÀ, ³«»í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãÝçÝóÝÉÝÈÁÛÁ; ÈÁ•Á°²óÈð; È, ááí°ÈÀ, °È³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÈÀ, Áá»ß; çXÁ+ççµ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÈÀ, Áá»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 C. ;ãXÁ+ççµ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```

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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 C.
0130 C. ***** XRT START *****
0131 C.
0132 +. DC 07-F0 MDP_XRT_CTRL_MANU
0133 BC (c1)
0134 + DC 07-F0 MDP_XRT_MODE_STBY
0135 BC (c3)
0136 . C. ----- Success Verify ? OK / NG____
0137 C.
0138 C. XRT Obs. Table Upload
0139 . S. RAM ram-291:MDP_OBS_X
0140 ( )
0141 C.
0142 +. DC 07-F0 MDP_DUMP_XRTTBL
0143 BC (84 07 00 00 00 3a d4)
0144 . C. ----- Comparison Check ? OK / ERR ____
0145 C.
0146 C.
0147 +. DC 07-F0 MDP_XRT_ROI_SET
0148 BC (cd 01 b1 b1 04 04)
0149 + DC 07-F0 MDP_XRT_ROI_SET
0150 BC (cd 02 b1 b1 08 08)
0151 + DC 07-F0 MDP_XRT_ROI_SET
0152 BC (cd 03 b1 b1 08 08)
0153 + DC 07-F0 MDP_XRT_ROI_SET
0154 BC (cd 04 b1 b1 06 06)
0155 + DC 07-F0 MDP_XRT_ROI_SET
0156 BC (cd 06 80 80 20 04)
0157 + DC 07-F0 MDP_XRT_ROI_SET
0158 BC (cd 07 c0 c0 10 10)
0159 + DC 07-F0 MDP_XRT_ROI_SET
0160 BC (cd 08 80 80 20 20)
0161 + DC 07-F0 MDP_XRT_ROI_SET
0162 BC (cd 09 40 c0 10 10)
0163 + DC 07-F0 MDP_XRT_ROI_SET
0164 BC (cd 0a 40 40 10 10)
0165 + DC 07-F0 MDP_XRT_ROI_SET
0166 BC (cd 0b c0 40 10 10)
0167 + DC 07-F0 MDP_XRT_ROI_SET
0168 BC (cd 0c 70 71 0c 0c)
0169 + DC 07-F0 MDP_XRT_ROI_SET
0170 BC (cd 0d 85 83 06 06)
0171 + DC 07-F0 MDP_XRT_ROI_SET
0172 BC (cd 0f 80 80 04 04)
0173 + DC 07-F0 MDP_XRT_ROI_SET
0174 BC (cd 10 80 80 10 10)
0175 . C. ----- Success Verify ? OK / NG ____
0176 C.
0177 C.
0178 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0179 C.
0180 +. DC 07-F0 MDP_XRT_MODE_OBSV
0181 BC (c2)
0182 +. TI 2009-09-17 11:03:02.0
0183 DC 07-F0 MDP_XRT_MODE_OBSV
0184 BC (c2)
0185 . C. ----- Success Verify ? OK / NG ____
0186 C.
0187 C. ***** XRT END *****
0188 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0189 +. DC 07-FC EIS_MODE_MANU
0190 BC (21 02)
0191 . C. Verify EIS in MANUAL mode
0192 . C. Estimated OBSTBL upload time is 13s
0193 C. *****

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0194 C. EIS START OBSTBL LOAD
0195 C. *****
0196 . S. RAM ram-820:EIS_OBSTBL
0197 ( )
0198 +. DC 07-FC EIS_DUMP_OBSTBL
0199 BC (07 07 07 00 00 70 00)
0200 C.
0201 C. Execute, after the success of OBSTBL upload.
0202 C. Set EIS TI-commands
0203 +. TI 2009-09-17 11:03:50.0
0204 DC 07-FC EIS_MODE_CHG_ENA
0205 BC (20)
0206 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0207 C. *****
0208 C. EIS END OBSTBL LOAD
0209 C. *****
0210 C. *****
0211 C. START of XRT_CCD_HEATER_ON operation
0212 C. *****
0213 C.
0214 +. DC 07-F0 MDP_XRT_CTRL_MANU
0215 BC (c1)
0216 C. ----- Success Verify ? OK / NG;
0217 C.
0218 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0219 BC (c4 06)
0220 + DC 07-F0 MDP_XRT_FLD_DIS
0221 BC (d9)
0222 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0223 BC (c9)
0224 + DC 07-F0 MDP_XRT_ARS_DIS
0225 BC (d5)
0226 C. ----- Success Verify ? OK / NG ____
0227 C.
0228 C.
0229 C. All OK? Yes--> Please Proceed. / No --> Stop here.
0230 C.
0231 +. DC 07-F0 MDP_XRT_CTRL_AUTO
0232 BC (c0)
0233 C. ----- Success Verify ? OK / NG;
0234 C.
0235 +. DC 04-BC TCIB_XRT_S_HTR_A_ENA
0236 C. ----- Success Verify ? OK / NG;
0237 C.
0238 C. -----
0239 C. If anomalous situation appeared, execute TCIB_XRT_S_HTR_A_DIS using DCBC-441 (line 24)
0240 C. -----
0241 C. *****
0242 C. END of XRT_CCD_HEATER_ON operation
0243 C. *****
0244 C.
0245 C.
0246 C.
0247 . C. ***** MDP 'uÃiãî»ö¼ÝãÉÃÐã¹æDCBC•x²è *****
0248 C. (%ã°iYÓYÁYÉYÞYÉYáYçYèãE¼ã¼Ã»Ûã¹æ)
0249 . S. DC-BC dcbc-402:DCBC
0250 (MDP_known_event)
0251 C.
0252 C.
0253 . C. ***** YÐY¹•ï Daily±;îÑãÉ'Øã¹æDCBC•x²è *****
0254 . S. DC-BC dcbc-153:DCBC
0255 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0256 C.
0257 C.
0258 . C. ;ãLOS¥Á¥S¥Ã¥-¼Ã»Û;ã
0259 C.
0260 . C. ***** LOS *****
0261 C.

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*** OP Sequence for XRT ***

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2009/09/17 11:13:54.0 XRT_CTRL_MANU_428_OG [0x1ac]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2009/09/17 11:13:56.0 XRT_QT_PROG_SET_431_OG [0x1af]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 06
2009/09/17 11:14:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 02 00 00 00 00
2009/09/17 11:16:54.0 XRT_ARS_DIS_422_OG [0x1a6]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2009/09/17 11:16:56.0 XRT_FLD_DIS_445_OG [0x1bd]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2009/09/17 11:16:58.0 XRT_FLRCTRL_DIS_416_OG [0x1a0]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2009/09/17 11:17:00.0 XRT_CTRL_AUTO_403_OG [0x193]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2009/09/17 15:46:00.0 XRT_CTRL_MANU_435_OG [0x1b3]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2009/09/17 16:09:30.0 XRT_Custom_430_OG [0x1ae]
2009/09/17 16:10:30.0 XRT_CTRL_AUTO_432_OG [0x1b0]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2009/09/17 17:22:30.0 XRT_CTRL_MANU_435_OG [0x1b3]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2009/09/17 17:46:00.5 XRT_Custom_430_OG [0x1ae]
2009/09/17 17:47:00.5 XRT_CTRL_AUTO_432_OG [0x1b0]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2009/09/17 17:56:54.0 XRT_CTRL_MANU_428_OG [0x1ac]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2009/09/17 17:56:56.0 XRT_QT_PROG_SET_406_OG [0x196]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0a
2009/09/17 17:59:54.0 XRT_ARS_DIS_422_OG [0x1a6]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2009/09/17 17:59:56.0 XRT_FLD_DIS_445_OG [0x1bd]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2009/09/17 17:59:58.0 XRT_FLRCTRL_DIS_416_OG [0x1a0]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2009/09/17 18:00:00.0 XRT_CTRL_AUTO_403_OG [0x193]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2009/09/17 18:59:30.0 XRT_CTRL_MANU_435_OG [0x1b3]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2009/09/17 19:23:00.5 XRT_Custom_430_OG [0x1ae]
2009/09/17 19:24:00.5 XRT_CTRL_AUTO_432_OG [0x1b0]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2009/09/17 20:36:30.0 XRT_CTRL_MANU_435_OG [0x1b3]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2009/09/17 21:00:00.5 XRT_Custom_430_OG [0x1ae]
2009/09/17 21:01:00.5 XRT_CTRL_AUTO_432_OG [0x1b0]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2009/09/17 21:49:40.0 XRT_CTRL_MANU_435_OG [0x1b3]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2009/09/17 21:49:42.0 XRT_TCIB_XRT_S_HTR_A_DIS_427_OG [0x1ab]
                        TCIB_XRT_S_HTR_A_DIS 0 04-C0
2009/09/17 21:52:40.0 XRT_CTRL_MANU_428_OG [0x1ac]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2009/09/17 21:52:42.0 XRT_QT_PROG_SET_431_OG [0x1af]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 06
2009/09/17 21:52:44.0 XRT_ARS_DIS_422_OG [0x1a6]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2009/09/17 21:52:46.0 XRT_FLD_DIS_445_OG [0x1bd]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2009/09/17 21:52:48.0 XRT_FLRCTRL_DIS_416_OG [0x1a0]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2009/09/17 21:52:50.0 XRT_CTRL_AUTO_403_OG [0x193]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2009/09/17 22:14:00.5 XRT_CTRL_MANU_435_OG [0x1b3]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2009/09/17 22:36:00.5 XRT_Custom_430_OG [0x1ae]
2009/09/17 22:37:00.5 XRT_CTRL_AUTO_432_OG [0x1b0]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2009/09/17 23:51:30.0 XRT_CTRL_MANU_435_OG [0x1b3]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2009/09/18 00:03:00.0 XRT_Custom_430_OG [0x1ae]
2009/09/18 00:04:00.0 XRT_CTRL_AUTO_432_OG [0x1b0]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2009/09/18 01:18:30.0 XRT_CTRL_MANU_435_OG [0x1b3]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2009/09/18 01:34:30.0 XRT_Custom_430_OG [0x1ae]
2009/09/18 01:35:30.0 XRT_CTRL_AUTO_432_OG [0x1b0]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2009/09/18 02:50:30.0 XRT_CTRL_MANU_435_OG [0x1b3]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2009/09/18 03:11:30.0 XRT_Custom_430_OG [0x1ae]
2009/09/18 03:12:30.0 XRT_CTRL_AUTO_432_OG [0x1b0]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2009/09/18 03:29:54.0 XRT_CTRL_MANU_418_OG [0x1a2]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2009/09/18 03:30:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 2e f9 2e f9
2009/09/18 03:32:32.0 XRT_FOCUS_POSITION_442_OG [0x1ba]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2009/09/18 03:32:52.0 XRT_QT_PROG_SET_421_OG [0x1a5]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0b
2009/09/18 03:32:54.0 XRT_FLD_DIS_419_OG [0x1a3]

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2009/09/18	03:32:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLD_DIS	1	07-F0	d9			
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/09/18	03:32:58.0	XRT_ARS_DIS_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2009/09/18	03:33:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/09/18	03:34:54.0	XRT_CTRL_MANU_436_OG [0x1b4]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/09/18	03:37:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00		
2009/09/18	03:37:52.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03		
2009/09/18	03:37:54.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/09/18	03:37:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/09/18	03:37:58.0	XRT_ARS_DIS_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2009/09/18	03:38:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/09/18	03:39:54.0	XRT_CTRL_MANU_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/09/18	03:40:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 2e f9	d1 07		
2009/09/18	03:42:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00		
2009/09/18	03:42:52.0	XRT_QT_PROG_SET_411_OG [0x19b]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f		
2009/09/18	03:42:54.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/09/18	03:42:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/09/18	03:42:58.0	XRT_ARS_DIS_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2009/09/18	03:43:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/09/18	03:44:54.0	XRT_CTRL_MANU_436_OG [0x1b4]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/09/18	03:47:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00		
2009/09/18	03:47:52.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03		
2009/09/18	03:47:54.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/09/18	03:47:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/09/18	03:47:58.0	XRT_ARS_DIS_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2009/09/18	03:48:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/09/18	03:49:54.0	XRT_CTRL_MANU_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/09/18	03:50:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 d1 07	d1 07		
2009/09/18	03:52:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00		
2009/09/18	03:52:52.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02		
2009/09/18	03:52:54.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/09/18	03:52:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/09/18	03:52:58.0	XRT_ARS_DIS_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2009/09/18	03:53:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/09/18	03:54:54.0	XRT_CTRL_MANU_436_OG [0x1b4]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/09/18	03:57:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00		
2009/09/18	03:57:52.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03		
2009/09/18	03:57:54.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/09/18	03:57:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/09/18	03:57:58.0	XRT_ARS_DIS_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2009/09/18	03:58:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/09/18	03:59:54.0	XRT_CTRL_MANU_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/09/18	04:00:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 d1 07	2e f9		
2009/09/18	04:02:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00		
2009/09/18	04:02:52.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14		
2009/09/18	04:02:54.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/09/18	04:02:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			

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2009/09/18	04:02:58.0	XRT_ARS_DIS_446_OG [0x1be]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/09/18	04:03:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/09/18	04:04:54.0	XRT_CTRL_MANU_436_OG [0x1b4]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/09/18	04:07:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/09/18	04:07:52.0	XRT_QT_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2009/09/18	04:07:54.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/09/18	04:07:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/09/18	04:07:58.0	XRT_ARS_DIS_446_OG [0x1be]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/09/18	04:08:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/09/18	04:09:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/09/18	04:09:56.0	XRT_FOCUS_POSITION_402_OG [0x192]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2009/09/18	04:10:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 3e 35 35 58				
2009/09/18	04:10:16.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2009/09/18	04:12:52.0	XRT_AEC_RESET_415_OG [0x19f]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2009/09/18	04:12:54.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/09/18	04:12:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/09/18	04:12:58.0	XRT_ARS_DIS_446_OG [0x1be]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/09/18	04:13:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/09/18	04:19:30.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/09/18	04:48:30.0	XRT_Custom_430_OG [0x1ae]							
2009/09/18	04:49:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/09/18	05:32:24.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/09/18	05:32:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/09/18	05:32:30.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2009/09/18	05:32:46.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/09/18	05:32:48.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/09/18	05:32:50.5	XRT_ARS_DIS_410_OG [0x19a]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/09/18	05:35:28.5	XRT_QT_PROG_SET_423_OG [0x1a7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2009/09/18	05:35:30.5	XRT_CTRL_AUTO_403_OG [0x193]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/09/18	05:42:24.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/09/18	05:42:26.0	XRT_FOCUS_POSITION_402_OG [0x192]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2009/09/18	05:42:30.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 3e 35 35 58				
2009/09/18	05:42:46.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2009/09/18	05:45:22.0	XRT_AEC_RESET_415_OG [0x19f]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2009/09/18	05:45:24.0	XRT_FLD_DIS_419_OG [0x1a3]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/09/18	05:45:26.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/09/18	05:45:28.0	XRT_ARS_DIS_446_OG [0x1be]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/09/18	05:45:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/09/18	06:00:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/09/18	06:26:00.0	XRT_Custom_430_OG [0x1ae]							
2009/09/18	06:27:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/09/18	07:40:00.0	XRT_CTRL_MANU_435_OG [0x1b3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/09/18	07:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/09/18	08:00:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2009/09/18	10:29:54.0	XRT_CTRL_MANU_428_OG [0x1ac]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/09/18	10:29:56.0	XRT_FOCUS_POSITION_402_OG [0x192]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2009/09/18	10:30:00.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00 af 1b 00 00				
2009/09/18	10:30:16.0	XRT_QT_PROG_SET_413_OG [0x19d]							

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2009/09/18	10:32:52.0	XRT_AEC_RESET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04
2009/09/18	10:32:54.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2009/09/18	10:32:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/09/18	10:32:58.0	XRT_ARS_DIS_446_OG [0x1be]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/09/18	10:33:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/09/18	11:03:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/09/18	11:04:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/18	11:05:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/09/18	14:48:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/18	14:58:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/18	14:59:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/09/18	16:22:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/09/18	16:46:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/18	16:47:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/09/18	17:59:00.5	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/18	18:22:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/18	18:22:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2009/09/18	18:23:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 00 00 00 00	
2009/09/18	18:23:16.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/09/18	18:23:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/09/18	18:23:20.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/09/18	18:25:58.0	XRT_QT_PROG_SET_424_OG [0x1a8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05	
2009/09/18	18:26:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/09/18	18:32:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/18	18:32:56.0	XRT_FOCUS_POSITION_402_OG [0x192]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2009/09/18	18:33:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 3e 35 35 58	
2009/09/18	18:33:16.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2009/09/18	18:35:52.0	XRT_AEC_RESET_415_OG [0x19f]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2009/09/18	18:35:54.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/09/18	18:35:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/09/18	18:35:58.0	XRT_ARS_DIS_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/09/18	18:36:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/09/18	19:36:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/18	19:59:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/18	20:00:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/09/18	21:13:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/18	21:37:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/18	21:38:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/09/18	22:51:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/18	23:12:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/18	23:13:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/09/19	00:28:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/19	00:36:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/19	00:37:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/09/19	01:52:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/19	02:11:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/19	02:12:00.5	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/09/19	03:27:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/19	05:54:24.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/19	05:54:26.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	

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2009/09/19	05:54:30.0	AOCS_Orpoint_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00	00	00	00	00
2009/09/19	05:54:46.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/09/19	05:54:48.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/09/19	05:54:50.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/09/19	05:57:28.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2009/09/19	05:57:30.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/09/19	06:04:30.0	AOCS_Orpoint_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00	3e	35	35	58
2009/09/19	10:33:00.0	AOCS_Orpoint_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00	00	00	00	00