

# XRT Timeline to be uploaded on 2009/09/22

Period: 2009/09/22 10:36:00 - 2009/09/26 10:07:00

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Normal mode

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XOB #170A: CME watch - C/poly - 2x2 long and short AEC2/3 - Be/thin - 2x2 - AEC2														
Term	Pointing (x, y)							Comment						
09/22 10:53:00 - 09/22 17:52:54	Track ( -744.2, -547.9) <sup>© 09/22 10:46:00</sup>							# OP start + 10min   AR near SE limb						
09/22 18:06:00 - 09/22 20:26:30	Track ( -721.7, -554.0) <sup>© 09/22 18:03:00</sup>							# AR near the SE limb						
09/23 11:33:06 - 09/23 17:49:30	Track ( -675.6, -564.0) <sup>© 09/23 06:36:00</sup>							# HOP 132 : AR nearthe SE limb						
<b>PROG= 11 Inf.-time(s)</b>														
└─ Subr= 1 20-time(s) 300.0sec														
└─ Seqn= 96 1-time(s) 2.0sec														
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	1024x1024	(1024, 1024)	DPCM	2	0	2.0sec
└─ Seqn= 82 1-time(s) 4.0sec														
	C-poly/Open	C-poly/Open	close	Safe	Norm	2.00s	Obs	2x2	1024x1024	(1024, 1024)	DPCM	2	0	2.0sec
	C-poly/Open	C-poly/Open	close	Safe	Norm	177ms	Obs	2x2	768x768	(1024, 1024)	DPCM	3	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec														
└─ Seqn= 13 1-time(s) 4.0sec														
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	1024x1024	(1024, 1024)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #16E5: Synoptic Q95 2x2 - Al/poly(128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 2048x512 -1x1 512x2048) + Ti-poly(181/2048) + G-band(16)														
Term	Pointing (x, y)							Comment						
09/22 17:56:00 - 09/22 18:02:54	Fixed ( 0.0, 0.0)							synoptic, shifted -7.0 min						
09/23 18:16:00 - 09/24 05:59:54	Fixed ( 0.0, 0.0)							synoptic, shifted 13.0 min						
<b>PROG= 16 1-time(s)</b>														
└─ Subr= 1 1-time(s) 12.0sec														
└─ Seqn= 63 1-time(s) 4.0sec														
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 85 1-time(s) 2.0sec														
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 37 1-time(s) 4.0sec														
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	177ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 92 1-time(s) 2.0sec														
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #16E6: Synoptic Q95 2x2 - Al/mesh(88/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 2048x512 -1x1 512x2048) + Ti-poly(181/2048) + G-band(16)														
Term	Pointing (x, y)							Comment						
09/23 06:29:00 - 09/23 08:00:00	Fixed ( 0.0, 0.0)							synoptic, shifted 26.0 min						
09/24 06:03:00 - 09/24 08:31:30	Fixed ( 0.0, 0.0)							synoptic						
<b>PROG= 07 1-time(s)</b>														
└─ Subr= 1 1-time(s) 12.0sec														
└─ Seqn= 42 1-time(s) 4.0sec														
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	86ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 85 1-time(s) 2.0sec														
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 37 1-time(s) 4.0sec														
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	177ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 92 1-time(s) 2.0sec														
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1709: AR HOP 132 (5 pairs-AEC1 Med-Be 32s-AEC0 +G-band) - 1x1-384x384 -5 min cadence														
Term	Pointing (x, y)							Comment						
09/23 08:03:06 - 09/23 11:30:00	Track ( -675.6, -564.0) <sup>© 09/23 06:36:00</sup>							# HOP 132 : AR nearthe SE limb						
09/24 08:34:36 - 09/24 09:32:30	Track ( -565.4, -580.7) <sup>© 09/24 06:10:00</sup>							# HOP 132 : AR near the SE limb (cont'd)						
<b>PROG= 17 Inf.-time(s)</b>														
└─ Subr= 1 10-time(s) 300.0sec														
└─ Seqn= 17 1-time(s) 2.0sec														
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	1x1	384x384	(1064, 1048)	DPCM	1	0	2.0sec
	C-poly/Open	C-poly/Open	close	Safe	Norm	177ms	Obs	1x1	384x384	(1064, 1048)	DPCM	1	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	DPCM	1	0	2.0sec

Subr= 2	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	DPCM	1	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	DPCM	1	0	2.0sec
	med-Be/Open	Open/thick-Be	close	Safe	Norm	32.0s	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Seqn= 72	1-time(s)		2.0sec										
	1-time(s)		4.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Flare mode**

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NOT USED

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**Active Region Search**

\* \* \* \* \*

NOT USED

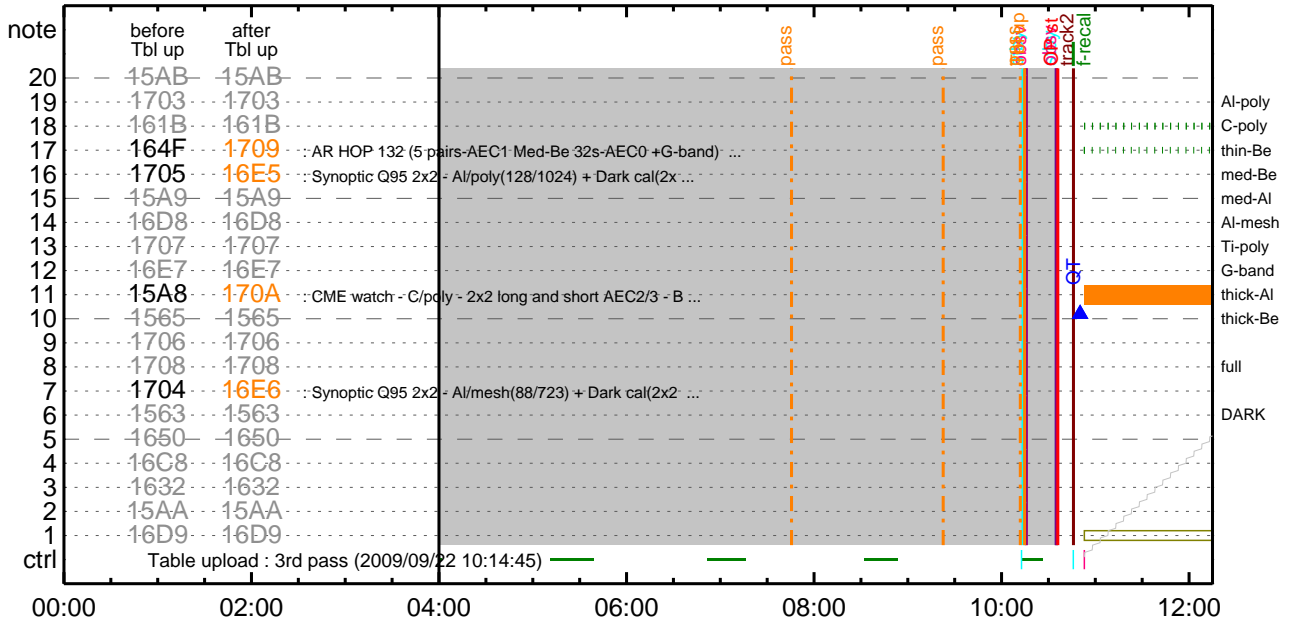
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**Flare Detection**

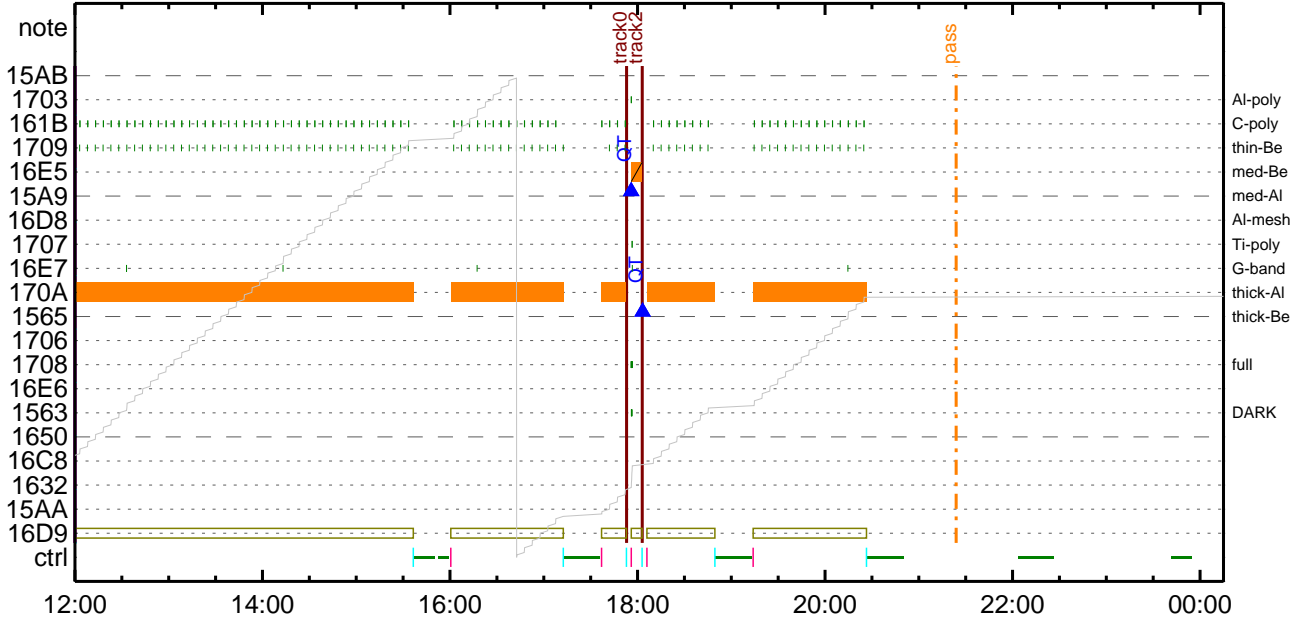
\* \* \* \* \*

NOT USED

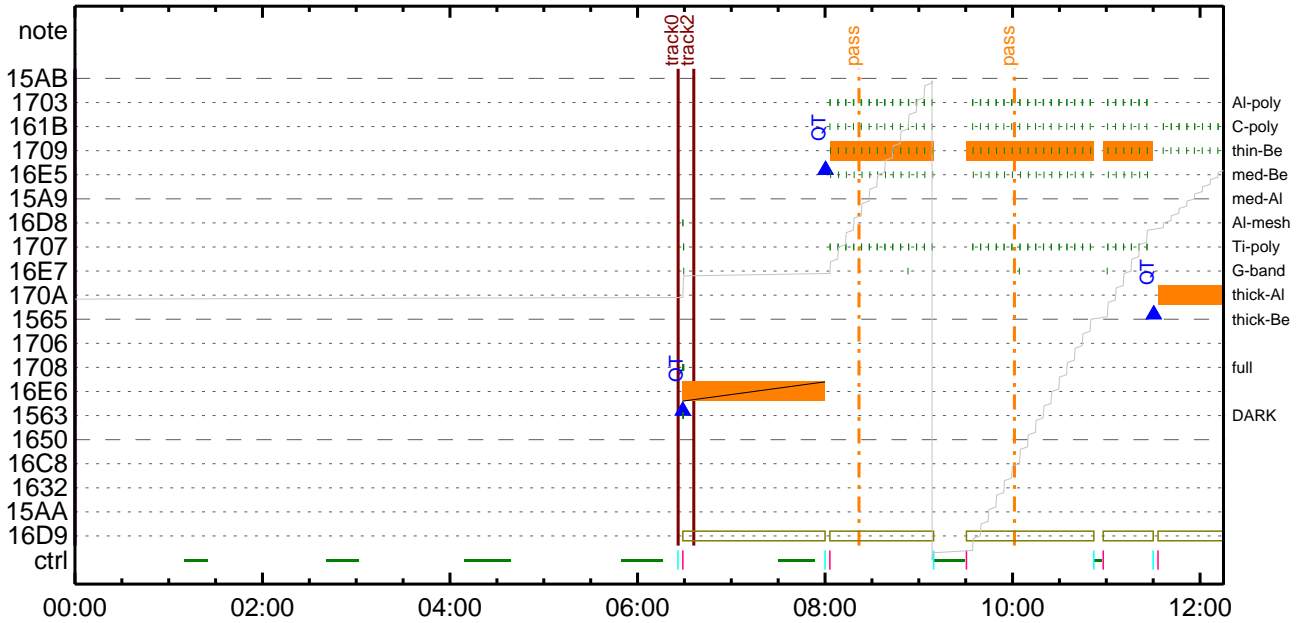
### CMDI #0812 2009/09/22



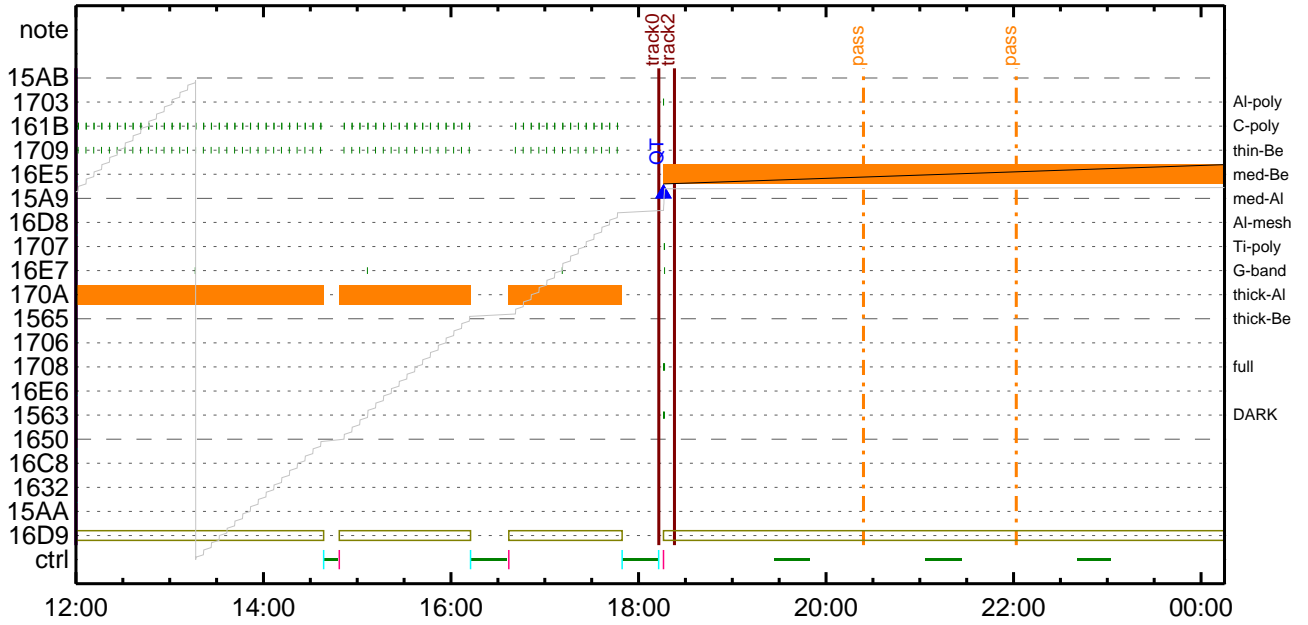
### CMDI #0812 2009/09/22



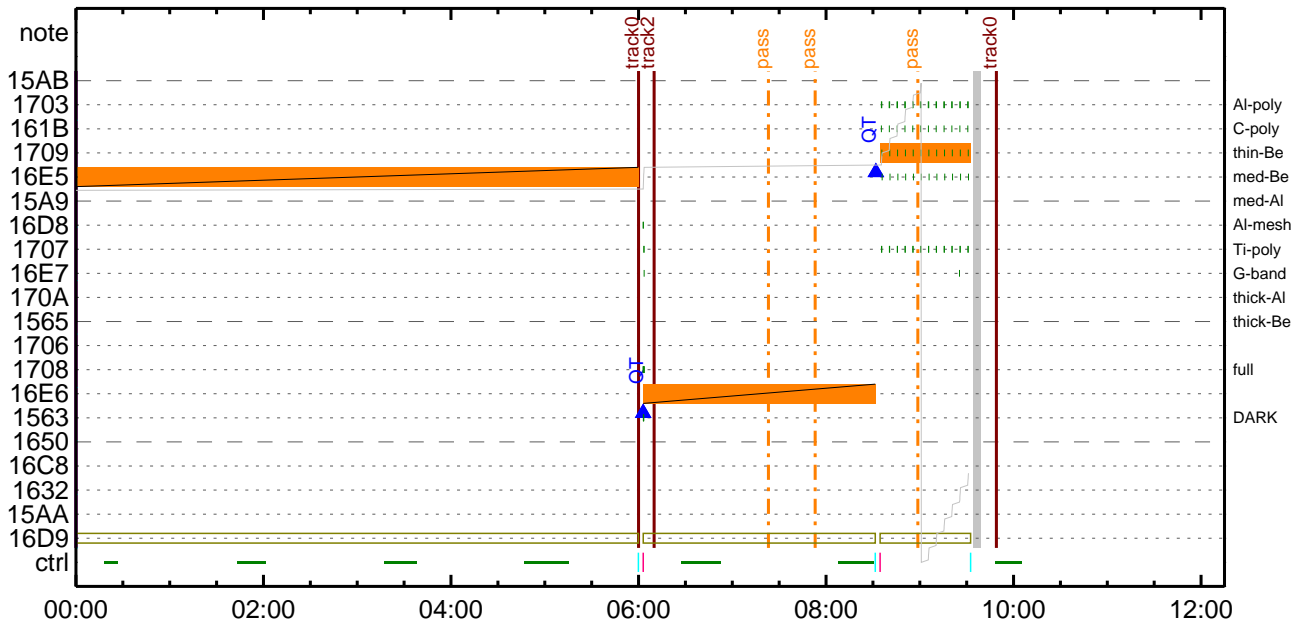
### CMDI #0812 2009/09/23



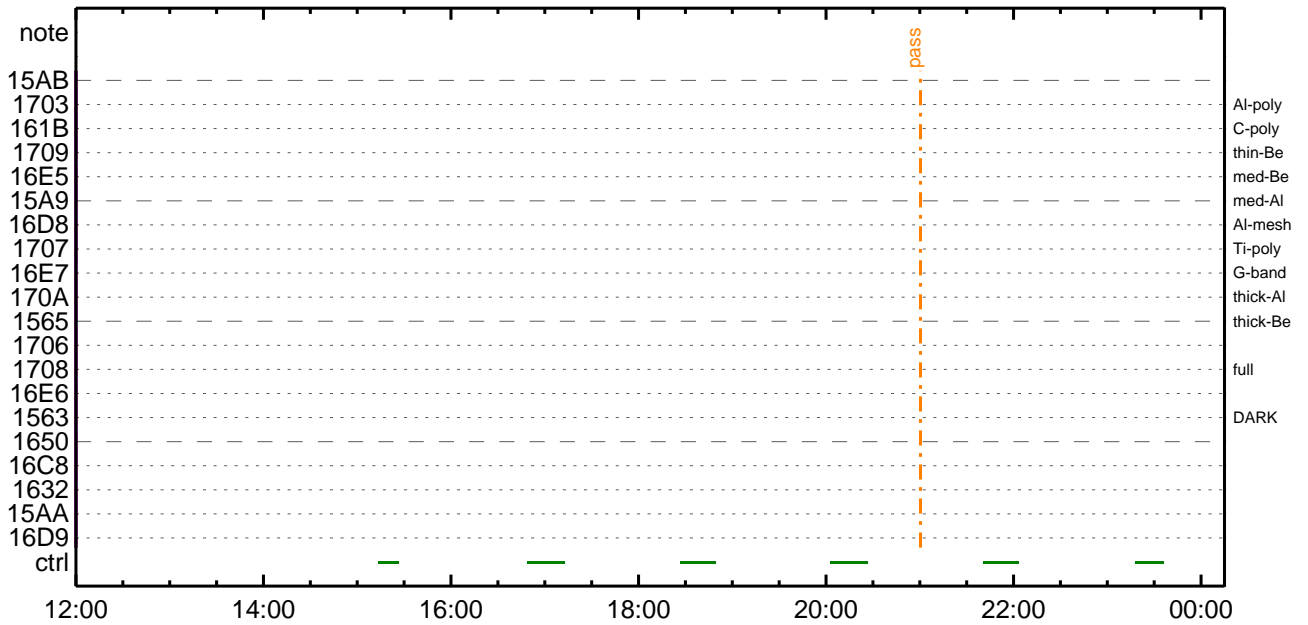
CMDI #0812 2009/09/23



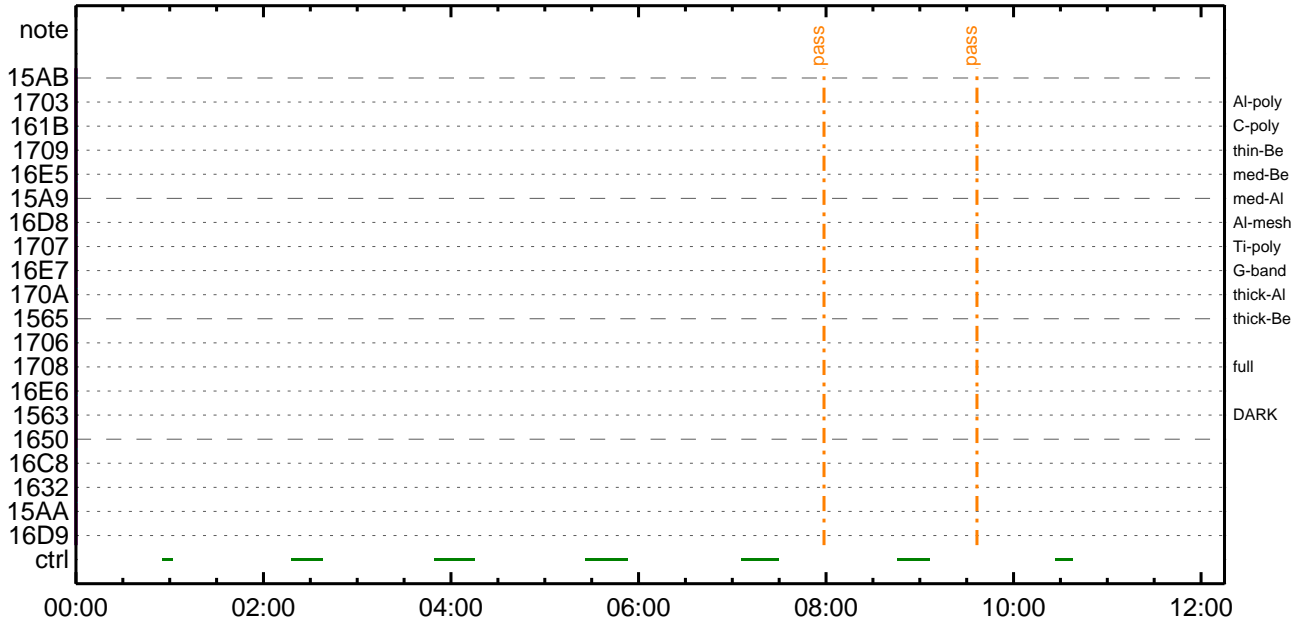
CMDI #0812 2009/09/24



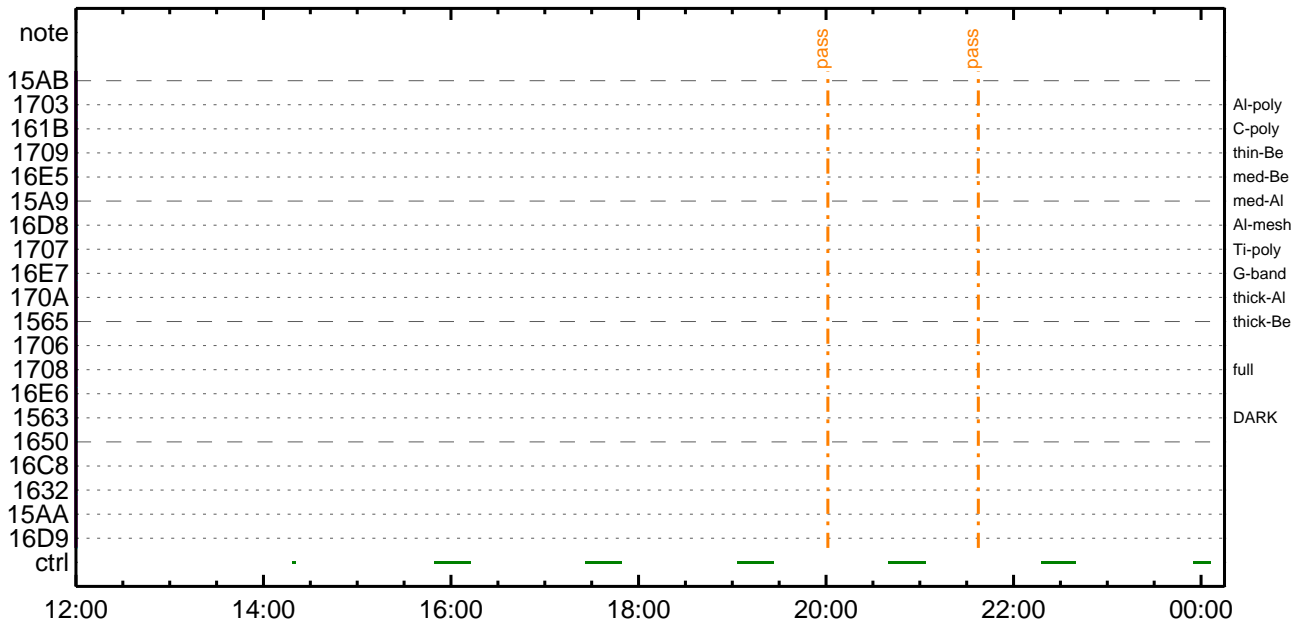
CMDI #0812 2009/09/24



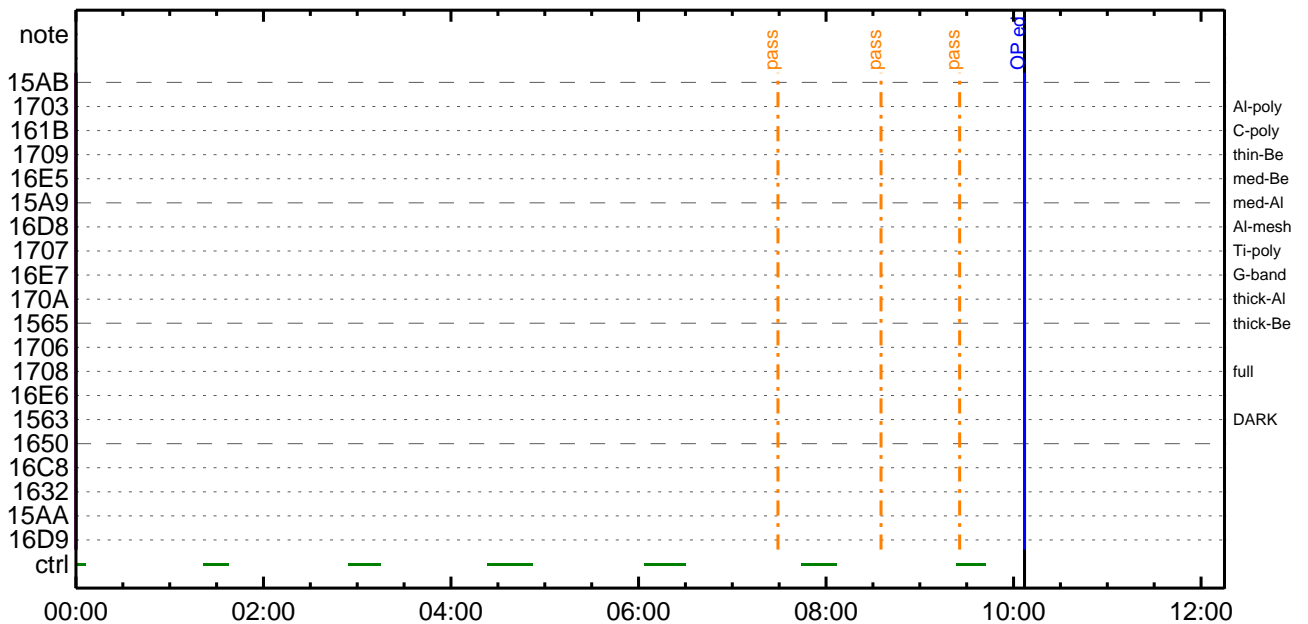
CMDI #0812 2009/09/25



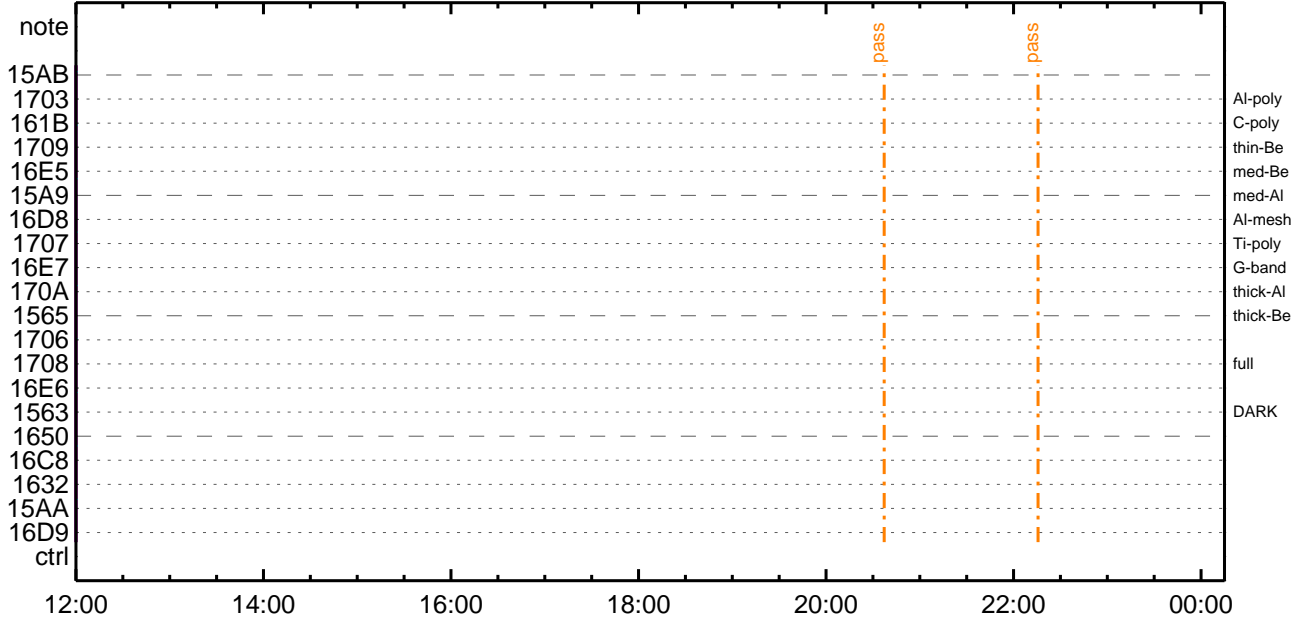
CMDI #0812 2009/09/25



CMDI #0812 2009/09/26



CMDI #0812 2009/09/26





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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ä
0100 C. *****
0101 C.
0102 . C. ;äOP/OGY1;4YE;ä
0103 . S. OP op-862:OP
0104 ( )
0105 . S. OG og-862:OG
0106 ( )
0107 C.
0108 . C. ;änMOG&OPîî°è¥ÅYóYx;ä
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 . C. ¥ÅYóYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 . C. RAM ID=NMOG²î½Ê¹ç·è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 . C. ¥ÅYóYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 . C. RAM ID=NMOG²î½Ê¹ç·è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 . C. ¥ÅYóYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 . C. RAM ID=NMOG, RAM ID=OP²î½Ê¹ç·è²îOKò³îÇ§
0165 C.
0166 . C. ***** °Ê²¼òî½Ä´¶Á°òÊÊ¬ò°Ä÷¿@ (¼âµ-¥ÅYóYx½ªê½çòðÁÔæòÇ¼ª°²òè¼î¹çòçòâ) *****
0167 C. DHUYâ;½YÊ;Ê½Y½, ¥î;½YÊ;Êòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE ;§ OPOG UPLOAD²¬Ä÷¿@NG²î½î¹ç;ç°Ê²¼òîTI-CMDÄ÷¿@²î½î¹ò²·²è²²²³²è;f
0180 C. ²²²¿;çSET²èDUMPA²î±²²î½Y¹²ç¹ò²|²³²è;f
0181 C.
0182 . C. TIY³Y²YóYÊòðîÄî¿¿(UT)
0183 +. TI 2009-09-22 10:31:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2009-09-22 10:31:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2009-09-22 10:31:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2009-09-22 10:35:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0198 C.
0199 C. °È²¼œîÄë%îíñœîî¥Á¥§¥Á¥-¹àîÛ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]                    EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]                        EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]                     EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]                       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TÍîî°è¥Á¥Ö¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]                    EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]                    EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]                      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]                     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]                       EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]                       EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]                       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]                     EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]                     EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]                       EQ      EXEC
0224 C.
0225 C. ¥Á¥Ö¥×½ªî»œ³ÍÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]                       EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLœîî¾È¹ç•è²ïOKœ³ÍÇ§
0229 C.
0230 C. DHU¥â;¼¥È;È¼¥½. ¥ì;¼¥È;Èœ³Íäœ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]                       EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]                       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]                     EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]                     EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2009-09-22 10:35:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C.      HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2009-09-22 10:35:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2009-09-22 10:35:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]                      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2009-09-22 10:35:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîœî»ö¼ÝœÈÁœ¹œèDCBC•x²è *****
0276 C. (¼á°î¥Ö¥Á¥È¥¥¥È¥á¥ç¥èœÈ¼œœ¼Á»Ûœ¹œè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ¥Ð¥¹•İ Daily±çİñœÈ¹œœ¹œèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOS¥Á¥§¥Á¥-¼Á»Û;ä
0287 C.
0288 C. ***** LOS *****
0289 C.

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(a) Spacecraft Operation Procedure (real-commands)

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main-864 2009-09-22 13:14:25 160 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È□¿□□□•µ°È»Í×ÁÇ□íYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;È□È¼°Ç□□•□¿¼í¹ç□í;çÀ®, ù□¹□è□□□çÁ+¿®□•□È□□□□è;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 06 80 80 10 10)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 07 80 80 0c 0c)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 08 80 80 20 20)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 09 80 80 20 08)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 0a 80 80 08 20)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0b 85 83 06 06)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0c 80 80 08 08)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0f 80 80 04 04)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 10 80 80 10 10)
0058 . C. ----- Success Verify ? OK / NG ____
0059 C.
0060 C.
0061 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0062 C.
0063 +. DC 07-F0 MDP_XRT_MODE_OBSV
0064 BC (c2)
0065 +. TI 2009-09-22 10:35:02.0
0066 DC 07-F0 MDP_XRT_MODE_OBSV
0067 BC (c2)
0068 . C. ----- Success Verify ? OK / NG ____
0069 C.
0070 C. ***** XRT END *****
0071 . C. *****
0072 C. SOT table upload
0073 C. *****
0074 . C. < Stop FG table >
0075 +. DC 07-F0 MDP_FG_CTRL_MANU
0076 BC (51)
0077 . C. -----
0078 C. MDP_FG_CTRL_MODE = MANU [ ]
0079 C. -----
0080 C.
0081 . C. <Upload FG Observation Table>
0082 . S. RAM ram-268:MDP_OBS_F
0083 ( )
0084 C.
0085 . C. < Dump RAMID=MDP_OBS_F >
0086 +. DC 07-F0 MDP_DUMP_FGTBL
0087 BC (82 07 00 00 00 38 b8)
0088 C. -----
0089 C. MDP_OBS_F verify = OK/NG [ ]
0090 C. -----
0091 C.
0092 . C. < Stop SP table >
0093 +. DC 07-F0 MDP_SP_CTRL_MANU
0094 BC (61)
0095 C. -----
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0096 C. MDP_SP_CTRL_MODE = MANU [ ]
0097 C. -----
0098 C.
0099 . C. <Upload SP Observation Table>
0100 . S. RAM ram-282:MDP_OBS_S
0101 ( )
0102 C.
0103 . C. < Dump RAMID=MDP_OBS_S >
0104 +. DC 07-F0 MDP_DUMP_SPTBL
0105 BC (83 07 00 00 00 38 b8)
0106 C. -----
0107 C. MDP_OBS_S verify = OK/NG [ ]
0108 C. -----
0109 C.
0110 . C. < Upload DPL table >
0111 C.
0112 C. ¥¢¥Ã¥×¥í;¥¥É¤îÂ°¤ÈSTS_CHK¤ðOFF¤È¤¹¤ë
0113 C.
0114 . S. RAM ram-271:MDP_DPL
0115 ( )
0116 C.
0117 . C. < Dump RAMID=MDP_DPL >
0118 +. DC 07-F0 MDP_DUMP_FGTBL
0119 BC (82 07 00 38 b8 00 40)
0120 C. -----
0121 C. MDP_DPL verify = OK [ ]
0122 C. -----
0123 C.
0124 C. STS_CHK¤ðON¤È¤¹¤ë
0125 C.
0126 . C. < Update MDP DSC PAR1 >
0127 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0128 BC (4c)
0129 C. MDP_CMD_CODE = F04C0700[ ]
0130 C. MDP_CMD_CNT (count-up 1) [ ]
0131 C. -----
0132 C.
0133 . C.
0134 C. *****
0135 C. SOT TI command set
0136 C. *****
0137 C. Execute, after the success of TBL upload.
0138 +. TI 2009-09-22 10:35:18.0
0139 DC 07-F0 MDP_SOT_MODE_OBSV
0140 BC (40)
0141 C. -----
0142 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0143 C. -----
0144 C.
0145 C.
0146 . C. ***** MDP `úÃî¤î»ð¼¥¤È¤¤¹¤ëDCBC•x²è *****
0147 C. (%ã°î¥Ö¥Ã¥È¥¤¥¥¥¤¥¢¥¤¤È¤¼¤¤¼¤¤»Û¤¹¤ë)
0148 . S. DC-BC dcbc-402:DCBC
0149 (MDP_known_event)
0150 C.
0151 C.
0152 . C. ***** ¥¤¥¹•î Daily±;î¤¤È´ð¤¹¤ëDCBC•x²è *****
0153 . S. DC-BC dcbc-153:DCBC
0154 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0155 C.
0156 C.
0157 . C. ;ãLOS¥Ã¥¤¥¤¥¤¥¹¼¤¤»Û;ã
0158 C.
0159 . C. ***** LOS *****
0160 C.

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\*\*\* OP Sequence for XRT \*\*\*

2009/09/22	10:45:54.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/09/22	10:45:56.0	XRT_FOCUS_RECALIBRATE_437_OG [0x1b5]			
		XRT_FOCUS_RECAL	2	07-F8	78 00
2009/09/22	10:46:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 00 00 00 00
2009/09/22	10:49:56.0	XRT_FOCUS_POSITION_402_OG [0x192]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/09/22	10:50:16.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2009/09/22	10:52:52.0	XRT_AEC_RESET_415_OG [0x19f]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2009/09/22	10:52:54.0	XRT_FLD_DIS_419_OG [0x1a3]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/09/22	10:52:56.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/09/22	10:52:58.0	XRT_ARS_DIS_446_OG [0x1be]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/09/22	10:53:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/09/22	15:36:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/09/22	15:59:30.0	XRT_Custom_430_OG [0x1ae]			
2009/09/22	16:00:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/09/22	17:12:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/09/22	17:36:00.0	XRT_Custom_430_OG [0x1ae]			
2009/09/22	17:37:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/09/22	17:52:54.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/09/22	17:52:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2009/09/22	17:53:00.0	AOCS_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2009/09/22	17:53:16.0	XRT_FLD_DIS_419_OG [0x1a3]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/09/22	17:53:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/09/22	17:53:20.0	XRT_ARS_DIS_410_OG [0x19a]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/09/22	17:55:58.0	XRT_QT_PROG_SET_404_OG [0x194]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2009/09/22	17:56:00.0	XRT_CTRL_AUTO_403_OG [0x193]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/09/22	18:02:54.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/09/22	18:02:56.0	XRT_FOCUS_POSITION_402_OG [0x192]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/09/22	18:03:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 00 00 00 00
2009/09/22	18:03:16.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2009/09/22	18:05:52.0	XRT_FLD_DIS_419_OG [0x1a3]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/09/22	18:05:54.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/09/22	18:05:56.0	XRT_ARS_DIS_446_OG [0x1be]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/09/22	18:06:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/09/22	18:49:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/09/22	19:13:00.0	XRT_Custom_430_OG [0x1ae]			
2009/09/22	19:14:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/09/22	20:26:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/09/23	06:25:54.0	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/09/23	06:25:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2009/09/23	06:26:00.0	AOCS_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2009/09/23	06:26:16.0	XRT_FLD_DIS_419_OG [0x1a3]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/09/23	06:26:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/09/23	06:26:20.0	XRT_ARS_DIS_410_OG [0x19a]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/09/23	06:28:58.0	XRT_QT_PROG_SET_408_OG [0x198]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2009/09/23	06:29:00.0	XRT_CTRL_AUTO_403_OG [0x193]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/09/23	06:36:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 00 00 00 00
2009/09/23	08:00:00.5	XRT_CTRL_MANU_428_OG [0x1ac]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/09/23	08:00:02.5	XRT_FOCUS_POSITION_402_OG [0x192]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00

2009/09/23	08:00:22.5	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2009/09/23	08:02:58.5	XRT_AEC_RESET_415_OG [0x19f]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2009/09/23	08:03:00.5	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/09/23	08:03:02.5	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/09/23	08:03:04.5	XRT_ARS_DIS_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/09/23	08:03:06.5	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/09/23	09:09:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/23	09:29:30.0	XRT_Custom_430_OG [0x1ae]					
2009/09/23	09:30:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/09/23	10:52:00.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/23	10:57:00.0	XRT_Custom_430_OG [0x1ae]					
2009/09/23	10:58:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/09/23	11:30:00.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/23	11:30:02.0	XRT_FOCUS_POSITION_402_OG [0x192]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2009/09/23	11:30:22.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
2009/09/23	11:32:58.0	XRT_AEC_RESET_415_OG [0x19f]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2009/09/23	11:33:00.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/09/23	11:33:02.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/09/23	11:33:04.0	XRT_ARS_DIS_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/09/23	11:33:06.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/09/23	14:38:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/23	14:47:30.0	XRT_Custom_430_OG [0x1ae]					
2009/09/23	14:48:30.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/09/23	16:12:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/23	16:36:00.0	XRT_Custom_430_OG [0x1ae]					
2009/09/23	16:37:00.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/09/23	17:49:30.0	XRT_CTRL_MANU_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/23	18:12:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/23	18:12:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff	aa 00
2009/09/23	18:13:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00	00 00 00
2009/09/23	18:13:16.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/09/23	18:13:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/09/23	18:13:20.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/09/23	18:15:58.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10
2009/09/23	18:16:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/09/23	18:23:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00	00 00 00 00
2009/09/24	05:59:54.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/24	05:59:56.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff	aa 00
2009/09/24	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00	00 00 00 00
2009/09/24	06:00:16.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/09/24	06:00:18.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/09/24	06:00:20.0	XRT_ARS_DIS_410_OG [0x19a]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/09/24	06:02:58.0	XRT_QT_PROG_SET_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
2009/09/24	06:03:00.0	XRT_CTRL_AUTO_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/09/24	06:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00	00 00 00 00
2009/09/24	08:31:30.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/09/24	08:31:32.0	XRT_FOCUS_POSITION_402_OG [0x192]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2009/09/24	08:31:52.0	XRT_QT_PROG_SET_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2009/09/24	08:34:28.0	XRT_AEC_RESET_415_OG [0x19f]					

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2009/09/24	08:34:30.0	XRT_FLD_DIS_419_OG [0x1a3]	MDP_XRT_AEC_RESET	1	07-F0	d0						
			MDP_XRT_FLD_DIS	1	07-F0	d9						
2009/09/24	08:34:32.0	XRT_FLRCTRL_DIS_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9						
2009/09/24	08:34:34.0	XRT_ARS_DIS_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5						
2009/09/24	08:34:36.0	XRT_CTRL_AUTO_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2009/09/24	09:32:30.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1						
2009/09/24	09:49:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00	00	00	00