XRT Timeline to be uploaded on 2009/10/22

Period: 2009/10/22 11:14:00 - 2009/10/26 10:27:00

* * * * * **Normal mode** * * * * *

XOB #1563: CCD Monitor During Bakeout - G-band + dark - wide FOV

Term	Pointing (x, y)	Comment
10/22 11:27:00 - 10/22 12:59:54	Track (-454.4, 363.4) @ 10/22 11:24:00	# OP start + 10min / HOP 133
10/22 17:03:00 - 10/23 02:09:54	Track (246.4, -467.2) @ 10/22 17:00:00	EIS coronal hole boundary

PROG= 16 Inf.-time(s)
Subr= 1 1-time(s) 600.0sec
Seqn= 48 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec

XOB #1565: CCD Monitor During Bakeout - G-band + dark - wide FOV - lower cadence (30min)

Term	Pointing (x, y)	Comment
10/22 13:03:00 - 10/22 16:59:54	Track (-443.4, 362.9) @ 10/22 13:00:00	HOP 140

PROG= 14 Inf.-time(s)
Subr= 1 1-time(s) 1800.0sec
Seqn= 48 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec

XOB #15A9: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant

Term	Pointing (x, y)	Comment
10/23 02:13:00 - 10/23 02:19:54	Fixed (528.4, -528.4)	Quadra pointing 1

PROG= 19 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 50 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec

XOB #15A8: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant

Term	Pointing (x, y)	Comment
10/23 02:23:00 - 10/23 02:29:54	Fixed (-528.4, -528.4)	Quadra pointing 2

PROG= 18 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 49 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec

XOB #15AA: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant

Term	Pointing (x, y)	Comment
10/23 02:33:00 - 10/23 02:39:54	Fixed (528.4, 528.4)	Quadra pointing 3

PROG= 11 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 51 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec

XOB #15AB: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant

Term	Pointing (x, y)	Comment
10/23 02:43:00 - 10/23 03:18:00	Fixed (-528.4, 528.4)	Quadra pointing 4
10/23 03:40:00 - 10/23 05:59:54	Track (324.0, -464.3) @ 10/23 02:50:00	EIS coronal hole boundary

PROG= 05 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 52 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec

XOB #1726: Synoptic 9 Filter- 2x2 Q98 Shorter exp 2 loop

Term	Pointing (x, y)	Comment
10/23 06:03:00 - 10/23 07:00:00	Fixed (0.0, 0.0)	synoptic (20 minutes)

PROG= 10 2-time(s)

Subr= 1 1-time(s) 180.0sec

Seqn= 22 1-time(s) 25.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 23 1-time(s) 25.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 24 1-time(s) 25.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 62 1-time(s) 25.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
C-poly/Open	C-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
C-poly/Open	C-poly/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 25 1-time(s) 25.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 26 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 360.0sec

Seqn= 18 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Al/Open	med-Al/Open	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 19 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 20 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 11 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1728: Op heater off test - med-Al/thick-Be

Term	Pointing (x, y)	Comment
10/23 07:03:06 - 10/23 09:49:54	Fixed (0.0, 0.0)	synoptic (20 minutes)

PROG= 13 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 79 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Al/thick-Be	Open/G-band	close	Safe	Dark	8.00s	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec

* * * * * **Flare mode** * * * * *

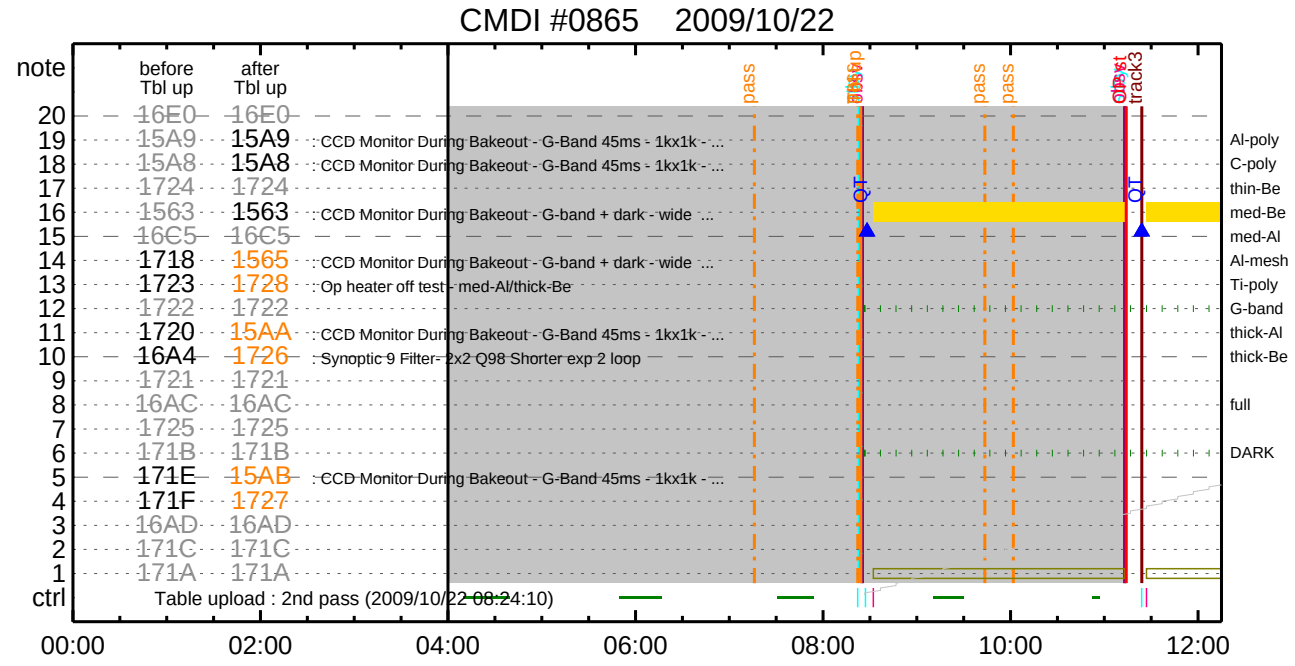
NOT USED

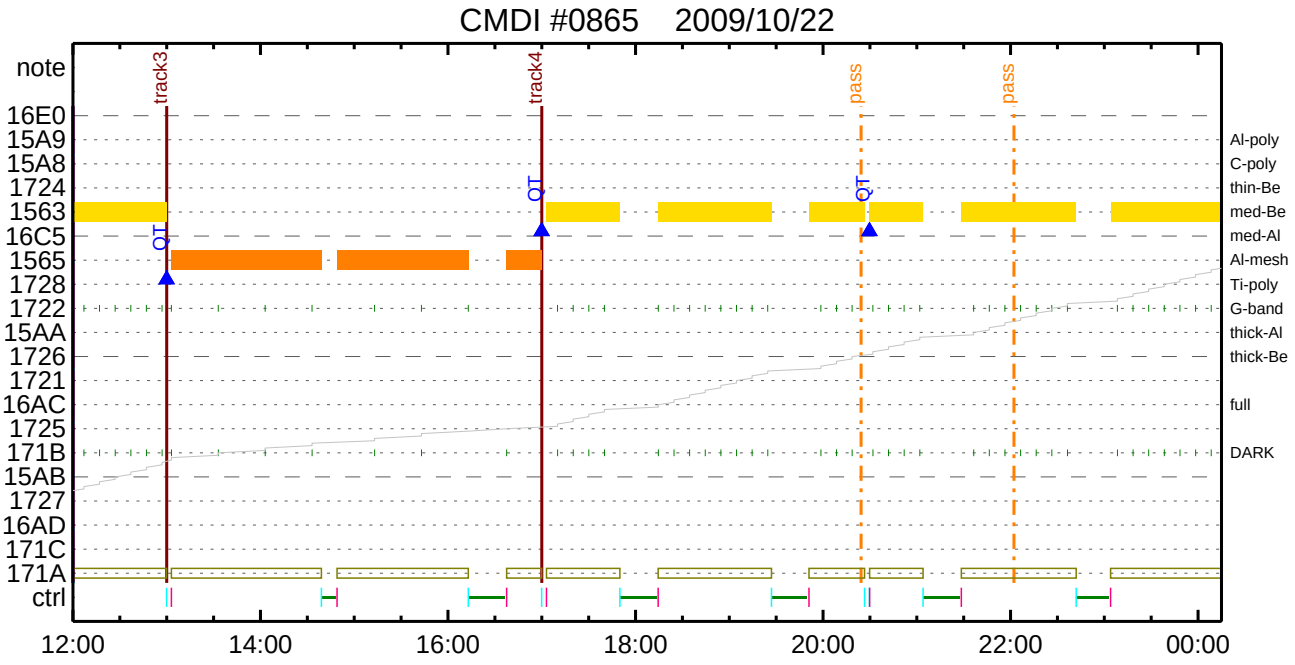
* * * * * **Active Region Search** * * * * *

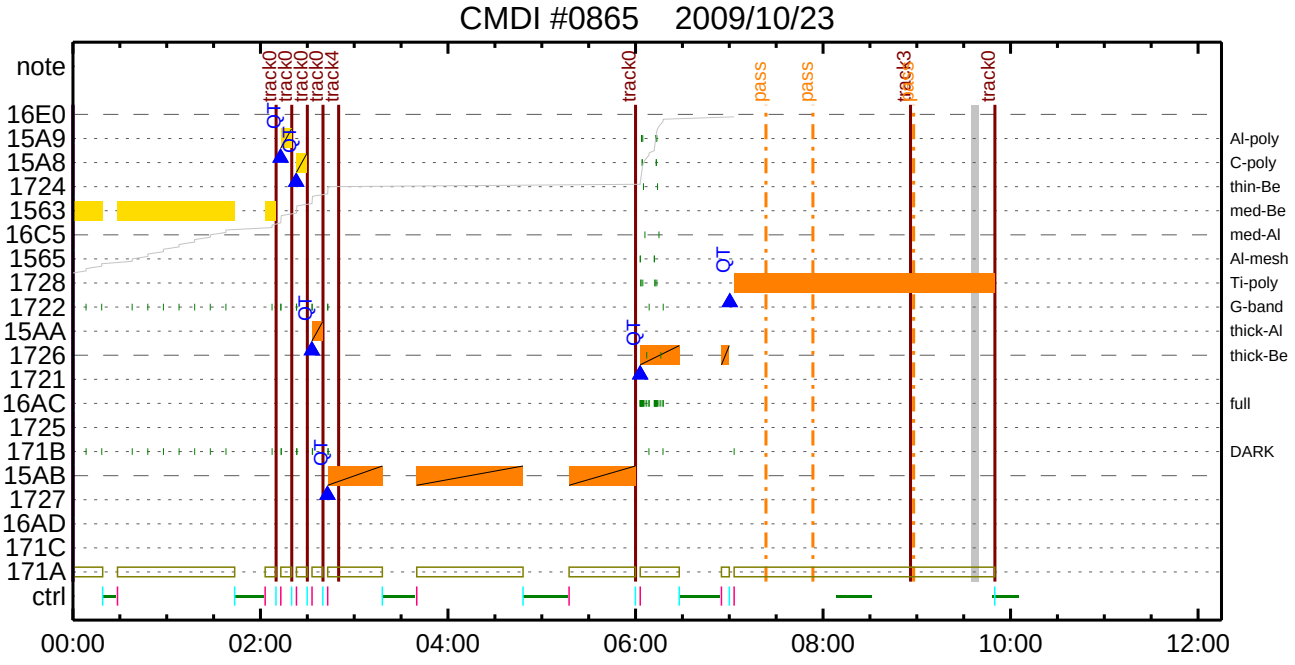
NOT USED

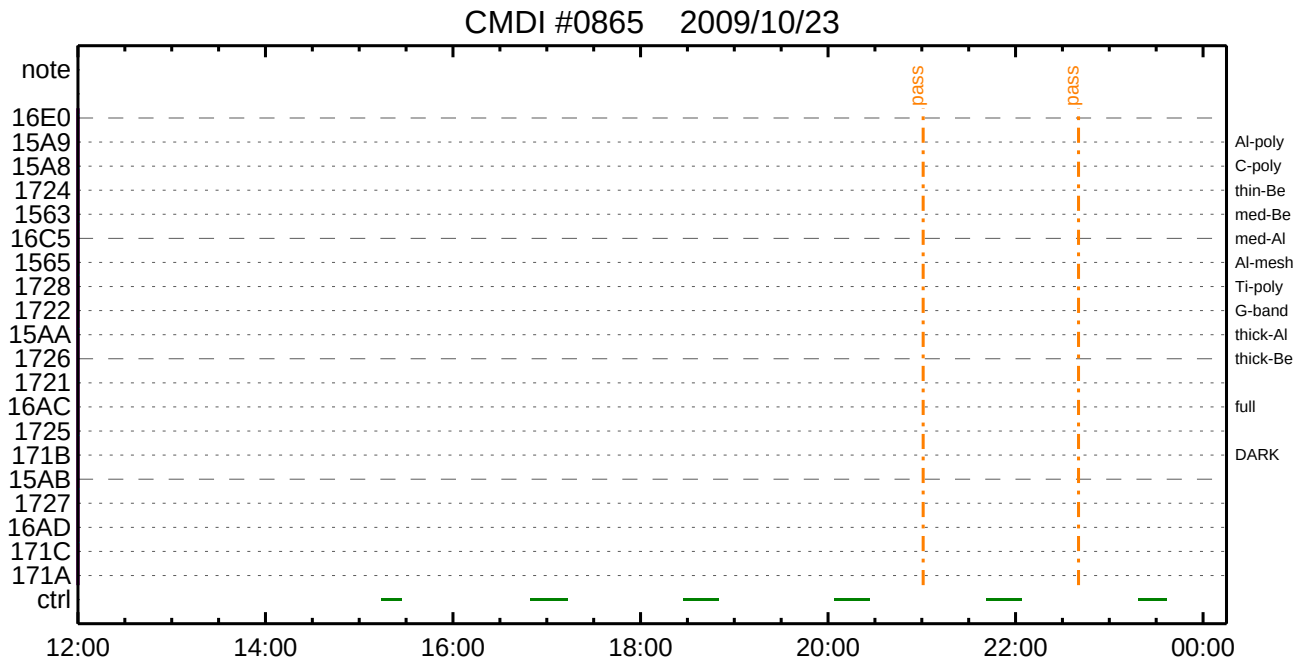
* * * * * **Flare Detection** * * * * *

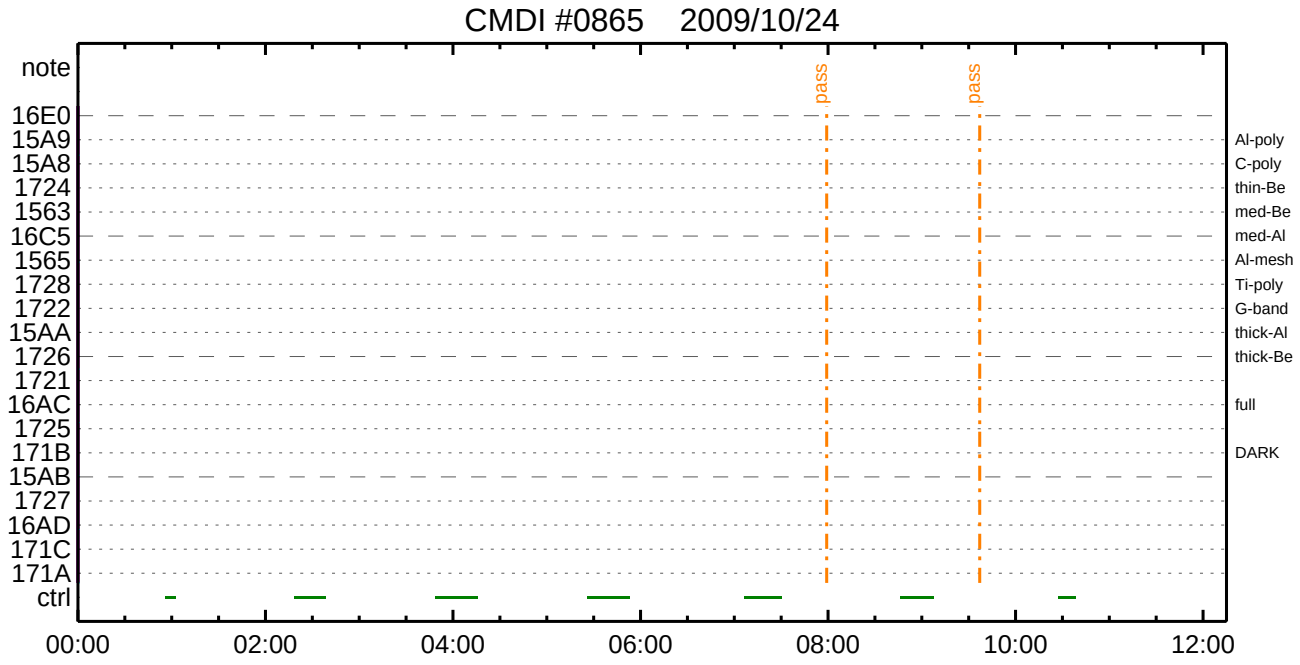
NOT USED

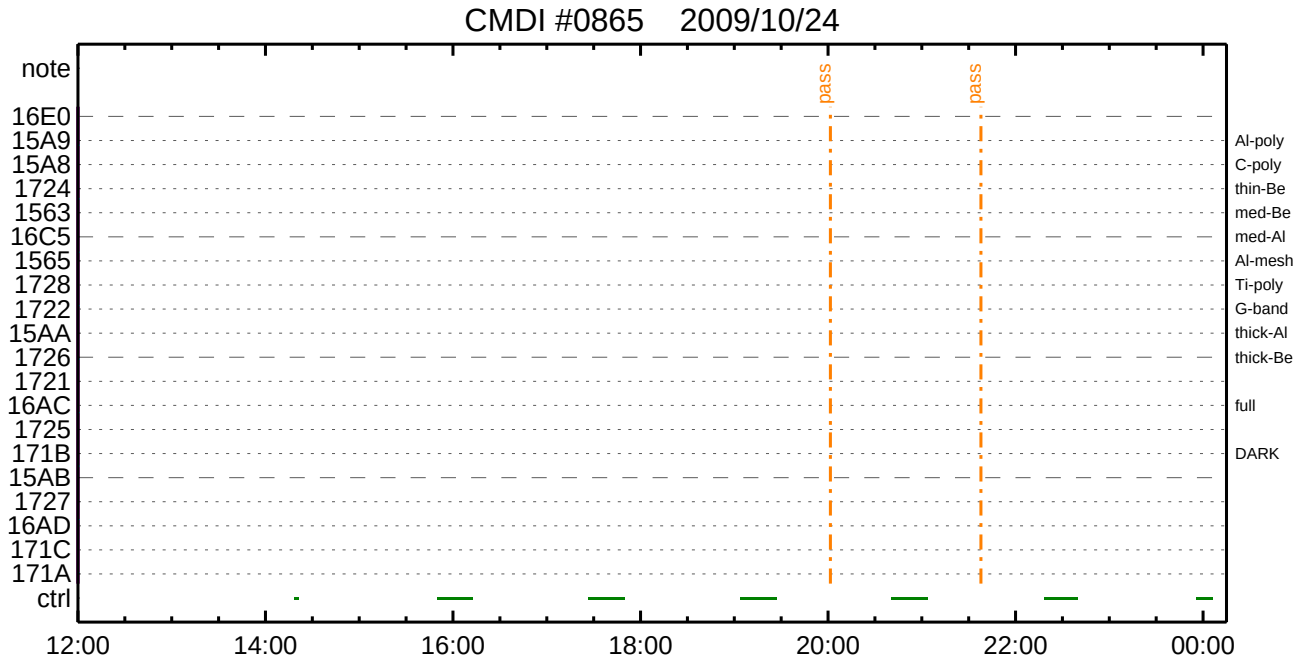

CMDI #0865 2009/10/22
note
before Tbl up
after Tbl up
20 16E0 16E0
19 15A9 15A9 : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
18 15A8 15A8 : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
17 1724 1724
16 1563 1563 : CCD Monitor During Bakeout - G-band + dark - wide ...
15 16C5 16C5
14 1718 1565 : CCD Monitor During Bakeout - G-band + dark - wide ...
13 1723 1728 : Op heater off test - med-Al/thick-Be
12 1722 1722
11 1720 15AA : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
10 16A4 1726 : Synoptic 9 Filter- 2x2 Q98 Shorter exp 2 loop
9 1721 1721
8 16AC 16AC
7 1725 1725
6 171B 171B
5 171E 15AB : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
4 171F 1727
3 16AD 16AD
2 171C 171C
1 171A 171A
ctrl Table upload : 2nd pass (2009/10/22 08:24:10)
pass
QT
track3
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

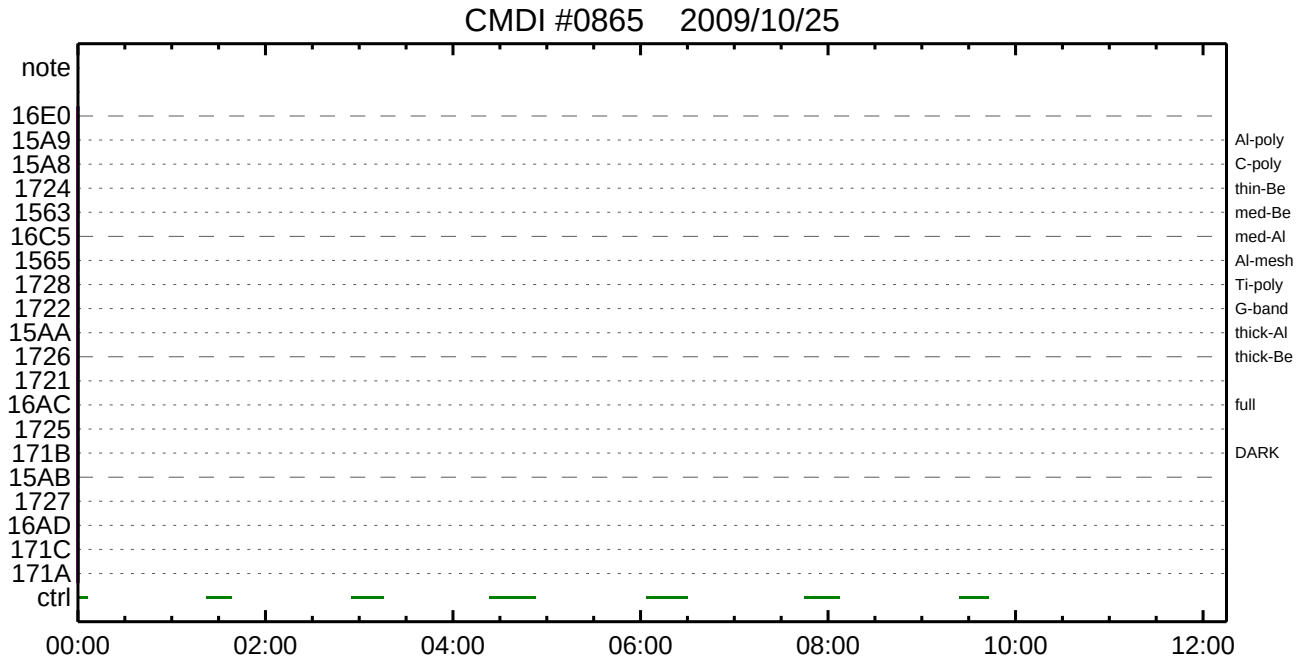

CMDI #0865 2009/10/22
note
track3
track4
pass
QT
16E0
15A9
15A8
1724
1563
16C5
1565
1728
1722
15AA
1726
1721
16AC
1725
171B
15AB
1727
16AD
171C
171A
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

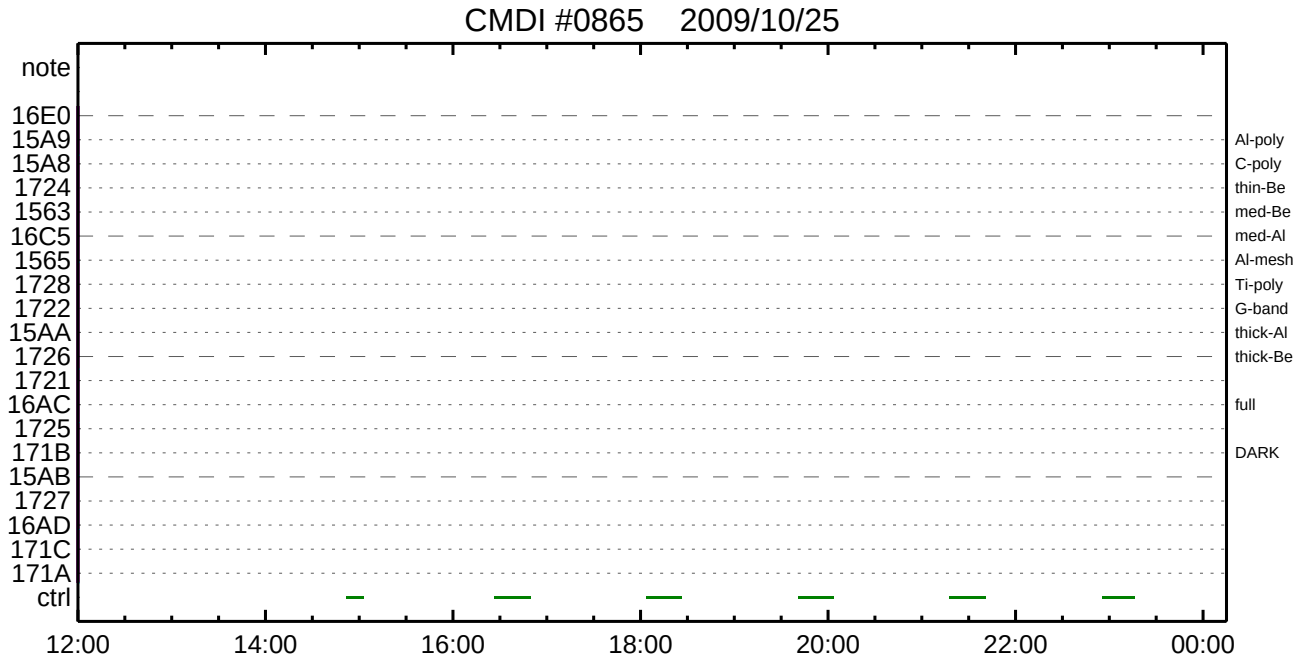

CMDI #0865 2009/10/23
note
track0
track4
pass
track3
QT
16E0
15A9
15A8
1724
1563
16C5
1565
1728
1722
15AA
1726
1721
16AC
1725
171B
15AB
1727
16AD
171C
171A
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0865 2009/10/23
note
16E0
15A9
15A8
1724
1563
16C5
1565
1728
1722
15AA
1726
1721
16AC
1725
171B
15AB
1727
16AD
171C
171A
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0865 2009/10/24
note
16E0
15A9
15A8
1724
1563
16C5
1565
1728
1722
15AA
1726
1721
16AC
1725
171B
15AB
1727
16AD
171C
171A
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0865 2009/10/24
note
16E0
15A9
15A8
1724
1563
16C5
1565
1728
1722
15AA
1726
1721
16AC
1725
171B
15AB
1727
16AD
171C
171A
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0865 2009/10/25
note
16E0
15A9
15A8
1724
1563
16C5
1565
1728
1722
15AA
1726
1721
16AC
1725
171B
15AB
1727
16AD
171C
171A
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00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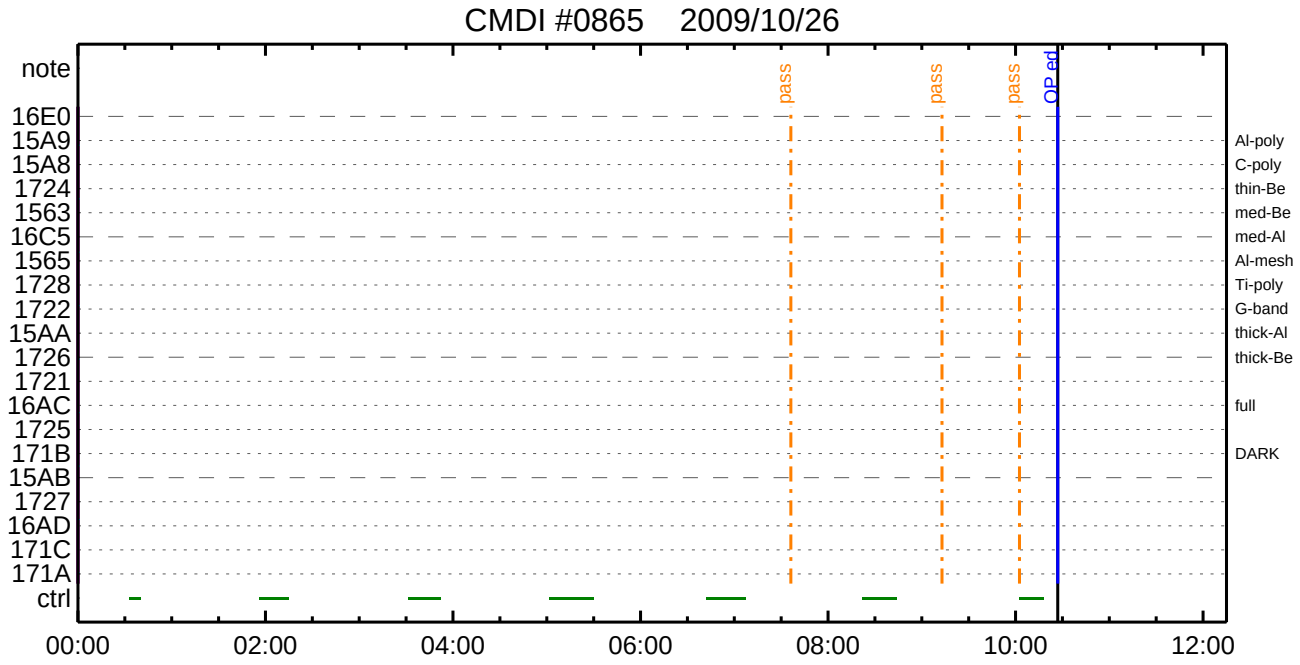
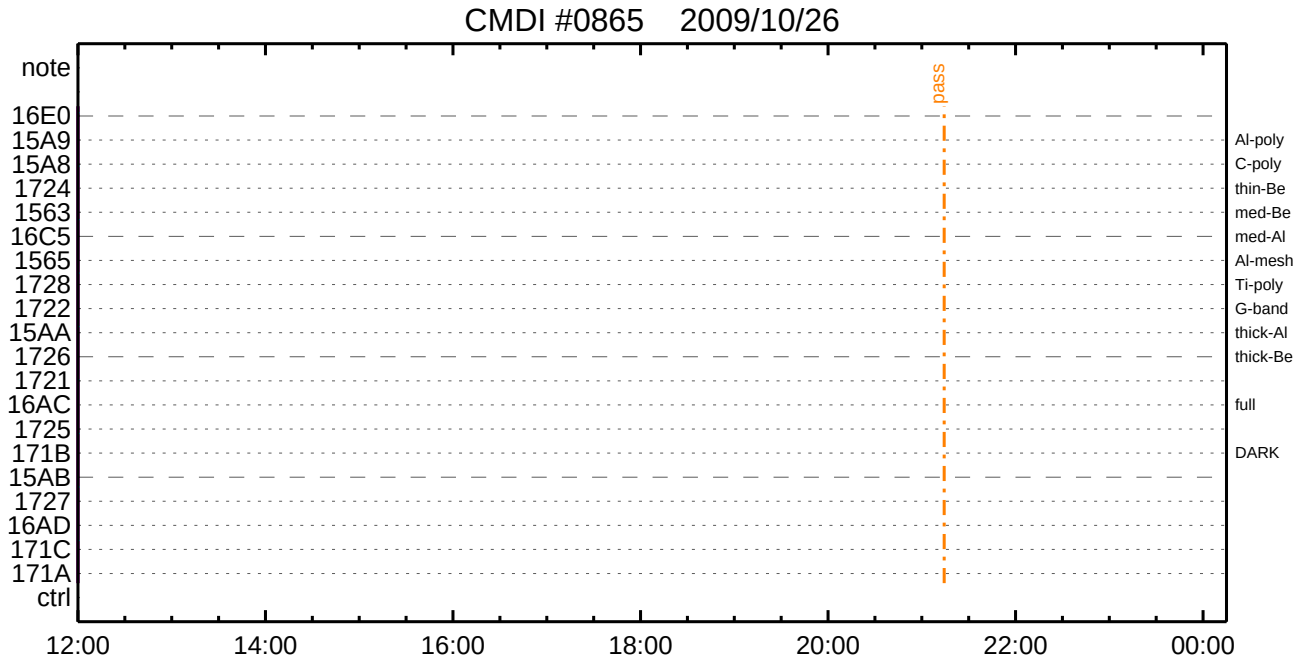
note
16E0
15A9
15A8
1724
1563
16C5
1565
1728
1722
15AA
1726
1721
16AC
1725
171B
15AB
1727
16AD
171C
171A
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0865 2009/10/26
note
pass
pass
pass
OP ed
16E0
15A9
15A8
1724
1563
16C5
1565
1728
1722
15AA
1726
1721
16AC
1725
171B
15AB
1727
16AD
171C
171A
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0865 2009/10/26
note
16E0
15A9
15A8
1724
1563
16C5
1565
1728
1722
15AA
1726
1721
16AC
1725
171B
15AB
1727
16AD
171C
171A
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-925 2009-10-22 13:28:56 205 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0016     C. ****************************
0017     C.
0018   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0019   . S. OP    op-925:OP
0020     ()
0021   . S. OG    og-925:OG
0022     ()
0023     C.
0024   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0025     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0026   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027     BC       (20 00 7f 01 02)
0028     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0029     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0030     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0031     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0032     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0033   +. DC 01-22 DHU_MODE_CHNG
0034     BC       (07 0b f8)
0035     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0036     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0037     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0038     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0039     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0040   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0041     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0042   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0043     C.
0044     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0045   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046     BC       (20 80 7f 01 02)
0047     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0048     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0049     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0050     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0051     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0052   +. DC 01-22 DHU_MODE_CHNG
0053     BC       (07 0b f8)
0054     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0055     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0056     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0057     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0058     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0059   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0060     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0061   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0062     C.
0063     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065     BC       (21 00 41 01 02)
0066     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0067     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0068     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0069     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0070     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0071   +. DC 01-22 DHU_MODE_CHNG
0072     BC       (07 0b f8)
0073     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0074     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0075     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0076     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0077     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0078   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0079     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0080   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0081     C.
0082   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0083     C. DHU¥â¡¼¥É¡Ê¼ý¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0084   +. DC 01-22 DHU_MODE_CHNG
0085     BC       (02 0a f8)
0086     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0087     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0088     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0089     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0090     C.
0091   . C. ********************************
0092     C. TI-CMD SET (OPOG STOP/COPY/START)
0093     C. ********************************
0094     C.
0095   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
```

```
0096     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
0097     C.
0098   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0099   +. TI 2009-10-22 11:09:00.0
0100     DC 01-B3 DHU_OP_STOP
0101     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0102     C.
0103   +. TI 2009-10-22 11:09:01.0
0104     DC 01-B4 DHU_OP_COPY
0105     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0106     C.
0107   +. TI 2009-10-22 11:09:01.0
0108     DC 01-B5 DHU_OPOG_COPY
0109     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0110     C.
0111   +. TI 2009-10-22 11:13:59.5
0112     DC 01-B2 DHU_OP_START
0113     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0114     C.
0115     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0116     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0117     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0118     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0119     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0120     C.
0121   . C. ****************************
0122     C. TIÎÎ°è¥À¥ó¥×
0123     C. ****************************
0124     C.
0125     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0126   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127     BC       (03 ab 03 01 02)
0128     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0129     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0130     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0131     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0132     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0133   +. DC 01-22 DHU_MODE_CHNG
0134     BC       (07 0b f8)
0135     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0136     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0137     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0138     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0139     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0140     C.
0141   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0142     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0143     C.
0144   . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0145     C.
0146   . C. DHU¥â¡¼¥É¡Ê¼ý¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0147   +. DC 01-22 DHU_MODE_CHNG
0148     BC       (02 0a f8)
0149     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0150     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0151     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0152     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0153     C.
0154   . C. Stop EIS observation and temporarily disable EIS mode changes
0155     C.
0156     C.
0157     C. ********** Start EIS operation (TI set) **********
0158     C. Execute, after the success of OP upload.
0159     C. Set EIS TI-commands
0160   +. TI 2009-10-22 11:13:30.0
0161     DC 07-FC EIS_MODE_MANU
0162     BC       (21 02)
0163   +. TI 2009-10-22 11:13:40.0
0164     DC 07-FC EIS_MODE_CHG_DIS
0165     BC       (22)
0166   . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0167     C. *********** End EIS operation (TI set) ***********
0168     C.
0169     C.
0170     C. ********************************
0171     C.  SOT TI command set
0172     C. ********************************
0173     C. Execute, after the success of OP upload.
0174   +. TI 2009-10-22 11:13:16.0
0175     DC 07-F0 MDP_SOT_MODE_STBY
0176     BC       (41)
0177   . C. ------------------------------------
0178     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0179     C. ------------------------------------
0180     C. ************ SOT END ***********
0181     C.
0182     C. ************ XRT START ***********
0183     C. Execute, after the success of OP upload.
0184   +. TI 2009-10-22 11:13:00.0
0185     DC 07-F0 MDP_XRT_MODE_STBY
0186     BC       (c3)
0187   . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0188     C.
0189     C. ************ XRT END ***********
0190     C.
0191   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0192     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0193   . S. DC-BC dcbc-402:DCBC
```

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0194     (MDP_known_event)
0195     C.
0196     C.
0197   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0198   . S. DC-BC dcbc-153:DCBC
0199     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200     C.
0201     C.
0202   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0203     C.
0204   . C. ********** LOS **********
0205     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-926 2009-10-22 13:28:56 261 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106   +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113   +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                      Result = OK [ ]
0121     C.
0122   +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                         <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127   +. DC 02-8E AOCU_ORB_UPD
0128    . C.
0129     C.
0130     C. ************ XRT START ************
0131     C.
0132   +. DC 07-F0 MDP_XRT_CTRL_MANU
0133     BC       (c1)
0134   +  DC 07-F0 MDP_XRT_MODE_STBY
0135     BC       (c3)
0136    . C. ------------ Success Verify ?              OK / NG____
0137     C.
0138     C. XRT Obs. Table Upload
0139    . S. RAM   ram-291:MDP_OBS_X
0140     ()
0141     C.
0142   +. DC 07-F0 MDP_DUMP_XRTTBL
0143     BC       (84 07 00 00 00 3a d4)
0144    . C. ------------ Comparison Check ?                        OK / ERR ____
0145     C.
0146     C.
0147   +. DC 07-F0 MDP_XRT_ROI_SET
0148     BC       (cd 01 b1 b1 04 04)
0149   +  DC 07-F0 MDP_XRT_ROI_SET
0150     BC       (cd 02 b1 b1 08 08)
0151   +  DC 07-F0 MDP_XRT_ROI_SET
0152     BC       (cd 03 b1 b1 08 08)
0153   +  DC 07-F0 MDP_XRT_ROI_SET
0154     BC       (cd 04 b1 b1 06 06)
0155   +  DC 07-F0 MDP_XRT_ROI_SET
0156     BC       (cd 06 80 80 20 04)
0157   +  DC 07-F0 MDP_XRT_ROI_SET
0158     BC       (cd 07 40 c0 10 10)
0159   +  DC 07-F0 MDP_XRT_ROI_SET
0160     BC       (cd 08 c0 c0 10 10)
0161   +  DC 07-F0 MDP_XRT_ROI_SET
0162     BC       (cd 09 40 40 10 10)
0163   +  DC 07-F0 MDP_XRT_ROI_SET
0164     BC       (cd 0a c0 40 10 10)
0165   +  DC 07-F0 MDP_XRT_ROI_SET
0166     BC       (cd 0b 80 80 20 20)
0167   +  DC 07-F0 MDP_XRT_ROI_SET
0168     BC       (cd 0c 80 80 06 06)
0169   +  DC 07-F0 MDP_XRT_ROI_SET
0170     BC       (cd 0f 80 80 04 04)
0171   +  DC 07-F0 MDP_XRT_ROI_SET
0172     BC       (cd 10 80 80 10 10)
0173   +  DC 07-F0 MDP_XRT_AEC_RESET
0174     BC       (d0)
0175    . C. ---------- Success Verify ?            OK / NG ____
0176     C.
0177     C.
0178    . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0179     C.
0180   +. DC 07-F0 MDP_XRT_MODE_OBSV
0181     BC       (c2)
0182   +. TI 2009-10-22 11:13:02.0
0183     DC 07-F0 MDP_XRT_MODE_OBSV
0184     BC       (c2)
0185    . C. ---------- Success Verify ?            OK / NG ____
0186     C.
0187     C. ************ XRT END ************
0188     C. ************************************************
0189     C.  START of XRT_CCD_HEATER_ON operation
0190     C. ************************************************
0191     C.
0192   +. DC 07-F0 MDP_XRT_CTRL_MANU
0193     BC       (c1)
```

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0194     C. ---------- Success Verify ?          OK / NG;
0195     C.
0196   +. DC 07-F0 MDP_XRT_QT_PROG_SET
0197     BC       (c4 10)
0198   +  DC 07-F0 MDP_XRT_FLD_DIS
0199     BC       (d9)
0200   +  DC 07-F0 MDP_XRT_FLRCTRL_DIS
0201     BC       (c9)
0202   +  DC 07-F0 MDP_XRT_ARS_DIS
0203     BC       (d5)
0204     C. ---------- Success Verify ?           OK / NG ____
0205     C.
0206     C.
0207     C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0208     C.
0209   +. DC 07-F0 MDP_XRT_CTRL_AUTO
0210     BC       (c0)
0211     C. ---------- Success Verify ?          OK / NG;
0212     C.
0213   +. DC 04-BC TCIB_XRT_S_HTR_A_ENA
0214     C. ---------- Success Verify ?          OK / NG;
0215     C.
0216     C. ---------------------------------------
0217     C. If anomalous situation appeared, execute TCIB_XRT_S_HTR_A_DIS using DCBC-441 (line 24)
0218     C. ---------------------------------------
0219     C. ************************************************
0220     C.  END of XRT_CCD_HEATER_ON operation
0221     C. ************************************************
0222     C.
0223     C.
0224    . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0225   +. DC 07-FC EIS_MODE_MANU
0226     BC       (21 02)
0227    . C. Verify EIS in MANUAL mode
0228    . C. Estimated OBSTBL upload time is 11s
0229     C. ********************************
0230     C. EIS START OBSTBL LOAD
0231     C. ********************************
0232    . S. RAM   ram-820:EIS_OBSTBL
0233     ()
0234   +. DC 07-FC EIS_DUMP_OBSTBL
0235     BC       (07 07 07 00 00 70 00)
0236     C.
0237     C. Execute, after the success of OBSTBL upload.
0238     C. Set EIS TI-commands
0239   +. TI 2009-10-22 11:13:50.0
0240     DC 07-FC EIS_MODE_CHG_ENA
0241     BC       (20)
0242    . C.                    [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0243     C. ********************************
0244     C. EIS END OBSTBL LOAD
0245     C. ********************************
0246     C.
0247    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0248     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0249    . S. DC-BC dcbc-402:DCBC
0250     (MDP_known_event)
0251     C.
0252     C.
0253    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0254    . S. DC-BC dcbc-153:DCBC
0255     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0256     C.
0257     C.
0258    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0259     C.
0260    . C. ********** LOS **********
0261     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-927 2009-10-22 13:28:56 79 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011  +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ********************************
0015     C.  SOT table upload
0016     C. ********************************
0017   . C. < Stop FG table >
0018  +. DC 07-F0 MDP_FG_CTRL_MANU
0019     BC       (51)
0020   . C. ------------------------------------
0021     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0022     C. ------------------------------------
0023     C.
0024   . C. <Upload FG Observation Table>
0025   . S. RAM   ram-261:MDP_OBS_F
0026     ()
0027     C.
0028   . C. < Dump RAMID=MDP_OBS_F >
0029  +. DC 07-F0 MDP_DUMP_FGTBL
0030     BC       (82 07 00 00 00 38 b8)
0031     C. ------------------------------------
0032     C.  MDP_OBS_F verify = OK/NG        [ ]
0033     C. ------------------------------------
0034     C.
0035   . C. < Stop SP table >
0036  +. DC 07-F0 MDP_SP_CTRL_MANU
0037     BC       (61)
0038     C. ------------------------------------
0039     C.  MDP_SP_CTRL_MODE      = MANU    [ ]
0040     C. ------------------------------------
0041     C.
0042   . C. <Upload SP Observation Table>
0043   . S. RAM   ram-286:MDP_OBS_S
0044     ()
0045     C.
0046   . C. < Dump RAMID=MDP_OBS_S >
0047  +. DC 07-F0 MDP_DUMP_SPTBL
0048     BC       (83 07 00 00 00 38 b8)
0049     C. ------------------------------------
0050     C.  MDP_OBS_S verify = OK/NG        [ ]
0051     C. ------------------------------------
0052     C.
0053     C. ********************************
0054     C.  SOT TI command set
0055     C. ********************************
0056     C. Execute, after the success of TBL upload.
0057  +. TI 2009-10-22 11:13:18.0
0058     DC 07-F0 MDP_SOT_MODE_OBSV
0059     BC       (40)
0060   . C. ------------------------------------
0061     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0062     C. ------------------------------------
0063     C.
0064     C.
0065   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0066     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0067   . S. DC-BC dcbc-402:DCBC
0068     (MDP_known_event)
0069     C.
0070     C.
0071   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0072   . S. DC-BC dcbc-153:DCBC
0073     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0074     C.
0075     C.
0076   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0077     C.
0078   . C. *********** LOS ***********
0079     C.
```

```
*** OP Sequence for XRT ***

2009/10/22  11:23:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/10/22  11:23:56.0  XRT_QT_PROG_SET_429_OG [0x1ad]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 10
2009/10/22  11:24:00.5  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 03 00 00 00 00
2009/10/22  11:26:54.0  XRT_ARS_DIS_404_OG [0x194]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2009/10/22  11:26:56.0  XRT_FLD_DIS_405_OG [0x195]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2009/10/22  11:26:58.0  XRT_FLRCTRL_DIS_423_OG [0x1a7]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/10/22  11:27:00.0  XRT_CTRL_AUTO_407_OG [0x197]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/10/22  12:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/10/22  12:59:56.0  XRT_QT_PROG_SET_437_OG [0x1b5]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0e
2009/10/22  13:00:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 03 00 00 00 00
2009/10/22  13:02:54.0  XRT_ARS_DIS_404_OG [0x194]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2009/10/22  13:02:56.0  XRT_FLD_DIS_405_OG [0x195]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2009/10/22  13:02:58.0  XRT_FLRCTRL_DIS_423_OG [0x1a7]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/10/22  13:03:00.0  XRT_CTRL_AUTO_407_OG [0x197]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/10/22  14:39:00.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/10/22  14:48:00.0  XRT_Custom_420_OG [0x1a4]
2009/10/22  14:49:00.0  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/10/22  16:13:00.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/10/22  16:36:30.0  XRT_Custom_420_OG [0x1a4]
2009/10/22  16:37:30.0  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/10/22  16:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/10/22  16:59:56.0  XRT_QT_PROG_SET_429_OG [0x1ad]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 10
2009/10/22  17:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 04 00 00 00 00
2009/10/22  17:02:54.0  XRT_ARS_DIS_404_OG [0x194]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2009/10/22  17:02:56.0  XRT_FLD_DIS_405_OG [0x195]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2009/10/22  17:02:58.0  XRT_FLRCTRL_DIS_423_OG [0x1a7]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/10/22  17:03:00.0  XRT_CTRL_AUTO_407_OG [0x197]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/10/22  17:50:00.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/10/22  18:13:30.0  XRT_Custom_420_OG [0x1a4]
2009/10/22  18:14:30.0  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/10/22  19:27:00.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/10/22  19:50:00.0  XRT_Custom_420_OG [0x1a4]
2009/10/22  19:51:00.0  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/10/22  20:26:33.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/10/22  20:26:35.0  XRT_TCIB_XRT_S_HTR_A_DIS_431_OG [0x1af]
                                TCIB_XRT_S_HTR_A_DIS 0 04-C0
2009/10/22  20:29:40.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/10/22  20:29:42.0  XRT_QT_PROG_SET_429_OG [0x1ad]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 10
2009/10/22  20:29:44.0  XRT_ARS_DIS_404_OG [0x194]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2009/10/22  20:29:46.0  XRT_FLD_DIS_405_OG [0x195]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2009/10/22  20:29:48.0  XRT_FLRCTRL_DIS_423_OG [0x1a7]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/10/22  20:29:50.0  XRT_CTRL_AUTO_407_OG [0x197]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/10/22  21:04:00.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/10/22  21:27:30.0  XRT_Custom_420_OG [0x1a4]
2009/10/22  21:28:30.0  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/10/22  22:42:00.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/10/22  23:03:00.0  XRT_Custom_420_OG [0x1a4]
2009/10/22  23:04:00.5  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/10/23  00:19:00.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/10/23  00:27:30.0  XRT_Custom_420_OG [0x1a4]
```

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2009/10/23  00:28:30.0  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/10/23  01:43:30.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/10/23  02:02:00.0  XRT_Custom_420_OG [0x1a4]
2009/10/23  02:03:00.0  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/10/23  02:09:54.0  XRT_CTRL_MANU_432_OG [0x1b0]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/10/23  02:10:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 2e f9 d1 07
2009/10/23  02:12:32.0  XRT_FOCUS_POSITION_408_OG [0x198]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2009/10/23  02:12:52.0  XRT_QT_PROG_SET_433_OG [0x1b1]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2009/10/23  02:12:54.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2009/10/23  02:12:56.0  XRT_FLRCTRL_DIS_410_OG [0x19a]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/10/23  02:12:58.0  XRT_ARS_DIS_415_OG [0x19f]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2009/10/23  02:13:00.0  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/10/23  02:19:54.0  XRT_CTRL_MANU_432_OG [0x1b0]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/10/23  02:20:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 2e f9 2e f9
2009/10/23  02:22:32.0  XRT_FOCUS_POSITION_408_OG [0x198]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2009/10/23  02:22:52.0  XRT_QT_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 12
2009/10/23  02:22:54.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2009/10/23  02:22:56.0  XRT_FLRCTRL_DIS_410_OG [0x19a]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/10/23  02:22:58.0  XRT_ARS_DIS_415_OG [0x19f]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2009/10/23  02:23:00.0  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/10/23  02:29:54.0  XRT_CTRL_MANU_432_OG [0x1b0]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/10/23  02:30:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM              5 02-76 00 d1 07 d1 07
2009/10/23  02:32:32.0  XRT_FOCUS_POSITION_408_OG [0x198]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2009/10/23  02:32:52.0  XRT_QT_PROG_SET_427_OG [0x1ab]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2009/10/23  02:32:54.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2009/10/23  02:32:56.0  XRT_FLRCTRL_DIS_410_OG [0x19a]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/10/23  02:32:58.0  XRT_ARS_DIS_415_OG [0x19f]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2009/10/23  02:33:00.0  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/10/23  02:39:54.0  XRT_CTRL_MANU_432_OG [0x1b0]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/10/23  02:40:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM              5 02-76 00 d1 07 2e f9
2009/10/23  02:42:32.0  XRT_FOCUS_POSITION_408_OG [0x198]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2009/10/23  02:42:52.0  XRT_QT_PROG_SET_417_OG [0x1a1]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 05
2009/10/23  02:42:54.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2009/10/23  02:42:56.0  XRT_FLRCTRL_DIS_410_OG [0x19a]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/10/23  02:42:58.0  XRT_ARS_DIS_415_OG [0x19f]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2009/10/23  02:43:00.0  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/10/23  02:50:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 04 00 00 00 00
2009/10/23  03:18:00.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/10/23  03:39:00.0  XRT_Custom_420_OG [0x1a4]
2009/10/23  03:40:00.0  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/10/23  04:48:00.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/10/23  05:16:30.0  XRT_Custom_420_OG [0x1a4]
2009/10/23  05:17:30.0  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/10/23  05:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/10/23  05:59:56.0  XRT_FOCUS_POSITION_408_OG [0x198]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2009/10/23  06:00:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                AOCU_NM              5 02-76 00 00 00 00 00
2009/10/23  06:00:16.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2009/10/23  06:00:18.0  XRT_FLRCTRL_DIS_410_OG [0x19a]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
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2009/10/23  06:00:20.5  XRT_ARS_DIS_411_OG [0x19b]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2009/10/23  06:02:58.5  XRT_QT_PROG_SET_425_OG [0x1a9]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0a
2009/10/23  06:03:00.5  XRT_CTRL_AUTO_407_OG [0x197]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/10/23  06:28:00.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/10/23  06:54:00.0  XRT_Custom_420_OG [0x1a4]
2009/10/23  06:55:00.0  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/10/23  07:00:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/10/23  07:00:02.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2009/10/23  07:00:22.0  XRT_QT_PROG_SET_441_OG [0x1b9]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2009/10/23  07:00:24.0  XRT_AEC_RESET_403_OG [0x193]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2009/10/23  07:00:26.0  XRT_ARS_DIS_404_OG [0x194]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2009/10/23  07:00:28.0  XRT_FLD_DIS_405_OG [0x195]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2009/10/23  07:00:30.0  XRT_FLRCTRL_DIS_406_OG [0x196]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2009/10/23  07:03:06.0  XRT_CTRL_AUTO_407_OG [0x197]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2009/10/23  08:56:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 03 00 00 00 00
2009/10/23  09:49:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2009/10/23  09:50:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                AOCU_NM              5 02-76 00 00 00 00 00
```