

XRT Timeline to be uploaded on 2009/11/26

Period: 2009/11/26 09:31:00 - 2009/12/01 09:21:00

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Normal mode

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XOB #1732: Dark - Med-Al+Thick-Be - 8x8 - 512x512											
Term		Pointing (x, y)				Comment					
11/26 09:43:40 - 11/26 09:44:40		Fixed (-945.0, 0.0)				# OP start + 10min Fixed Pointing at limb for EIS slit calibration test					
11/26 15:02:40 - 11/26 15:03:40		Fixed (-10.0, -955.0)				HOP 138 1/5					
11/27 07:42:40 - 11/27 07:43:40		Track (796.7, 318.7) @ 11/27 06:10:00				# AR 11033 cont. / HOP 142 (11-13 UT)					
11/28 09:12:46 - 11/28 09:13:46		Track (879.5, 323.7) @ 11/28 06:40:30				# AR cont.					
PROG= 12 1-time(s)											
└ Subr= 1 1-time(s) 2.0sec											
└└ Seqn= 91 1-time(s) 2.0sec											
└└└ med-Al/thick-Be med-Al/thick-Be close Safe Dark 500ms Obs 8x8 512x512 (1024, 1024) DPCM 0 0 2.0sec											
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval											

XOB #1733: AR Dynamics - 2x2 - Med-Al - inner (wide) and outer - Q95 - AEC 5 and AEC 2											
Term		Pointing (x, y)				Comment					
11/26 10:58:00 - 11/26 14:59:54		Track (701.0, 314.6) @ 11/26 10:55:00				Track AR 11033 & HOP 142 11-13UT					
11/27 06:13:00 - 11/27 07:39:54		Track (796.7, 318.7) @ 11/27 06:10:00				# AR 11033 cont. / HOP 142 (11-13 UT)					
PROG= 02 Inf.-time(s)											
└ Subr= 1 1-time(s) 2.0sec											
└└ Seqn= 92 4-time(s) 18.0sec											
└└└ med-Al/Open med-Al/thick-Al close Safe Norm 16.0s Obs 2x2 1024x512 (1024, 1024) Q=95 5 0 2.0sec											
└ Subr= 2 1-time(s) 65.0sec											
└└ Seqn= 89 1-time(s) 2.0sec											
└└└ med-Al/Open med-Al/thick-Al close Safe Norm 32.0s Obs 2x2 1024x1024 (1024, 1024) Q=98 2 0 2.0sec											
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval											

XOB #172D: Synoptic Q95 med-Al + Dark (2x2 4x4 8x8 and cross strips)											
Term		Pointing (x, y)				Comment					
11/26 17:56:30 - 11/26 18:17:00		Fixed (0.0, 0.0)				synoptic, shifted -6.5 min + EIS Test					
11/27 06:03:00 - 11/27 06:09:54		Fixed (0.0, 0.0)				synoptic					
11/27 18:00:30 - 11/27 18:07:24		Fixed (0.0, 0.0)				synoptic, shifted -2.5 min					
11/28 06:33:30 - 11/28 06:40:24		Fixed (0.0, 0.0)				synoptic, shifted 30.5 min					
PROG= 03 1-time(s)											
└ Subr= 1 1-time(s) 2.0sec											
└└ Seqn= 85 2-time(s) 2.0sec											
└└└ med-Al/Open med-Al/thick-Al close Safe Norm 16.0s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec											
└└└ med-Al/Open med-Al/thick-Al close Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec											
└└└ med-Al/Open med-Al/thick-Al close Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec											
└ Subr= 2 1-time(s) 2.0sec											
└└ Seqn= 90 1-time(s) 2.0sec											
└└└ med-Al/thick-Be med-Al/thick-Be close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec											
└└└ med-Al/thick-Be med-Al/thick-Be close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec											
└└└ med-Al/thick-Be med-Al/thick-Be close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec											
└└└ med-Al/thick-Be med-Al/thick-Be close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec											
└└└ med-Al/thick-Be med-Al/thick-Be close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec											
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval											

XOB #1734: AR Dynamics - 2x2 - Med-Al - inner (wide) and outer - lower cadence- Q95 - AEC 5 and AEC 2											
Term		Pointing (x, y)				Comment					
11/26 18:56:30 - 11/27 05:59:54		Track (743.7, 316.3) @ 11/26 18:53:30				# AR 11033 Continued					
11/27 18:10:30 - 11/28 06:30:24		Track (842.8, 321.2) @ 11/27 18:07:30				# AR cont.					
11/28 06:43:30 - 11/28 09:10:00		Track (879.5, 323.7) @ 11/28 06:40:30				# AR cont.					
PROG= 07 Inf.-time(s)											
└ Subr= 1 1-time(s) 2.0sec											
└└ Seqn= 92 4-time(s) 45.0sec											
└└└ med-Al/Open med-Al/thick-Al close Safe Norm 16.0s Obs 2x2 1024x512 (1024, 1024) Q=95 5 0 2.0sec											
└ Subr= 2 1-time(s) 65.0sec											
└└ Seqn= 89 1-time(s) 2.0sec											
└└└ med-Al/Open med-Al/thick-Al close Safe Norm 32.0s Obs 2x2 1024x1024 (1024, 1024) Q=98 2 0 2.0sec											
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval											

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Flare mode

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NOT USED

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Active Region Search

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NOT USED

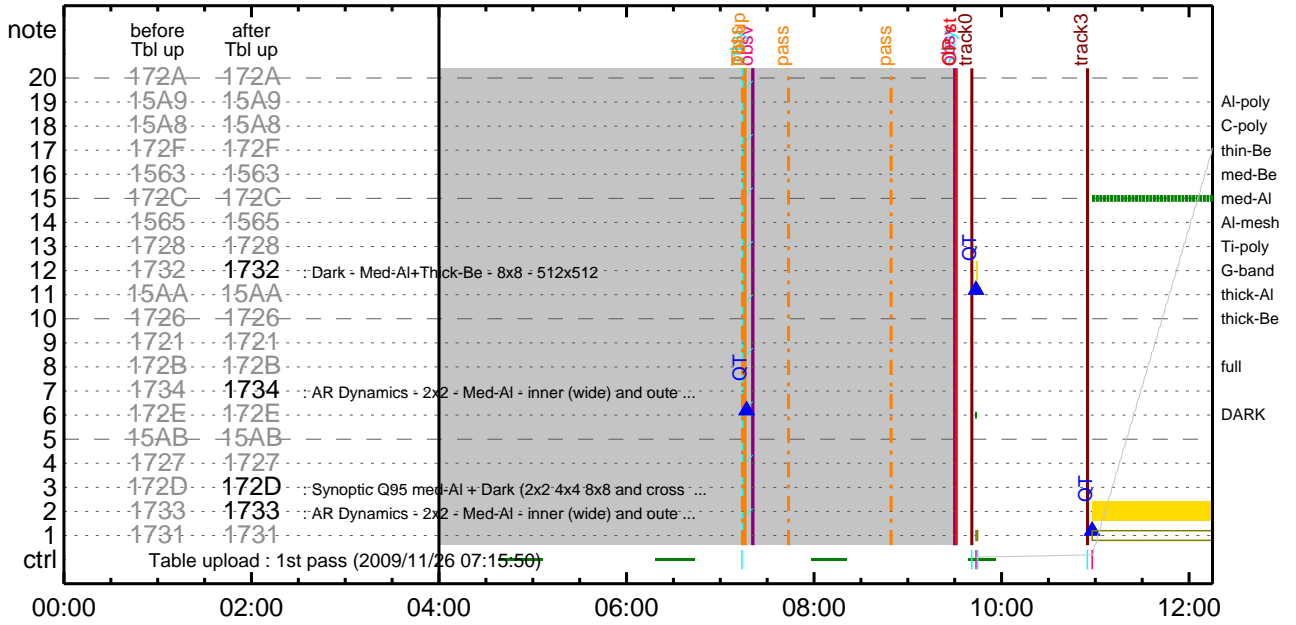
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Flare Detection

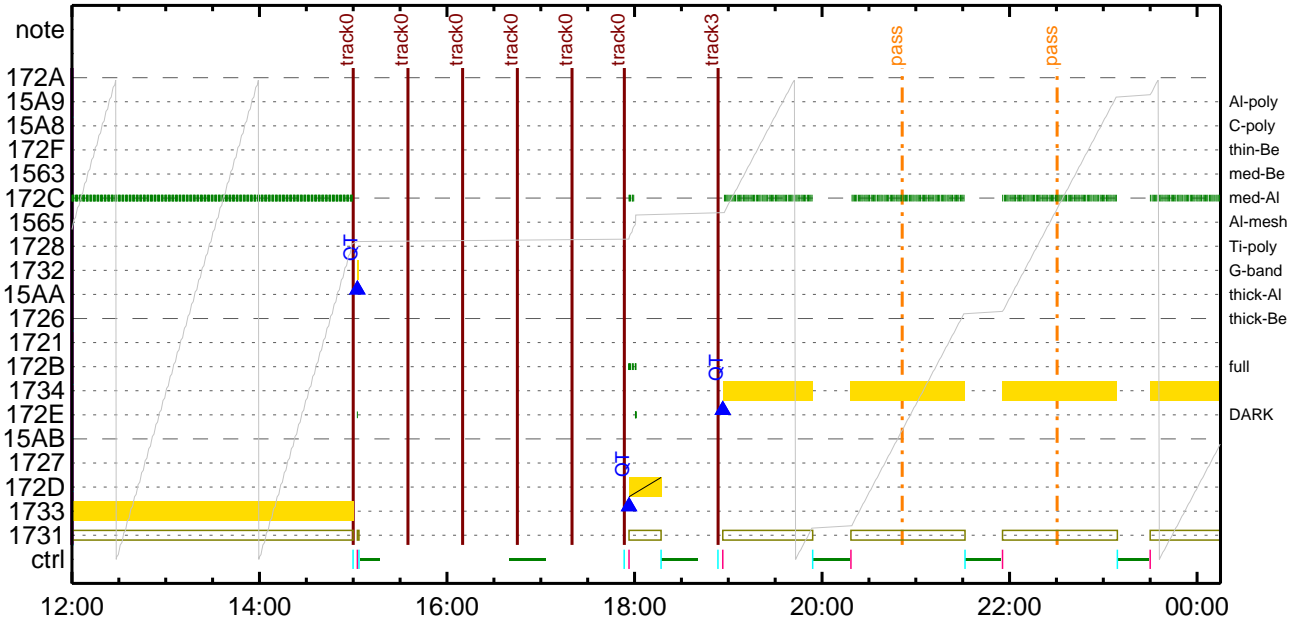
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NOT USED

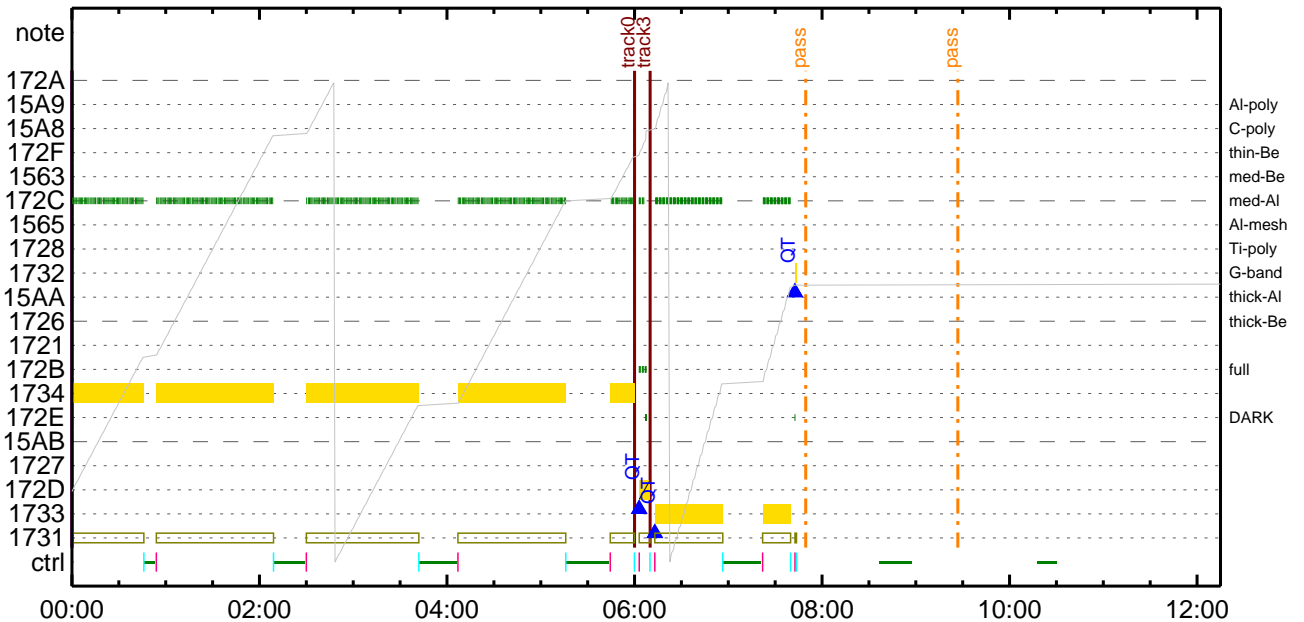
CMDI #0927 2009/11/26



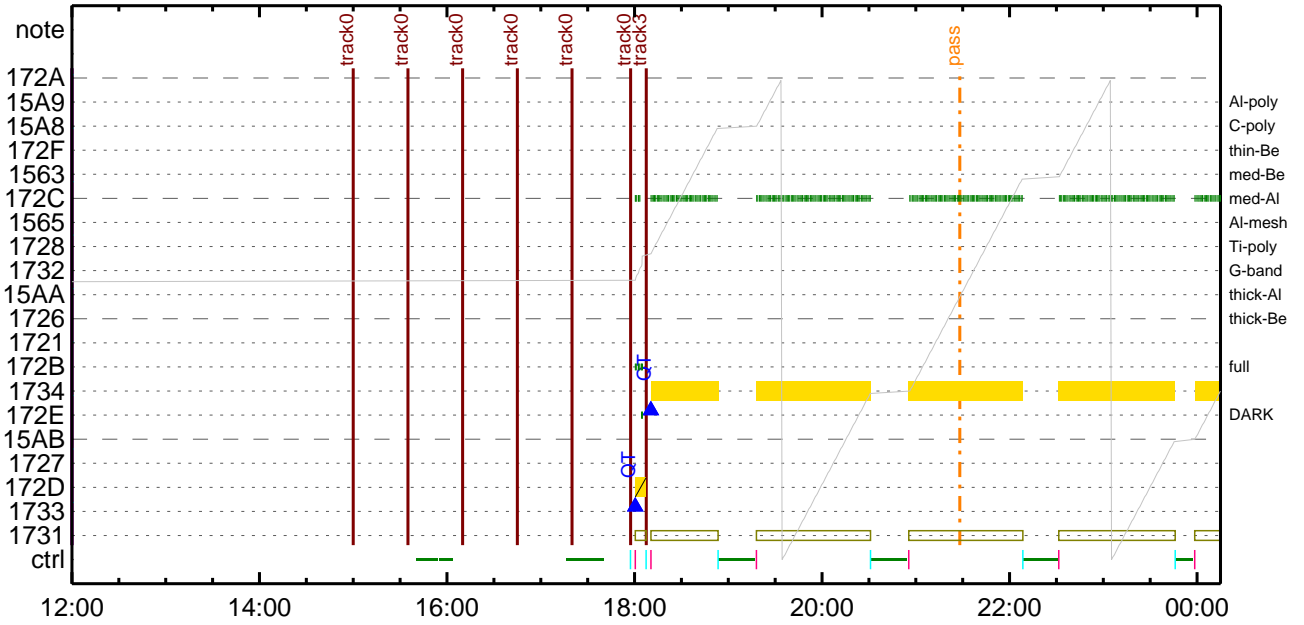
CMDI #0927 2009/11/26



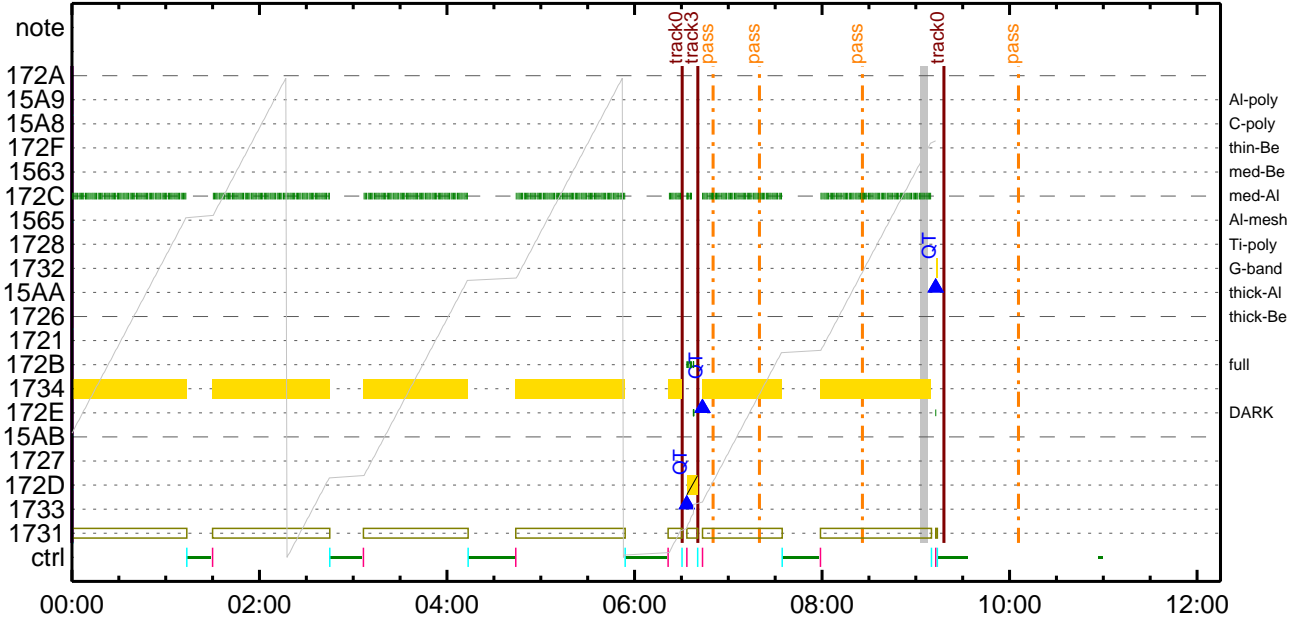
CMDI #0927 2009/11/27



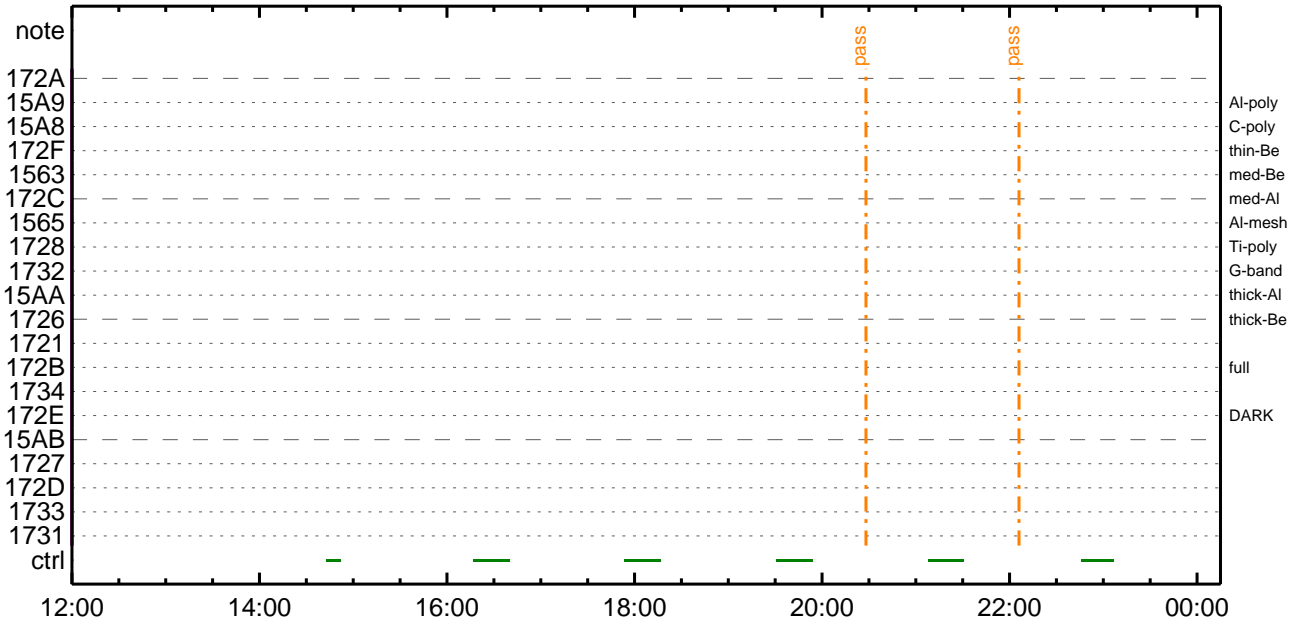
CMDI #0927 2009/11/27



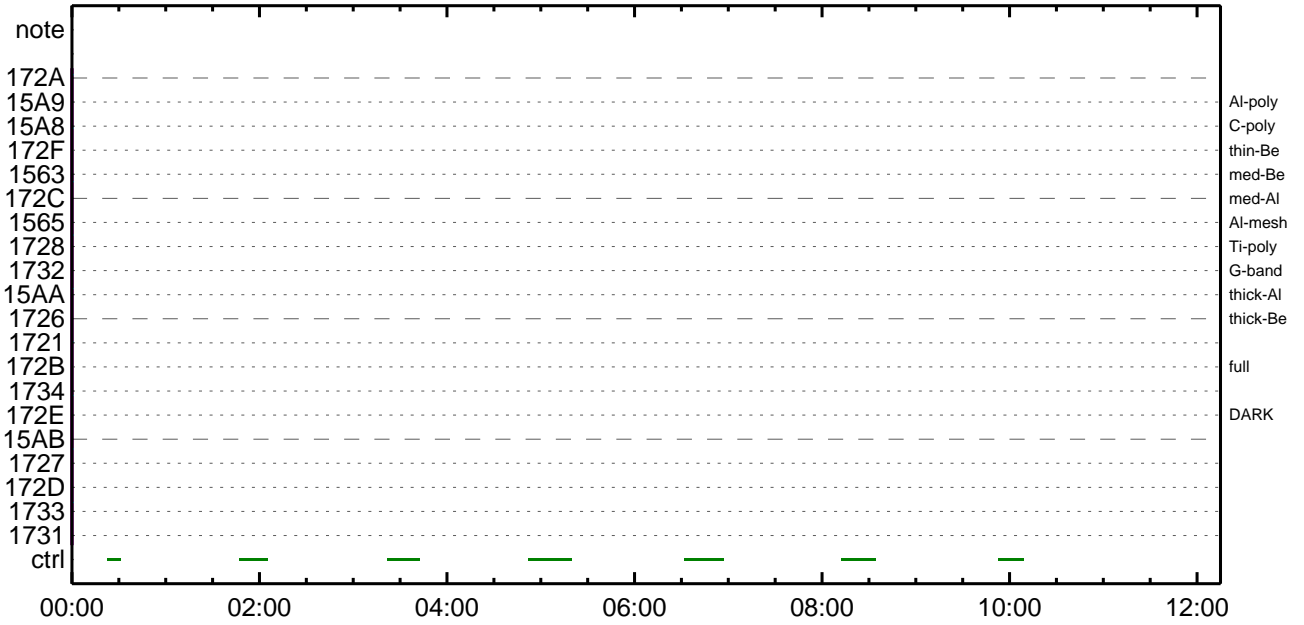
CMDI #0927 2009/11/28



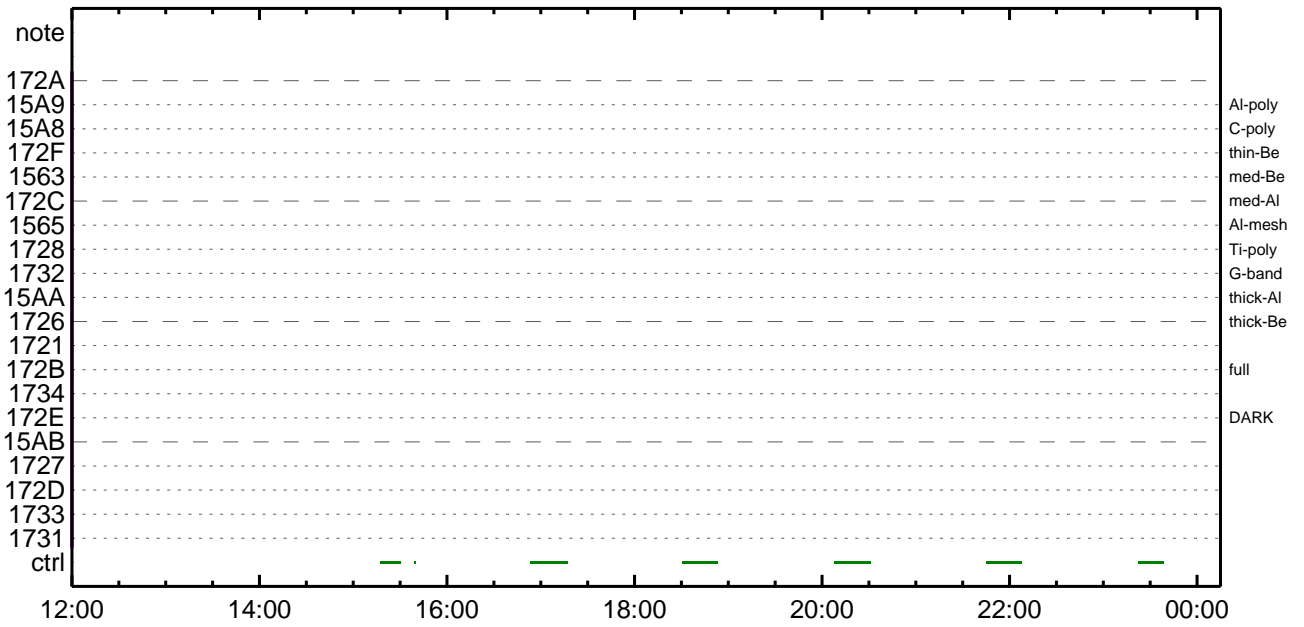
CMDI #0927 2009/11/28



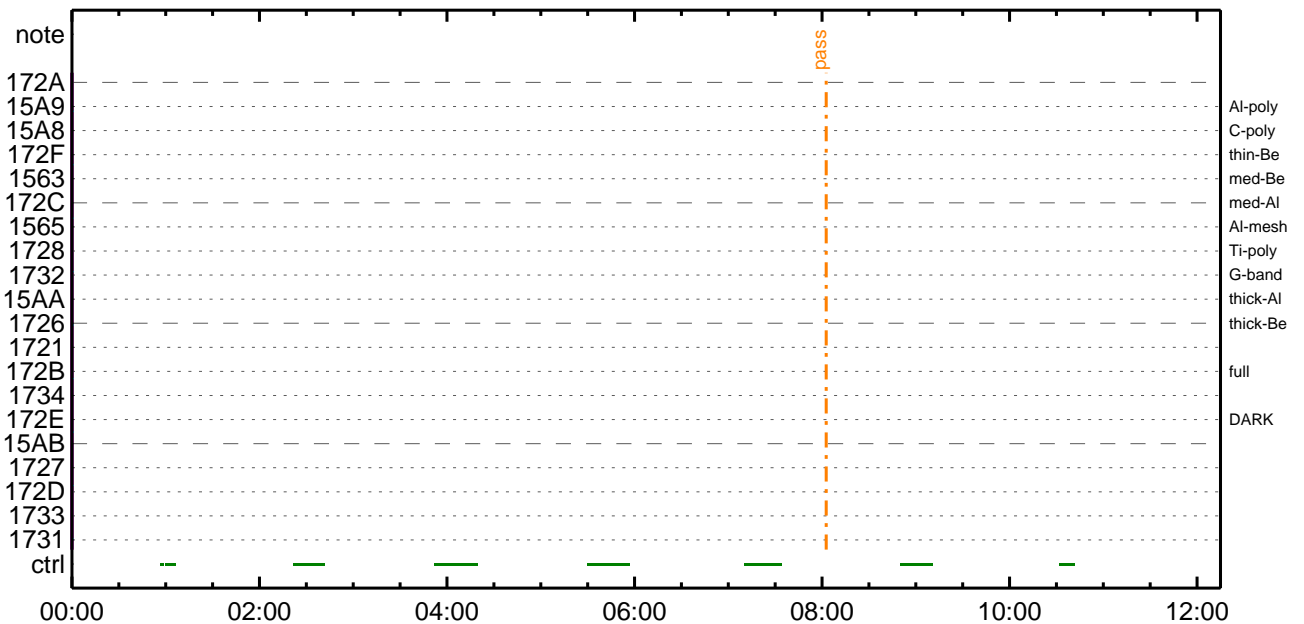
CMDI #0927 2009/11/29



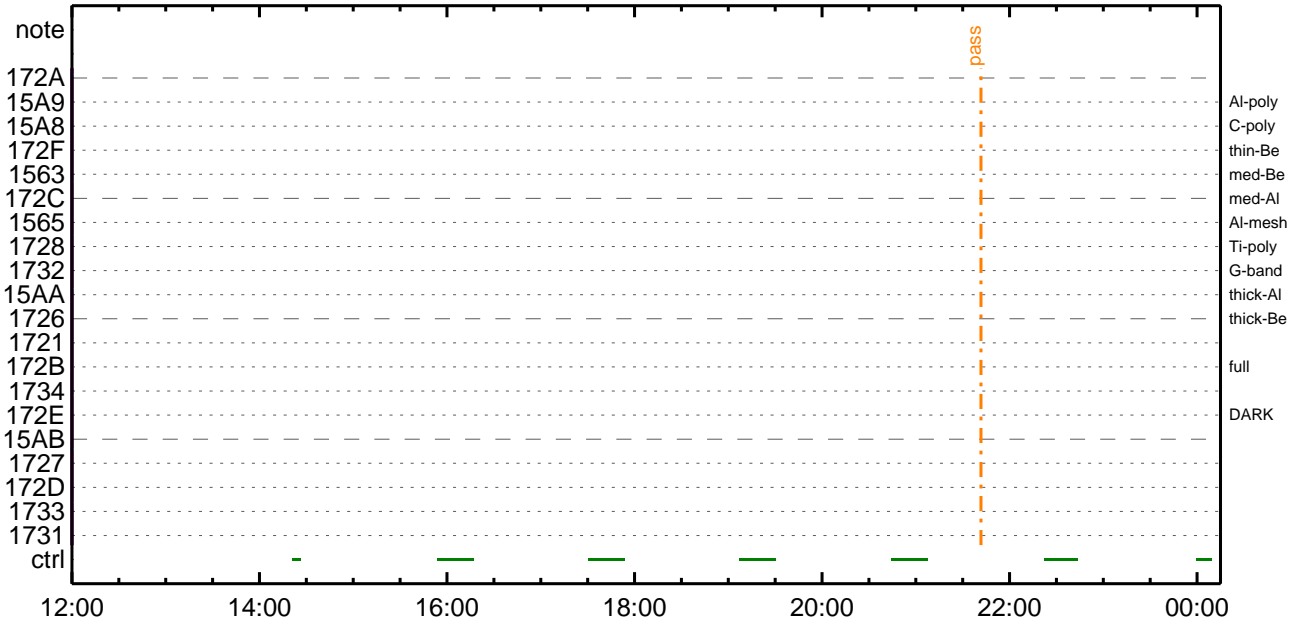
CMDI #0927 2009/11/29



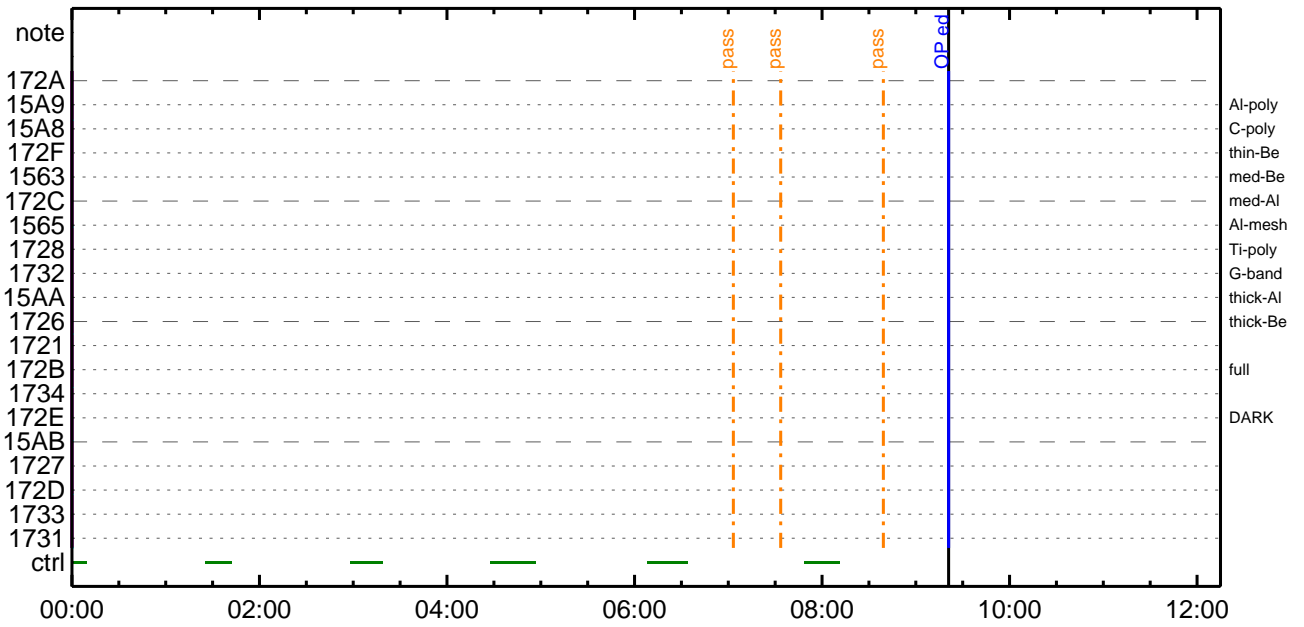
CMDI #0927 2009/11/30



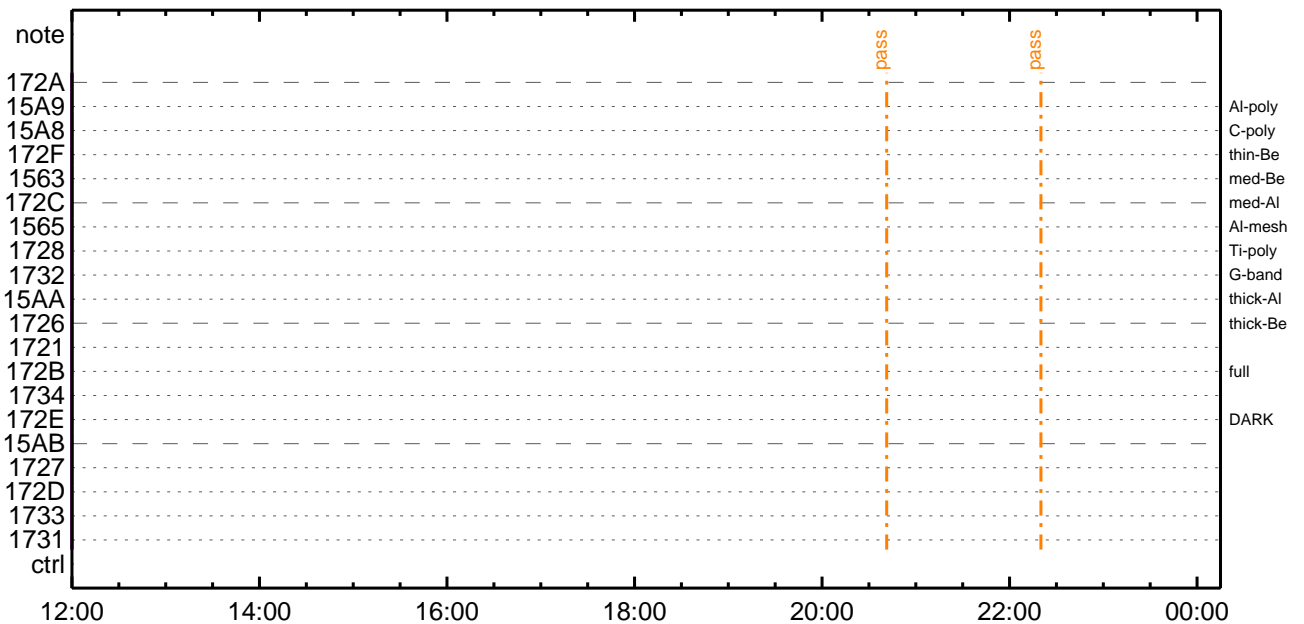
CMDI #0927 2009/11/30



CMDI #0927 2009/12/01



CMDI #0927 2009/12/01



(a) Spacecraft Operation Procedure (real-commands)

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main-998 2009-11-26 11:34:08 278 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY§YÁY~¼Å»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YFYOYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Éû¿áß·µ°È»Í×ÁÇúÍYÇYÁY×YÍ;¼YÉ;ÉÈÈ%µ·ÍÉ;ÈßÈ%ÇÖá·ú¿l¹Çú;çÀ®, ù¹áßßßáÇÁ+¿®·ßÈáááßÈ;É
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿µ;ON
0016 C. *****
0017 C. ¸°ÈÀ, Í×ÈYáßáLOSßßáÇúÏ»´Ïáá¹ÍÍ, á·; çÉÏÍ×áÈXÁÓONá¹¹ÏáÈáßáááááá;É
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C.
0025 C.
0026 C.
0027 C.
0028 C.
0029 . C. XYDYÓYÉYÍYÁY~¾ÔÁÖá·ç;ç°È²¼á¹Ïá¹á¹, ¼È¾Çááááá¹ááá;É
0030 C.
0031 . C. *****
0032 C. DR PT1 ÁÍ¼í°ÈÀ,
0033 C. *****
0034 C. ¸° RESTART;ÈPT1;Èá·á¿áááá¹Çú;ç°È²¼á¹Ïá¹á¹á¹á¹á¹;çDCBC-150áá¿¿Èáá;É
0035 C.
0036 . C. ;ãPT1°ÈÀ, ³«»Í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C.
0043 C.
0044 C.
0045 C.
0046 . C. ;ãYçYóYÉYÉÁÚÁØ;ÈÁ·ÁºóÈò;È, ááá¹°ÈÀ, °È³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C.
0050 C.
0051 C.
0052 C.
0053 C.
0054 . C. PT1°ÈÀ, á·¼«È°Áá»ßá·á·;á;ç°È²¼á¹Ïá¹á¹á¹á¹;É
0055 C. YçYóYÉYÉÁÚÁØáÁ·ÁºóÈòá·¼ááááá¹Çú;ç°È²¼á¹Ïá¹á¹á¹á¹á¹;É
0056 C.
0057 . C. *****
0058 C. DR PT2 ÁÍ¼í°ÈÀ,
0059 C. *****
0060 C. ¸° RESTART;ÈPT2;Èá·á¿áááá¹Çú;ç°È²¼á¹Ïá¹á¹á¹á¹á¹;çDCBC-151áá¿¿Èáá;É
0061 C.
0062 . C. ;ãPT2°ÈÀ, ³«»Í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C.
0069 C.
0070 C.
0071 C.
0072 . C. ;ãYçYóYÉYÉÁÚÁØ;ÈÁ·ÁºóÈò;È, ááá¹°ÈÀ, °È³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C.
0076 C.
0077 C.
0078 C.
0079 . C. *****
0080 C. DR°ÈÀ, Áá»ß;çXÁ+¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÈÀ, Áá»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C.
0087 C.
0088 C.
0089 C.
0090 C. ;ãXÁ+¿µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C.
0095 C.
    
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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-998:OP
0104 ( )
0105 S. OG og-998:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ª î»ò³ ÎÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGñî¼Ë¹ç·è² ïOKò³ ÎÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ª î»ò³ ÎÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGñî¼Ë¹ç·è² ïOKò³ ÎÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ª î»ò³ ÎÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPñî¼Ë¹ç·è² ïOKò³ ÎÇ§
0165 C.
0166 C. ***** òË²¼ò Ì¼Ã´ ¶Á°òËË¬ò Æ÷¿® (¼åµ-YAYOXx½ª è¼çò ÆÏÆòç¼ª° òè¼¿¹ çòç¼å) *****
0167 C. DHUYâ;4YE;Ë¼Y½;Yi;4YE;Ëò Ìã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADò-Á÷¿®NGñî¼Ë¹ç;ç°Ë²¼ò ÌTI-CMDÁ÷¿® Ì¼Ã¹ Ò°·òËòò³ òË; f
0180 C. òËò¿;çSETòËDUMPñî¼Ë¹ç°Ë²¼ò Ì¼Ë¹ç°Ë²¼ò òËò¿; f
0181 C.
0182 C. TIY³YpYóYËò ÆÏË¿¿ (UT)
0183 +. TI 2009-11-26 09:26:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2009-11-26 09:26:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2009-11-26 09:26:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
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0194 C.
0195 +. TI 2009-11-26 09:30:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.      ÷÷[HK1_TI_CMD_NUM]                EQ       1COUNTUP
0198 C.
0199 C. °È²¼ºãĭÄē%ĩĩñºĩŷÄŷ§ŷÄŷ~¹ãĭŭ
0200 C.      ÷÷[HK1_TI_CMD_ENA/DIS]            EQ       ENA
0201 C.      ÷÷[HK1_TI_CMD_NUM]                EQ       4
0202 C.      ÷÷[HK1_NEXT_EXEC_PIM]            EQ       DHU
0203 C.      ÷÷[HK1_NEXT_EXEC_DC]             EQ       0xB3
0204 C.
0205 C. *****
0206 C. TIĭĭ°èŷÄŷÓŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PIM_SET
0211 BC      (03 ab 03 01 02)
0212 C.      ÷÷[HK1_DMP_TOP_ADRS_1]          EQ       07
0213 C.      ÷÷[HK1_DMP_TOP_ADRS_0]          EQ       2B
0214 C.      ÷÷[HK1_DMP_BLOCK_NUM]           EQ       3
0215 C.      ÷÷[HK1_DMP_REPEAT_NUM]         EQ       0
0216 C.      ÷÷[HK1_DMA_DMP_PIM]            EQ       DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.      ÷÷[HK1_PKT_FORM_NO]             EQ       7
0220 C.      ÷÷[HK1_PKT_GEN_TIME]            EQ       0.25 s
0221 C.      ÷÷[HK1_S_TLM_BIT_RATE]         EQ       32k
0222 C.      ÷÷[HK1_X_TLM_BIT_RATE]         EQ       4M
0223 C.      ÷÷[HK1_DMP_CHK_FLG]            EQ       EXEC
0224 C.
0225 C. ŷÄŷÓŷ×½ªĭ»ºð³ĭÇ§
0226 C.      ÷÷[HK1_DMP_CHK_FLG]            EQ       NON
0227 C.
0228 C. RAM ID=TI_TBLºĩ¼È¹ç•è²İOKºð³ĭÇ§
0229 C.
0230 C. DHUŷã;¼ŷÈ;È¼ŷ½. ŷĩ;¼ŷÈ;Èºðĭãº¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.      ÷÷[HK1_PKT_FORM_NO]             EQ       2
0234 C.      ÷÷[HK1_PKT_GEN_TIME]            EQ       0.5S
0235 C.      ÷÷[HK1_S_TLM_BIT_RATE]         EQ       32K
0236 C.      ÷÷[HK1_X_TLM_BIT_RATE]         EQ       4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2009-11-26 09:30:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2009-11-26 09:30:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.      [      ] [HK1_TI_CMD_NUM]       EQ       2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2009-11-26 09:30:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 C.      [      ] [HK1_TI_CMD_NUM]       EQ       1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP ^ũÃĭºĩ»ò¼ŷºÈÄºº¹ºèDCBC•×²è *****
0265 C. (¼ã°ĩŷÓŷÄŷÈŷŷŷÈŷãŷçŷèºÈ¼ºã¼Ã»ũº¹ºè)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ŷDŷ¹.ĭ Daily±;ĩĩñºË´ºº¹ºèDCBC•×²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. ĭãLOSŷÄŷŷŷÄŷ~¼Ã»ŷ;ã
0276 C.
0277 C. ***** LOS *****
0278 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-999 2009-11-26 11:34:08 82 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÄY$YÄY-¼Ä»Û;ã
0005 C.
0006 C. YÄY$;¼Y³YFYÖYÉÄ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ÈçðÄñ•µ°®»Í×ÄÇçíYçYÄY×Yí;¼YÉ;ÈÈ%µ•íÉ;ÈÈ%°ÇÖñ•ñç¼í¹çñí;çÄ®, ùñ¹ñèñÈñçÄ+ç®ñ•ñÈñññ³ñÈ;ñ
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCS_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS_DUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCS_ORB_UPD
0044 . C.
0045 C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047 BC (21 02)
0048 . C. Verify EIS in MANUAL mode
0049 . C. Estimated OBSTBL upload time is 46s
0050 C. *****
0051 C. EIS START OBSTBL LOAD
0052 C. *****
0053 . S. RAM ram-820:EIS_OBSTBL
0054 ( )
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056 BC (07 07 07 00 00 70 00)
0057 C.
0058 C. Execute, after the success of OBSTBL upload.
0059 C. Set EIS TI-commands
0060 +. TI 2009-11-26 09:30:50.0
0061 DC 07-FC EIS_MODE_CHG_ENA
0062 BC (20)
0063 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064 C. *****
0065 C. EIS END OBSTBL LOAD
0066 C. *****
0067 C.
0068 . C. ***** MDP `ûÄîçí»ö¼YçÈÄðñ¹ñèDCBC•×²è *****
0069 C. (¼á°íYÖYÄYÈYÈYÈYáYçYÈñÈ%¼ç¼Ä»Ûñ¹ñè)
0070 . S. DC-BC dcbc-402:DCBC
0071 (MDP_known_event)
0072 C.
0073 C.
0074 . C. ***** YDÿ¹.Ï Daily±;íÑñÈ´Øñ¹ñèDCBC•×²è *****
0075 . S. DC-BC dcbc-153:DCBC
0076 (SPECIAL-CMD_DAILY_OPERATING_DCB)
0077 C.
0078 C.
0079 . C. ;ãLOSÄY$YÄY-¼Ä»Û;ã
0080 C.
0081 . C. ***** LOS *****
0082 C.
```

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*** OP Sequence for XRT ***

2009/11/26	09:40:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/11/26	09:40:56.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/11/26	09:40:58.0	XRT_FLRCTRL_DIS_410_OG [0x19a]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/11/26	09:41:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 54 00				
2009/11/26	09:41:00.5	XRT_ARS_DIS_411_OG [0x19b]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/11/26	09:43:38.5	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2009/11/26	09:43:40.5	XRT_CTRL_AUTO_447_OG [0x1bf]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/11/26	09:44:40.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/11/26	10:54:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/11/26	10:54:56.0	XRT_FOCUS_POSITION_413_OG [0x19d]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2009/11/26	10:55:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2009/11/26	10:55:16.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/11/26	10:55:18.0	XRT_FLRCTRL_DIS_410_OG [0x19a]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/11/26	10:55:20.0	XRT_ARS_DIS_411_OG [0x19b]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/11/26	10:57:58.0	XRT_QT_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2009/11/26	10:58:00.0	XRT_CTRL_AUTO_407_OG [0x197]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/11/26	14:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/11/26	14:59:56.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/11/26	14:59:58.0	XRT_FLRCTRL_DIS_410_OG [0x19a]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/11/26	15:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 54 e5 00 e5				
2009/11/26	15:00:00.5	XRT_ARS_DIS_411_OG [0x19b]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/11/26	15:02:38.5	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2009/11/26	15:02:40.5	XRT_CTRL_AUTO_447_OG [0x1bf]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/11/26	15:03:40.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/11/26	15:35:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 4e 7e e0 b5				
2009/11/26	16:10:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 3c 41 c5 82				
2009/11/26	16:45:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 00 e5 ac e6				
2009/11/26	17:20:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00 ac e6 00 e5				
2009/11/26	17:53:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/11/26	17:53:26.0	XRT_FOCUS_POSITION_408_OG [0x198]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/11/26	17:53:30.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2009/11/26	17:53:46.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/11/26	17:53:48.0	XRT_FLRCTRL_DIS_410_OG [0x19a]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/11/26	17:53:50.0	XRT_ARS_DIS_411_OG [0x19b]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/11/26	17:56:28.0	XRT_QT_PROG_SET_414_OG [0x19e]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2009/11/26	17:56:30.0	XRT_CTRL_AUTO_407_OG [0x197]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/11/26	18:17:00.0	XRT_CTRL_MANU_419_OG [0x1a3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/11/26	18:53:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/11/26	18:53:26.0	XRT_FOCUS_POSITION_413_OG [0x19d]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2009/11/26	18:53:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2009/11/26	18:53:46.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/11/26	18:53:48.0	XRT_FLRCTRL_DIS_410_OG [0x19a]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/11/26	18:53:50.0	XRT_ARS_DIS_411_OG [0x19b]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/11/26	18:56:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2009/11/26	18:56:30.0	XRT_CTRL_AUTO_407_OG [0x197]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/11/26	19:54:00.0	XRT_CTRL_MANU_419_OG [0x1a3]							

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2009/11/26	20:17:30.0	XRT_Custom_420_OG [0x1a4]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/11/26	20:18:30.0	XRT_CTRL_AUTO_416_OG [0x1a0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/11/26	21:31:30.0	XRT_CTRL_MANU_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/11/26	21:54:30.0	XRT_Custom_420_OG [0x1a4]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/11/26	21:55:30.0	XRT_CTRL_AUTO_416_OG [0x1a0]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/11/26	23:09:00.0	XRT_CTRL_MANU_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/11/26	23:29:00.0	XRT_Custom_420_OG [0x1a4]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/11/26	23:30:00.0	XRT_CTRL_AUTO_416_OG [0x1a0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/11/27	00:46:00.0	XRT_CTRL_MANU_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/11/27	00:53:00.0	XRT_Custom_420_OG [0x1a4]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/11/27	00:54:00.0	XRT_CTRL_AUTO_416_OG [0x1a0]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/11/27	02:09:00.0	XRT_CTRL_MANU_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/11/27	02:29:00.0	XRT_Custom_420_OG [0x1a4]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/11/27	02:30:00.0	XRT_CTRL_AUTO_416_OG [0x1a0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/11/27	03:42:00.0	XRT_CTRL_MANU_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/11/27	04:06:00.0	XRT_Custom_420_OG [0x1a4]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/11/27	04:07:00.0	XRT_CTRL_AUTO_416_OG [0x1a0]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/11/27	05:16:00.0	XRT_CTRL_MANU_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/11/27	05:43:30.0	XRT_Custom_420_OG [0x1a4]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/11/27	05:44:30.0	XRT_CTRL_AUTO_416_OG [0x1a0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/11/27	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/11/27	05:59:56.0	XRT_FOCUS_POSITION_408_OG [0x198]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2009/11/27	06:00:00.0	AOCs_OrE-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00 00 00 00 00				
2009/11/27	06:00:16.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/11/27	06:00:18.0	XRT_FLRCTRL_DIS_410_OG [0x19a]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/11/27	06:00:20.0	XRT_ARS_DIS_411_OG [0x19b]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/11/27	06:02:58.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2009/11/27	06:03:00.0	XRT_CTRL_AUTO_407_OG [0x197]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/11/27	06:09:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/11/27	06:09:56.0	XRT_FOCUS_POSITION_413_OG [0x19d]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2009/11/27	06:10:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03 00 00 00 00				
2009/11/27	06:10:16.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/11/27	06:10:18.0	XRT_FLRCTRL_DIS_410_OG [0x19a]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/11/27	06:10:20.0	XRT_ARS_DIS_411_OG [0x19b]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/11/27	06:12:58.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2009/11/27	06:13:00.0	XRT_CTRL_AUTO_407_OG [0x197]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/11/27	06:56:30.0	XRT_CTRL_MANU_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/11/27	07:21:00.0	XRT_Custom_420_OG [0x1a4]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/11/27	07:22:00.0	XRT_CTRL_AUTO_416_OG [0x1a0]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/11/27	07:39:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/11/27	07:39:56.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2009/11/27	07:39:58.0	XRT_FLRCTRL_DIS_410_OG [0x19a]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2009/11/27	07:40:00.0	XRT_ARS_DIS_411_OG [0x19b]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2009/11/27	07:42:38.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2009/11/27	07:42:40.0	XRT_CTRL_AUTO_447_OG [0x1bf]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2009/11/27	07:43:40.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2009/11/27	15:00:00.0	AOCs_OrE-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 ac e6 00 e5				
2009/11/27	15:35:00.0	AOCs_OrE-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 00 e5 ac e6				
2009/11/27	16:10:00.0	AOCs_OrE-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 3c 41 c5 82				
2009/11/27	16:45:00.0	AOCs_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 4e 7e e0 b5				

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2009/11/27	17:20:00.0	AOCS_OrE-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 54 e5 00 e5
2009/11/27	17:57:24.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/11/27	17:57:26.0	XRT_FOCUS_POSITION_408_OG [0x198] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2009/11/27	17:57:30.0	AOCS_OrE-point_Start_8_OG [0x09e] AOCU_NM	5	02-76	00 00 00 00 00
2009/11/27	17:57:46.0	XRT_FLD_DIS_409_OG [0x199] MDP_XRT_FLD_DIS	1	07-F0	d9
2009/11/27	17:57:48.0	XRT_FLRCTRL_DIS_410_OG [0x19a] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/11/27	17:57:50.0	XRT_ARS_DIS_411_OG [0x19b] MDP_XRT_ARS_DIS	1	07-F0	d5
2009/11/27	18:00:28.0	XRT_QT_PROG_SET_414_OG [0x19e] MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2009/11/27	18:00:30.0	XRT_CTRL_AUTO_407_OG [0x197] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/11/27	18:07:24.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/11/27	18:07:26.0	XRT_FOCUS_POSITION_413_OG [0x19d] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/11/27	18:07:30.0	AOCS_OrE-point_Start_2_OG [0x098] AOCU_NM	5	02-76	03 00 00 00 00
2009/11/27	18:07:46.0	XRT_FLD_DIS_409_OG [0x199] MDP_XRT_FLD_DIS	1	07-F0	d9
2009/11/27	18:07:48.0	XRT_FLRCTRL_DIS_410_OG [0x19a] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/11/27	18:07:50.0	XRT_ARS_DIS_411_OG [0x19b] MDP_XRT_ARS_DIS	1	07-F0	d5
2009/11/27	18:10:28.0	XRT_QT_PROG_SET_421_OG [0x1a5] MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2009/11/27	18:10:30.0	XRT_CTRL_AUTO_407_OG [0x197] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/11/27	18:53:30.0	XRT_CTRL_MANU_419_OG [0x1a3] MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/11/27	19:17:00.0	XRT_Custom_420_OG [0x1a4]			
2009/11/27	19:18:00.0	XRT_CTRL_AUTO_416_OG [0x1a0] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/11/27	20:31:00.0	XRT_CTRL_MANU_419_OG [0x1a3] MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/11/27	20:54:30.0	XRT_Custom_420_OG [0x1a4]			
2009/11/27	20:55:30.0	XRT_CTRL_AUTO_416_OG [0x1a0] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/11/27	22:08:30.0	XRT_CTRL_MANU_419_OG [0x1a3] MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/11/27	22:30:30.0	XRT_Custom_420_OG [0x1a4]			
2009/11/27	22:31:30.0	XRT_CTRL_AUTO_416_OG [0x1a0] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/11/27	23:46:00.0	XRT_CTRL_MANU_419_OG [0x1a3] MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/11/27	23:57:30.0	XRT_Custom_420_OG [0x1a4]			
2009/11/27	23:58:30.0	XRT_CTRL_AUTO_416_OG [0x1a0] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/11/28	01:13:30.0	XRT_CTRL_MANU_419_OG [0x1a3] MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/11/28	01:29:00.0	XRT_Custom_420_OG [0x1a4]			
2009/11/28	01:30:00.0	XRT_CTRL_AUTO_416_OG [0x1a0] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/11/28	02:45:00.0	XRT_CTRL_MANU_419_OG [0x1a3] MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/11/28	03:05:30.0	XRT_Custom_420_OG [0x1a4]			
2009/11/28	03:06:30.0	XRT_CTRL_AUTO_416_OG [0x1a0] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/11/28	04:13:30.0	XRT_CTRL_MANU_419_OG [0x1a3] MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/11/28	04:43:00.0	XRT_Custom_420_OG [0x1a4]			
2009/11/28	04:44:00.0	XRT_CTRL_AUTO_416_OG [0x1a0] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/11/28	05:54:00.0	XRT_CTRL_MANU_419_OG [0x1a3] MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/11/28	06:20:30.0	XRT_Custom_420_OG [0x1a4]			
2009/11/28	06:21:30.0	XRT_CTRL_AUTO_416_OG [0x1a0] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/11/28	06:30:24.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/11/28	06:30:26.0	XRT_FOCUS_POSITION_408_OG [0x198] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2009/11/28	06:30:30.0	AOCS_OrE-point_Start_8_OG [0x09e] AOCU_NM	5	02-76	00 00 00 00 00
2009/11/28	06:30:46.0	XRT_FLD_DIS_409_OG [0x199] MDP_XRT_FLD_DIS	1	07-F0	d9
2009/11/28	06:30:48.0	XRT_FLRCTRL_DIS_410_OG [0x19a] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/11/28	06:30:50.0	XRT_ARS_DIS_411_OG [0x19b] MDP_XRT_ARS_DIS	1	07-F0	d5
2009/11/28	06:33:28.0	XRT_QT_PROG_SET_414_OG [0x19e] MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2009/11/28	06:33:30.0	XRT_CTRL_AUTO_407_OG [0x197] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/11/28	06:40:24.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/11/28	06:40:26.0	XRT_FOCUS_POSITION_413_OG [0x19d]			

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2009/11/28	06:40:30.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
			AOCU_NM	5	02-76	03	00	00	00
2009/11/28	06:40:46.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/11/28	06:40:48.0	XRT_FLRCTRL_DIS_410_OG [0x19a]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/11/28	06:40:50.0	XRT_ARS_DIS_411_OG [0x19b]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2009/11/28	06:43:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07		
2009/11/28	06:43:30.0	XRT_CTRL_AUTO_407_OG [0x197]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/11/28	07:34:30.0	XRT_CTRL_MANU_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/11/28	07:58:00.0	XRT_Custom_420_OG [0x1a4]							
2009/11/28	07:59:00.0	XRT_CTRL_AUTO_416_OG [0x1a0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/11/28	09:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/11/28	09:10:02.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2009/11/28	09:10:04.0	XRT_FLRCTRL_DIS_410_OG [0x19a]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2009/11/28	09:10:06.0	XRT_ARS_DIS_411_OG [0x19b]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2009/11/28	09:12:44.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2009/11/28	09:12:46.0	XRT_CTRL_AUTO_447_OG [0x1bf]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2009/11/28	09:13:46.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2009/11/28	09:18:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00	00	00	00