

XRT Timeline to be uploaded on 2009/12/17

Period: 2009/12/17 10:18:00 - 2009/12/21 10:51:00

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Normal mode

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XOB #1745: AR Dynamics - Med-AI - 1x1 - 512x512 - AEC 5											
Term	Pointing (x, y)		Comment								
12/17 10:31:02 - 12/17 17:49:54	Track (341.3, 496.9)	[Ⓢ] 12/17 10:28:00	# OP start + 10min Track Flaring active region AR 11035							
12/17 18:03:02 - 12/18 05:59:54	Track (394.9, 496.8)	[Ⓢ] 12/17 18:00:00	# AR11035 cont.							
12/18 06:13:02 - 12/18 10:09:54	Track (477.2, 496.4)	[Ⓢ] 12/18 06:10:00	# AR11035 cont.							
PROG= 02 1-time(s)											
└─ Subr= 1 1-time(s) 90.0sec											
└─┬─ Seqn= 10 1-time(s) 2.0sec											
	med-AI/Open	med-AI/thick-AI close	Safe	Norm	16.0s	Obs	1x1	512x512 (1024, 1024)	Q=95	5	0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

XOB #1744: Synoptic Q95 med-AI (0.5s, 2s, 8s) + Dark (2x2 4x4 8x8 and cross strips)											
Term	Pointing (x, y)		Comment								
12/17 17:53:00 - 12/17 17:59:54	Fixed (0.0, 0.0)	synoptic, shifted -10.0 min								
12/18 06:03:00 - 12/18 06:09:54	Fixed (0.0, 0.0)	synoptic								
PROG= 17 1-time(s)											
└─ Subr= 1 1-time(s) 2.0sec											
└─┬─ Seqn= 7 2-time(s) 2.0sec											
	med-AI/Open	med-AI/thick-AI close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec	
	med-AI/Open	med-AI/thick-AI close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec	
	med-AI/Open	med-AI/thick-AI close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec	
└─┬─ Subr= 2 1-time(s) 2.0sec											
└─┬─┬─ Seqn= 90 1-time(s) 2.0sec											
	med-AI/thick-Be	med-AI/thick-Be close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0 0 2.0sec	
	med-AI/thick-Be	med-AI/thick-Be close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0 0 2.0sec	
	med-AI/thick-Be	med-AI/thick-Be close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0 0 2.0sec	
	med-AI/thick-Be	med-AI/thick-Be close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0 0 2.0sec	
	med-AI/thick-Be	med-AI/thick-Be close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0 0 2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

XOB #1732: Dark - Med-AI+Thick-Be - 8x8 - 512x512											
Term	Pointing (x, y)		Comment								
12/18 10:12:40 - 12/18 10:13:40	Track (477.2, 496.4)	[Ⓢ] 12/18 06:10:00	# AR11035 cont.							
PROG= 12 1-time(s)											
└─ Subr= 1 1-time(s) 2.0sec											
└─┬─ Seqn= 91 1-time(s) 2.0sec											
	med-AI/thick-Be	med-AI/thick-Be close	Safe	Dark	500ms	Obs	8x8	512x512 (1024, 1024)	DPCM	0 0 2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

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Flare mode

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NOT USED

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Active Region Search

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NOT USED

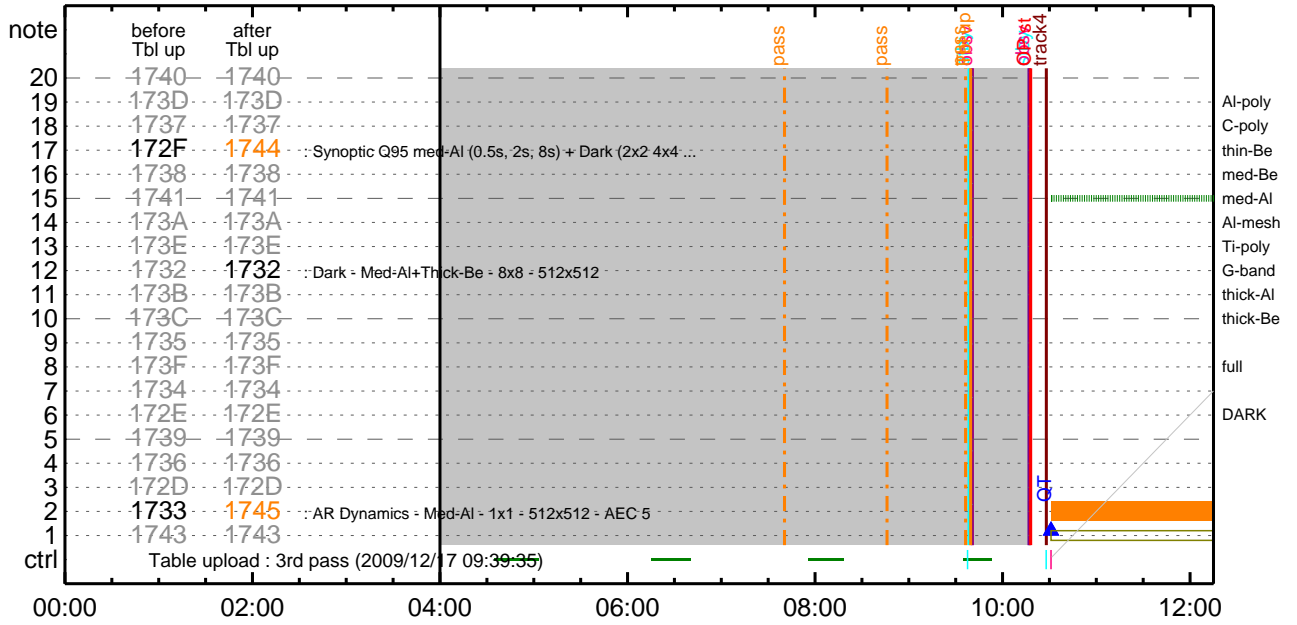
* * * * *

Flare Detection

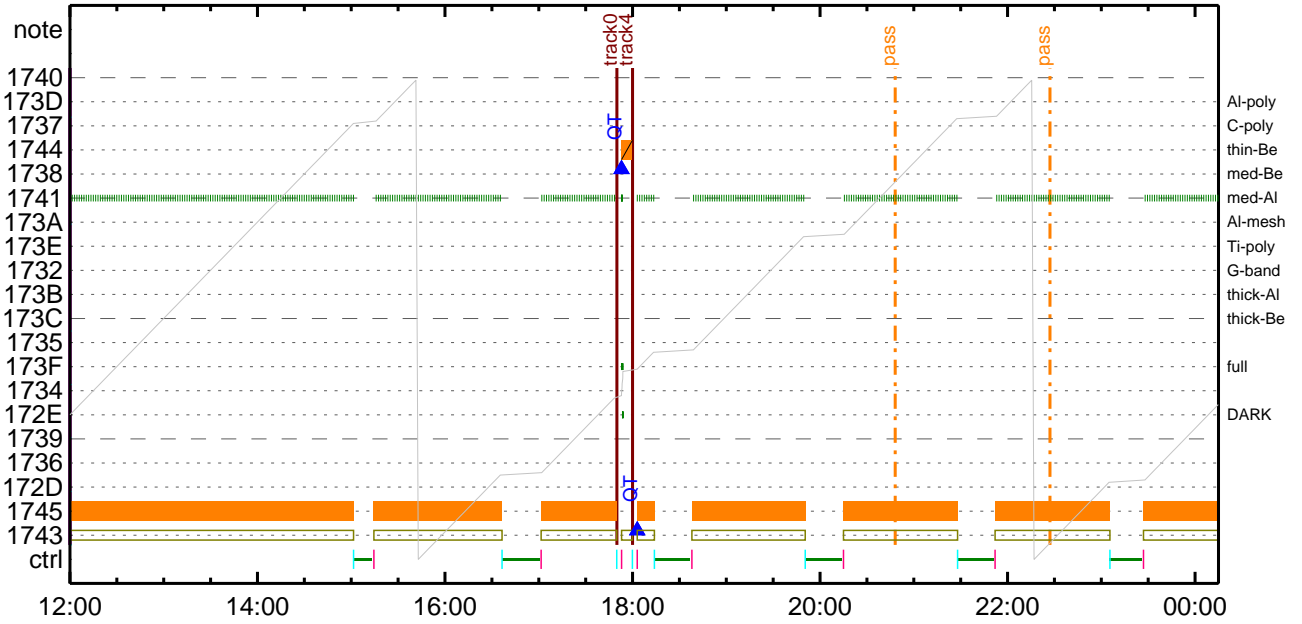
* * * * *

NOT USED

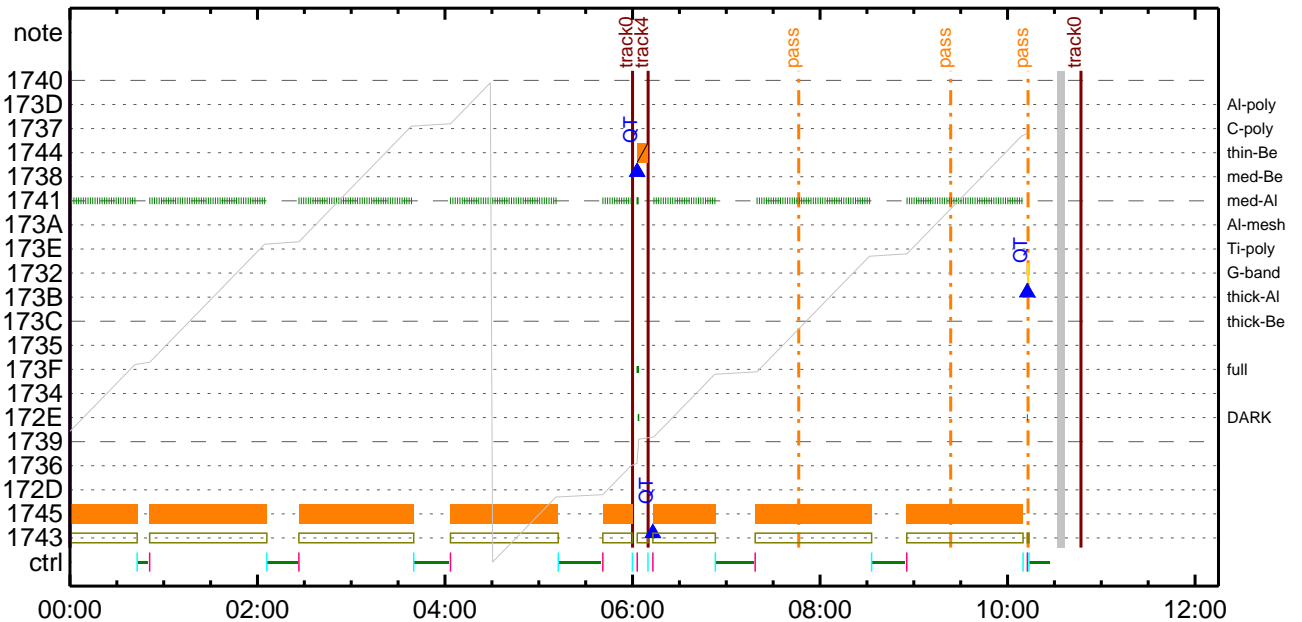
CMDI #0972 2009/12/17



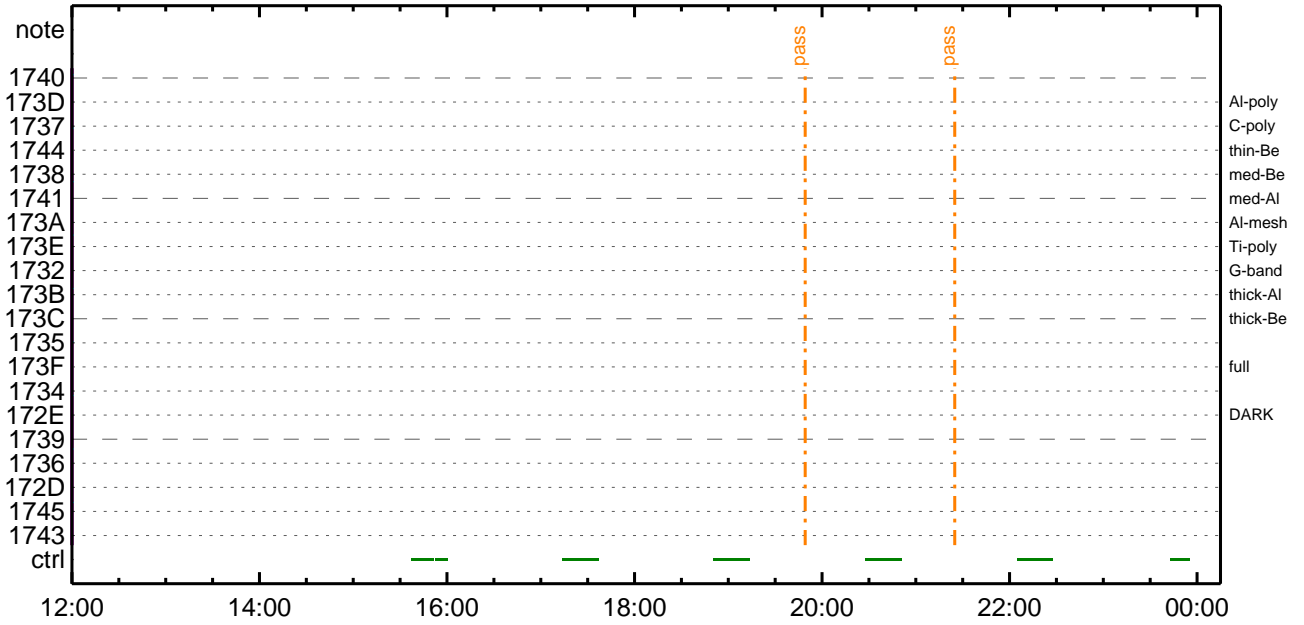
CMDI #0972 2009/12/17



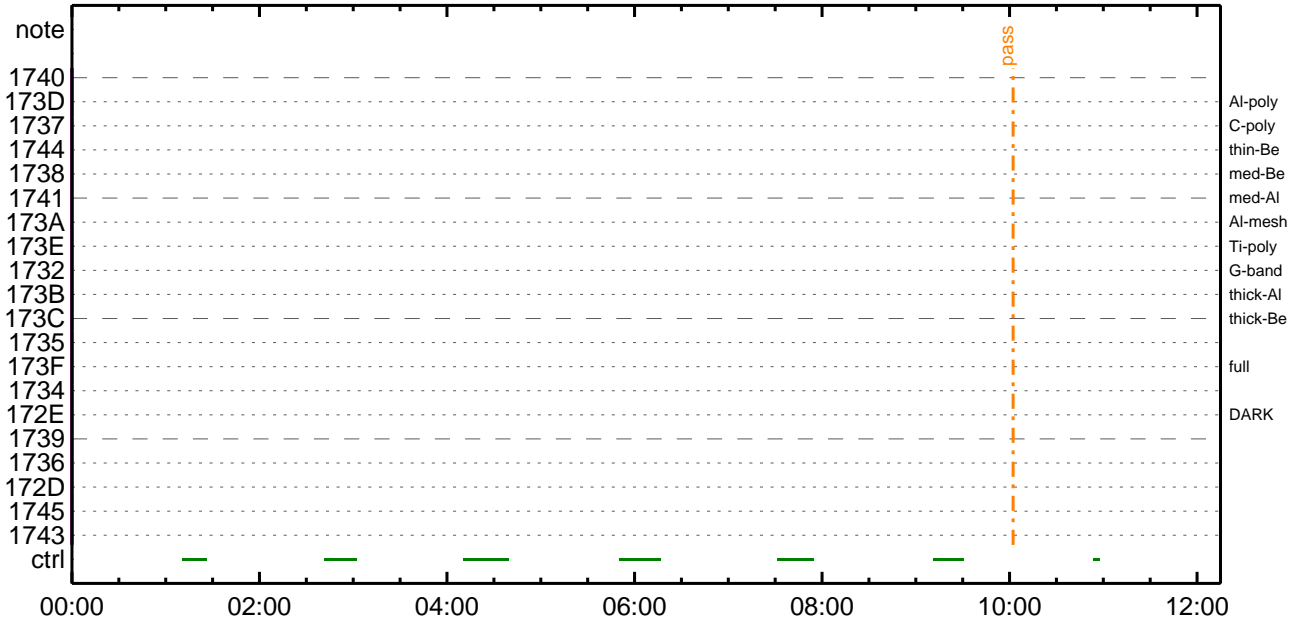
CMDI #0972 2009/12/18



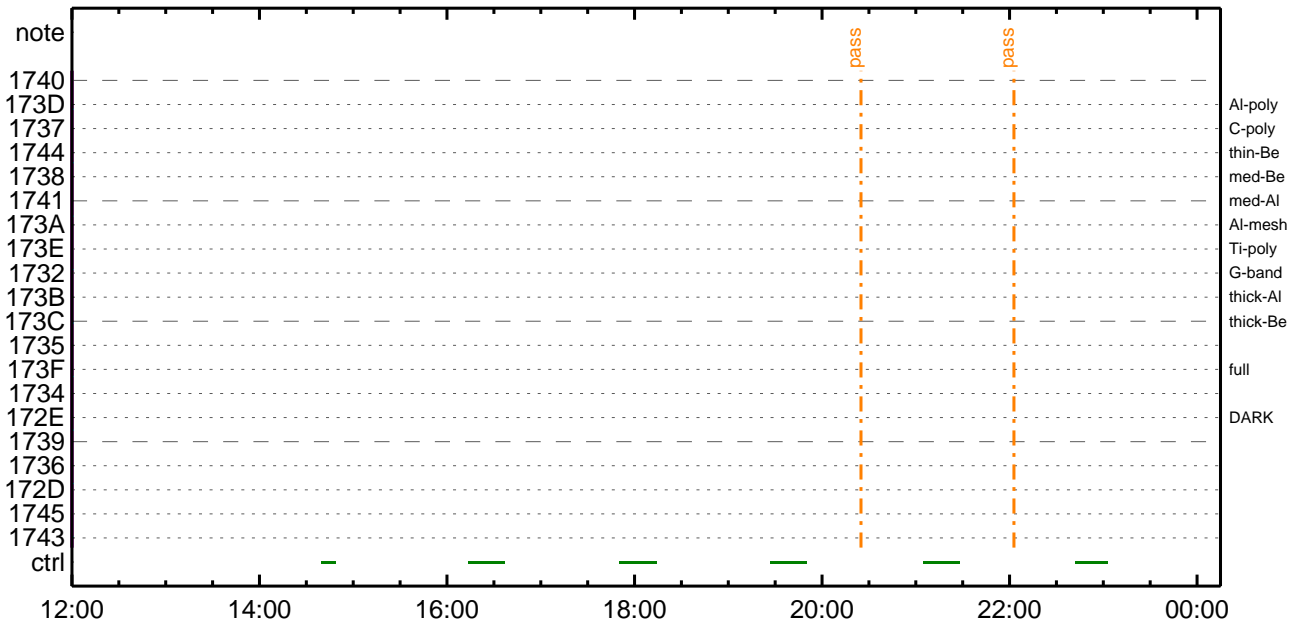
CMDI #0972 2009/12/18



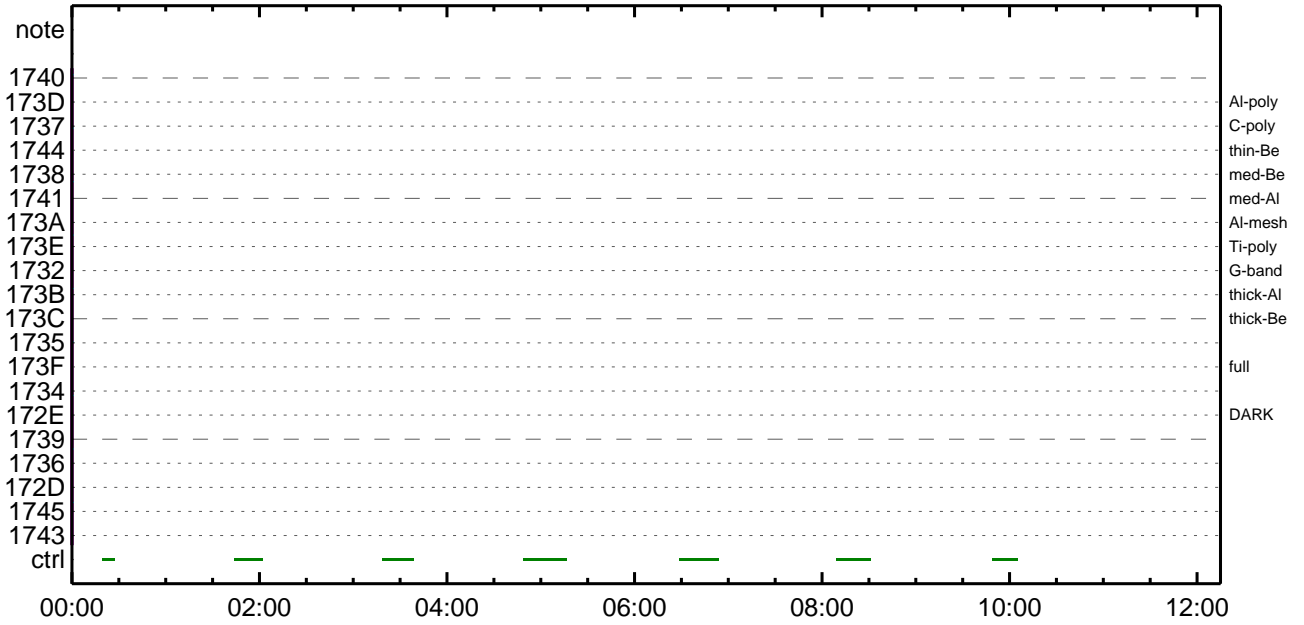
CMDI #0972 2009/12/19



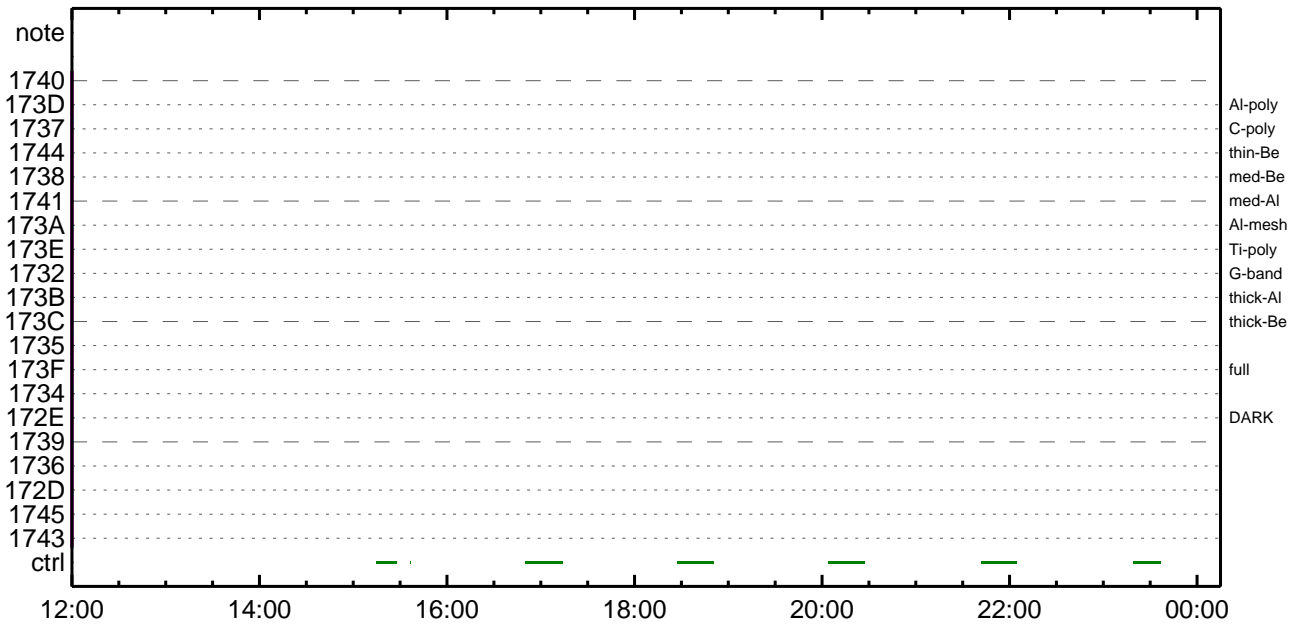
CMDI #0972 2009/12/19



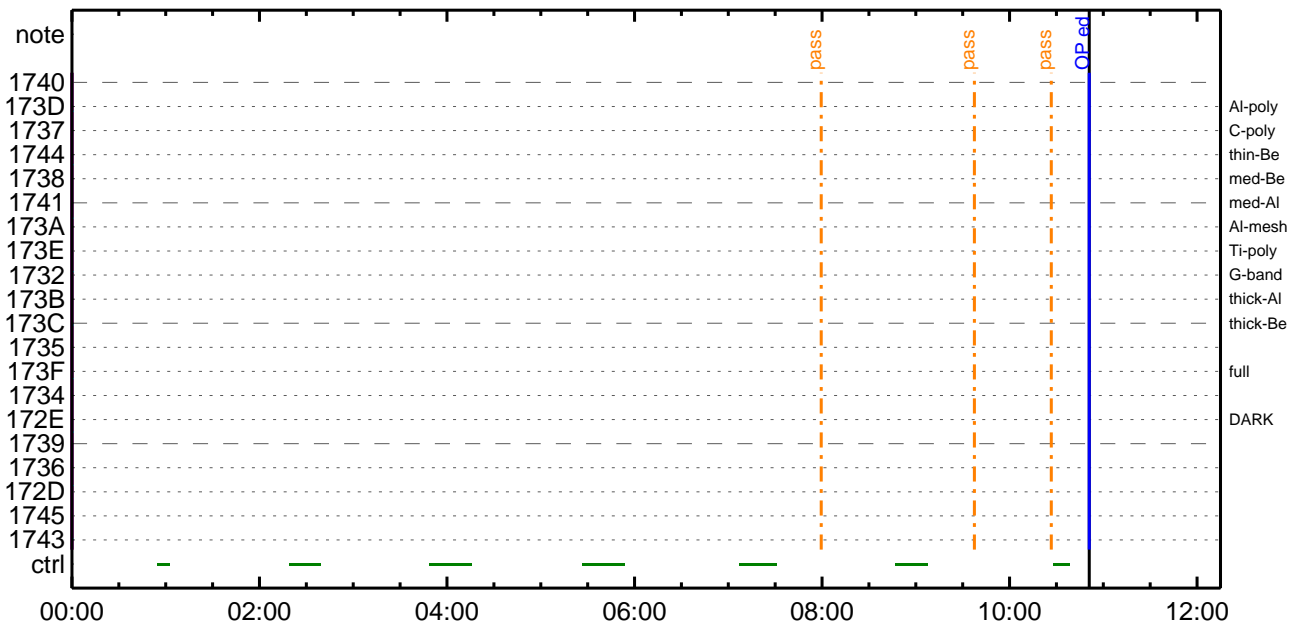
CMDI #0972 2009/12/20



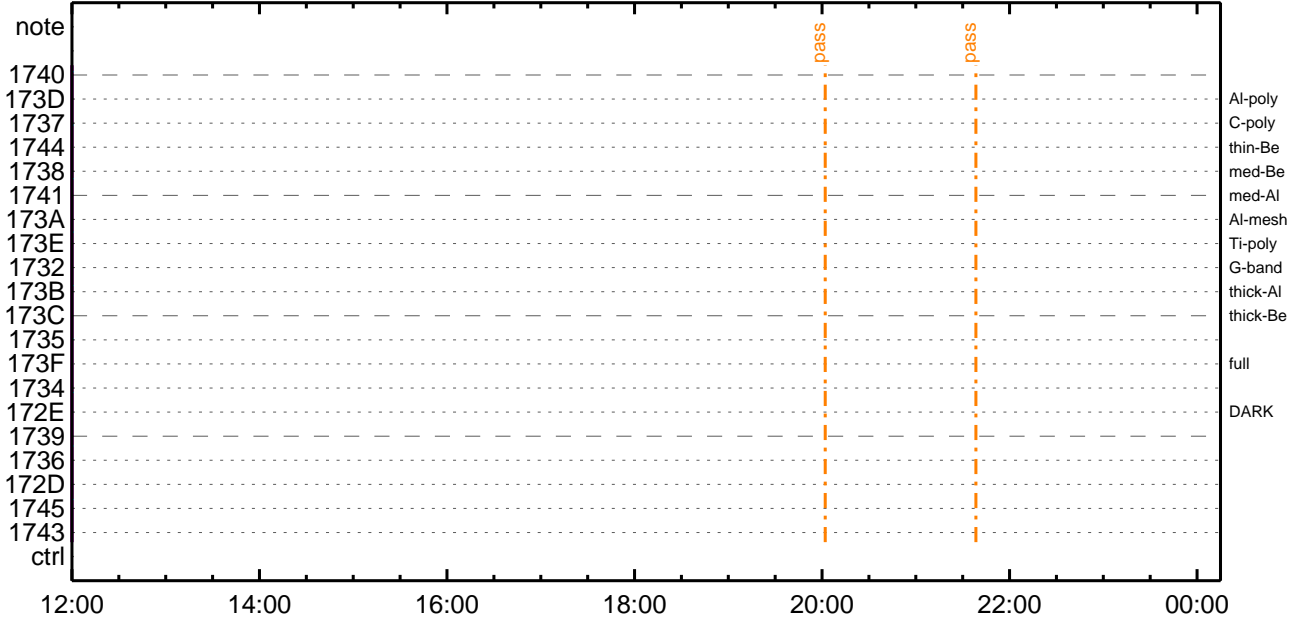
CMDI #0972 2009/12/20



CMDI #0972 2009/12/21



CMDI #0972 2009/12/21



(a) Spacecraft Operation Procedure (real-commands)

main-051 2009-12-17 12:03:18 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY\$YÁY~¼Ä»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF6YÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Ë¿¿ðÁð•µ°Æ»Í×ÁÇ¿ÍYçYÁY×Yí;¼YÉ;ËËè¼µ•íÉ;Ëè¼°ÇÓð•¿¼í¹ç¿Í;çÁ®, ùª¹ðèðßçÁ+¿®ð•ðÈððð³ðÈ; Æ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ; | YÁY6Y×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ã
0019 . S. OP op-051:OP
0020 ()
0021 . S. OG og-051:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPÎ°èYÁY6Y×;ã
0025 C. NMOG(0x200000-0x207FFF; § 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÁY6Y×¼ªª î»ðð³ ÎÇ§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGðÎ¼È¹ç•è² ÎOKðð³ ÎÇ§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF; § 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÁY6Y×¼ªª î»ðð³ ÎÇ§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGðÎ¼È¹ç•è² ÎOKðð³ ÎÇ§
0062 C.
0063 C. NMOG(0x210000-0x2100FF; § 256byte)+OP(0x210100-0x2141FF; 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÁY6Y×¼ªª î»ðð³ ÎÇ§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OPðÎ¼È¹ç•è² ÎOKðð³ ÎÇ§
0081 C.
0082 . C. ***** øÈ²¼ø¿¼Ä´ ¶¼°øÈÈ-øÀ+¿® (¼åµ-YÁY6Y×¼è¼çððÄÓÄæç¼ª°µ°ðÈ¼¿¹çøçðâ) *****
0083 C. DHUYã;¼YÉ; È¼Y¼; Yí;¼YÉ; ËøðÍá¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE | § OPOG UPLOADø-Ä+¿®NGø¼¿¹¹ç; ç°È²¼ø¿¼ø¿¼TI-CMDÄ+¿®¼¿¹¹Ôø•ðÈððð³ðÈ; Æ

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0096 C.                0B0Z;CSET0EDUMPAIÆ±°iYNY¹aÇ¹Ôa|a³aE;E
0097 C.
0098 . C. TII³YBYÖYÉ00dÅDi¿(UT)
0099 +. TI 2009-12-17 10:13:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0102 C.
0103 +. TI 2009-12-17 10:13:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0106 C.
0107 +. TI 2009-12-17 10:13:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0110 C.
0111 +. TI 2009-12-17 10:17:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0114 C.
0115 C.  °E²¼aIÄê%îÍÑaÎYÁY$YÄY-¹àÛ
0116 C.                ÇÇ[HK1_TI_CMD_ENA/DIS]             EQ      ENA
0117 C.                ÇÇ[HK1_TI_CMD_NUM]               EQ      4
0118 C.                ÇÇ[HK1_NEXT_EXEC_PIM]             EQ      DHU
0119 C.                ÇÇ[HK1_NEXT_EXEC_DC]             EQ      0xB3
0120 C.
0121 . C. *****
0122 C. TIIÎ°èYÄYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC                (03 ab 03 01 02)
0128 C.                ÇÇ[HK1_DMP_TOP_ADRS_1]            EQ      07
0129 C.                ÇÇ[HK1_DMP_TOP_ADRS_0]            EQ      2B
0130 C.                ÇÇ[HK1_DMP_BLOCK_NUM]             EQ      3
0131 C.                ÇÇ[HK1_DMP_REPEAT_NUM]           EQ      0
0132 C.                ÇÇ[HK1_DMA_DMP_PIM]             EQ      DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC                (07 0b f8)
0135 C.                ÇÇ[HK1_PKT_FORM_NO]              EQ      7
0136 C.                ÇÇ[HK1_PKT_GEN_TIME]             EQ      0.25 s
0137 C.                ÇÇ[HK1_S_TLM_BIT_RATE]           EQ      32k
0138 C.                ÇÇ[HK1_X_TLM_BIT_RATE]          EQ      4M
0139 C.                ÇÇ[HK1_DMP_CHK_FLG]             EQ      EXEC
0140 C.
0141 . C. YÄYÖY×¼³¹Î»0ð³ÎÇ$
0142 C.                ÇÇ[HK1_DMP_CHK_FLG]             EQ      NON
0143 C.
0144 . C. RAM ID=TI_TBL0I%È¹Ç•ê²ÌOK0ð³ÎÇ$
0145 C.
0146 . C. DHUYâ;¼YÉ;È¼Y¼, Yî;¼YÈ;Ë0ð³Íá¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC                (02 0a f8)
0149 C.                ÇÇ[HK1_PKT_FORM_NO]              EQ      2
0150 C.                ÇÇ[HK1_PKT_GEN_TIME]             EQ      0.5S
0151 C.                ÇÇ[HK1_S_TLM_BIT_RATE]           EQ      32K
0152 C.                ÇÇ[HK1_X_TLM_BIT_RATE]          EQ      4M
0153 C.
0154 . C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2009-12-17 10:17:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC                (21 02)
0163 +. TI 2009-12-17 10:17:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC                (22)
0166 . C.                [ ] [HK1_TI_CMD_NUM]            EQ      2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C. *****
0171 C. SOT TI command set
0172 C. *****
0173 C. Execute, after the success of OP upload.
0174 +. TI 2009-12-17 10:17:16.0
0175 DC 07-F0 MDP_SOT_MODE_STBY
0176 BC                (41)
0177 . C. -----
0178 C. HK1_TI_CMD_NUM                = 1 CNTUP [ ]
0179 C. -----
0180 C. ***** SOT END *****
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2009-12-17 10:17:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC                (c3)
0187 . C.                [ ] [HK1_TI_CMD_NUM]            EQ      1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP `úÄÎ0Î»ö¼Y0ÈÄ0¹0èDCBC•×²è *****
0192 C. (%â°iYÖYÄYÈYÏYÉYÁYÇYê0E¼0a¼Ä»Û0¹0é)
0193 . S. DC-BC dcbc-402:DCBC
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```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥D¥!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```


(a) Spacecraft Operation Procedure (real-commands)

```
main-053 2009-12-17 12:03:22 118 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãÁ•µ°È»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ÈÈèµ•íÉ;ÈÈÈ¼°ÇÓã•¿¿¼í¹çãÍ;çÀ®, ùã¹ãèãÈãÇÁ+¿®ã•èããã³ãÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-289:MDP_OBS_S
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2009-12-17 10:17:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR ____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 06 80 80 08 08)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 07 80 80 10 10)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 08 85 83 08 08)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 09 80 80 20 20)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 0a 80 80 20 08)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0b 80 80 08 20)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0f 80 80 06 06)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 10 80 80 04 04)
0088 + DC 07-F0 MDP_XRT_AEC_RESET
0089 BC (d0)
0090 . C. ----- Success Verify ? OK / NG ____
0091 C.
0092 C.
0093 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0094 C.
0095 +. DC 07-F0 MDP_XRT_MODE_OBSV
```


Dec 17, 09 12:03

XRT_OGLIST_0972.chk

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*** OP Sequence for XRT ***

2009/12/17	10:27:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/12/17	10:27:56.0	XRT_AEC_RESET_403_OG [0x193]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2009/12/17	10:27:58.0	XRT_FOCUS_POSITION_413_OG [0x19d]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/12/17	10:28:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	04 00 00 00 00
2009/12/17	10:28:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/12/17	10:28:20.0	XRT_FLRCTRL_DIS_410_OG [0x19a]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/12/17	10:28:22.0	XRT_ARS_DIS_411_OG [0x19b]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/12/17	10:31:00.0	XRT_QT_PROG_SET_418_OG [0x1a2]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2009/12/17	10:31:02.5	XRT_CTRL_AUTO_407_OG [0x197]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/12/17	15:01:30.0	XRT_CTRL_MANU_419_OG [0x1a3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/12/17	15:13:30.0	XRT_Custom_420_OG [0x1a4]			
2009/12/17	15:14:30.0	XRT_CTRL_AUTO_416_OG [0x1a0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/12/17	16:36:30.0	XRT_CTRL_MANU_419_OG [0x1a3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/12/17	17:00:30.0	XRT_Custom_420_OG [0x1a4]			
2009/12/17	17:01:30.0	XRT_CTRL_AUTO_416_OG [0x1a0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/12/17	17:49:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/12/17	17:49:56.0	XRT_FOCUS_POSITION_408_OG [0x198]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2009/12/17	17:50:00.0	AOCS_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2009/12/17	17:50:16.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/12/17	17:50:18.0	XRT_FLRCTRL_DIS_410_OG [0x19a]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/12/17	17:50:20.0	XRT_ARS_DIS_411_OG [0x19b]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/12/17	17:52:58.0	XRT_QT_PROG_SET_430_OG [0x1ae]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2009/12/17	17:53:00.0	XRT_CTRL_AUTO_407_OG [0x197]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/12/17	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/12/17	17:59:56.0	XRT_AEC_RESET_403_OG [0x193]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2009/12/17	17:59:58.0	XRT_FOCUS_POSITION_413_OG [0x19d]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2009/12/17	18:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	04 00 00 00 00
2009/12/17	18:00:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2009/12/17	18:00:20.0	XRT_FLRCTRL_DIS_410_OG [0x19a]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2009/12/17	18:00:22.0	XRT_ARS_DIS_411_OG [0x19b]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2009/12/17	18:03:00.0	XRT_QT_PROG_SET_418_OG [0x1a2]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2009/12/17	18:03:02.5	XRT_CTRL_AUTO_407_OG [0x197]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/12/17	18:14:00.0	XRT_CTRL_MANU_419_OG [0x1a3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/12/17	18:37:00.0	XRT_Custom_420_OG [0x1a4]			
2009/12/17	18:38:00.0	XRT_CTRL_AUTO_416_OG [0x1a0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/12/17	19:50:30.0	XRT_CTRL_MANU_419_OG [0x1a3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/12/17	20:14:00.0	XRT_Custom_420_OG [0x1a4]			
2009/12/17	20:15:00.0	XRT_CTRL_AUTO_416_OG [0x1a0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/12/17	21:28:00.0	XRT_CTRL_MANU_419_OG [0x1a3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/12/17	21:51:00.0	XRT_Custom_420_OG [0x1a4]			
2009/12/17	21:52:00.0	XRT_CTRL_AUTO_416_OG [0x1a0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/12/17	23:05:30.0	XRT_CTRL_MANU_419_OG [0x1a3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/12/17	23:26:00.0	XRT_Custom_420_OG [0x1a4]			
2009/12/17	23:27:00.5	XRT_CTRL_AUTO_416_OG [0x1a0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/12/18	00:43:00.0	XRT_CTRL_MANU_419_OG [0x1a3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/12/18	00:50:00.0	XRT_Custom_420_OG [0x1a4]			
2009/12/18	00:51:00.0	XRT_CTRL_AUTO_416_OG [0x1a0]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2009/12/18	02:06:00.0	XRT_CTRL_MANU_419_OG [0x1a3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2009/12/18	02:25:30.0	XRT_Custom_420_OG [0x1a4]			
2009/12/18	02:26:30.0	XRT_CTRL_AUTO_416_OG [0x1a0]			

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2009/12/18	03:40:00.0	XRT_CTRL_MANU_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/12/18	04:02:30.0	XRT_Custom_420_OG [0x1a4]					
2009/12/18	04:03:30.0	XRT_CTRL_AUTO_416_OG [0x1a0]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/12/18	05:12:30.0	XRT_CTRL_MANU_419_OG [0x1a3]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/12/18	05:40:00.0	XRT_Custom_420_OG [0x1a4]					
2009/12/18	05:41:00.0	XRT_CTRL_AUTO_416_OG [0x1a0]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/12/18	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/12/18	05:59:56.0	XRT_FOCUS_POSITION_408_OG [0x198]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2009/12/18	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2009/12/18	06:00:16.0	XRT_FLD_DIS_409_OG [0x199]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/12/18	06:00:18.0	XRT_FLRCTRL_DIS_410_OG [0x19a]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/12/18	06:00:20.0	XRT_ARS_DIS_411_OG [0x19b]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/12/18	06:02:58.0	XRT_QT_PROG_SET_430_OG [0x1ae]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2009/12/18	06:03:00.0	XRT_CTRL_AUTO_407_OG [0x197]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/12/18	06:09:54.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/12/18	06:09:56.0	XRT_AEC_RESET_403_OG [0x193]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2009/12/18	06:09:58.0	XRT_FOCUS_POSITION_413_OG [0x19d]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2009/12/18	06:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	04 00 00 00 00	
2009/12/18	06:10:18.0	XRT_FLD_DIS_409_OG [0x199]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/12/18	06:10:20.0	XRT_FLRCTRL_DIS_410_OG [0x19a]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/12/18	06:10:22.0	XRT_ARS_DIS_411_OG [0x19b]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/12/18	06:13:00.0	XRT_QT_PROG_SET_418_OG [0x1a2]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 02	
2009/12/18	06:13:02.0	XRT_CTRL_AUTO_407_OG [0x197]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/12/18	06:53:00.0	XRT_CTRL_MANU_419_OG [0x1a3]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/12/18	07:17:30.0	XRT_Custom_420_OG [0x1a4]					
2009/12/18	07:18:30.0	XRT_CTRL_AUTO_416_OG [0x1a0]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/12/18	08:33:00.0	XRT_CTRL_MANU_419_OG [0x1a3]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/12/18	08:54:30.0	XRT_Custom_420_OG [0x1a4]					
2009/12/18	08:55:30.0	XRT_CTRL_AUTO_416_OG [0x1a0]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/12/18	10:09:54.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/12/18	10:09:56.0	XRT_FLD_DIS_409_OG [0x199]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2009/12/18	10:09:58.0	XRT_FLRCTRL_DIS_410_OG [0x19a]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2009/12/18	10:10:00.0	XRT_ARS_DIS_411_OG [0x19b]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2009/12/18	10:12:38.0	XRT_QT_PROG_SET_440_OG [0x1b8]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c	
2009/12/18	10:12:40.0	XRT_CTRL_AUTO_447_OG [0x1bf]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2009/12/18	10:13:40.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2009/12/18	10:47:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	