

# XRT Timeline to be uploaded on 2010/01/05

Period: 2010/01/05 09:48:00 - 2010/01/08 10:34:00

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## Normal mode

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<b>XOB #1756: AR - Ti/Poly(512FOV), Al-mesh, Ti-poly AEC1, Thick-Al fixed 32, G-band(512FOV) - AEC1 - 1.5 min cadence</b>												
Term	Pointing (x, y)		Comment									
01/05 10:22:02 - 01/05 16:56:30	Fixed ( 845.0, -464.0)		# OP start + 10min, AR 11039, fixed									
01/05 18:13:02 - 01/06 05:33:30	Fixed ( 845.0, -464.0)		# AR 11039, fixed									
01/06 07:41:02 - 01/06 08:53:30	Fixed ( 845.0, -464.0)		# AR 11039, fixed									
<b>PROG= 06 1-time(s) 30.0sec</b>												
└─ <b>Subr= 1 1-time(s) 30.0sec</b>												
└─ <b>Seqn= 41 1-time(s) 30.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec
└─ <b>Seqn= 66 1-time(s) 40.0sec</b>												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	125ms	Obs	1x1	512x512 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	512x512 (1024, 1024)	Q=95	1	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	32.0s	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
└─ <b>Subr= 2 20-time(s) 2.0sec</b>												
└─ <b>Seqn= 77 1-time(s) 90.0sec</b>												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	1	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1754: Synoptic Q95 2x2 - Al/mesh(64/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(128/2048) + G-band(16</b>												
Term	Pointing (x, y)		Comment									
01/05 18:03:00 - 01/05 18:09:54	Fixed ( 0.0, 0.0)		synoptic									
01/06 06:13:00 - 01/06 06:25:00	Fixed ( 0.0, 0.0)		synoptic, shifted 10.0 min									
<b>PROG= 02 1-time(s)</b>												
└─ <b>Subr= 1 1-time(s) 12.0sec</b>												
└─ <b>Seqn= 59 1-time(s) 4.0sec</b>												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ <b>Seqn= 2 1-time(s) 2.0sec</b>												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ <b>Seqn= 60 1-time(s) 4.0sec</b>												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ <b>Seqn= 11 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1747: Dark - Open+Thick-Be - 8x8 - 512x512</b>												
Term	Pointing (x, y)		Comment									
01/06 08:57:46 - 01/06 08:58:46	Fixed ( 845.0, -464.0)		# AR 11039, fixed									
<b>PROG= 16 1-time(s)</b>												
└─ <b>Subr= 1 1-time(s) 2.0sec</b>												
└─ <b>Seqn= 47 1-time(s) 2.0sec</b>												
Open/thick-Be	Open/thick-Be	close	Safe	Dark	63ms	Obs	8x8	512x512 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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## Flare mode

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NOT USED

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## Active Region Search

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NOT USED

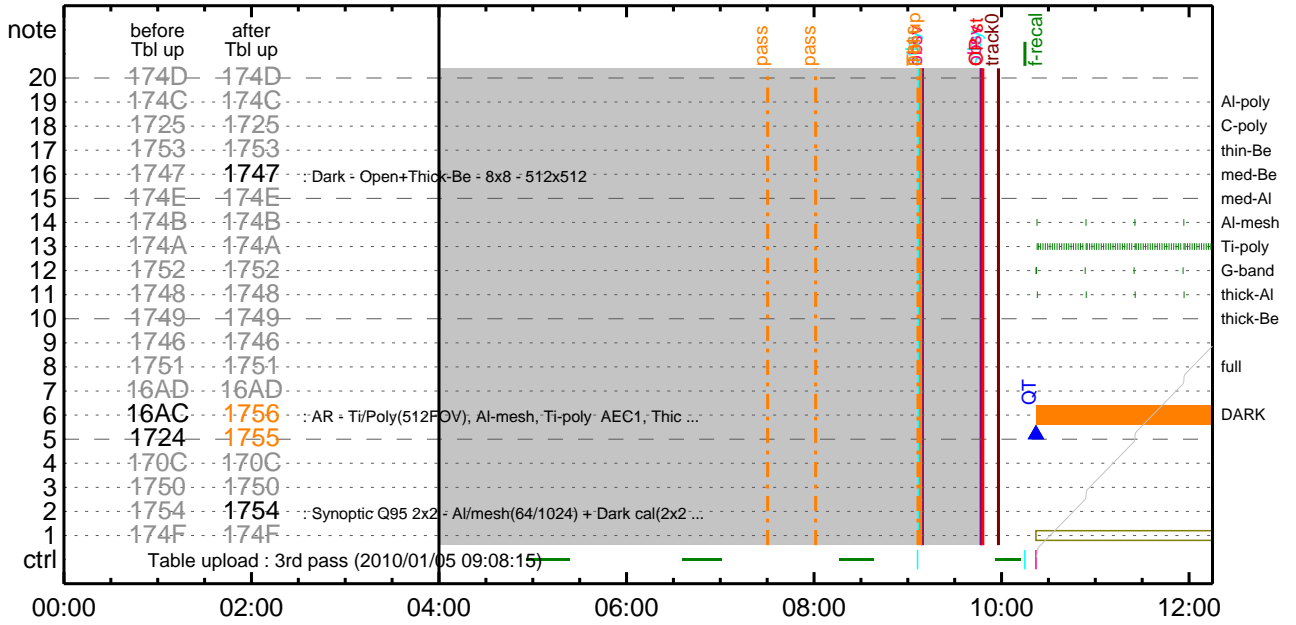
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## Flare Detection

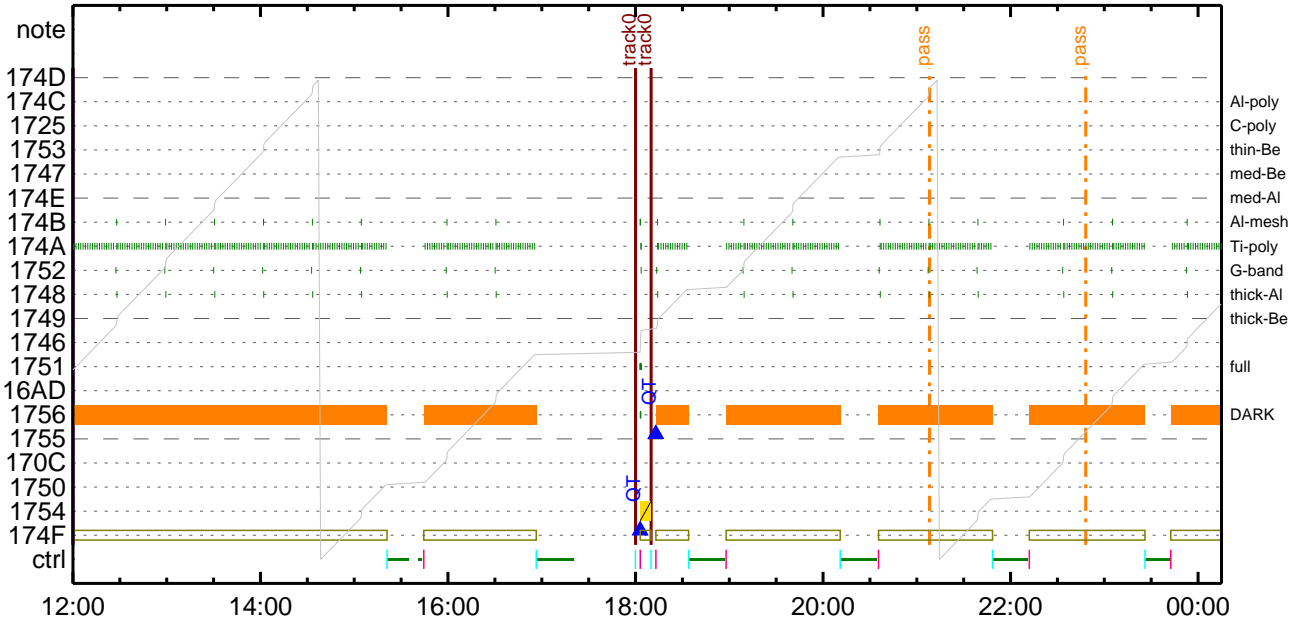
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NOT USED

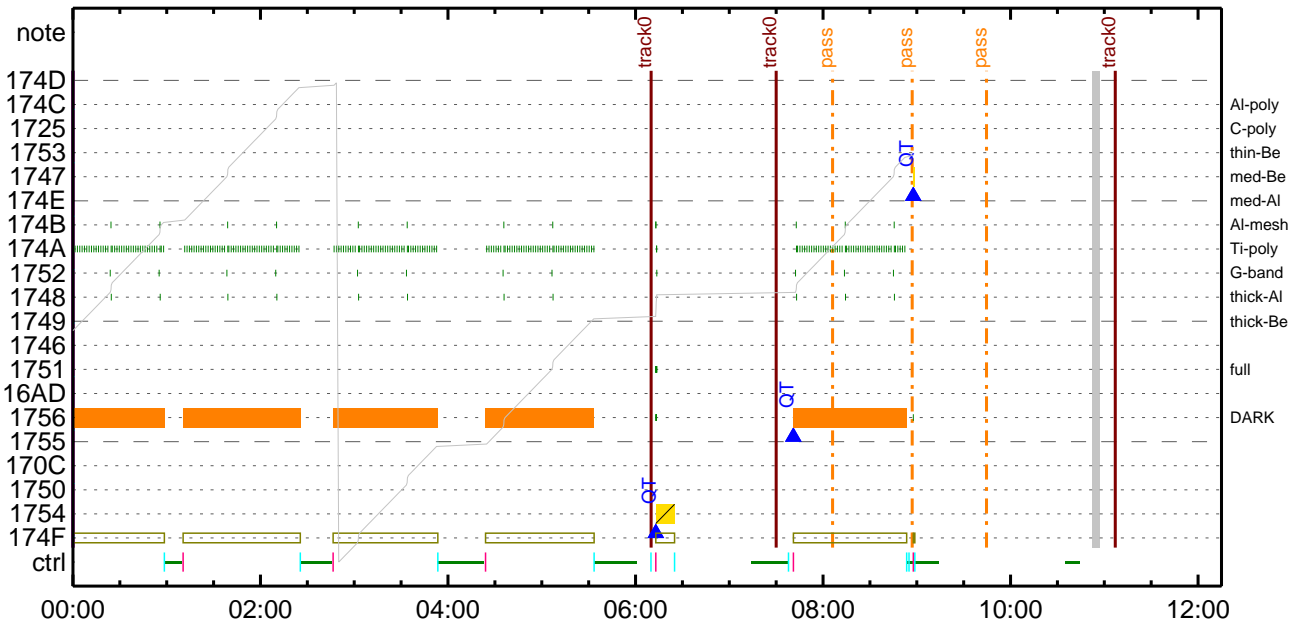
### CMDI #0998 2010/01/05



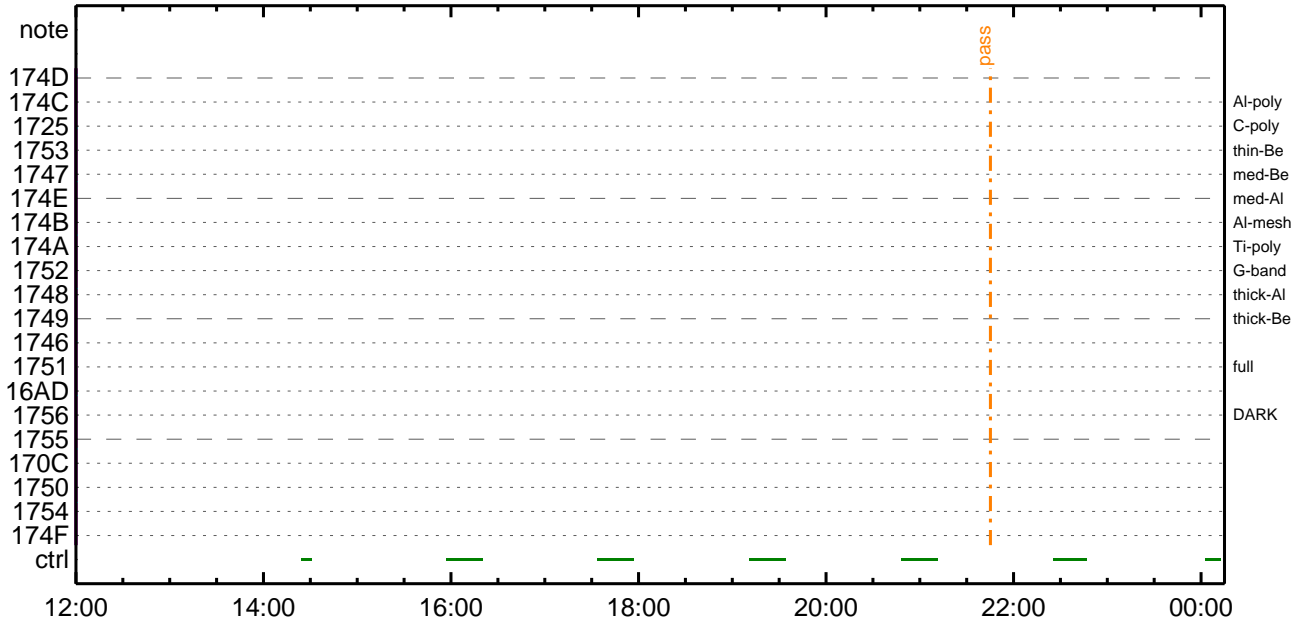
### CMDI #0998 2010/01/05



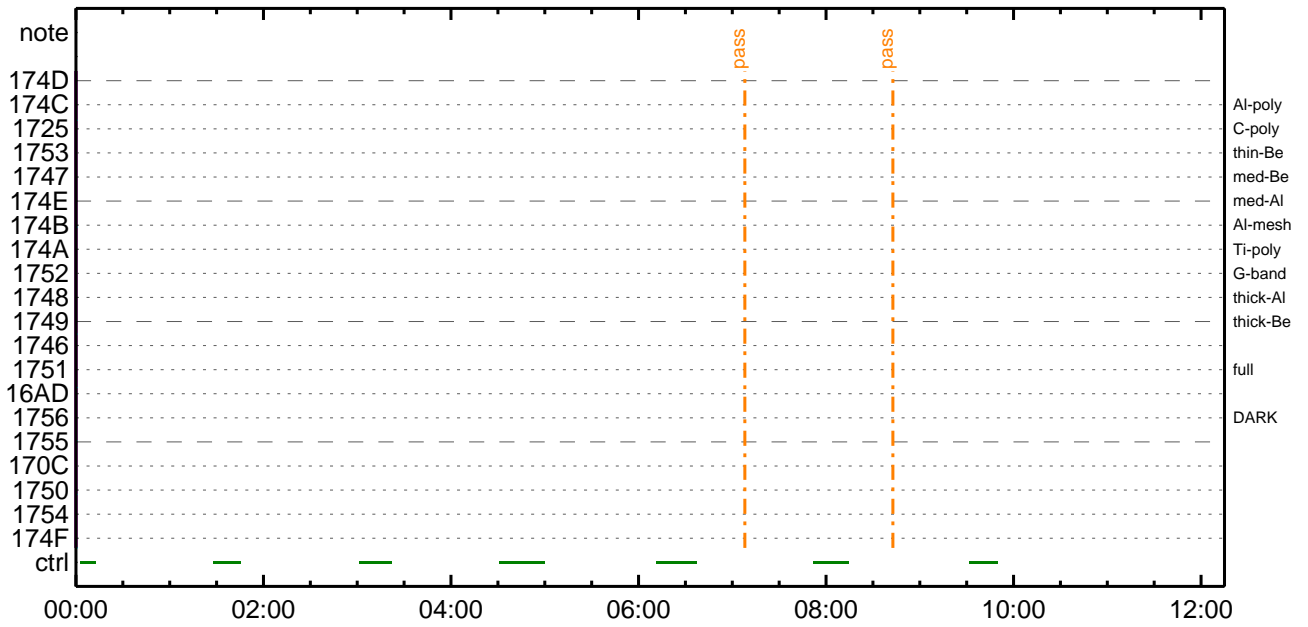
### CMDI #0998 2010/01/06



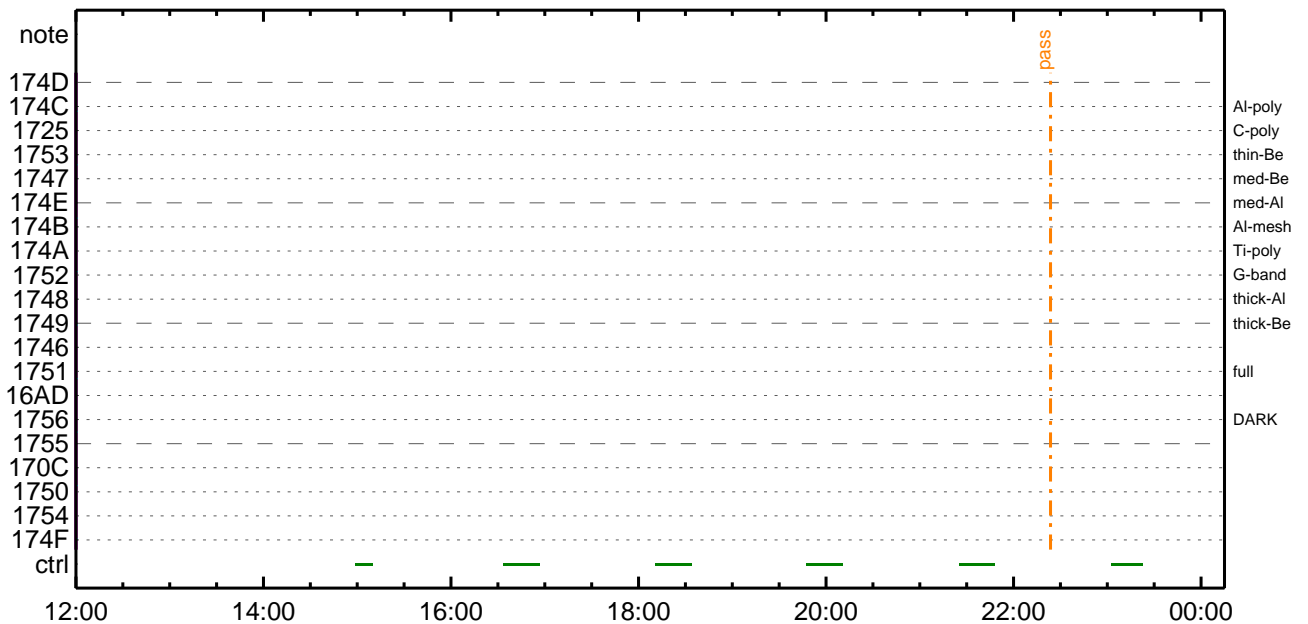
CMDI #0998 2010/01/06



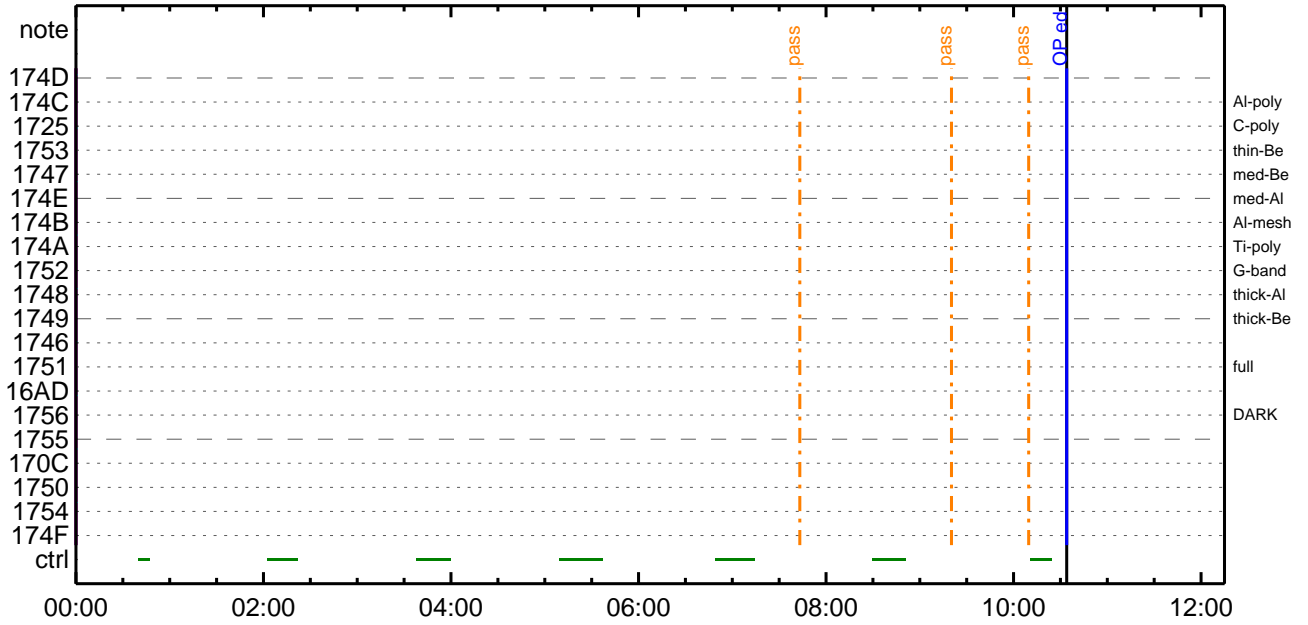
CMDI #0998 2010/01/07



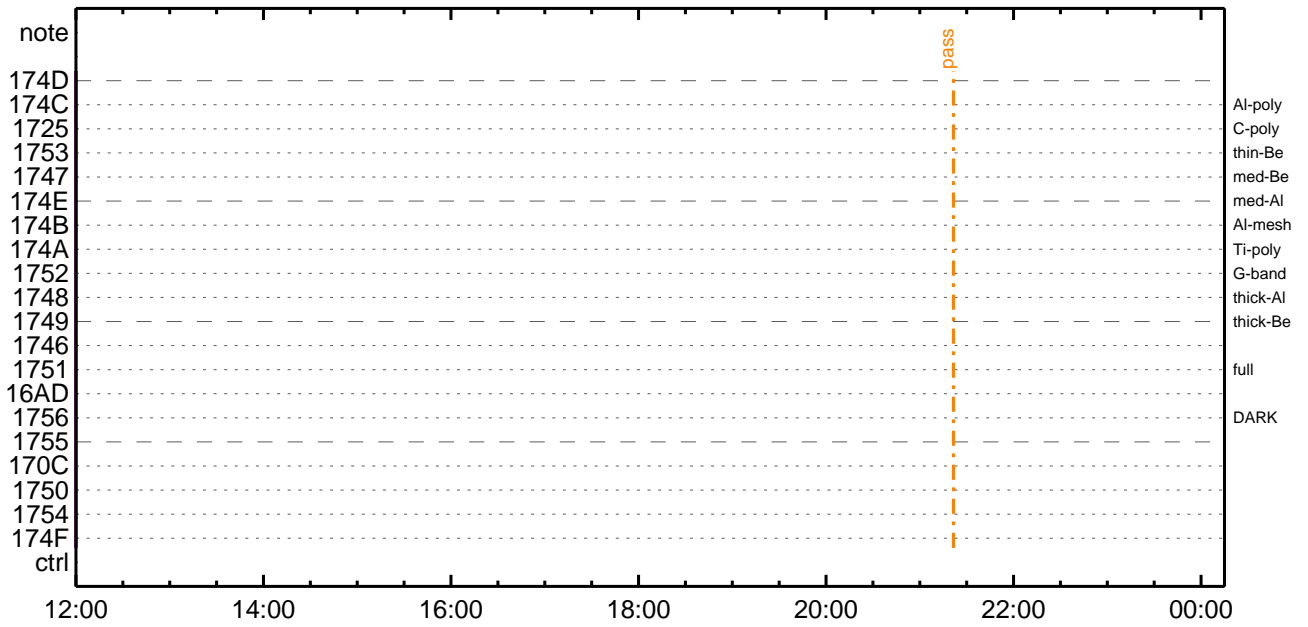
CMDI #0998 2010/01/07



CMDI #0998 2010/01/08



CMDI #0998 2010/01/08



(a) Spacecraft Operation Procedure (real-commands)

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main-083 2010-01-05 13:19:23 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Ä»Û;ä
0005 C.
0006 C. YÄYŞ;¼Y³YŞYÖYÉÄ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Äí;ËçâÄ®•µ°Æ»ÍxÄÇçÍYçYÁYxYí;¼YÉ;ËÈÈ%µ•íÉ;Ë®È%ÇÖ®•®ç¼l¹ççí;çÄ®, ù®¹®®®®®®çÄ+ç®®•®È®®®®®È; f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ+ç®µ;ON
0016 C. *****
0017 C. ç“ °ÆÄ, ÍxËYçâÄLOS®®çççÏ»p´Ö®®¹íí, ®•; çÉÖÍx®ÈXÄÖON®í¹Ö®Èç®ì®È®®®®®È; f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDçYÖYÉYíYÄY-¾ÖÄÖ®-®ÄÆ®•®ç®é; ç°È²¼®í°ÆÄ, ¼È¾ç®®®¼Ä¹Ö®¹®é; f
0030 C.
0031 . C. *****
0032 C. DR PT1 Äí¼í°ÆÄ,
0033 C. *****
0034 C. ç“ RESTART; ÈPT1; È®•®ççç¼l¹ççí; ç°È²¼®í¼Ä¹Ö®®®; çDCBC-150®®çÈ®®; f
0035 C.
0036 . C. ;ãPT1°ÆÄ, ³«»í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Û)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Û)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Û)
0045 C.
0046 . C. ;ãYçYÖYÉYÄÜÄÜ; ÈÄ•Ä°²®È®; È, á®í°ÆÄ, °Æ³«; ä
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Û)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Û)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Û)
0052 C.
0053 C.
0054 . C. PT1°ÆÄ, ®-¾«Æ°Äá»ß®•®çç, á; ç°È²¼®®¼Ä¹Ö®¹®é; f
0055 C. YçYçYÖYÄÜÄÜ®®®Ä•Ä°²®È®®-®á®®¼l¹ççí´°í»®¹®È®®®®®çÄÖ®Ä; f
0056 C.
0057 . C. *****
0058 C. DR PT2 Äí¼í°ÆÄ,
0059 C. *****
0060 C. ç“ RESTART; ÈPT2; È®•®ççç¼l¹ççí; ç°È²¼®í¼Ä¹Ö®®®; çDCBC-151®®çÈ®®; f
0061 C.
0062 . C. ;ãPT2°ÆÄ, ³«»í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Û)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Û)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Û)
0071 C.
0072 . C. ;ãYçYçYÖYÉYÄÜÄÜ; ÈÄ•Ä°²®È®; È, á®í°ÆÄ, °Æ³«; ä
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Û)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Û)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Û)
0078 C.
0079 . C. *****
0080 C. DR°ÆÄ, Äá»ß; çXÄ+ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÄ, Äá»ß; ä
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ+ç®µ;OFF; ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|AYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-083:OP
0104 ( )
0105 S. OG og-083:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. AYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGâî½E¹ç·è²ïOKòð³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. AYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGâî½E¹ç·è²ïOKòð³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. AYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG,RAM ID=OPâî½E¹ç·è²ïOKòð³îÇ§
0165 C.
0166 C. ***** òÊ²¼òî½Ã´¶Á°òÊÊ¬ò°Á÷¿@ (¼âµ-AYOXx½ê½çòðÁÓæòÇ¼ª°¬òè¼î¹çòçòâ) *****
0167 C. DHUÿâ;¼YE;Ê½Y½;Yi;¼YE;Êòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADò-Á÷¿@NGûî½¹ç;ç°Ê²¼òî½TI-CMDÁ÷¿@î½Á¹Ôª·òÊòòò³òÊ;f
0180 C. òÊò¿;çSETòÊDUMPrâî±òî½Y¹ç¹Ôª·òÊò¿;f
0181 C.
0182 C. TIY³YÿYóYÊòððÁî¿(UT)
0183 +. TI 2010-01-05 09:43:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2010-01-05 09:43:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2010-01-05 09:43:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2010-01-05 09:47:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄè%îíñαîîŷÄŷ§ŷÄŷ-¹àîü
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]       EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]       EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»αò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]       EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLαîî¾È¹ç•è²îOKαò³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¾ŷ¼.ŷî;¼ŷÈ;Èαòîãα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2010-01-05 09:47:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2010-01-05 09:47:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2010-01-05 09:47:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2010-01-05 09:47:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîαî»ö¼ŷαÈÄα¹αèDCBC•x²è *****
0276 C. (¼á°îŷÖŷÄŷÈŷŷŷÈŷáŷçŷèè¾¼αα¼Ä»Ûα¹αè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹•î Daily±çîñèË´Øα¹αèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-084 2010-01-05 13:19:23 91 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY~¼Á»Ü;ã
0005 C.
0006 C. YÁYB;¼Y³YFYOYÉÁ+¿@
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Èq¿qÁq•µ°È»Í×ÁÇqÍYçYÁY×Yí;¼YÉ;ÈÈèµ•íÉ;ÈqÈ¼°ÇÔq•q¿¼i¹çqÍ;çÁ@,ùq¹qèqBqÇÁ+¿@q•qÈqçq³qÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. ***** AOCS Commands (Orbital Element Update) *****
0046 C. Update the orbital element
0047 +. DC 02-50 AOCU_ORB_PRPGT_START
0048 BC (16)
0049 + DC 02-8E AOCU_ORB_UPD
0050 C.
0051 C. <A_ORB>[ORBIT] EPC = 2683296.6 +- 1.0 (s) [ ]
0052 C.
0053 . C.
0054 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055 +. DC 07-FC EIS_MODE_MANU
0056 BC (21 02)
0057 . C. Verify EIS in MANUAL mode
0058 . C. Estimated OBSTBL upload time is 1s
0059 C. *****
0060 C. EIS START OBSTBL LOAD
0061 C. *****
0062 . S. RAM ram-820:EIS_OBSTBL
0063 ( )
0064 +. DC 07-FC EIS_DUMP_OBSTBL
0065 BC (07 07 07 00 00 70 00)
0066 C.
0067 C. Execute, after the success of OBSTBL upload.
0068 C. Set EIS TI-commands
0069 +. TI 2010-01-05 09:47:50.0
0070 DC 07-FC EIS_MODE_CHG_ENA
0071 BC (20)
0072 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0073 C. *****
0074 C. EIS END OBSTBL LOAD
0075 C. *****
0076 C.
0077 . C. ***** MDP `îÁîqî`ó¼YqÈÁDq¹qèDCBC•x²è *****
0078 C. (¼á°îYÓYÁYÈYBpYÈYáYçYèqE¾qçq¼Á»Üq¹qè)
0079 . S. DC-BC dcbc-402:DCBC
0080 (MDP_known_event)
0081 C.
0082 C.
0083 . C. ***** YDÝ¹•İ Daily±¿İÑqÈ`Øq¹qèDCBC•x²è *****
0084 . S. DC-BC dcbc-153:DCBC
0085 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0086 C.
0087 C.
0088 . C. ;ãLOSŸÁY$YÁY~¼Á»Ü;ã
0089 C.
0090 . C. ***** LOS *****
0091 C.
```



(a) Spacecraft Operation Procedure (real-commands)

```
main-085 2010-01-05 13:19:23 222 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY-¼Ä»Û;ä
0005 C.
0006 C. YÄYß;¼Y³YÞYÓYÉÄ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;Ëççãâ•µ°Æ»Í×ÁÇçÍYçYÄY×YÍ;¼YÉ;ËÈÈµ•íÉ;ËÈÈ¼°ÇÖã•çç¼í¹ççÍ;çÄ®, ùã¹ãèãèãçÄ+ç®ã•ãèãããè;ç
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ+ç®µ;ON
0016 C. *****
0017 C. ç" °ÆÄ, Í×ËYçãâLOSãèççãÍ»p´Öãð¹íí, ç. ; çÉÖÍ×ãÈXÄÖONãí¹ÖãÈíãèãããè;ç
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÄY-¾ÖÄÖã-°ÄÄêã•ççç; ç°È²¼ççÍ°ÆÄ, ¼êççççç¼Ä¹Öççççç;ç
0030 C.
0031 . C. *****
0032 C. DR PT1 Äí¼í°ÆÄ,
0033 C. *****
0034 C. ç" RESTART;ËPT1;Ëã•çççç¼í¹ççÍ; ç°È²¼ççÍ¼Ä¹Öççççç; çDCBC-150ççççç;ç
0035 C.
0036 . C. ;ãPT1°ÆÄ, ³«»Í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÓYÉYÉÄÜÄÖ;ËÄ•Ä°²óÈð;Ë, äãí°ÆÄ, °Æ³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÄ, ç-¼«Æ°Äã»ßã•ççç, ä; ç°È²¼ççç¼Ä¹Öççççç;ç
0055 C. YçYÓYÉYÉÄÜÄÖãÄ•Ä°²óÈðã-¼ãççç¼í¹ççÍ°í»ã¹ãèãèãçÄÖãÄ;ç
0056 C.
0057 . C. *****
0058 C. DR PT2 Äí¼í°ÆÄ,
0059 C. *****
0060 C. ç" RESTART;ËPT2;Ëã•çççç¼í¹ççÍ; ç°È²¼ççç¼Ä¹Öççççç; çDCBC-151ççççç;ç
0061 C.
0062 . C. ;ãPT2°ÆÄ, ³«»Í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÓYÉYÉÄÜÄÖ;ËÄ•Ä°²óÈð;Ë, äãí°ÆÄ, °Æ³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÄ, Äã»ß;çXÄ+ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÄ, Äã»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ+ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 06 80 80 08 08)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 07 85 83 08 08)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 08 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 09 80 80 20 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 0a 80 80 08 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0f 80 80 06 06)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 10 80 80 04 04)
0138 . C. ----- Success Verify ? OK / NG ____
0139 C.
0140 C.
0141 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0142 C.
0143 +. DC 07-F0 MDP_XRT_MODE_OBSV
0144 BC (c2)
0145 +. TI 2010-01-05 09:47:02.0
0146 DC 07-F0 MDP_XRT_MODE_OBSV
0147 BC (c2)
0148 . C. ----- Success Verify ? OK / NG ____
0149 C.
0150 C. ***** XRT END *****
0151 . C. *****
0152 C. SOT table upload
0153 C. *****
0154 . C. < Stop FG table >
0155 +. DC 07-F0 MDP_FG_CTRL_MANU
0156 BC (51)
0157 . C. -----
0158 C. MDP_FG_CTRL_MODE = MANU [ ]
0159 C. -----
0160 C.
0161 . C. <Upload FG Observation Table>
0162 . S. RAM ram-269:MDP_OBS_F
0163 ( )
0164 C.
0165 . C. < Dump RAMID=MDP_OBS_F >
0166 +. DC 07-F0 MDP_DUMP_FGTBL
0167 BC (82 07 00 00 00 38 b8)
0168 C. -----
0169 C. MDP_OBS_F verify = OK/NG [ ]
0170 C. -----
0171 C.
0172 . C. < Upload DPL table >
0173 C.
0174 C. ¥¢¥Ã¥×¥í;¼¥ÉºîÁº±ÈSTS_CHKºðOFFºÈº±ºë
0175 C.
0176 . S. RAM ram-271:MDP_DPL
0177 ( )
0178 C.
0179 . C. < Dump RAMID=MDP_DPL >
0180 +. DC 07-F0 MDP_DUMP_FGTBL
0181 BC (82 07 00 38 b8 00 40)
0182 C. -----
0183 C. MDP_DPL verify = OK [ ]
0184 C. -----
0185 C.
0186 C. STS_CHKºðONºÈº±ºë
0187 C.
0188 . C. < Update MDP DSC PAR1 >
0189 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0190 BC (4c)
0191 C. MDP_CMD_CODE = F04C0700[ ]
0192 C. MDP_CMD_CNT (count-up 1) [ ]
0193 C. -----

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```
0194 . C.
0195 . C.
0196 C. *****
0197 C. SOT TI command set
0198 C. *****
0199 C. Execute, after the success of TBL upload.
0200 +. TI 2010-01-05 09:47:18.0
0201 DC 07-F0 MDP_SOT_MODE_OBSV
0202 BC (40)
0203 . C. -----
0204 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0205 C. -----
0206 C.
0207 C.
0208 . C. ***** MDP 'úÃîâî»ö¼ÝðÊÂð¹æDCBC•x²è *****
0209 C. (¼ã°îÝÓÝÃÝÊÝÞÝËÝáÝçÝèæ¼¼¼¼¼»Û¹æ)
0210 . S. DC-BC dcbc-402:DCBC
0211 (MDP_known_event)
0212 C.
0213 C.
0214 . C. ***** ÝÐÝ¹•Ï Daily±¿ÎÑæË'Ø¹æDCBC•x²è *****
0215 . S. DC-BC dcbc-153:DCBC
0216 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0217 C.
0218 C.
0219 . C. ;ãLOSÝÁÝ$ÝÃÝ-¼Ã»Û;ã
0220 C.
0221 . C. ***** LOS *****
0222 C.
```

Jan 05, 10 13:19

## XRT\_OGLIST\_0998.chk

Page 1/2

\*\*\* OP Sequence for XRT \*\*\*

2010/01/05	09:58:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 29 3f b4 e6				
2010/01/05	10:14:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/01/05	10:14:56.0	XRT_FOCUS_RECALIBRATE_424_OG [0x1a8]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2010/01/05	10:18:56.0	XRT_FOCUS_POSITION_413_OG [0x19d]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/01/05	10:19:16.0	XRT_AEC_RESET_403_OG [0x193]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/01/05	10:19:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/01/05	10:19:20.0	XRT_FLRCTRL_DIS_410_OG [0x19a]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/01/05	10:19:22.0	XRT_ARS_DIS_411_OG [0x19b]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/01/05	10:22:00.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2010/01/05	10:22:02.0	XRT_CTRL_AUTO_407_OG [0x197]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/01/05	15:21:00.0	XRT_CTRL_MANU_419_OG [0x1a3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/01/05	15:43:30.0	XRT_Custom_420_OG [0x1a4]							
2010/01/05	15:44:30.0	XRT_CTRL_AUTO_416_OG [0x1a0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/01/05	16:56:30.0	XRT_CTRL_MANU_419_OG [0x1a3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/01/05	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/01/05	17:59:56.0	XRT_FOCUS_POSITION_408_OG [0x198]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/01/05	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/01/05	18:00:16.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/01/05	18:00:18.0	XRT_FLRCTRL_DIS_410_OG [0x19a]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/01/05	18:00:20.0	XRT_ARS_DIS_411_OG [0x19b]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/01/05	18:02:58.0	XRT_QT_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2010/01/05	18:03:00.0	XRT_CTRL_AUTO_407_OG [0x197]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/01/05	18:09:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/01/05	18:09:56.0	XRT_FOCUS_POSITION_413_OG [0x19d]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/01/05	18:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 29 3f b4 e6				
2010/01/05	18:10:16.0	XRT_AEC_RESET_403_OG [0x193]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/01/05	18:10:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/01/05	18:10:20.0	XRT_FLRCTRL_DIS_410_OG [0x19a]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/01/05	18:10:22.0	XRT_ARS_DIS_411_OG [0x19b]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/01/05	18:13:00.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2010/01/05	18:13:02.5	XRT_CTRL_AUTO_407_OG [0x197]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/01/05	18:34:00.0	XRT_CTRL_MANU_419_OG [0x1a3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/01/05	18:57:00.0	XRT_Custom_420_OG [0x1a4]							
2010/01/05	18:58:00.0	XRT_CTRL_AUTO_416_OG [0x1a0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/01/05	20:11:00.0	XRT_CTRL_MANU_419_OG [0x1a3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/01/05	20:34:30.0	XRT_Custom_420_OG [0x1a4]							
2010/01/05	20:35:30.0	XRT_CTRL_AUTO_416_OG [0x1a0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/01/05	21:48:30.0	XRT_CTRL_MANU_419_OG [0x1a3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/01/05	22:11:00.0	XRT_Custom_420_OG [0x1a4]							
2010/01/05	22:12:00.0	XRT_CTRL_AUTO_416_OG [0x1a0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/01/05	23:26:00.0	XRT_CTRL_MANU_419_OG [0x1a3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/01/05	23:41:30.0	XRT_Custom_420_OG [0x1a4]							
2010/01/05	23:42:30.0	XRT_CTRL_AUTO_416_OG [0x1a0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/01/06	00:58:30.0	XRT_CTRL_MANU_419_OG [0x1a3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/01/06	01:09:30.0	XRT_Custom_420_OG [0x1a4]							
2010/01/06	01:10:30.0	XRT_CTRL_AUTO_416_OG [0x1a0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/01/06	02:25:30.0	XRT_CTRL_MANU_419_OG [0x1a3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/01/06	02:45:30.0	XRT_Custom_420_OG [0x1a4]							
2010/01/06	02:46:30.5	XRT_CTRL_AUTO_416_OG [0x1a0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

Jan 05, 10 13:19

## XRT\_OGLIST\_0998.chk

Page 2/2

2010/01/06	03:53:30.0	XRT_CTRL_MANU_419_OG [0x1a3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/01/06	04:23:00.0	XRT_Custom_420_OG [0x1a4]							
2010/01/06	04:24:00.0	XRT_CTRL_AUTO_416_OG [0x1a0]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/01/06	05:33:30.0	XRT_CTRL_MANU_419_OG [0x1a3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/01/06	06:09:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/01/06	06:09:56.0	XRT_FOCUS_POSITION_408_OG [0x198]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/01/06	06:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/01/06	06:10:16.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/01/06	06:10:18.0	XRT_FLRCTRL_DIS_410_OG [0x19a]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/01/06	06:10:20.0	XRT_ARS_DIS_411_OG [0x19b]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/01/06	06:12:58.0	XRT_QT_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2010/01/06	06:13:00.0	XRT_CTRL_AUTO_407_OG [0x197]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/01/06	06:25:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/01/06	07:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 29 3f b4 e6				
2010/01/06	07:37:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/01/06	07:37:56.0	XRT_FOCUS_POSITION_413_OG [0x19d]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/01/06	07:38:16.0	XRT_AEC_RESET_403_OG [0x193]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/01/06	07:38:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/01/06	07:38:20.0	XRT_FLRCTRL_DIS_410_OG [0x19a]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/01/06	07:38:22.0	XRT_ARS_DIS_411_OG [0x19b]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/01/06	07:41:00.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2010/01/06	07:41:02.0	XRT_CTRL_AUTO_407_OG [0x197]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/01/06	08:53:30.0	XRT_CTRL_MANU_419_OG [0x1a3]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/01/06	08:55:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/01/06	08:55:02.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/01/06	08:55:04.0	XRT_FLRCTRL_DIS_410_OG [0x19a]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/01/06	08:55:06.0	XRT_ARS_DIS_411_OG [0x19b]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/01/06	08:57:44.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2010/01/06	08:57:46.0	XRT_CTRL_AUTO_447_OG [0x1bf]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/01/06	08:58:46.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/01/06	11:07:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				