XRT Timeline to be uploaded on 2010/01/26

Period: 2010/01/26 10:49:00 - 2010/01/30 11:07:00

* * * * * **Normal mode** * * * * *

XOB #1769: AR multifilter - Al/mesh,Ti/Poly, G-bandFOV512 AEC3 Q95(5min)

Term	Pointing (x, y)	Comment
01/26 11:02:02 - 01/26 17:59:54	Track (-83.4, -338.2) @ 01/26 10:59:00	track AR 11041
01/26 18:13:00 - 01/26 21:19:54	Track (-24.2, -337.4) @ 01/26 18:10:00	track AR11041
01/27 05:03:02 - 01/27 06:06:24	Track (65.1, -337.0) @ 01/27 05:00:00	track AR11041
01/27 06:19:30 - 01/27 17:55:24	Track (75.6, -337.0) @ 01/27 06:16:30	AR 11041
01/27 18:08:30 - 01/28 05:40:24	Track (172.1, -337.7) @ 01/27 18:05:30	AR 11041
01/28 06:35:00 - 01/28 10:34:54	Track (271.1, -339.6) @ 01/28 06:30:00	AR 11041

PROG= 15 Inf.-time(s)
- **Subr= 2 1-time(s) 2.0sec**
  - **Seqn= 41 1-time(s) 2.0sec**
    - Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 512x512 (1024, 1024) Q=98 0 0 2.0sec
- **Subr= 1 16-time(s) 300.0sec**
  - **Seqn= 20 1-time(s) 4.0sec**
    - Open/Al-mesh Open/Ti-poly close Safe Norm 250ms Obs 1x1 512x512 (1024, 1024) Q=95 3 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 512x512 (1024, 1024) Q=95 3 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #170C: Synoptic Q95 2x2 - Al/mesh(45/512) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(128/1024) + G-band(16)

Term	Pointing (x, y)	Comment
01/26 18:03:00 - 01/26 18:09:54	Fixed (0.0, 0.0)	synoptic
01/27 06:09:30 - 01/27 06:16:24	Fixed (0.0, 0.0)	synoptic, shifted 6.5 min
01/27 17:58:30 - 01/27 18:05:24	Fixed (0.0, 0.0)	synoptic, shifted -4.5 min
01/28 05:43:30 - 01/28 06:08:00	Fixed (0.0, 0.0)	synoptic, shifted -19.5 min

PROG= 17 1-time(s)
- **Subr= 1 1-time(s) 12.0sec**
  - **Seqn= 43 1-time(s) 4.0sec**
    - Open/Al-mesh Open/Ti-poly close Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - **Seqn= 2 1-time(s) 2.0sec**
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
  - **Seqn= 40 1-time(s) 4.0sec**
    - Open/Ti-poly Open/Ti-poly close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - **Seqn= 11 1-time(s) 2.0sec**
    - Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #176C: AR - Al-mesh(AEC3), Ti/Poly(AEC3), Thick-Al(AEC3) - 512x512 - 2min cad, with G-band

Term	Pointing (x, y)	Comment
01/26 21:23:00 - 01/27 00:29:54	Track (1.9, -337.2) @ 01/26 21:20:00	HOP 100 - normal full FOV SP scan for 2.8 hours
01/27 00:33:02 - 01/27 01:49:54	Track (-91.9, -437.1) @ 01/27 00:30:00	HOP 100 - fast map scan (1/4)
01/27 01:53:02 - 01/27 02:29:54	Track (-170.9, -317.0) @ 01/27 01:50:00	HOP 100 - fast map scan (2/4)
01/27 02:33:02 - 01/27 03:59:54	Track (44.6, -227.0) @ 01/27 02:30:00	HOP 100 - fast map scan (3/4)
01/27 04:03:02 - 01/27 04:59:54	Track (261.9, -337.0) @ 01/27 04:00:00	HOP 100 - fast map scan (4/4)

PROG= 11 Inf.-time(s)
- **Subr= 1 1-time(s) 30.0sec**
  - **Seqn= 41 1-time(s) 30.0sec**
    - Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 512x512 (1024, 1024) Q=98 0 0 2.0sec
- **Subr= 2 40-time(s) 2.0sec**
  - **Seqn= 74 1-time(s) 120.0sec**
    - Open/Ti-poly Open/thick-Al close Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
  - **Seqn= 24 1-time(s) 2.0sec**
    - Open/Al-mesh Open/Ti-poly close Safe Norm 125ms Obs 1x1 512x512 (1024, 1024) Q=95 3 0 2.0sec
    - Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 512x512 (1024, 1024) Q=95 3 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1747: Dark - Open+Thick-Be - 8x8 - 512x512

Term	Pointing (x, y)	Comment
01/28 10:37:58 - 01/28 10:38:58	Fixed (0.0, 0.0)	Backup plan

PROG= 16 1-time(s)
- **Subr= 1 1-time(s) 2.0sec**
  - **Seqn= 47 1-time(s) 2.0sec**
    - Open/thick-Be Open/thick-Be close Safe Dark 63ms Obs 8x8 512x512 (1024, 1024) DPCM 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

✲ ✲ ✲ ✲ ✲ Flare mode ✲ ✲ ✲ ✲ ✲

XOB #175E: Flare Response - Dynamics - Thick-Al - Thick-Be - AEC 1 - 384x384 - Q95

Term	Pointing (x, y)	Comment
01/26 11:02:02 - 01/26 17:59:54	Track (-83.4, -338.2) @ 01/26 10:59:00	track AR 11041
01/26 18:13:00 - 01/26 21:19:54	Track (-24.2, -337.4) @ 01/26 18:10:00	track AR11041
01/26 21:23:00 - 01/27 00:29:54	Track (1.9, -337.2) @ 01/26 21:20:00	HOP 100 - normal full FOV SP scan for 2.8 hours
01/27 00:33:02 - 01/27 01:49:54	Track (-91.9, -437.1) @ 01/27 00:30:00	HOP 100 - fast map scan (1/4)
01/27 01:53:02 - 01/27 02:29:54	Track (-170.9, -317.0) @ 01/27 01:50:00	HOP 100 - fast map scan (2/4)
01/27 02:33:02 - 01/27 03:59:54	Track (44.6, -227.0) @ 01/27 02:30:00	HOP 100 - fast map scan (3/4)
01/27 04:03:02 - 01/27 04:59:54	Track (261.9, -337.0) @ 01/27 04:00:00	HOP 100 - fast map scan (4/4)
01/27 05:03:02 - 01/27 06:06:24	Track (65.1, -337.0) @ 01/27 05:00:00	track AR11041
01/27 06:19:30 - 01/27 17:55:24	Track (75.6, -337.0) @ 01/27 06:16:30	AR 11041
01/27 18:08:30 - 01/28 05:40:24	Track (172.1, -337.7) @ 01/27 18:05:30	AR 11041
01/28 06:35:00 - 01/28 10:34:54	Track (271.1, -339.6) @ 01/28 06:30:00	AR 11041

PROG= 20 1-time(s)

- **Subr= 1 1-time(s) 2.0sec**
  - **Seqn= 75 15-time(s) 20.0sec**
    - Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
    - Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
  - **Seqn= 35 1-time(s) 4.0sec**
    - Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
- **Subr= 2 8-time(s) 2.0sec**
  - **Seqn= 75 15-time(s) 60.0sec**
    - Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
    - Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
  - **Seqn= 35 1-time(s) 4.0sec**
    - Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
- **Subr= 3 25-time(s) 2.0sec**
  - **Seqn= 75 1-time(s) 600.0sec**
    - Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
    - Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
  - **Seqn= 35 1-time(s) 4.0sec**
    - Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

✲ ✲ ✲ ✲ ✲ Active Region Search ✲ ✲ ✲ ✲ ✲

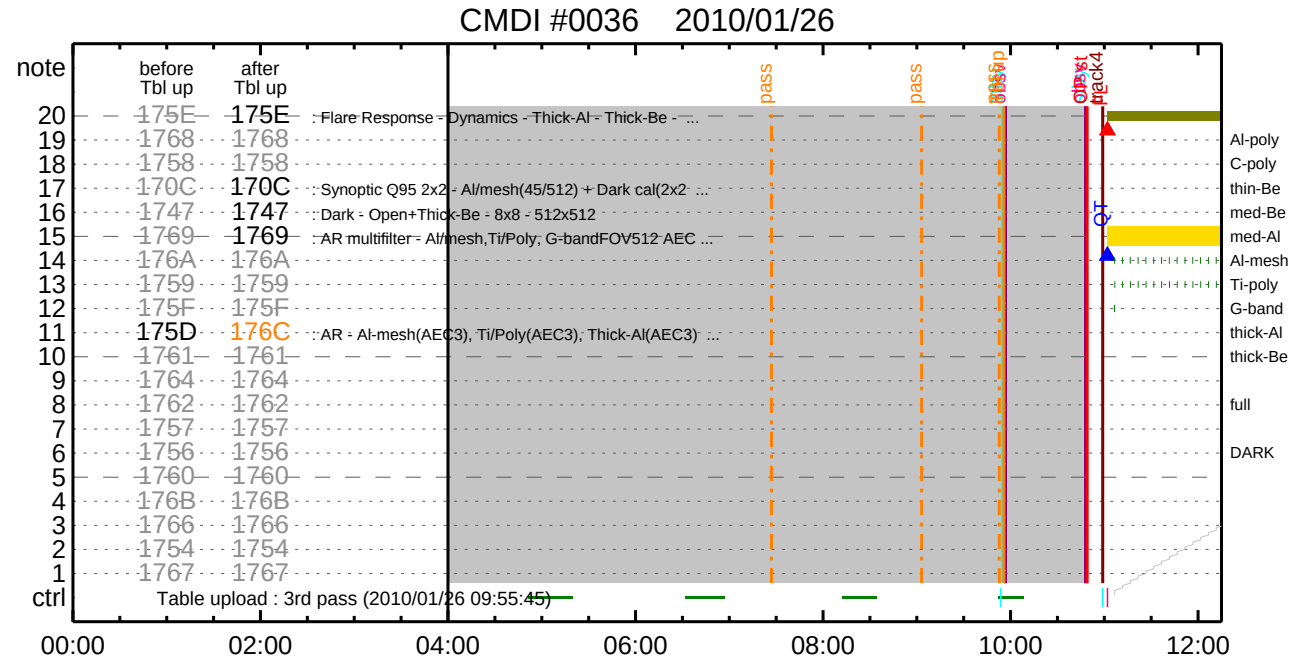
NOT USED

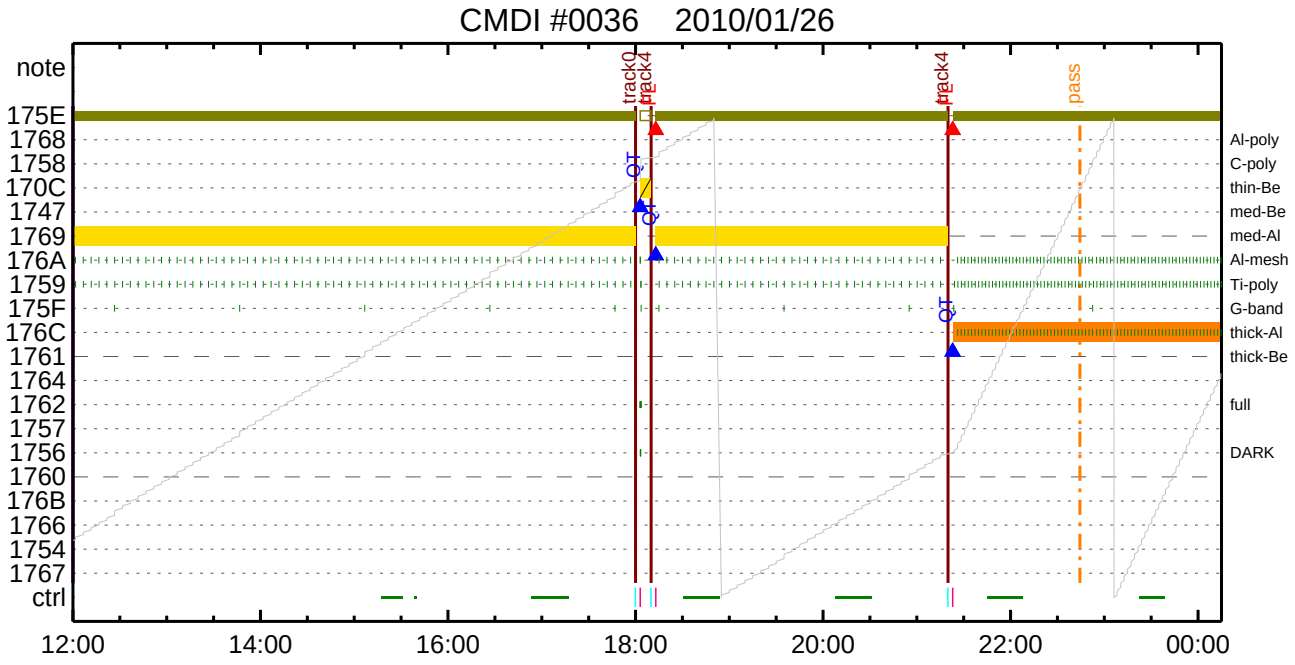
✲ ✲ ✲ ✲ ✲ Flare Detection ✲ ✲ ✲ ✲ ✲

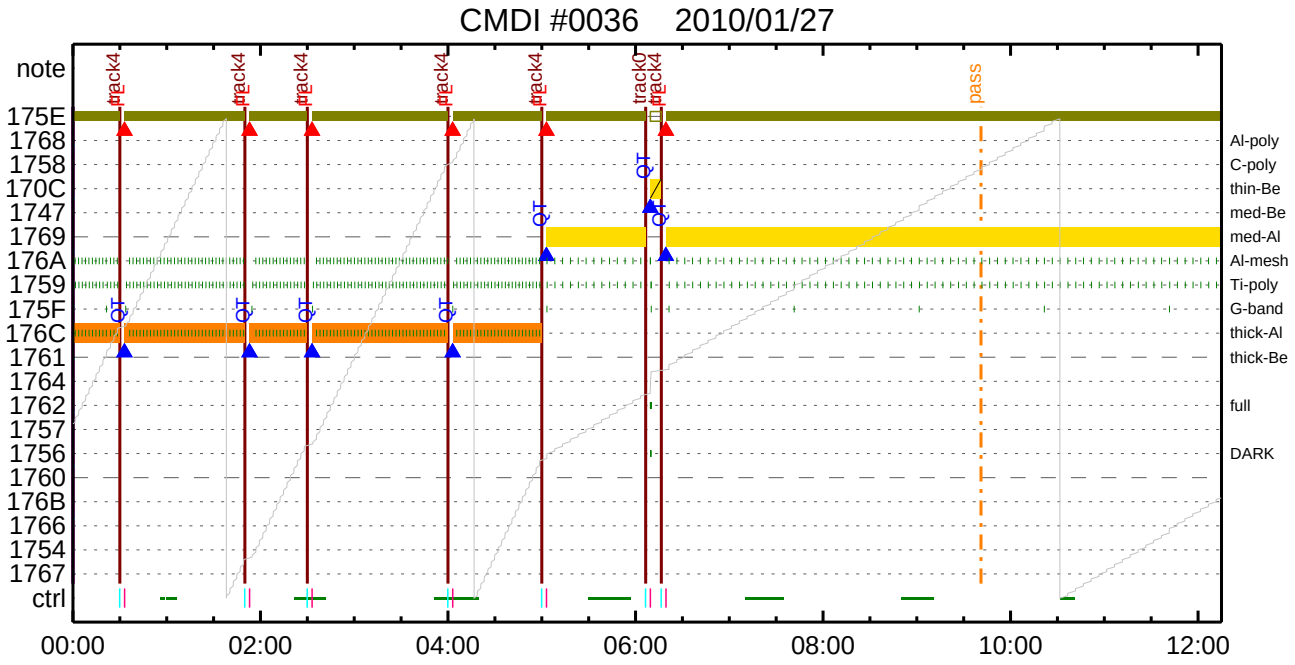
FLD Patrol

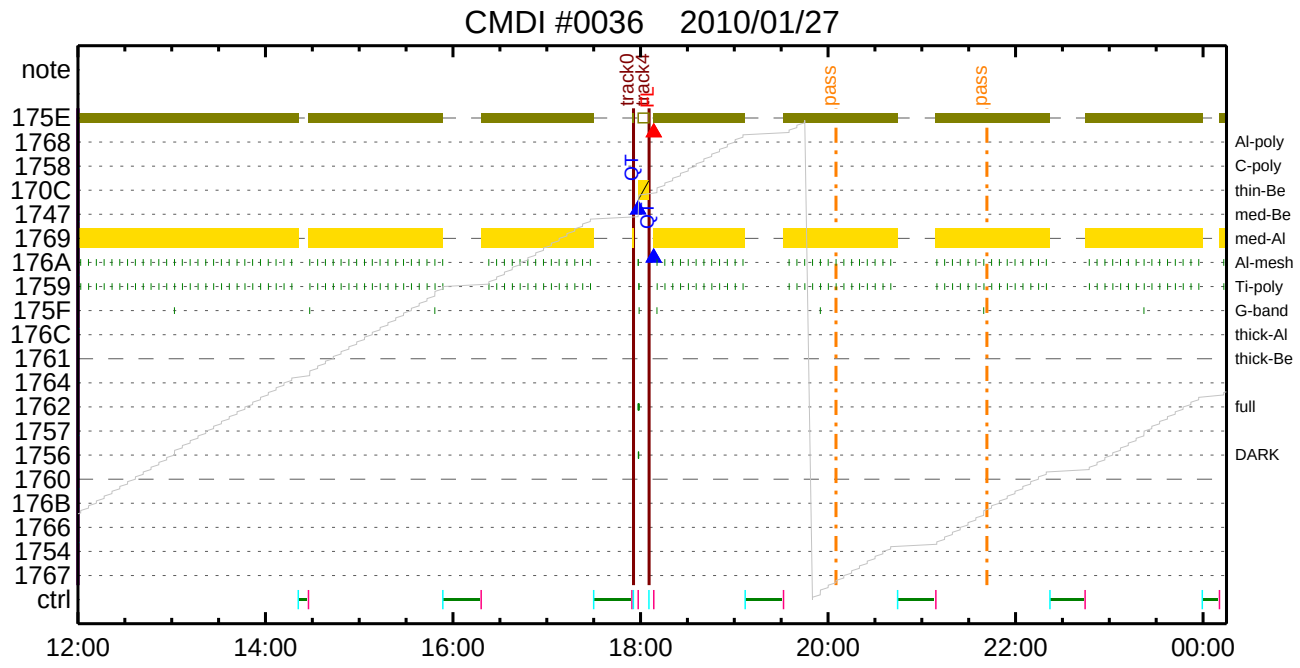
Term	Pointing (x, y)	Comment
01/26 10:59:16 - 01/26 18:00:16	Track (-83.4, -338.2) @ 01/26 10:59:00	track AR 11041
01/26 18:10:16 - 01/27 06:06:46	Track (-24.2, -337.4) @ 01/26 18:10:00	track AR11041
01/27 06:16:46 - 01/27 17:55:46	Track (75.6, -337.0) @ 01/27 06:16:30	AR 11041
01/27 18:05:46 - 01/28 05:40:46	Track (172.1, -337.7) @ 01/27 18:05:30	AR 11041
01/28 06:30:16 - 01/28 10:35:16	Track (271.1, -339.6) @ 01/28 06:30:00	AR 11041

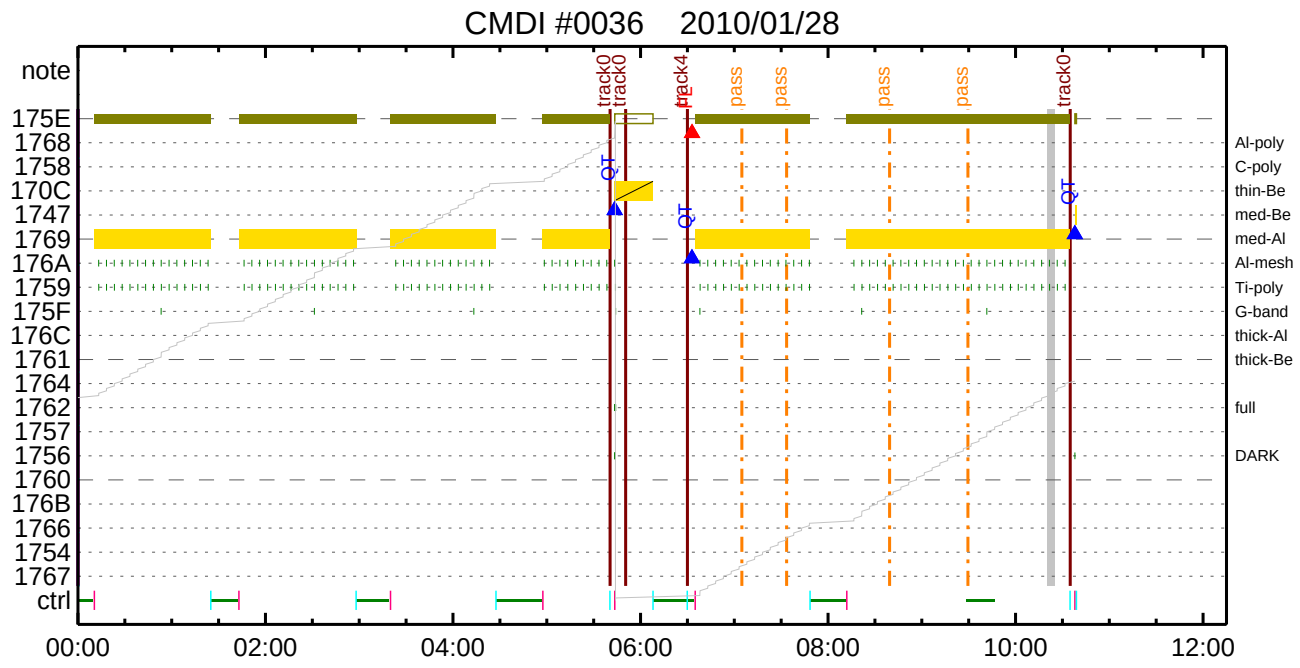
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			120sec

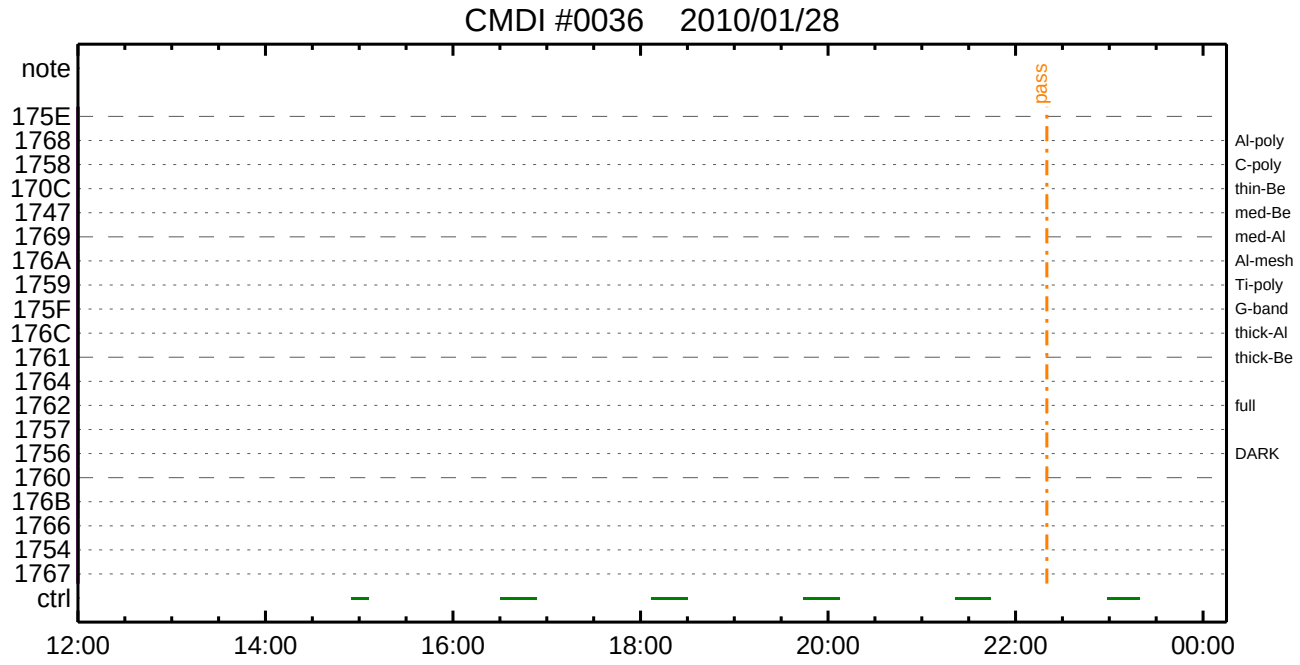

CMDI #0036 2010/01/26
note
before Tbl up
after Tbl up
: Flare Response - Dynamics - Thick-Al - Thick-Be - ...
: Synoptic Q95 2x2 - Al/mesh(45/512) + Dark cal(2x2 ...
: Dark - Open+Thick-Be - 8x8 - 512x512
: AR multifilter - Al/mesh,Ti/Poly, G-bandFOV512 AEC ...
: AR - Al-mesh(AEC3), Ti/Poly(AEC3), Thick-Al(AEC3) ...
Table upload : 3rd pass (2010/01/26 09:55:45)
pass
track4
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

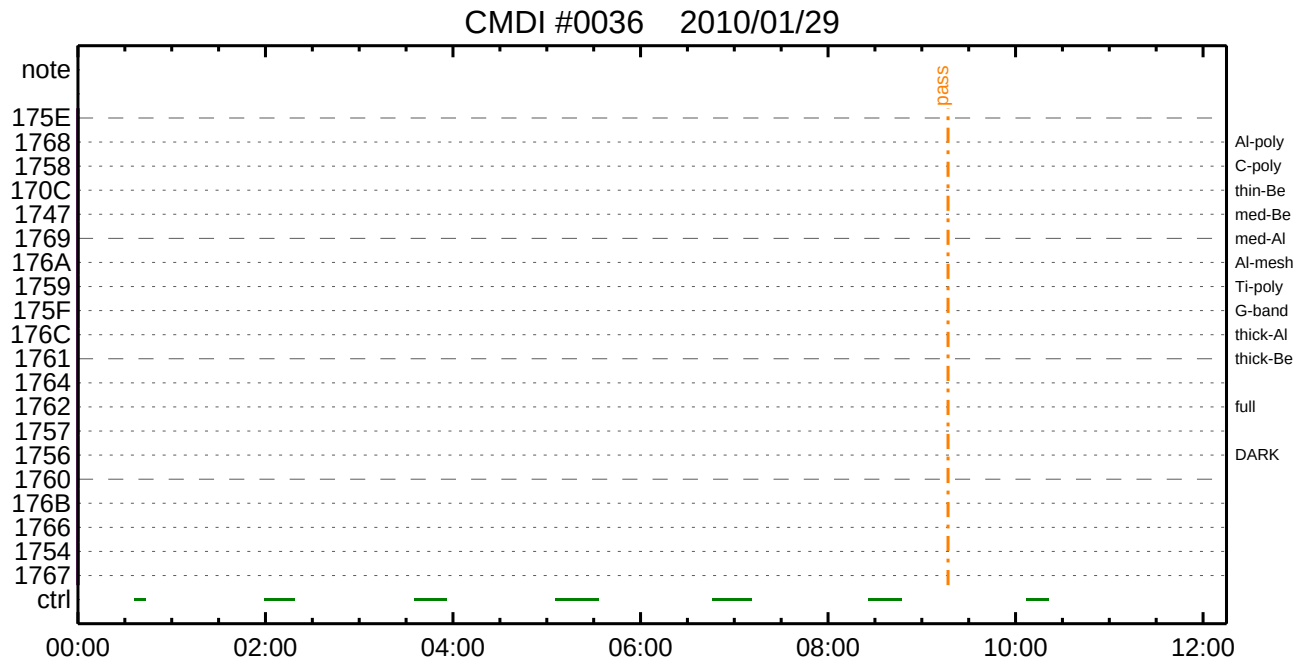

CMDI #0036 2010/01/26
note
track0
track4
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

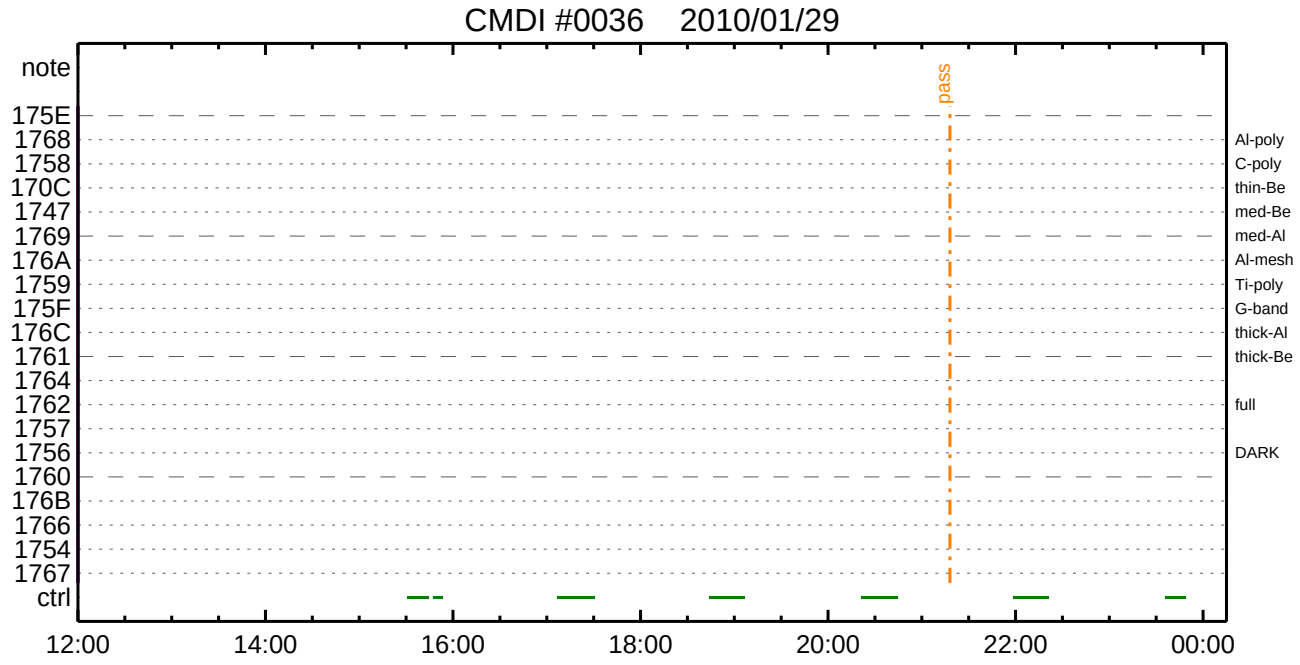

CMDI #0036 2010/01/27
note
track0
track4
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

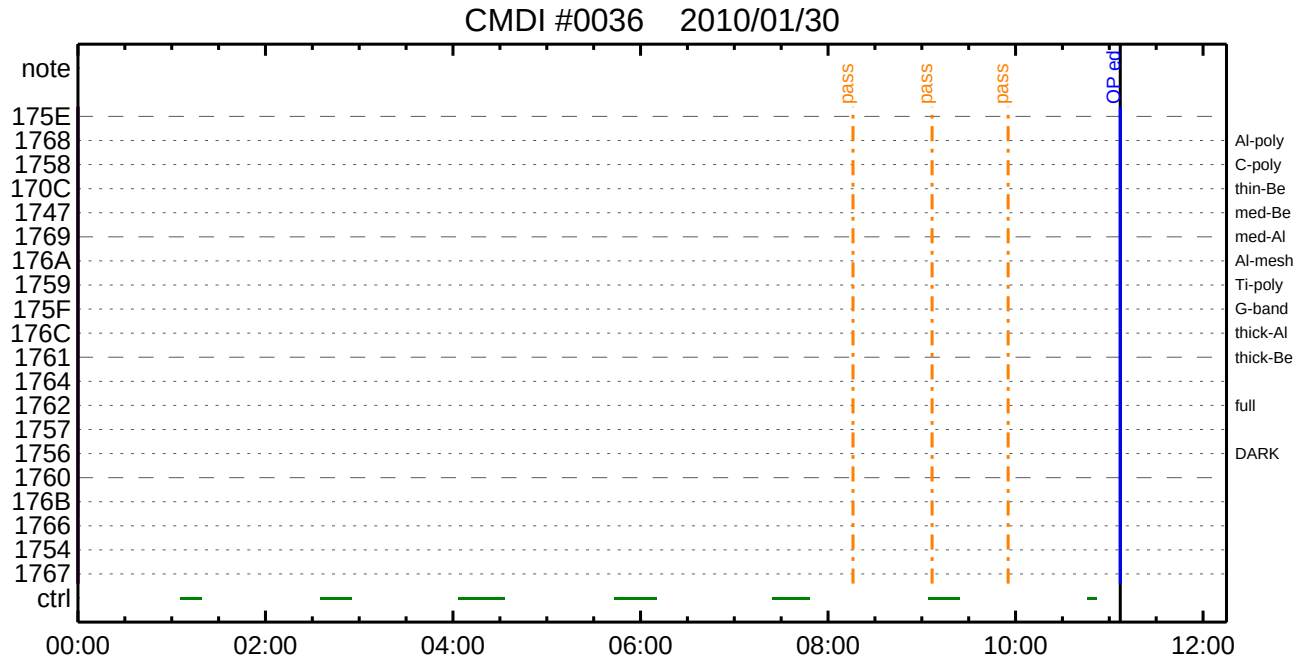

CMDI #0036 2010/01/27
note
175E
1768
1758
170C
1747
1769
176A
1759
175F
176C
1761
1764
1762
1757
1756
1760
176B
1766
1754
1767
ctrl
track0
track4
pass
pass
QT
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

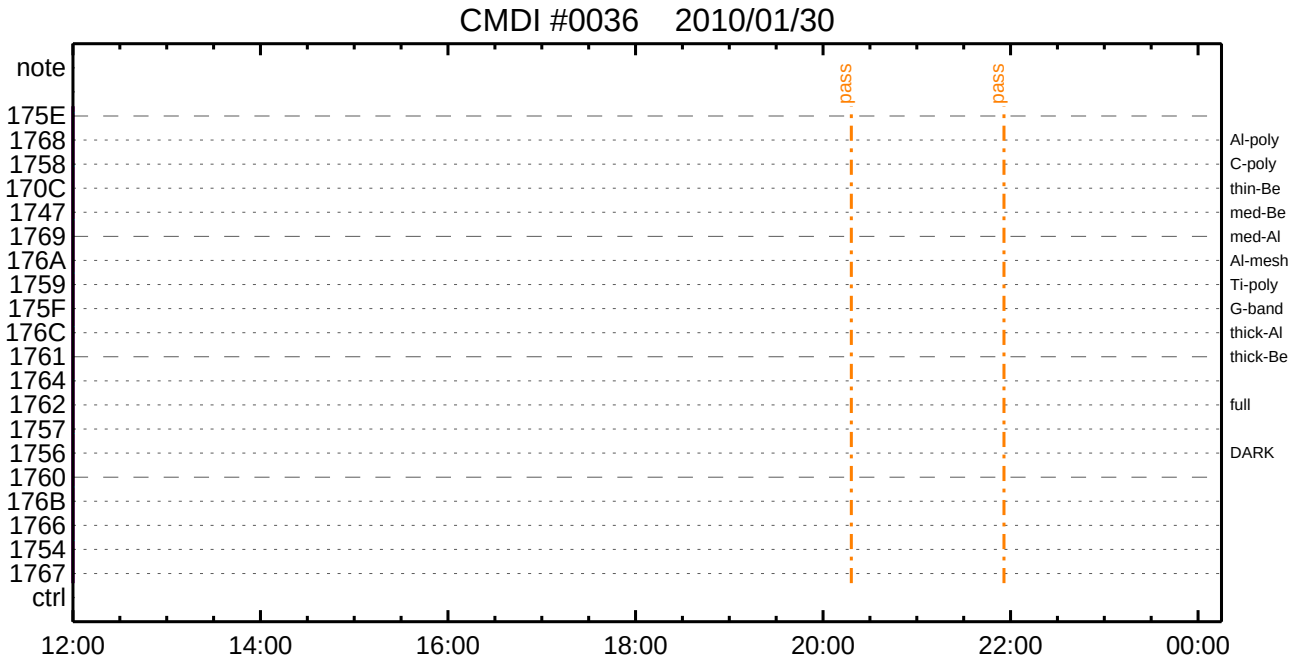

CMDI #0036 2010/01/28
note
175E
1768
1758
170C
1747
1769
176A
1759
175F
176C
1761
1764
1762
1757
1756
1760
176B
1766
1754
1767
ctrl
track0
track0
track4
pass
pass
pass
pass
track0
QT
QT
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0036 2010/01/28
note
175E
1768
1758
170C
1747
1769
176A
1759
175F
176C
1761
1764
1762
1757
1756
1760
176B
1766
1754
1767
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0036 2010/01/29
note
175E
1768
1758
170C
1747
1769
176A
1759
175F
176C
1761
1764
1762
1757
1756
1760
176B
1766
1754
1767
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0036 2010/01/29
note
175E
1768
1758
170C
1747
1769
176A
1759
175F
176C
1761
1764
1762
1757
1756
1760
176B
1766
1754
1767
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0036 2010/01/30
note
175E
1768
1758
170C
1747
1769
176A
1759
175F
176C
1761
1764
1762
1757
1756
1760
176B
1766
1754
1767
ctrl
pass
pass
pass
OP ed
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0036 2010/01/30
note
175E
1768
1758
170C
1747
1769
176A
1759
175F
176C
1761
1764
1762
1757
1756
1760
176B
1766
1754
1767
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-129 2010-01-26 13:33:13 289 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
   C.
   C.
 . C. ****************************
   C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
   C. ****************************
   C.
 . C. ¡ãOP/OG¥í¡¼¥É¡ä
 . S. OP    op-129:OP
   ()
 . S. OG    og-129:OG
   ()
   C.
 . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
   C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
+. DC 01-23 DHU_DMA_DMP_PRM_SET
   BC       (20 00 7f 01 02)
   C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
   C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
   C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
   C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
   C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
+. DC 01-22 DHU_MODE_CHNG
   BC       (07 0b f8)
   C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
   C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
   C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
   C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
   C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
 . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
   C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
 . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
   C.
   C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
+. DC 01-23 DHU_DMA_DMP_PRM_SET
   BC       (20 80 7f 01 02)
   C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
   C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
   C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
   C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
   C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
+. DC 01-22 DHU_MODE_CHNG
   BC       (07 0b f8)
   C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
   C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
   C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
   C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
   C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
 . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
   C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
 . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
   C.
   C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
+. DC 01-23 DHU_DMA_DMP_PRM_SET
   BC       (21 00 41 01 02)
   C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
   C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
   C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
   C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
   C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
+. DC 01-22 DHU_MODE_CHNG
   BC       (07 0b f8)
   C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
   C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
   C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
   C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
   C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
 . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
   C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
 . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
   C.
 . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
   C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
+. DC 01-22 DHU_MODE_CHNG
   BC       (02 0a f8)
   C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
   C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
   C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
   C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
   C.
 . C. *********************************
   C. TI-CMD SET (OPOG STOP/COPY/START)
   C. *********************************
   C.
 . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
   C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
   C.
 . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
+. TI 2010-01-26 10:44:00.0
   DC 01-B3 DHU_OP_STOP
   C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
   C.
+. TI 2010-01-26 10:44:01.0
   DC 01-B4 DHU_OP_COPY
   C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
   C.
+. TI 2010-01-26 10:44:01.0
   DC 01-B5 DHU_OPOG_COPY
   C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195   +. TI 2010-01-26 10:48:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238    . C. Stop EIS observation and temporarily disable EIS mode changes
0239     C.
0240     C.
0241     C. ********** Start EIS operation (TI set) **********
0242     C. Execute, after the success of OP upload.
0243     C. Set EIS TI-commands
0244   +. TI 2010-01-26 10:48:30.0
0245     DC 07-FC EIS_MODE_MANU
0246     BC       (21 02)
0247   +. TI 2010-01-26 10:48:40.0
0248     DC 07-FC EIS_MODE_CHG_DIS
0249     BC       (22)
0250    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0251     C. ************ End EIS operation (TI set) ************
0252     C.
0253     C.
0254     C. ********************************
0255     C.  SOT TI command set
0256     C. ********************************
0257     C. Execute, after the success of OP upload.
0258   +. TI 2010-01-26 10:48:16.0
0259     DC 07-F0 MDP_SOT_MODE_STBY
0260     BC       (41)
0261    . C. ------------------------------------
0262     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0263     C. ------------------------------------
0264     C. ************ SOT END ************
0265     C.
0266     C. ************ XRT START ************
0267     C. Execute, after the success of OP upload.
0268   +. TI 2010-01-26 10:48:00.0
0269     DC 07-F0 MDP_XRT_MODE_STBY
0270     BC       (c3)
0271    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0272     C.
0273     C. ************ XRT END ************
0274     C.
0275    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0276     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0277    . S. DC-BC dcbc-402:DCBC
0278     (MDP_known_event)
0279     C.
0280     C.
0281    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0282    . S. DC-BC dcbc-153:DCBC
0283     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284     C.
0285     C.
0286    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0287     C.
0288    . C. ********** LOS **********
0289     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-130 2010-01-26 13:33:13 175 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇÎ»þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸ å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Äº²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106   +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113   +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                      Result = OK [ ]
0121     C.
0122   +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                         <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127   +. DC 02-8E AOCU_ORB_UPD
0128   . C.
0129   . C. ***** AOCS Commands (Orbital Element Update) *****
0130     C. Update the orbital element
0131   +. DC 02-50 AOCU_ORB_PRPGT_START
0132     BC       (16)
0133   +  DC 02-8E AOCU_ORB_UPD
0134     C.
0135     C.       <A_ORB>[ORBIT] EPC = 4504909.4 +- 1.0 (s) [ ]
0136     C.
0137   . C.
0138   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139   +. DC 07-FC EIS_MODE_MANU
0140     BC       (21 02)
0141   . C. Verify EIS in MANUAL mode
0142   . C. Estimated OBSTBL upload time is 1m5s
0143     C. ********************************
0144     C. EIS START OBSTBL LOAD
0145     C. ********************************
0146   . S. RAM   ram-820:EIS_OBSTBL
0147     ()
0148   +. DC 07-FC EIS_DUMP_OBSTBL
0149     BC       (07 07 07 00 00 70 00)
0150     C.
0151     C. Execute, after the success of OBSTBL upload.
0152     C. Set EIS TI-commands
0153   +. TI 2010-01-26 10:48:50.0
0154     DC 07-FC EIS_MODE_CHG_ENA
0155     BC       (20)
0156   . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0157     C. ********************************
0158     C. EIS END OBSTBL LOAD
0159     C. ********************************
0160     C.
0161   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0162     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0163   . S. DC-BC dcbc-402:DCBC
0164     (MDP_known_event)
0165     C.
0166     C.
0167   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0168   . S. DC-BC dcbc-153:DCBC
0169     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0170     C.
0171     C.
0172   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0173     C.
0174   . C. ********** LOS **********
0175     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-131 2010-01-26 13:33:13 158 33 SOLAR-B MAIN //
0001     C.
0002    . C. *********** AOS ***********
0003     C.
0004    . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007    +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009    . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011    +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014    . C. ********************************
0015     C.  SOT table upload
0016     C. ********************************
0017    . C. < Stop FG table >
0018    +. DC 07-F0 MDP_FG_CTRL_MANU
0019     BC       (51)
0020    . C. ------------------------------------
0021     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0022     C. ------------------------------------
0023     C.
0024    . C. <Upload FG Observation Table>
0025    . S. RAM   ram-268:MDP_OBS_F
0026     ()
0027     C.
0028    . C. < Dump RAMID=MDP_OBS_F >
0029    +. DC 07-F0 MDP_DUMP_FGTBL
0030     BC       (82 07 00 00 00 38 b8)
0031     C. ------------------------------------
0032     C.  MDP_OBS_F verify = OK/NG        [ ]
0033     C. ------------------------------------
0034     C.
0035    . C. < Stop SP table >
0036    +. DC 07-F0 MDP_SP_CTRL_MANU
0037     BC       (61)
0038     C. ------------------------------------
0039     C.  MDP_SP_CTRL_MODE      = MANU    [ ]
0040     C. ------------------------------------
0041     C.
0042    . C. <Upload SP Observation Table>
0043    . S. RAM   ram-288:MDP_OBS_S
0044     ()
0045     C.
0046    . C. < Dump RAMID=MDP_OBS_S >
0047    +. DC 07-F0 MDP_DUMP_SPTBL
0048     BC       (83 07 00 00 00 38 b8)
0049     C. ------------------------------------
0050     C.  MDP_OBS_S verify = OK/NG        [ ]
0051     C. ------------------------------------
0052     C.
0053    . C. < Upload DPL table >
0054     C.
0055     C. ¥¢¥Ã¥×¥í¡¼¥É¤ÎÁ°¤ËSTS_CHK¤òOFF¤Ë¤¹¤ë
0056     C.
0057    . S. RAM   ram-271:MDP_DPL
0058     ()
0059     C.
0060    . C. < Dump RAMID=MDP_DPL >
0061    +. DC 07-F0 MDP_DUMP_FGTBL
0062     BC       (82 07 00 38 b8 00 40)
0063     C. ------------------------------------
0064     C.  MDP_DPL verify          = OK    [ ]
0065     C. ------------------------------------
0066     C.
0067     C. STS_CHK¤òON¤Ë¤¹¤ë
0068     C.
0069    . C. < Update MDP DSC PAR1 >
0070    +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0071     BC       (4c)
0072     C.  MDP_CMD_CODE          = F04C0700[ ]
0073     C.  MDP_CMD_CNT        (count-up 1) [ ]
0074     C. ------------------------------------
0075     C.
0076    . C.
0077     C. ********************************
0078     C.  SOT TI command set
0079     C. ********************************
0080     C. Execute, after the success of TBL upload.
0081    +. TI 2010-01-26 10:48:18.0
0082     DC 07-F0 MDP_SOT_MODE_OBSV
0083     BC       (40)
0084    . C. ------------------------------------
0085     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0086     C. ------------------------------------
0087     C.
0088     C.
0089     C. ************ XRT START ************
0090     C.
0091    +. DC 07-F0 MDP_XRT_CTRL_MANU
0092     BC       (c1)
0093    +  DC 07-F0 MDP_XRT_MODE_STBY
0094     BC       (c3)
0095    . C. ----------- Success Verify ?             OK / NG____
```

```
0096     C.
0097     C. XRT Obs. Table Upload
0098   . S. RAM   ram-291:MDP_OBS_X
0099     ()
0100     C.
0101  +. DC 07-F0 MDP_DUMP_XRTTBL
0102     BC       (84 07 00 00 00 3a d4)
0103   . C. ------------ Comparison Check ?                        OK / ERR ____
0104     C.
0105     C.
0106  +. DC 07-F0 MDP_XRT_ROI_SET
0107     BC       (cd 01 b1 b1 04 04)
0108  +  DC 07-F0 MDP_XRT_ROI_SET
0109     BC       (cd 02 b1 b1 08 08)
0110  +  DC 07-F0 MDP_XRT_ROI_SET
0111     BC       (cd 03 b1 b1 08 08)
0112  +  DC 07-F0 MDP_XRT_ROI_SET
0113     BC       (cd 04 b1 b1 06 06)
0114  +  DC 07-F0 MDP_XRT_ROI_SET
0115     BC       (cd 06 80 80 08 08)
0116  +  DC 07-F0 MDP_XRT_ROI_SET
0117     BC       (cd 07 80 80 20 20)
0118  +  DC 07-F0 MDP_XRT_ROI_SET
0119     BC       (cd 08 80 80 20 08)
0120  +  DC 07-F0 MDP_XRT_ROI_SET
0121     BC       (cd 09 80 80 08 20)
0122  +  DC 07-F0 MDP_XRT_ROI_SET
0123     BC       (cd 0a 85 83 08 08)
0124  +  DC 07-F0 MDP_XRT_ROI_SET
0125     BC       (cd 0f 80 80 06 06)
0126  +  DC 07-F0 MDP_XRT_ROI_SET
0127     BC       (cd 10 80 80 04 04)
0128  +  DC 07-F0 MDP_XRT_AEC_RESET
0129     BC       (d0)
0130   . C. ---------- Success Verify ?            OK / NG ____
0131     C.
0132     C.
0133   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0134     C.
0135  +. DC 07-F0 MDP_XRT_MODE_OBSV
0136     BC       (c2)
0137  +. TI 2010-01-26 10:48:02.0
0138     DC 07-F0 MDP_XRT_MODE_OBSV
0139     BC       (c2)
0140   . C. ---------- Success Verify ?            OK / NG ____
0141     C.
0142     C. ************ XRT END ***********
0143     C.
0144   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0145     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0146   . S. DC-BC dcbc-402:DCBC
0147     (MDP_known_event)
0148     C.
0149     C.
0150   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0151   . S. DC-BC dcbc-153:DCBC
0152     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0153     C.
0154     C.
0155   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0156     C.
0157   . C. *********** LOS ***********
0158     C.
```

```
*** OP Sequence for XRT ***

2010/01/26  10:58:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/26  10:58:56.0  XRT_FOCUS_POSITION_413_OG [0x19d]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/01/26  10:59:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 00 00 00 00
2010/01/26  10:59:16.0  XRT_FLD_ENA_449_OG [0x1c1]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/01/26  10:59:18.0  XRT_FLD_RESET_448_OG [0x1c0]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/01/26  10:59:20.0  XRT_FLRCTRL_ENA_443_OG [0x1bb]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/01/26  10:59:22.0  XRT_ARS_DIS_432_OG [0x1b0]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/01/26  11:01:58.0  XRT_QT_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2010/01/26  11:02:00.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 14
2010/01/26  11:02:02.0  XRT_CTRL_AUTO_407_OG [0x197]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/26  17:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/26  17:59:56.0  XRT_FOCUS_POSITION_408_OG [0x198]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/01/26  18:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2010/01/26  18:00:16.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2010/01/26  18:00:18.0  XRT_FLRCTRL_DIS_410_OG [0x19a]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/01/26  18:00:20.0  XRT_ARS_DIS_411_OG [0x19b]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/01/26  18:02:58.0  XRT_QT_PROG_SET_406_OG [0x196]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2010/01/26  18:03:00.0  XRT_CTRL_AUTO_407_OG [0x197]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/26  18:09:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/26  18:09:56.0  XRT_FOCUS_POSITION_413_OG [0x19d]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/01/26  18:10:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 00 00 00 00
2010/01/26  18:10:16.0  XRT_FLD_ENA_449_OG [0x1c1]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/01/26  18:10:18.0  XRT_FLRCTRL_ENA_443_OG [0x1bb]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/01/26  18:10:20.0  XRT_ARS_DIS_432_OG [0x1b0]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/01/26  18:12:56.0  XRT_QT_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2010/01/26  18:12:58.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 14
2010/01/26  18:13:00.0  XRT_CTRL_AUTO_407_OG [0x197]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/26  21:19:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/26  21:19:56.0  XRT_FOCUS_POSITION_413_OG [0x19d]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/01/26  21:20:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 00 00 00 00
2010/01/26  21:20:16.0  XRT_FLD_ENA_449_OG [0x1c1]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/01/26  21:20:18.0  XRT_FLRCTRL_ENA_443_OG [0x1bb]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/01/26  21:20:20.0  XRT_ARS_DIS_432_OG [0x1b0]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/01/26  21:22:56.0  XRT_QT_PROG_SET_427_OG [0x1ab]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2010/01/26  21:22:58.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 14
2010/01/26  21:23:00.0  XRT_CTRL_AUTO_407_OG [0x197]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/27  00:29:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/27  00:29:56.0  XRT_FOCUS_POSITION_413_OG [0x19d]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/01/27  00:30:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 04 08 e5 0a a7
2010/01/27  00:30:16.0  XRT_FLD_ENA_449_OG [0x1c1]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/01/27  00:30:18.0  XRT_FLD_RESET_448_OG [0x1c0]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/01/27  00:30:20.0  XRT_FLRCTRL_ENA_443_OG [0x1bb]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/01/27  00:30:22.0  XRT_ARS_DIS_432_OG [0x1b0]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/01/27  00:32:58.0  XRT_QT_PROG_SET_427_OG [0x1ab]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2010/01/27  00:33:00.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 14
2010/01/27  00:33:02.5  XRT_CTRL_AUTO_407_OG [0x197]
```

```
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/27  01:49:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/27  01:49:56.0  XRT_FOCUS_POSITION_413_OG [0x19d]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/01/27  01:50:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                    AOCU_NM              5 02-76 04 fe 36 12 a7
2010/01/27  01:50:16.0  XRT_FLD_ENA_449_OG [0x1c1]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2010/01/27  01:50:18.0  XRT_FLD_RESET_448_OG [0x1c0]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2010/01/27  01:50:20.0  XRT_FLRCTRL_ENA_443_OG [0x1bb]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/01/27  01:50:22.0  XRT_ARS_DIS_432_OG [0x1b0]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2010/01/27  01:52:58.0  XRT_QT_PROG_SET_427_OG [0x1ab]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2010/01/27  01:53:00.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 14
2010/01/27  01:53:02.5  XRT_CTRL_AUTO_407_OG [0x197]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/27  02:29:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/27  02:29:56.0  XRT_FOCUS_POSITION_413_OG [0x19d]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/01/27  02:30:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                    AOCU_NM              5 02-76 04 f6 36 00 00
2010/01/27  02:30:16.0  XRT_FLD_ENA_449_OG [0x1c1]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2010/01/27  02:30:18.0  XRT_FLD_RESET_448_OG [0x1c0]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2010/01/27  02:30:20.0  XRT_FLRCTRL_ENA_443_OG [0x1bb]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/01/27  02:30:22.0  XRT_ARS_DIS_432_OG [0x1b0]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2010/01/27  02:32:58.0  XRT_QT_PROG_SET_427_OG [0x1ab]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2010/01/27  02:33:00.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 14
2010/01/27  02:33:02.5  XRT_CTRL_AUTO_407_OG [0x197]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/27  03:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/27  03:59:56.0  XRT_FOCUS_POSITION_413_OG [0x19d]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/01/27  04:00:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                    AOCU_NM              5 02-76 04 00 00 ed cb
2010/01/27  04:00:16.0  XRT_FLD_ENA_449_OG [0x1c1]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2010/01/27  04:00:18.0  XRT_FLD_RESET_448_OG [0x1c0]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2010/01/27  04:00:20.0  XRT_FLRCTRL_ENA_443_OG [0x1bb]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/01/27  04:00:22.0  XRT_ARS_DIS_432_OG [0x1b0]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2010/01/27  04:02:58.0  XRT_QT_PROG_SET_427_OG [0x1ab]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2010/01/27  04:03:00.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 14
2010/01/27  04:03:02.5  XRT_CTRL_AUTO_407_OG [0x197]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/27  04:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/27  04:59:56.0  XRT_FOCUS_POSITION_413_OG [0x19d]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/01/27  05:00:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                    AOCU_NM              5 02-76 04 00 00 00 00
2010/01/27  05:00:16.0  XRT_FLD_ENA_449_OG [0x1c1]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2010/01/27  05:00:18.0  XRT_FLD_RESET_448_OG [0x1c0]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2010/01/27  05:00:20.0  XRT_FLRCTRL_ENA_443_OG [0x1bb]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/01/27  05:00:22.0  XRT_ARS_DIS_432_OG [0x1b0]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2010/01/27  05:02:58.0  XRT_QT_PROG_SET_439_OG [0x1b7]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2010/01/27  05:03:00.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 14
2010/01/27  05:03:02.5  XRT_CTRL_AUTO_407_OG [0x197]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/27  06:06:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/27  06:06:26.0  XRT_FOCUS_POSITION_408_OG [0x198]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/01/27  06:06:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2010/01/27  06:06:46.0  XRT_FLD_DIS_409_OG [0x199]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2010/01/27  06:06:48.0  XRT_FLRCTRL_DIS_410_OG [0x19a]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/01/27  06:06:50.0  XRT_ARS_DIS_411_OG [0x19b]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
```

```
2010/01/27  06:09:28.0  XRT_QT_PROG_SET_406_OG [0x196]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2010/01/27  06:09:30.0  XRT_CTRL_AUTO_407_OG [0x197]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/27  06:16:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/27  06:16:26.0  XRT_FOCUS_POSITION_413_OG [0x19d]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/01/27  06:16:30.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 00 00 00 00
2010/01/27  06:16:46.0  XRT_FLD_ENA_449_OG [0x1c1]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/01/27  06:16:48.0  XRT_FLRCTRL_ENA_443_OG [0x1bb]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/01/27  06:16:50.0  XRT_ARS_DIS_432_OG [0x1b0]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/01/27  06:19:26.0  XRT_QT_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2010/01/27  06:19:28.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 14
2010/01/27  06:19:30.0  XRT_CTRL_AUTO_407_OG [0x197]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/27  14:21:00.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/27  14:26:30.0  XRT_Custom_420_OG [0x1a4]
2010/01/27  14:27:30.0  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/27  15:53:30.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/27  16:17:00.0  XRT_Custom_420_OG [0x1a4]
2010/01/27  16:18:00.0  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/27  17:30:00.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/27  17:53:30.0  XRT_Custom_420_OG [0x1a4]
2010/01/27  17:54:30.0  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/27  17:55:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/27  17:55:26.0  XRT_FOCUS_POSITION_408_OG [0x198]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/01/27  17:55:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2010/01/27  17:55:46.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2010/01/27  17:55:48.0  XRT_FLRCTRL_DIS_410_OG [0x19a]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/01/27  17:55:50.0  XRT_ARS_DIS_411_OG [0x19b]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/01/27  17:58:28.0  XRT_QT_PROG_SET_406_OG [0x196]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2010/01/27  17:58:30.0  XRT_CTRL_AUTO_407_OG [0x197]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/27  18:05:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/27  18:05:26.0  XRT_FOCUS_POSITION_413_OG [0x19d]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/01/27  18:05:30.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 00 00 00 00
2010/01/27  18:05:46.0  XRT_FLD_ENA_449_OG [0x1c1]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/01/27  18:05:48.0  XRT_FLRCTRL_ENA_443_OG [0x1bb]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/01/27  18:05:50.0  XRT_ARS_DIS_432_OG [0x1b0]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/01/27  18:08:26.0  XRT_QT_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2010/01/27  18:08:28.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 14
2010/01/27  18:08:30.0  XRT_CTRL_AUTO_407_OG [0x197]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/27  19:07:00.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/27  19:30:30.0  XRT_Custom_420_OG [0x1a4]
2010/01/27  19:31:30.0  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/27  20:44:30.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/27  21:08:00.5  XRT_Custom_420_OG [0x1a4]
2010/01/27  21:09:00.5  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/27  22:22:00.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/27  22:43:30.0  XRT_Custom_420_OG [0x1a4]
2010/01/27  22:44:30.0  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/27  23:59:30.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/28  00:09:30.0  XRT_Custom_420_OG [0x1a4]
2010/01/28  00:10:30.0  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/28  01:25:00.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
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2010/01/28  01:42:00.0  XRT_Custom_420_OG [0x1a4]
2010/01/28  01:43:00.0  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/28  02:58:00.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/28  03:19:00.5  XRT_Custom_420_OG [0x1a4]
2010/01/28  03:20:00.5  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/28  04:27:30.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/28  04:56:30.5  XRT_Custom_420_OG [0x1a4]
2010/01/28  04:57:30.5  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/28  05:40:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/28  05:40:26.0  XRT_FOCUS_POSITION_408_OG [0x198]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/01/28  05:40:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2010/01/28  05:40:46.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2010/01/28  05:40:48.0  XRT_FLRCTRL_DIS_410_OG [0x19a]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/01/28  05:40:50.0  XRT_ARS_DIS_411_OG [0x19b]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/01/28  05:43:28.0  XRT_QT_PROG_SET_406_OG [0x196]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2010/01/28  05:43:30.0  XRT_CTRL_AUTO_407_OG [0x197]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/28  05:50:30.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                AOCU_NM              5 02-76 00 00 00 55 58
2010/01/28  06:08:00.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/28  06:29:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/28  06:29:56.0  XRT_FOCUS_POSITION_413_OG [0x19d]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/01/28  06:30:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 00 00 00 00
2010/01/28  06:30:16.0  XRT_FLD_ENA_449_OG [0x1c1]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/01/28  06:30:18.0  XRT_FLRCTRL_ENA_443_OG [0x1bb]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/01/28  06:30:20.0  XRT_ARS_DIS_432_OG [0x1b0]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/01/28  06:32:56.0  XRT_QT_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2010/01/28  06:32:58.0  XRT_FL_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 14
2010/01/28  06:34:00.5  XRT_Custom_420_OG [0x1a4]
2010/01/28  06:35:00.5  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/28  07:48:30.0  XRT_CTRL_MANU_419_OG [0x1a3]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/28  08:11:00.0  XRT_Custom_420_OG [0x1a4]
2010/01/28  08:12:00.0  XRT_CTRL_AUTO_416_OG [0x1a0]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/28  10:34:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/01/28  10:34:56.0  XRT_FOCUS_POSITION_413_OG [0x19d]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/01/28  10:35:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2010/01/28  10:35:16.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2010/01/28  10:35:18.0  XRT_FLRCTRL_DIS_410_OG [0x19a]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/01/28  10:35:20.0  XRT_ARS_DIS_432_OG [0x1b0]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/01/28  10:37:56.0  XRT_QT_PROG_SET_429_OG [0x1ad]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 10
2010/01/28  10:37:58.0  XRT_CTRL_AUTO_447_OG [0x1bf]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/01/28  10:38:58.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
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