

XRT Timeline to be uploaded on 2010/03/02

Period: 2010/03/02 10:44:00 - 2010/03/05 10:57:00

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Normal mode

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XOB #1786: AR Dynamics - Ti/Poly - 512x512 - AEC 1 - Q92 - G-band Co-Align - Thick/Al-2													
Term	Pointing (x, y)	Comment											
03/02 10:56:26 - 03/02 14:59:54	Track (60.3, 373.7) @ 03/02 10:54:00	# OP start + 10min: AR11051 tracking											
03/02 19:32:52 - 03/03 05:29:54	Track (137.2, 372.6) @ 03/02 19:30:00	# AR11051 tracking											
03/03 05:49:12 - 03/03 09:47:50	Track (226.7, 370.4) @ 03/03 05:40:00	# AR 11051 tracking											
PROG= 04 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 6 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 4 7-time(s) 120.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	512x512 (1024, 1024)	Q=92	1	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1781: HOP 144 - Ti/Poly - 384x384 - AEC 1 - 40s - G-band Co-align													
Term	Pointing (x, y)	Comment											
03/02 15:09:12 - 03/02 18:56:30	Track (60.0, -950.0) @ 03/02 15:00:00	HOP144 : spicule obs with tracking allocated in (60, -950) at 2010/03/02 15:00:00.											
PROG= 01 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 3 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
└─ Seqn= 9 20-time(s) 40.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1772: Synoptic Q95 2x2 - Al/mesh(33/512) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1024) + G-band(16)-2													
Term	Pointing (x, y)	Comment											
03/02 19:23:00 - 03/02 19:29:54	Fixed (0.0, 0.0)	synoptic											
03/03 05:33:00 - 03/03 05:39:54	Fixed (0.0, 0.0)	synoptic, shifted -30.0 min											
PROG= 02 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 5 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 10 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 1 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 8 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1776: Flare Response short exp at first- Dynamics - Thick-Al - Thick-Be - AEC 1 - 384x384 - Q95													
Term	Pointing (x, y)	Comment											
03/02 10:56:26 - 03/02 14:59:54	Track (60.3, 373.7) @ 03/02 10:54:00	# OP start + 10min: AR11051 tracking											
03/02 15:09:12 - 03/02 18:56:30	Track (60.0, -950.0) @ 03/02 15:00:00	HOP144 : spicule obs with tracking allocated in (60, -950) at 2010/03/02 15:00:00.											
03/02 19:32:52 - 03/03 05:29:54	Track (137.2, 372.6) @ 03/02 19:30:00	# AR11051 tracking											
03/03 05:49:12 - 03/03 09:47:50	Track (226.7, 370.4) @ 03/03 05:40:00	# AR 11051 tracking											
PROG= 03 1-time(s)													
└─ Subr= 1 2-time(s) 2.0sec													
└─ Seqn= 7 15-time(s) 20.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
└─ Seqn= 2 1-time(s) 4.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└─ Subr= 2 8-time(s) 2.0sec													
└─ Seqn= 7 15-time(s) 60.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
└─ Seqn= 2 1-time(s) 4.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└─ Subr= 3 25-time(s) 2.0sec													
└─ Seqn= 7 1-time(s) 600.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec

Seqn= 2 1-time(s) 4.0sec

Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

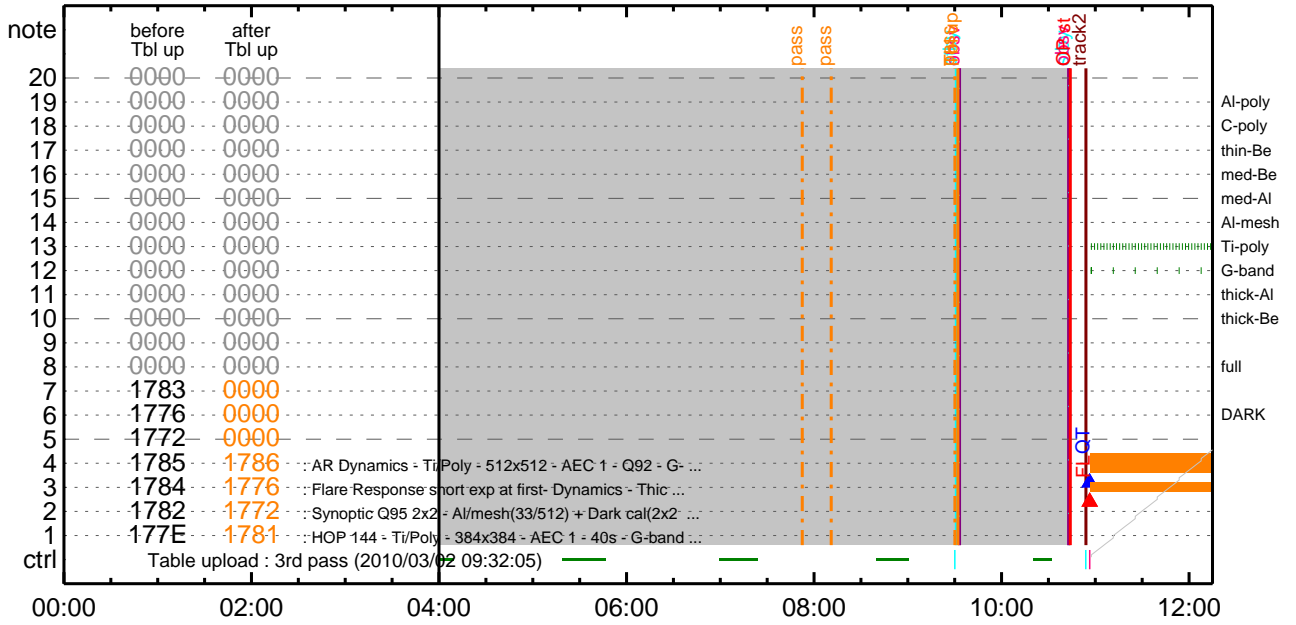
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Flare Detection

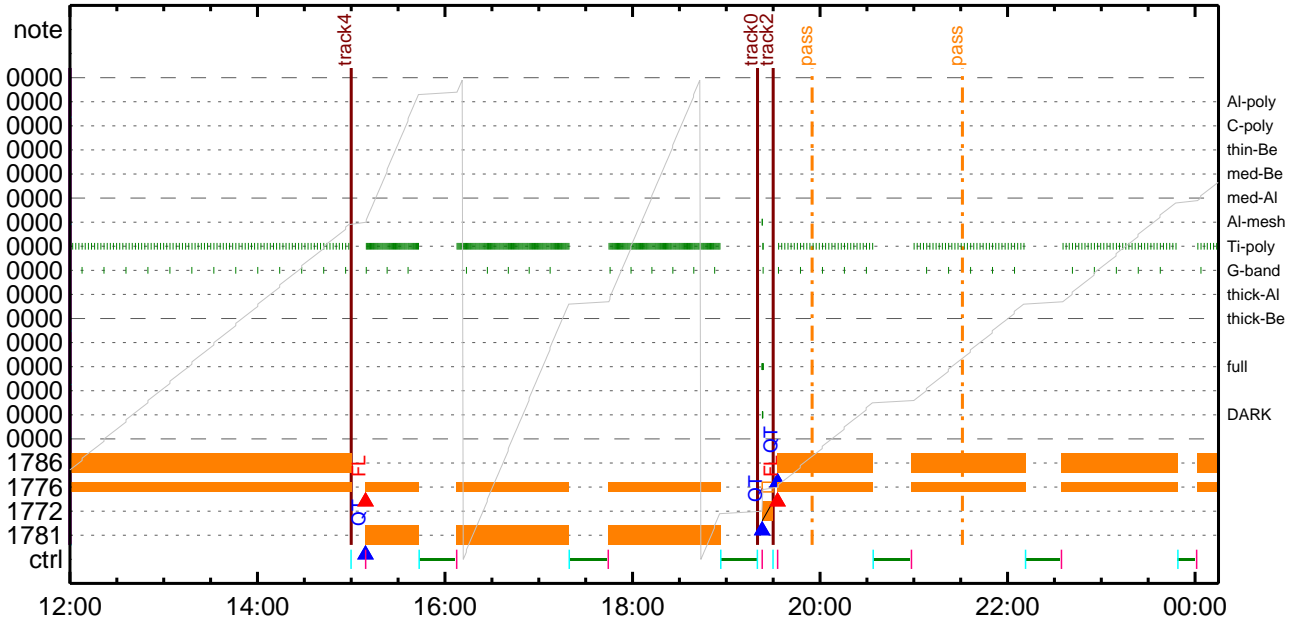
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FLD Patrol												
Term	Pointing (x, y)		Comment									
03/02 10:54:16 - 03/02 19:20:16	Track (60.3, 373.7)	[Ⓜ] 03/02 10:54:00	# OP start + 10min: AR11051 tracking									
03/02 19:30:16 - 03/03 05:30:16	Track (137.2, 372.6)	[Ⓜ] 03/02 19:30:00	# AR11051 tracking									
03/03 05:40:16 - 03/05 10:57:00	Track (226.7, 370.4)	[Ⓜ] 03/03 05:40:00	# AR 11051 tracking									
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	120sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

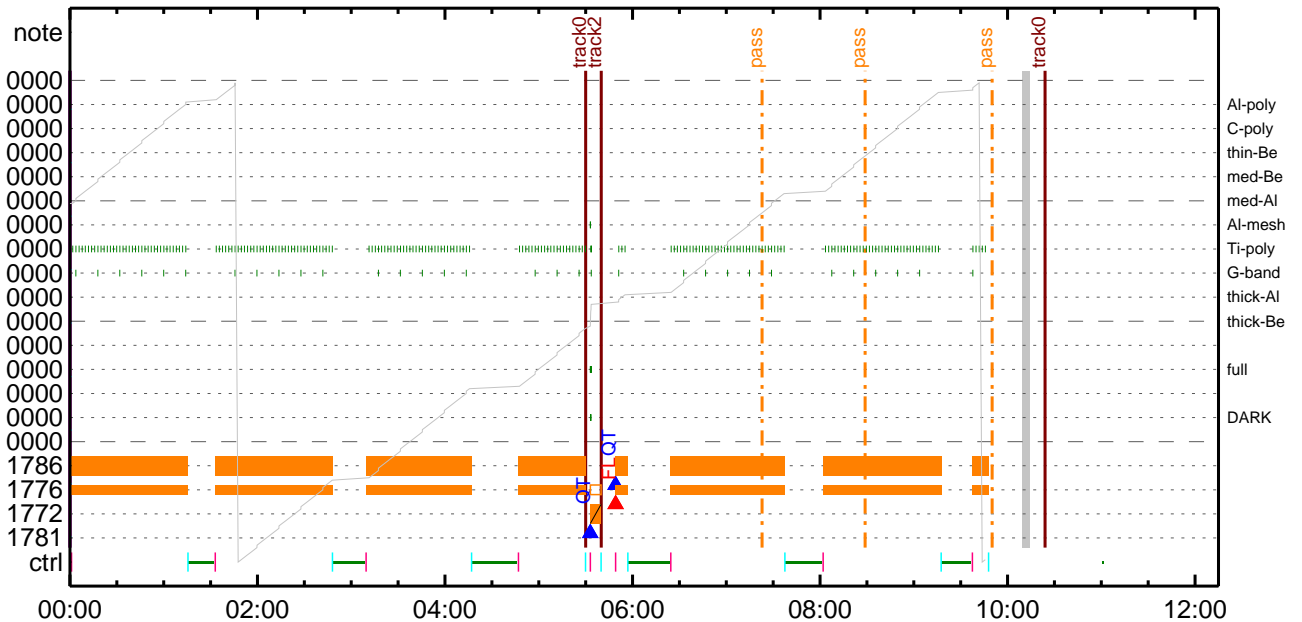
CMDI #0121 2010/03/02



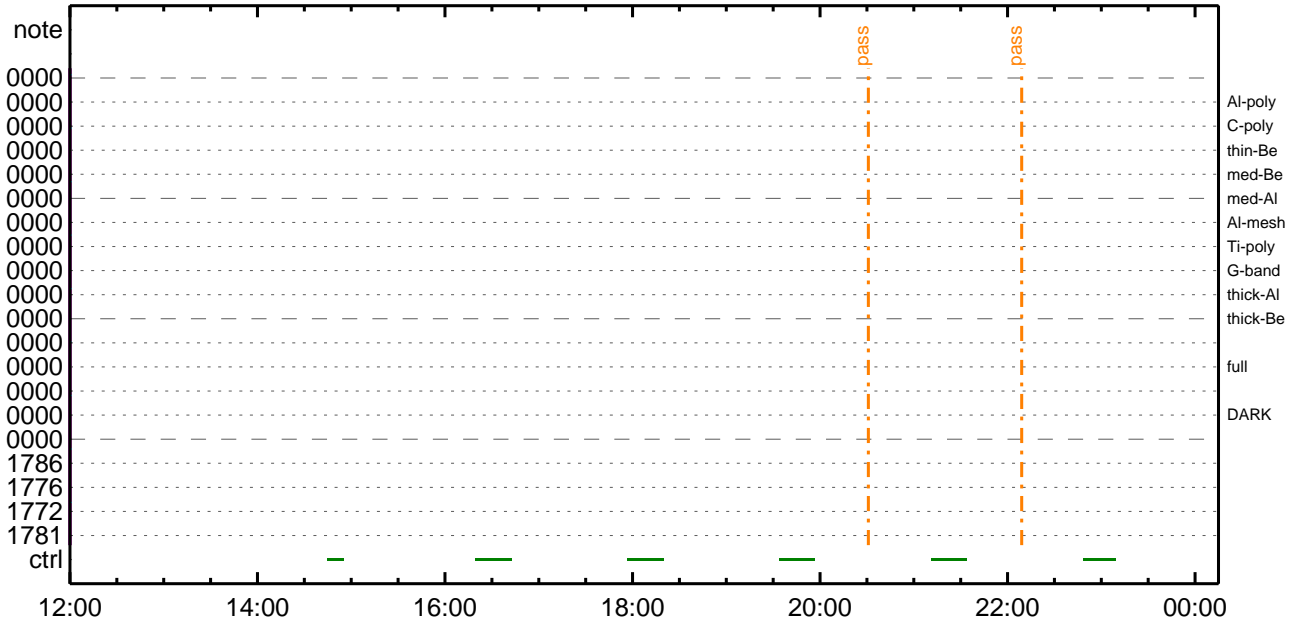
CMDI #0121 2010/03/02



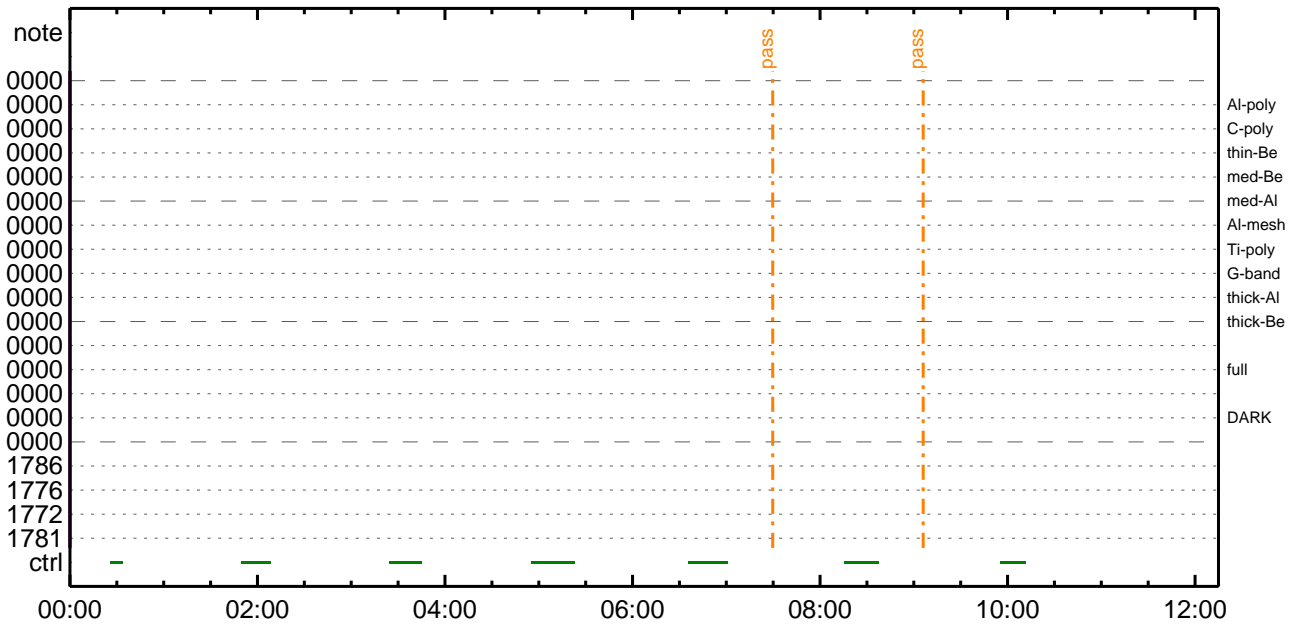
CMDI #0121 2010/03/03



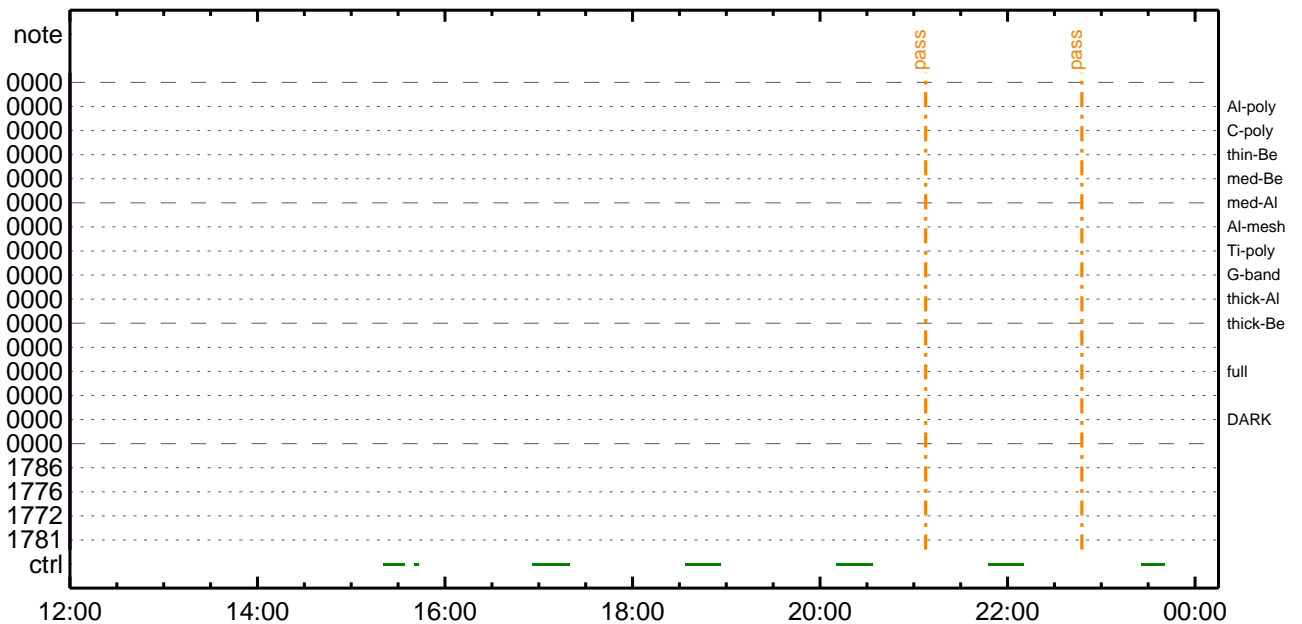
CMDI #0121 2010/03/03



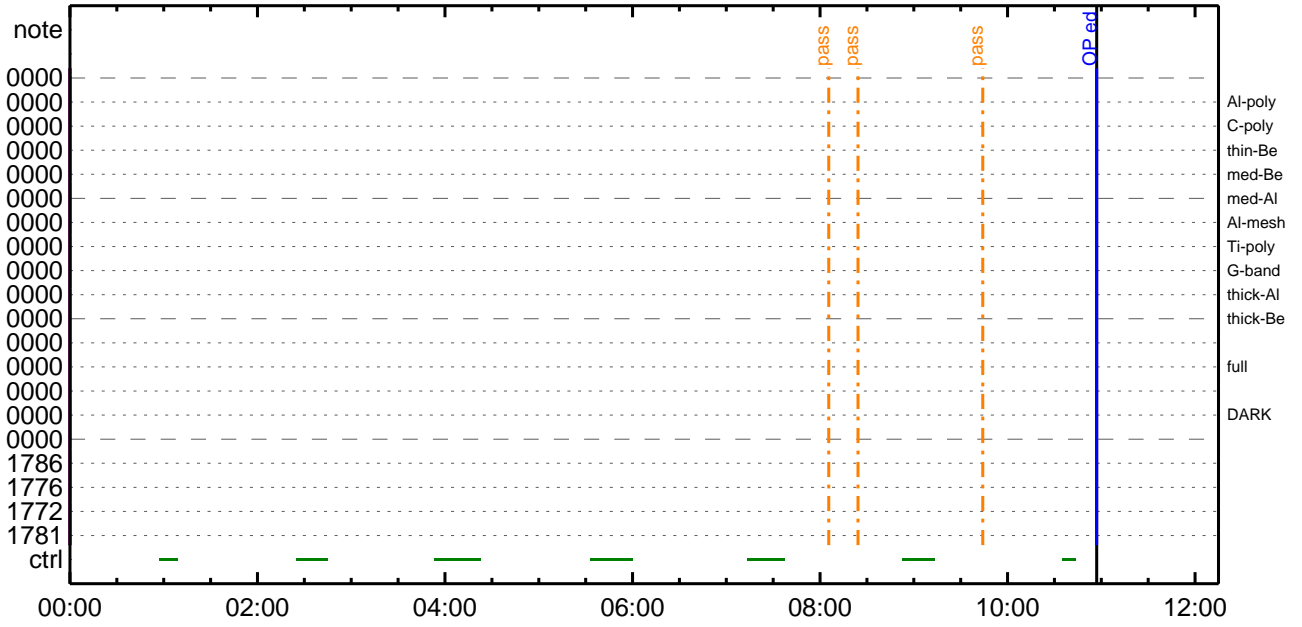
CMDI #0121 2010/03/04



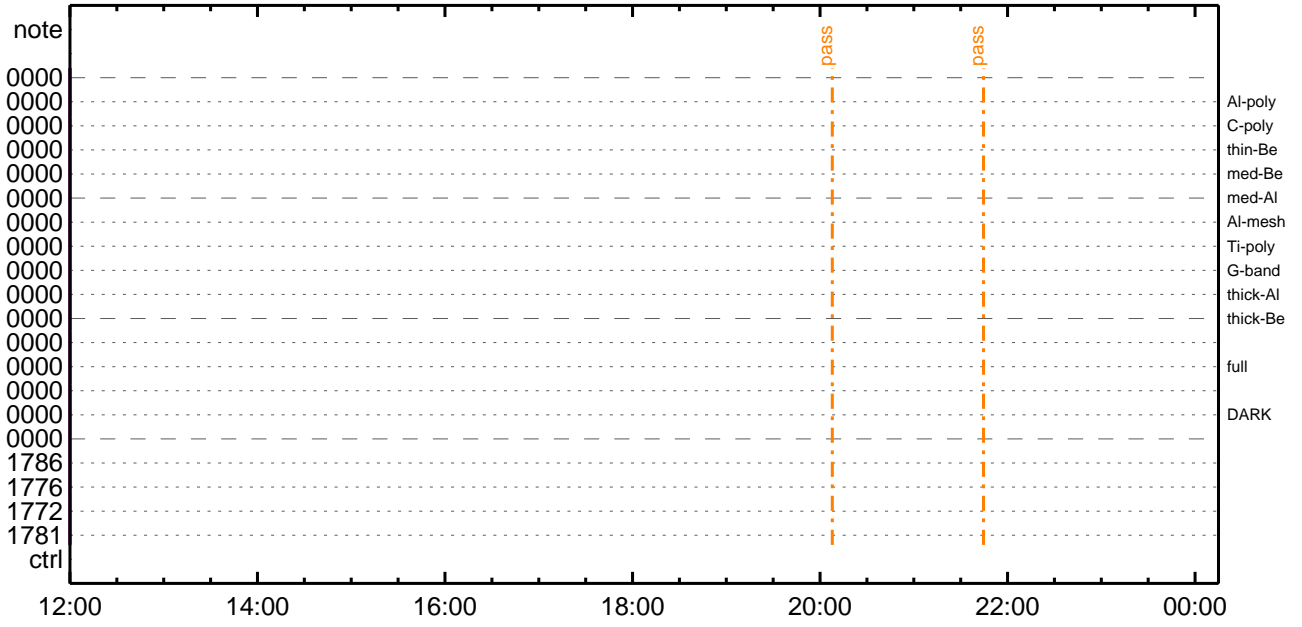
CMDI #0121 2010/03/04



CMDI #0121 2010/03/05



CMDI #0121 2010/03/05




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;Y4E;YAY6Yx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;Y4E;ã
0103 S. OP op-205:OP
0104 ( )
0105 S. OG og-205:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAY6Yx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAY6Yx½ªî»ð³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGñî¼E¹ç•è²îOKð³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAY6Yx½ªî»ð³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGñî¼E¹ç•è²îOKð³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAY6Yx½ªî»ð³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG,RAM ID=OPñî¼E¹ç•è²îOKð³îÇ§
0165 C.
0166 C. ***** òÈ²¼ðî¼Ä´¶Á°òÈÈ¬ò°Á÷¿@ (%âµ-YAY6Yx½ªî»ð³îÇ§) *****
0167 C. DHUÿâ;Y4E;È¼Y½;Y1;Y4E;ÈðîÄª¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADñ-Á÷¿@NGñî¼E¹ç;ç°È²¼ðî¼TI-CMDÁ÷¿@ñî¼Ä¹Òª•ñÈªªª³ñÈ;f
0180 C. òÈª¿;çSETòÈDUMPñî¼E±°îYÑY¹ç¹Òª|ª³ñÈ;f
0181 C.
0182 C. TIY³Y½Y6Y4EòðÄDî¿(UT)
0183 +. TI 2010-03-02 10:39:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2010-03-02 10:39:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2010-03-02 10:39:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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(a) Spacecraft Operation Procedure (real-commands)

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main-207 2010-03-02 12:40:05 224 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŠYÁY-¼Á»Ü;ã
0005 C.
0006 C. YÀYB;¼Y³YBYÓYÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ÈççðÀð•µ°È»Í×ÁÇçÍYçYÁY×Yí;¼YÉ;ÈÈè%µ•ííÈ;ÈðÈ¼°ÇÓñ•çç¼l¹ççí;çÀ®, ùñ¹ñðñðçÁ+ç®ñ•ñÈñññ³ñÈ; Æ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ççµ;ON
0016 C. *****
0017 C. ç” °ÆÀ, Í×ÈYñÀLOSñðççí»p´Öñð¹íí, ñ•; çÉÔÍ×ñÈXÁÓONñí¹ÖñÈñíñÈññ³ñÈ; Æ
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYíYÁY-¼ÔÁÖñ-°ÁÀèñ•çç; ç°È²¼ñí°ÆÀ, ¼è%ççðð¼Á¹Öñ¹ñÈ; Æ
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÆÀ,
0033 C. *****
0034 C. ç” RESTART; ÈPT1; Èñ•çççç¼l¹ççí; ç°È²¼ñí°ÆÀ¹Öñ»ñ°; çDCBC-150ñççÈñà; Æ
0035 C.
0036 . C. ;ãPT1°ÆÀ, ³«»í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÓYÉYÈÁÜÁØ; ÈÁ•Á°²óÈð; È, áñí°ÆÀ, °Æ³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ, ñ-¼«Æ°Áá»ññ•çç, á; ç°È²¼ñð¼Á¹Öñ¹ñÈ; Æ
0055 C. YçYÓYÉYÈÁÜÁØñÁ•Á°²óÈðñ-¶áñç¼l¹ççí°í»ñ¹ñÈñðççÁÖñÁ; Æ
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÆÀ,
0059 C. *****
0060 C. ç” RESTART; ÈPT2; Èñ•çççç¼l¹ççí; ç°È²¼ñí°ÆÀ¹Öñ»ñ°; çDCBC-151ñççÈñà; Æ
0061 C.
0062 . C. ;ãPT2°ÆÀ, ³«»í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÓYÉYÈÁÜÁØ; ÈÁ•Á°²óÈð; È, áñí°ÆÀ, °Æ³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ, Áá»ñ; çXÁ+ççµ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ, Áá»ñ;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ççµ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 06 80 80 08 08)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 07 80 60 20 18)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 08 85 83 06 06)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 09 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 0a 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0b 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_AEC_RESET
0139 BC (d0)
0140 . C. ----- Success Verify ? OK / NG ____
0141 C.
0142 C.
0143 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0144 C.
0145 +. DC 07-F0 MDP_XRT_MODE_OBSV
0146 BC (c2)
0147 +. TI 2010-03-02 10:43:02.0
0148 DC 07-F0 MDP_XRT_MODE_OBSV
0149 BC (c2)
0150 . C. ----- Success Verify ? OK / NG ____
0151 C.
0152 C. ***** XRT END *****
0153 . C. *****
0154 C. SOT table upload
0155 C. *****
0156 . C. < Stop FG table >
0157 +. DC 07-F0 MDP_FG_CTRL_MANU
0158 BC (51)
0159 . C. -----
0160 C. MDP_FG_CTRL_MODE = MANU [ ]
0161 C. -----
0162 C.
0163 . C. <Upload FG Observation Table>
0164 . S. RAM ram-263:MDP_OBS_F
0165 ( )
0166 C.
0167 . C. < Dump RAMID=MDP_OBS_F >
0168 +. DC 07-F0 MDP_DUMP_FGTBL
0169 BC (82 07 00 00 00 38 b8)
0170 C. -----
0171 C. MDP_OBS_F verify = OK/NG [ ]
0172 C. -----
0173 C.
0174 . C. < Upload DPL table >
0175 C.
0176 C. ¥ç¥Ã¥×¥í;¼¥É¥î¥°¥ESTS_CHK¥ð¥OFF¥È¥¹¥ë
0177 C.
0178 . S. RAM ram-271:MDP_DPL
0179 ( )
0180 C.
0181 . C. < Dump RAMID=MDP_DPL >
0182 +. DC 07-F0 MDP_DUMP_FGTBL
0183 BC (82 07 00 38 b8 00 40)
0184 C. -----
0185 C. MDP_DPL verify = OK [ ]
0186 C. -----
0187 C.
0188 C. STS_CHK¥ð¥ON¥È¥¹¥ë
0189 C.
0190 . C. < Update MDP DSC PAR1 >
0191 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0192 BC (4c)
0193 C. MDP_CMD_CODE = F04C0700[ ]

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*** OP Sequence for XRT ***

2010/03/02	10:53:54.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/03/02	10:53:56.0	XRT_FOCUS_POSITION_401_OG [0x191]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2010/03/02	10:54:00.0	AOCS_Ore-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	02 00 00 00 00			
2010/03/02	10:54:16.0	XRT_FLD_ENA_402_OG [0x192]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2010/03/02	10:54:18.0	XRT_FLD_RESET_403_OG [0x193]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2010/03/02	10:54:20.0	XRT_FLRCTRL_ENA_404_OG [0x194]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2010/03/02	10:54:22.0	XRT_ARS_DIS_418_OG [0x1a2]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/03/02	10:56:22.0	XRT_QT_PROG_SET_406_OG [0x196]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04			
2010/03/02	10:56:24.0	XRT_FL_PROG_SET_407_OG [0x197]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03			
2010/03/02	10:56:26.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/03/02	14:59:54.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/03/02	14:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2010/03/02	15:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	04 00 00 00 00			
2010/03/02	15:00:16.0	XRT_FLD_ENA_402_OG [0x192]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2010/03/02	15:00:18.0	XRT_FLD_RESET_403_OG [0x193]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2010/03/02	15:00:20.5	XRT_FLRCTRL_ENA_404_OG [0x194]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2010/03/02	15:00:22.5	XRT_ARS_DIS_405_OG [0x195]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/03/02	15:09:08.5	XRT_QT_PROG_SET_409_OG [0x199]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01			
2010/03/02	15:09:10.5	XRT_FL_PROG_SET_407_OG [0x197]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03			
2010/03/02	15:09:12.5	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/03/02	15:43:30.0	XRT_CTRL_MANU_410_OG [0x19a]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/03/02	16:06:30.0	XRT_Custom_411_OG [0x19b]						
2010/03/02	16:07:30.0	XRT_CTRL_AUTO_412_OG [0x19c]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/03/02	17:19:30.0	XRT_CTRL_MANU_410_OG [0x19a]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/03/02	17:43:30.0	XRT_Custom_411_OG [0x19b]						
2010/03/02	17:44:30.0	XRT_CTRL_AUTO_412_OG [0x19c]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/03/02	18:56:30.0	XRT_CTRL_MANU_410_OG [0x19a]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/03/02	19:19:54.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/03/02	19:19:56.0	XRT_FOCUS_POSITION_413_OG [0x19d]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2010/03/02	19:20:00.0	AOCS_Ore-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2010/03/02	19:20:16.0	XRT_FLD_DIS_414_OG [0x19e]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2010/03/02	19:20:18.0	XRT_FLRCTRL_DIS_415_OG [0x19f]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2010/03/02	19:20:20.0	XRT_ARS_DIS_416_OG [0x1a0]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/03/02	19:22:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02			
2010/03/02	19:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/03/02	19:29:54.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/03/02	19:29:56.0	XRT_FOCUS_POSITION_401_OG [0x191]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2010/03/02	19:30:00.0	AOCS_Ore-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	02 00 00 00 00			
2010/03/02	19:30:16.0	XRT_FLD_ENA_402_OG [0x192]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2010/03/02	19:30:18.0	XRT_FLD_RESET_403_OG [0x193]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2010/03/02	19:30:20.0	XRT_FLRCTRL_ENA_404_OG [0x194]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2010/03/02	19:30:22.0	XRT_ARS_DIS_419_OG [0x1a3]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/03/02	19:32:48.0	XRT_QT_PROG_SET_406_OG [0x196]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04			
2010/03/02	19:32:50.0	XRT_FL_PROG_SET_407_OG [0x197]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03			
2010/03/02	19:32:52.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/03/02	20:34:00.0	XRT_CTRL_MANU_410_OG [0x19a]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/03/02	20:57:30.0	XRT_Custom_411_OG [0x19b]						

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2010/03/02	20:58:30.0	XRT_CTRL_AUTO_412_OG [0x19c]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/02	22:11:30.0	XRT_CTRL_MANU_410_OG [0x19a]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/02	22:33:30.0	XRT_Custom_411_OG [0x19b]							
2010/03/02	22:34:30.0	XRT_CTRL_AUTO_412_OG [0x19c]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/02	23:49:00.0	XRT_CTRL_MANU_410_OG [0x19a]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/03	00:00:00.0	XRT_Custom_411_OG [0x19b]							
2010/03/03	00:01:00.0	XRT_CTRL_AUTO_412_OG [0x19c]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/03	01:15:30.0	XRT_CTRL_MANU_410_OG [0x19a]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/03	01:32:00.0	XRT_Custom_411_OG [0x19b]							
2010/03/03	01:33:00.0	XRT_CTRL_AUTO_412_OG [0x19c]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/03	02:48:00.0	XRT_CTRL_MANU_410_OG [0x19a]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/03	03:08:30.0	XRT_Custom_411_OG [0x19b]							
2010/03/03	03:09:30.0	XRT_CTRL_AUTO_412_OG [0x19c]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/03	04:17:00.0	XRT_CTRL_MANU_410_OG [0x19a]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/03	04:46:00.0	XRT_Custom_411_OG [0x19b]							
2010/03/03	04:47:00.0	XRT_CTRL_AUTO_412_OG [0x19c]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/03	05:29:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/03	05:29:56.0	XRT_FOCUS_POSITION_413_OG [0x19d]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/03/03	05:30:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/03/03	05:30:16.0	XRT_FLD_DIS_414_OG [0x19e]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/03/03	05:30:18.0	XRT_FLRCTRL_DIS_415_OG [0x19f]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/03/03	05:30:20.0	XRT_ARS_DIS_416_OG [0x1a0]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/03/03	05:32:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2010/03/03	05:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/03	05:39:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/03	05:39:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/03/03	05:40:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2010/03/03	05:40:16.0	XRT_FLD_ENA_402_OG [0x192]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/03/03	05:40:18.0	XRT_FLD_RESET_403_OG [0x193]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/03/03	05:40:20.0	XRT_FLRCTRL_ENA_404_OG [0x194]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/03/03	05:40:22.0	XRT_ARS_DIS_405_OG [0x195]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/03/03	05:49:08.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2010/03/03	05:49:10.0	XRT_FL_PROG_SET_407_OG [0x197]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2010/03/03	05:49:12.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/03	05:57:00.0	XRT_CTRL_MANU_410_OG [0x19a]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/03	06:23:30.0	XRT_Custom_411_OG [0x19b]							
2010/03/03	06:24:30.0	XRT_CTRL_AUTO_412_OG [0x19c]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/03	07:37:30.0	XRT_CTRL_MANU_410_OG [0x19a]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/03	08:01:00.0	XRT_Custom_411_OG [0x19b]							
2010/03/03	08:02:00.0	XRT_CTRL_AUTO_412_OG [0x19c]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/03	09:17:30.0	XRT_CTRL_MANU_410_OG [0x19a]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/03	09:36:30.0	XRT_Custom_411_OG [0x19b]							
2010/03/03	09:37:30.0	XRT_CTRL_AUTO_412_OG [0x19c]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/03	09:47:50.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/03	10:24:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				