

# XRT Timeline to be uploaded on 2010/03/03

Period: 2010/03/03 10:14:00 - 2010/03/06 09:23:00

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## Normal mode

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XOB #1786: AR Dynamics - Ti/Poly - 512x512 - AEC 1 - Q92 - G-band Co-Align - Thick/Al-2													
Term	Pointing (x, y)							Comment					
03/03 10:27:00 - 03/03 14:32:54	Track ( 638.7, -213.8) <sup>Ⓜ 03/03 10:24:00</sup>	# OP start + 10min, Track AR 11052.											
03/04 11:03:00 - 03/04 14:59:54	Track ( 775.6, -234.6) <sup>Ⓜ 03/04 11:00:00</sup>	* Observe AR.											
03/04 19:13:00 - 03/05 06:09:24	Track ( 811.9, -242.3) <sup>Ⓜ 03/04 19:10:00</sup>	# Track AR 11052.											
03/05 06:22:00 - 03/05 10:17:30	Track ( 853.3, -253.3) <sup>Ⓜ 03/05 06:19:30</sup>	# Cont.											
<b>PROG= 04 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 6 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 4 7-time(s) 120.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	512x512 (1024, 1024)	Q=92	1	0	2.0sec
Default Filter    Thicker Filter    VLS    mode    image    Exp.    CCD    Bin    ROI: size (center)    Comp.    AEC Buffer    Interval													

XOB #1772: Synoptic Q95 2x2 - Al/mesh(33/512) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1024) + G-band(16)-2													
Term	Pointing (x, y)							Comment					
03/03 14:36:00 - 03/03 14:42:54	Fixed ( 0.0, 0.0)	+ Synoptic, shifted manually.											
03/04 19:03:00 - 03/04 19:09:54	Fixed ( 0.0, 0.0)	synoptic shifted manually.											
03/05 06:12:30 - 03/05 06:18:54	Fixed ( 0.0, 0.0)	synoptic, shifted 9.5 min											
<b>PROG= 02 1-time(s)</b>													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 5 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 10 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 1 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 8 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter    Thicker Filter    VLS    mode    image    Exp.    CCD    Bin    ROI: size (center)    Comp.    AEC Buffer    Interval													

XOB #1781: HOP 144 - Ti/Poly - 384x384 - AEC 1 - 40s - G-band Co-align													
Term	Pointing (x, y)							Comment					
03/03 14:46:00 - 03/03 18:59:54	Fixed ( 865.0, -370.0)	HOP 144, officially from 15:00 UT, spicule obs.											
03/04 15:03:00 - 03/04 18:59:54	Track ( 500.5, 355.4) <sup>Ⓜ 03/04 15:00:00</sup>	* HOP 144, spicule obs. in AR 11051 plage.											
<b>PROG= 01 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 3 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
└─ Seqn= 9 20-time(s) 40.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Default Filter    Thicker Filter    VLS    mode    image    Exp.    CCD    Bin    ROI: size (center)    Comp.    AEC Buffer    Interval													

XOB #1787: AR Half Band during South Polar Monitor (HOP 101) - 2084x512 - offset - Ti/Poly - 2x2 - AEC 1													
Term	Pointing (x, y)							Comment					
03/03 19:03:02 - 03/04 10:59:54	Fixed ( -16.0, -921.0)	# HOP 101, helioseismology at the S pole.											
<b>PROG= 05 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 11 20-time(s) 40.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	1024x512 (1384, 1656)	Q=95	1	0	2.0sec
Default Filter    Thicker Filter    VLS    mode    image    Exp.    CCD    Bin    ROI: size (center)    Comp.    AEC Buffer    Interval													

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## Flare mode

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XOB #1776: Flare Response short exp at first- Dynamics - Thick-Al - Thick-Be - AEC 1 - 384x384 - Q95												
Term	Pointing (x, y)							Comment				
03/03 10:27:00 - 03/03 14:32:54	Track ( 638.7, -213.8) <sup>Ⓜ 03/03 10:24:00</sup>	# OP start + 10min, Track AR 11052.										
03/03 14:46:00 - 03/03 18:59:54	Fixed ( 865.0, -370.0)	HOP 144, officially from 15:00 UT, spicule obs.										
03/03 19:03:02 - 03/04 10:59:54	Fixed ( -16.0, -921.0)	# HOP 101, helioseismology at the S pole.										
03/04 11:03:00 - 03/04 14:59:54	Track ( 775.6, -234.6) <sup>Ⓜ 03/04 11:00:00</sup>	* Observe AR.										
03/04 15:03:00 - 03/04 18:59:54	Track ( 500.5, 355.4) <sup>Ⓜ 03/04 15:00:00</sup>	* HOP 144, spicule obs. in AR 11051 plage.										
03/04 19:13:00 - 03/05 06:09:24	Track ( 811.9, -242.3) <sup>Ⓜ 03/04 19:10:00</sup>	# Track AR 11052.										
03/05 06:22:00 - 03/05 10:17:30	Track ( 853.3, -253.3) <sup>Ⓜ 03/05 06:19:30</sup>	# Cont.										
<b>PROG= 03 1-time(s)</b>												

<b>Subr= 1</b>		<b>2-time(s)</b>		<b>2.0sec</b>											
<b>Seqn= 7</b>		<b>15-time(s)</b>		<b>20.0sec</b>											
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec		
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec		
<b>Seqn= 2</b>		<b>1-time(s)</b>		<b>4.0sec</b>											
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
<b>Subr= 2</b>		<b>8-time(s)</b>		<b>2.0sec</b>											
<b>Seqn= 7</b>		<b>15-time(s)</b>		<b>60.0sec</b>											
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec		
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec		
<b>Seqn= 2</b>		<b>1-time(s)</b>		<b>4.0sec</b>											
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
<b>Subr= 3</b>		<b>25-time(s)</b>		<b>2.0sec</b>											
<b>Seqn= 7</b>		<b>1-time(s)</b>		<b>600.0sec</b>											
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec		
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec		
<b>Seqn= 2</b>		<b>1-time(s)</b>		<b>4.0sec</b>											
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval			

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### Active Region Search

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NOT USED

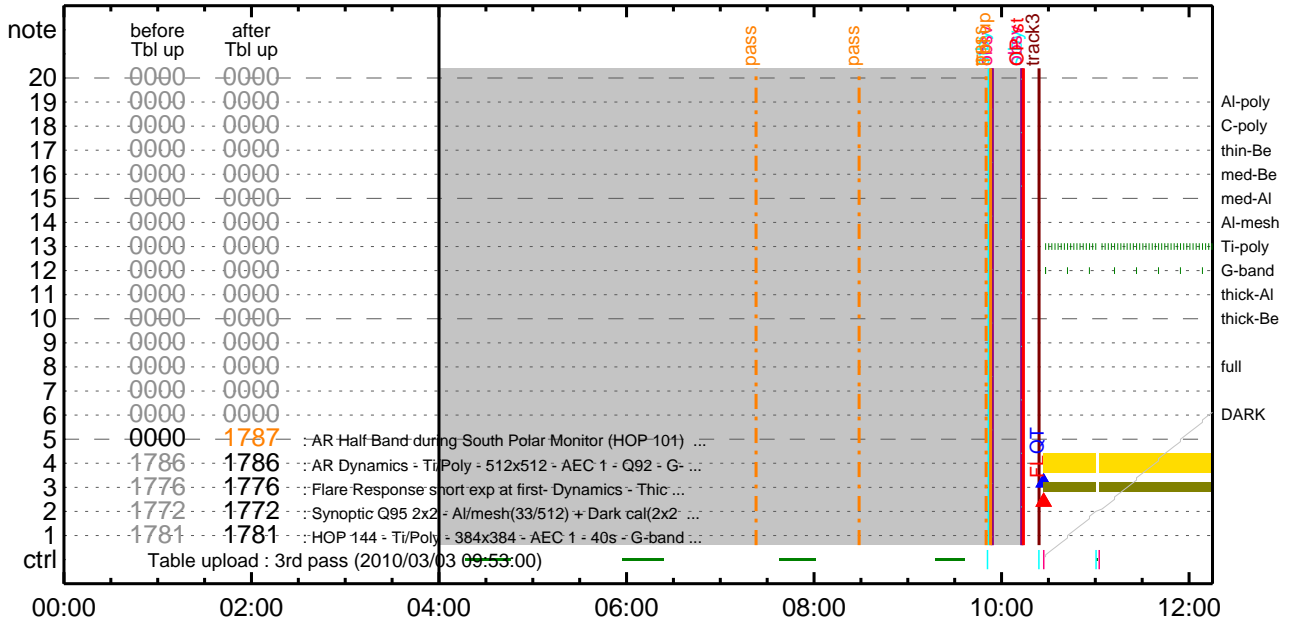
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### Flare Detection

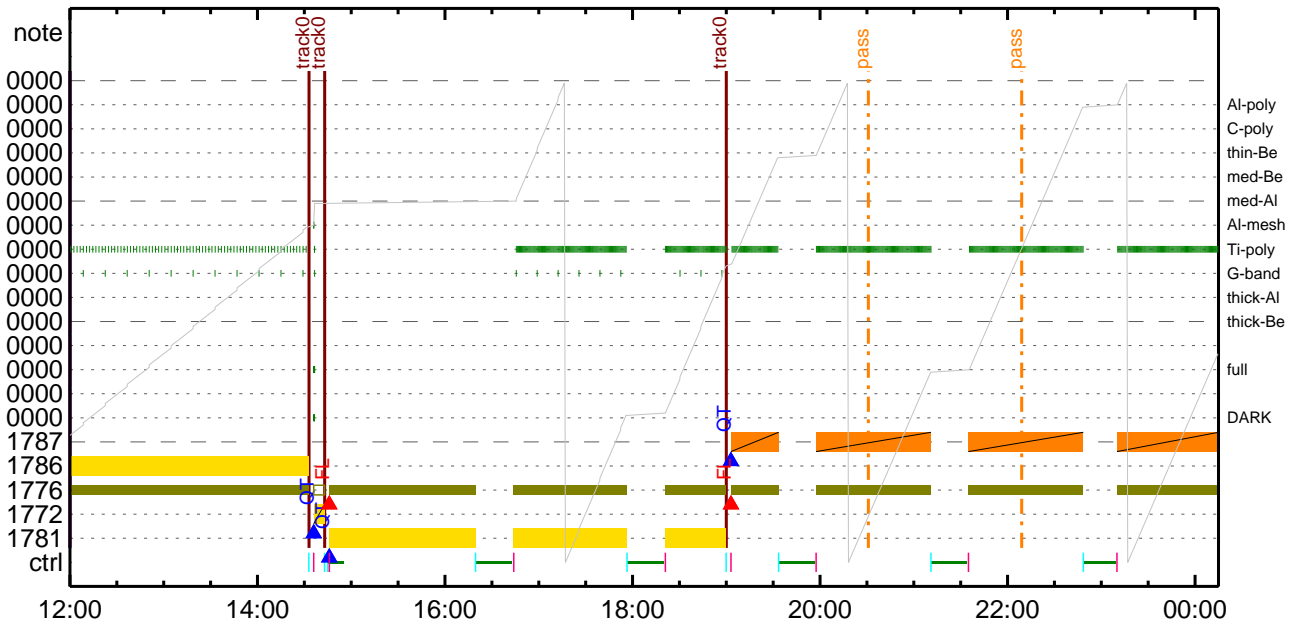
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<b>FLD Patrol</b>												
Term		Pointing (x, y)					Comment					
03/03 10:24:16 - 03/03 14:33:16	Track ( 638.7, -213.8)	© 03/03 10:24:00					# OP start + 10min, Track AR 11052.					
03/03 14:43:16 - 03/04 19:00:16	Fixed ( 865.0, -370.0)						HOP 144, officially from 15:00 UT, spicule obs.					
03/04 19:10:16 - 03/05 06:09:46	Track ( 811.9, -242.3)	© 03/04 19:10:00					# Track AR 11052.					
03/05 06:19:16 - 03/06 09:23:00	Fixed ( 0.0, 0.0)						synoptic, shifted 9.5 min					
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	120sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

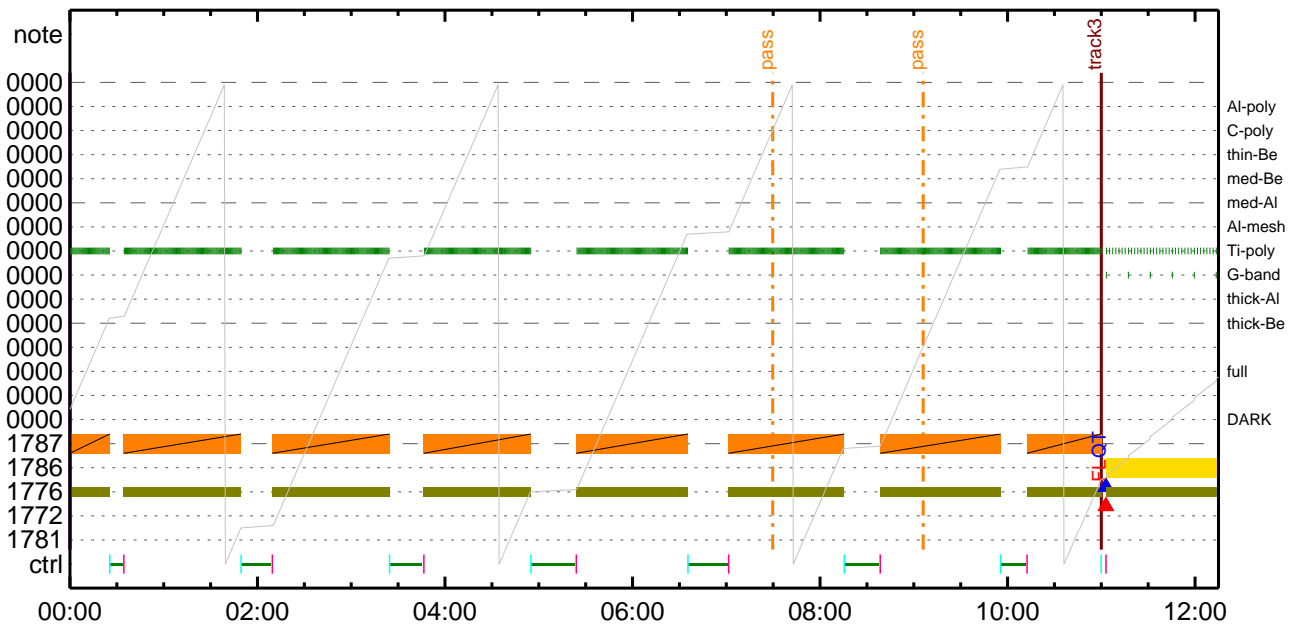
CMDI #0124 2010/03/03



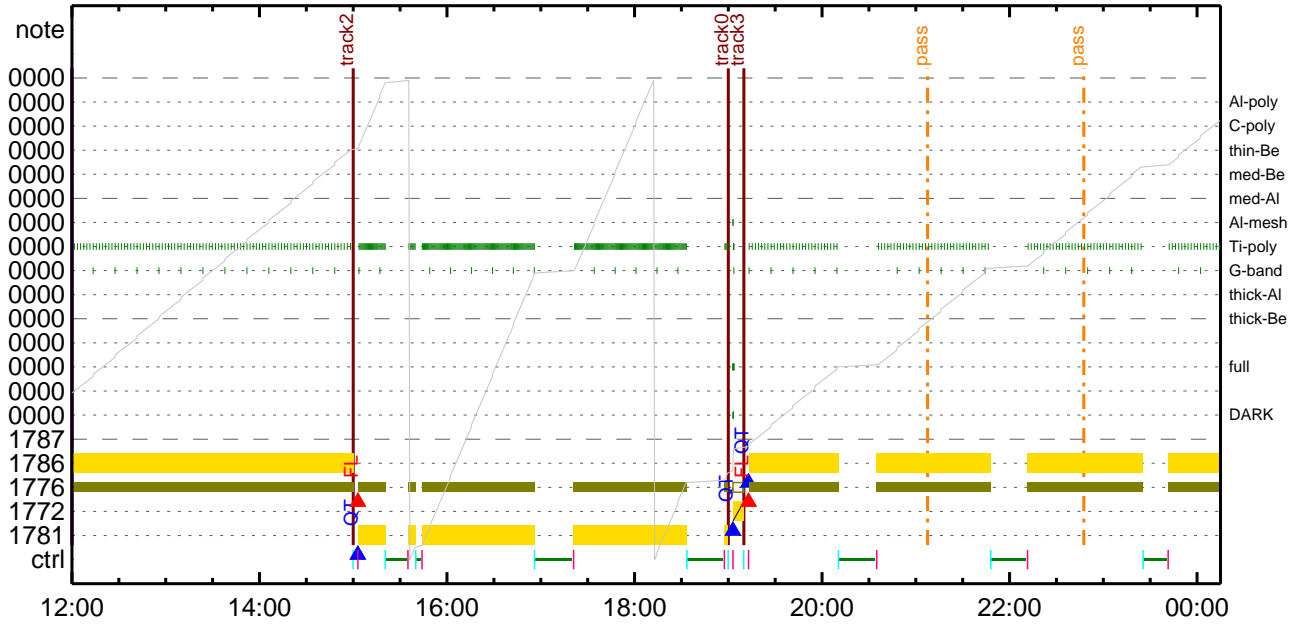
CMDI #0124 2010/03/03



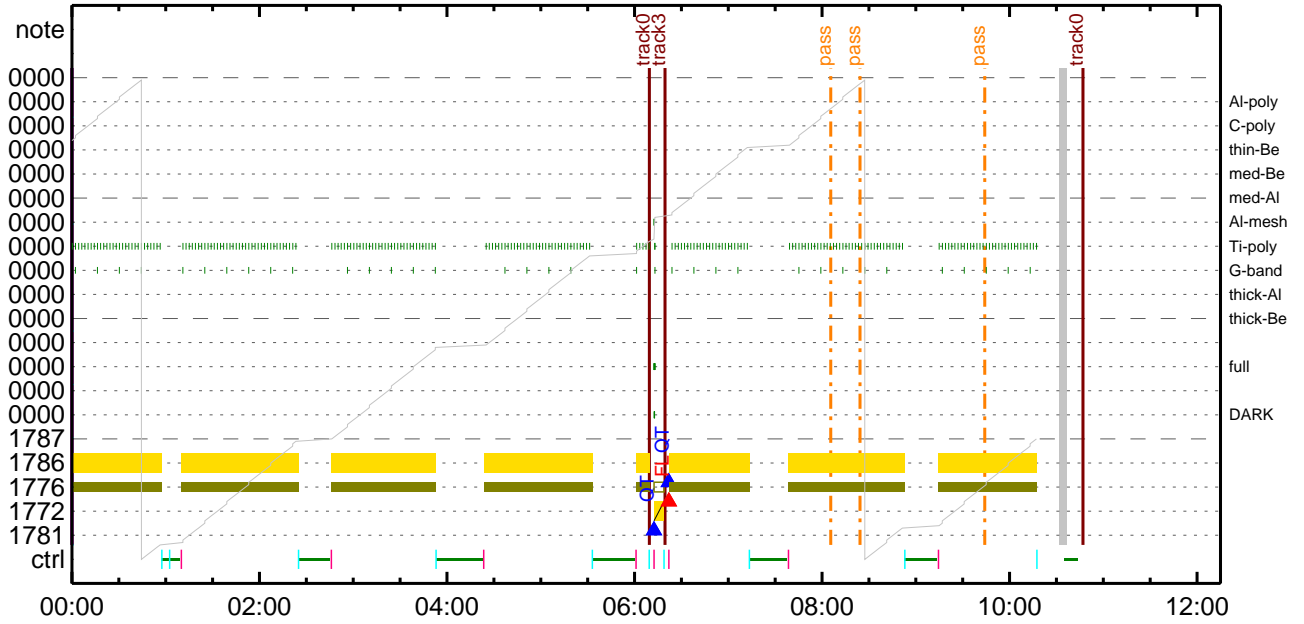
CMDI #0124 2010/03/04



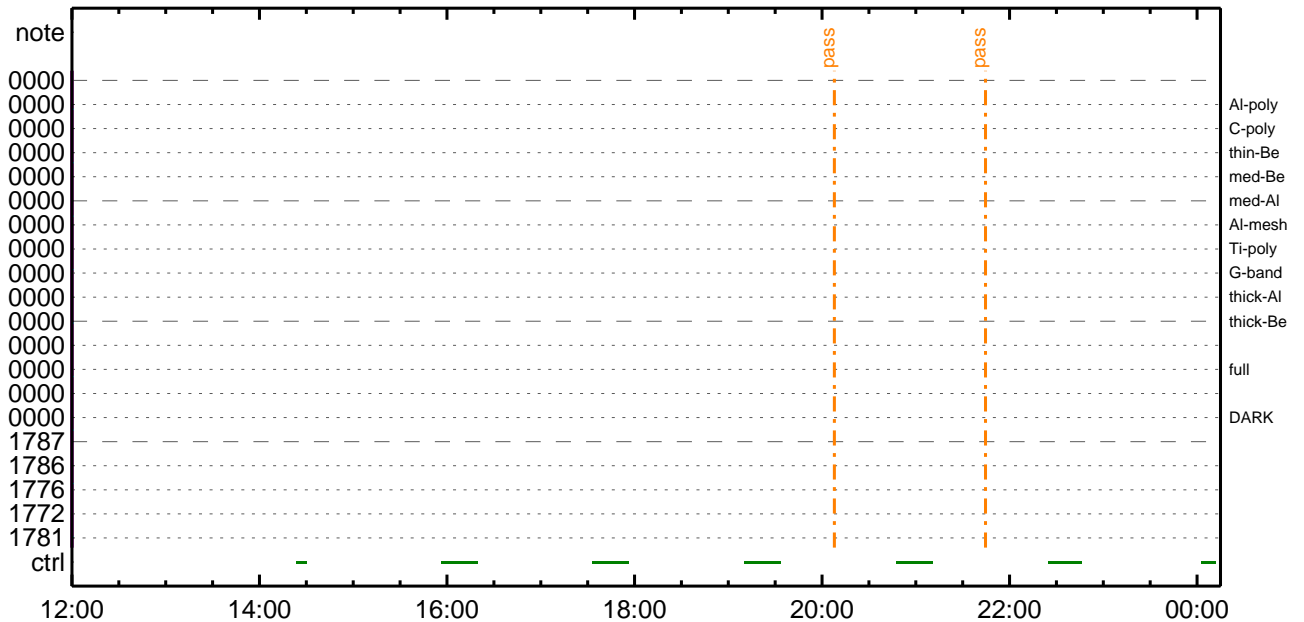
CMDI #0124 2010/03/04



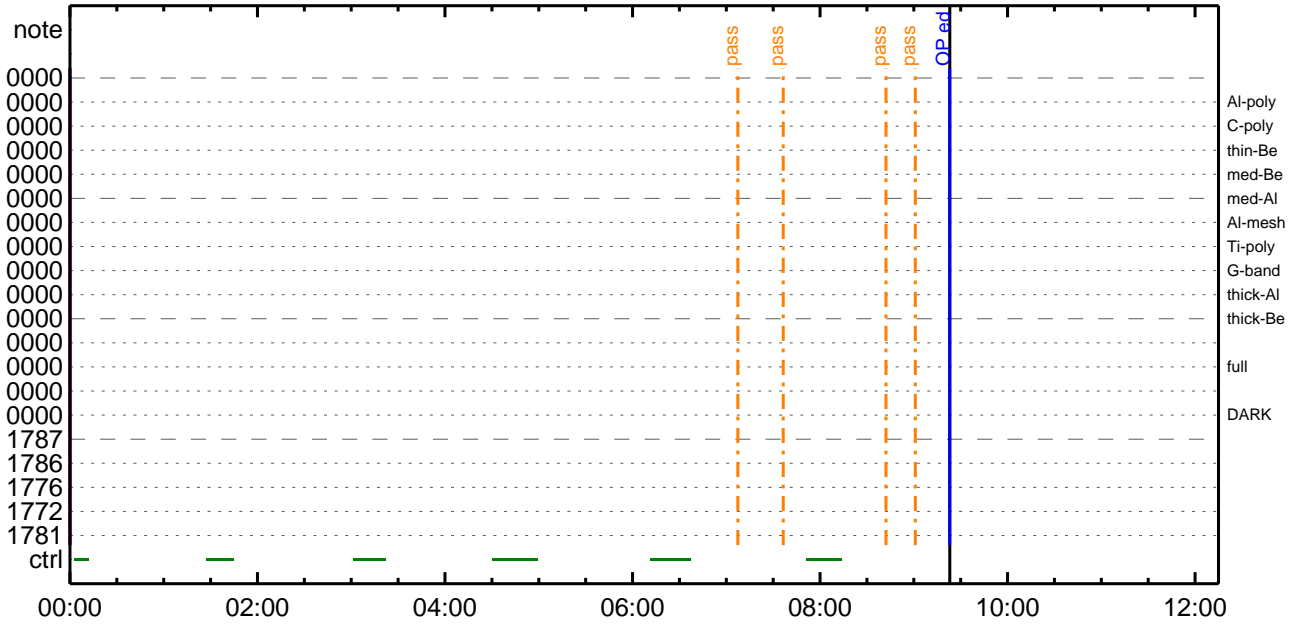
CMDI #0124 2010/03/05



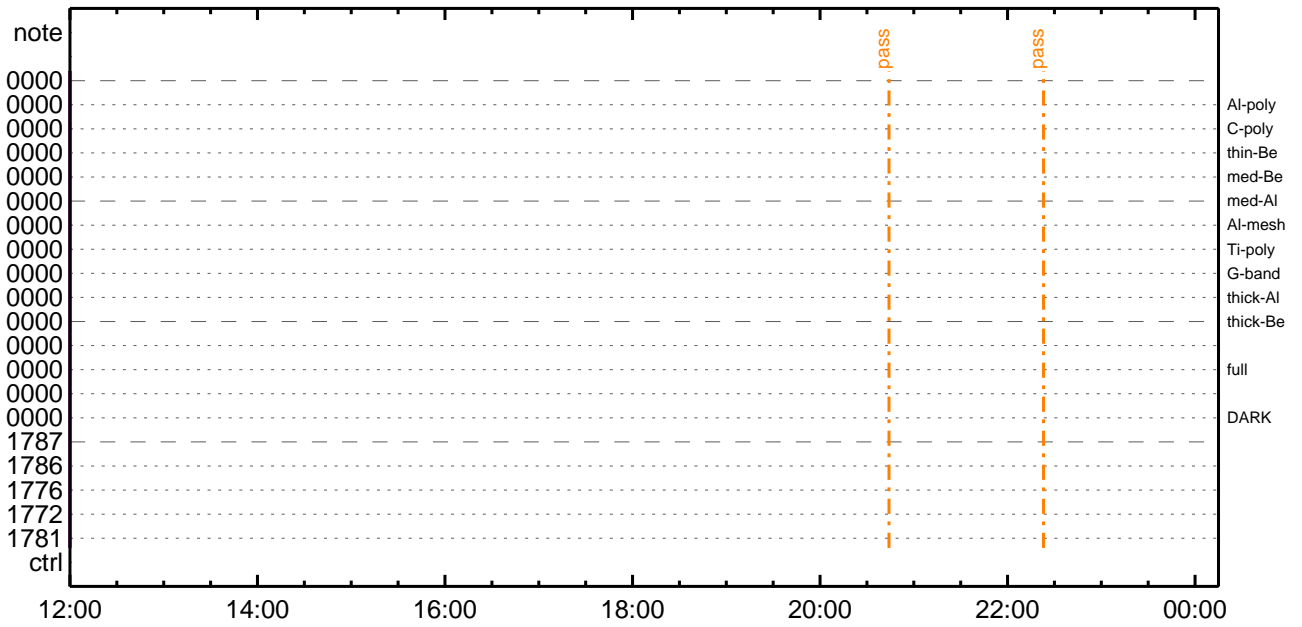
CMDI #0124 2010/03/05



CMDI #0124 2010/03/06



CMDI #0124 2010/03/06



(a) Spacecraft Operation Procedure (real-commands)

```
main-208 2010-03-03 13:14:12 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY~¼Ä»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YFÝÖYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èò¿òÀò•µ°Ë»Í×ÁÇòÍYçYÁY×YÍ;¼YÉ;ÈÈèµ•ííÉ;ÈòÈ¼°ÇÒò•ò¿¼l¹çòÍ;çÀ®, ùò¹òèòòòçÁ+¿®ò•òÈòòò³òÈ; f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYÍ;¼YÉ; | YÁYÖY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYÍ;¼YÉ;ä
0019 . S. OP op-208:OP
0020 ( )
0021 . S. OG og-208:OG
0022 ( )
0023 C.
0024 . C. ;ãNMOG&OPÍî°èYÁYÖY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÁYÖY×½ªî»òð³îÇ§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGòî¼È¹ç•è²ÏOKòð³îÇ§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÁYÖY×½ªî»òð³îÇ§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGòî¼È¹ç•è²ÏOKòð³îÇ§
0062 C.
0063 C. NMOG(0x210000-0x210FFF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÁYÖY×½ªî»òð³îÇ§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OPòî¼È¹ç•è²ÏOKòð³îÇ§
0081 C.
0082 . C. ***** òÈ²¼òî¼Ä´¶¼òÈÈ-òòÄ+¿® (¼ãµ-YÁYÖY×½ª¼è¼çòðÄÓÃæòçªºò"òè¼l¹çòçòâ) *****
0083 C. DHUYá;¼YÉ;È¼Y½, Yí;¼YÉ;Èòòíáò¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;§ OPOG UPLOADò-Á+¿®NGòî¼È¹ç; ç°È²¼òî¼TI-CMDÁ+¿®òî¼Ä¹Òò•òÈòòò³òÈ; f
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0096 C. SET EDUMP I±°iYÑY¹aÇ¹Ôa|a³aE;E  
0097 C.  
0098 C. TIY³YFYYÖYÉaδADİđ (UT)  
0099 +. TI 2010-03-03 10:09:00.0  
0100 DC 01-B3 DHU\_OP\_STOP  
0101 C. çç[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP  
0102 C.  
0103 +. TI 2010-03-03 10:09:01.0  
0104 DC 01-B4 DHU\_OP\_COPY  
0105 C. çç[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP  
0106 C.  
0107 +. TI 2010-03-03 10:09:01.0  
0108 DC 01-B5 DHU\_OPOG\_COPY  
0109 C. çç[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP  
0110 C.  
0111 +. TI 2010-03-03 10:13:59.5  
0112 DC 01-B2 DHU\_OP\_START  
0113 C. çç[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP  
0114 C.  
0115 C. °Ê²¼aİÄê%îİÑaİYÁYŞYÄY-¹àİÜ  
0116 C. çç[HK1\_TI\_CMD\_ENA/DIS] EQ ENA  
0117 C. çç[HK1\_TI\_CMD\_NUM] EQ 4  
0118 C. çç[HK1\_NEXT\_EXEC\_PIM] EQ DHU  
0119 C. çç[HK1\_NEXT\_EXEC\_DC] EQ 0xB3  
0120 C.  
0121 C. \*\*\*\*\*  
0122 C. TIİİî°èYÄYÖY×  
0123 C. \*\*\*\*\*  
0124 C.  
0125 C. TI\_TBL(0x03AB00-0x03AEFF;§ 1024byte)  
0126 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0127 BC (03 ab 03 01 02)  
0128 C. çç[HK1\_DMP\_TOP\_ADRS\_1] EQ 07  
0129 C. çç[HK1\_DMP\_TOP\_ADRS\_0] EQ 2B  
0130 C. çç[HK1\_DMP\_BLOCK\_NUM] EQ 3  
0131 C. çç[HK1\_DMP\_REPEAT\_NUM] EQ 0  
0132 C. çç[HK1\_DMA\_DMP\_PIM] EQ DHU  
0133 +. DC 01-22 DHU\_MODE\_CHNG  
0134 BC (07 0b f8)  
0135 C. çç[HK1\_PKT\_FORM\_NO] EQ 7  
0136 C. çç[HK1\_PKT\_GEN\_TIME] EQ 0.25 s  
0137 C. çç[HK1\_S\_TLM\_BIT\_RATE] EQ 32k  
0138 C. çç[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0139 C. çç[HK1\_DMP\_CHK\_FLG] EQ EXEC  
0140 C.  
0141 C. YÄYÖY×¼¹¹»aδ³İÇŞ  
0142 C. çç[HK1\_DMP\_CHK\_FLG] EQ NON  
0143 C.  
0144 C. RAM ID=TI\_TBLaİ%Ê¹Ç•è²İOKaδ³İÇŞ  
0145 C.  
0146 C. DHUYâ;¼YÉ;Ê¼Y¼, Yî;¼YÈ;Ëaδİâa¹  
0147 +. DC 01-22 DHU\_MODE\_CHNG  
0148 BC (02 0a f8)  
0149 C. çç[HK1\_PKT\_FORM\_NO] EQ 2  
0150 C. çç[HK1\_PKT\_GEN\_TIME] EQ 0.5S  
0151 C. çç[HK1\_S\_TLM\_BIT\_RATE] EQ 32K  
0152 C. çç[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0153 C.  
0154 C. \*\*\*\*\*  
0155 C. SOT TI command set  
0156 C. \*\*\*\*\*  
0157 C. Execute, after the success of OP upload.  
0158 +. TI 2010-03-03 10:13:16.0  
0159 DC 07-F0 MDP\_SOT\_MODE\_STBY  
0160 BC (41)  
0161 C. -----  
0162 C. HK1\_TI\_CMD\_NUM = 1 CNTUP [ ]  
0163 C. -----  
0164 C. \*\*\*\*\* SOT END \*\*\*\*\*  
0165 C. Stop EIS observation and temporarily disable EIS mode changes  
0166 C.  
0167 C.  
0168 C. \*\*\*\*\* Start EIS operation (TI set) \*\*\*\*\*  
0169 C. Execute, after the success of OP upload.  
0170 C. Set EIS TI-commands  
0171 +. TI 2010-03-03 10:13:30.0  
0172 DC 07-FC EIS\_MODE\_MANU  
0173 BC (21 02)  
0174 +. TI 2010-03-03 10:13:40.0  
0175 DC 07-FC EIS\_MODE\_CHG\_DIS  
0176 BC (22)  
0177 C. [ ] [HK1\_TI\_CMD\_NUM] EQ 2 COUNTUP  
0178 C. \*\*\*\*\* End EIS operation (TI set) \*\*\*\*\*  
0179 C.  
0180 C.  
0181 C.  
0182 C. \*\*\*\*\* XRT START \*\*\*\*\*  
0183 C. Execute, after the success of OP upload.  
0184 +. TI 2010-03-03 10:13:00.0  
0185 DC 07-F0 MDP\_XRT\_MODE\_STBY  
0186 BC (c3)  
0187 C. [ ] [HK1\_TI\_CMD\_NUM] EQ 1COUNTUP  
0188 C.  
0189 C. \*\*\*\*\* XRT END \*\*\*\*\*  
0190 C.  
0191 C. \*\*\*\*\* MDP 'üÄİaİ»ö¼YaËÄa¹aèDCBC•×²è \*\*\*\*\*  
0192 C. (%â°İYÖYÄYÉYÏYËYÁYÇYèaE¼a¼Ä»Üa¹aé)  
0193 C. DC-BC dcbc-402:DCBC

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```







(a) Spacecraft Operation Procedure (real-commands)

```
main-210 2010-03-03 13:14:12 118 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿øÀø•µ°È»Í×ÁÇøíYçYÁY×Yí;¼YÉ;ÈÈè%µ•ííÉ;ÈøÈ¼°ÇÒø•ø¿¼í¹çøí;çÀ®, ùø¹øÈøÈøÇÁ+¿®ø•øÈøøøøøÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 06 80 80 08 08)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 07 80 80 20 20)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 08 80 80 20 08)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 09 80 80 08 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 0a 80 60 20 18)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0b 85 83 06 06)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0c ad cf 10 08)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0f 80 80 06 06)
0056 + DC 07-F0 MDP_XRT_AEC_RESET
0057 BC (d0)
0058 . C. ----- Success Verify ? OK / NG ____
0059 C.
0060 C.
0061 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0062 C.
0063 +. DC 07-F0 MDP_XRT_MODE_OBSV
0064 BC (c2)
0065 +. TI 2010-03-03 10:13:02.0
0066 DC 07-F0 MDP_XRT_MODE_OBSV
0067 BC (c2)
0068 . C. ----- Success Verify ? OK / NG ____
0069 C.
0070 C. ***** XRT END *****
0071 . C. *****
0072 C. SOT table upload
0073 C. *****
0074 . C. < Stop FG table >
0075 +. DC 07-F0 MDP_FG_CTRL_MANU
0076 BC (51)
0077 . C. -----
0078 C. MDP_FG_CTRL_MODE = MANU [ ]
0079 C. -----
0080 C.
0081 . C. <Upload FG Observation Table>
0082 . S. RAM ram-264:MDP_OBS_F
0083 ( )
0084 C.
0085 . C. < Dump RAMID=MDP_OBS_F >
0086 +. DC 07-F0 MDP_DUMP_FGTBL
0087 BC (82 07 00 00 00 38 b8)
0088 C. -----
0089 C. MDP_OBS_F verify = OK/NG [ ]
0090 C. -----
0091 C.
0092 C. *****
0093 C. SOT TI command set
0094 C. *****
0095 C. Execute, after the success of TBL upload.
```

```
0096 +. TI 2010-03-03 10:13:18.0
0097 DC 07-F0 MDP_SOT_MODE_OBSV
0098 BC (40)
0099 . C. -----
0100 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0101 C. -----
0102 C.
0103 C.
0104 . C. ***** MDP 'úÃîñî»ô¼ÝðËÂðñ¹ñèDCBC•x²è *****
0105 C. (¼ã°îÝÓÝÃÝÈÝÞÝÈÝáÝçÝèñ¼ñ¼Ã»Ûñ¹ñè)
0106 . S. DC-BC dcbc-402:DCBC
0107 (MDP_known_event)
0108 C.
0109 C.
0110 . C. ***** ÝÐÝ¹•İ Daily±¿İÑñË´Øñ¹ñèDCBC•x²è *****
0111 . S. DC-BC dcbc-153:DCBC
0112 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0113 C.
0114 C.
0115 . C. ;ãLOSÝÁÝ§ÝÃÝ¬¼Ã»Û;ã
0116 C.
0117 . C. ***** LOS *****
0118 C.
```

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\*\*\* OP Sequence for XRT \*\*\*

2010/03/03	10:23:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/03	10:23:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/03/03	10:24:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2010/03/03	10:24:16.0	XRT_FLD_ENA_402_OG [0x192]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2010/03/03	10:24:18.0	XRT_FLD_RESET_403_OG [0x193]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/03/03	10:24:20.0	XRT_FLRCTRL_ENA_404_OG [0x194]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/03/03	10:24:22.0	XRT_ARS_DIS_418_OG [0x1a2]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/03/03	10:26:56.0	XRT_QT_PROG_SET_406_OG [0x196]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2010/03/03	10:26:58.0	XRT_FL_PROG_SET_407_OG [0x197]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2010/03/03	10:27:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/03/03	11:00:30.0	XRT_CTRL_MANU_410_OG [0x19a]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/03	11:01:30.0	XRT_Custom_411_OG [0x19b]			
2010/03/03	11:02:30.0	XRT_CTRL_AUTO_412_OG [0x19c]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/03/03	14:32:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/03	14:32:56.0	XRT_FOCUS_POSITION_413_OG [0x19d]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/03/03	14:33:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2010/03/03	14:33:16.0	XRT_FLD_DIS_414_OG [0x19e]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2010/03/03	14:33:18.0	XRT_FLRCTRL_DIS_415_OG [0x19f]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2010/03/03	14:33:20.0	XRT_ARS_DIS_416_OG [0x1a0]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/03/03	14:35:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2010/03/03	14:36:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/03/03	14:42:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/03	14:42:56.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/03/03	14:43:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 20 e5 b3 1b
2010/03/03	14:43:16.0	XRT_FLD_ENA_402_OG [0x192]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2010/03/03	14:43:18.0	XRT_FLD_RESET_403_OG [0x193]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/03/03	14:43:20.0	XRT_FLRCTRL_ENA_404_OG [0x194]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/03/03	14:43:22.0	XRT_ARS_DIS_418_OG [0x1a2]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/03/03	14:45:00.0	XRT_CTRL_MANU_410_OG [0x19a]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/03	14:45:56.0	XRT_QT_PROG_SET_409_OG [0x199]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2010/03/03	14:45:58.0	XRT_FL_PROG_SET_407_OG [0x197]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2010/03/03	14:46:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/03/03	16:19:30.0	XRT_CTRL_MANU_410_OG [0x19a]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/03	16:43:00.0	XRT_Custom_411_OG [0x19b]			
2010/03/03	16:44:00.0	XRT_CTRL_AUTO_412_OG [0x19c]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/03/03	17:56:30.0	XRT_CTRL_MANU_410_OG [0x19a]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/03	18:20:00.5	XRT_Custom_411_OG [0x19b]			
2010/03/03	18:21:00.5	XRT_CTRL_AUTO_412_OG [0x19c]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/03/03	18:59:54.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/03	18:59:56.0	XRT_FOCUS_POSITION_413_OG [0x19d]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/03/03	19:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	00 51 db 01 68
2010/03/03	19:00:16.0	XRT_FLD_ENA_402_OG [0x192]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2010/03/03	19:00:18.0	XRT_FLD_RESET_403_OG [0x193]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/03/03	19:00:20.0	XRT_FLRCTRL_ENA_404_OG [0x194]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/03/03	19:00:22.0	XRT_ARS_DIS_419_OG [0x1a3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/03/03	19:02:58.0	XRT_QT_PROG_SET_420_OG [0x1a4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2010/03/03	19:03:00.0	XRT_FL_PROG_SET_407_OG [0x197]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03

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2010/03/03	19:03:02.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/03	19:33:30.0	XRT_CTRL_MANU_410_OG [0x19a]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/03	19:56:30.0	XRT_Custom_411_OG [0x19b]							
2010/03/03	19:57:30.0	XRT_CTRL_AUTO_412_OG [0x19c]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/03	21:11:00.0	XRT_CTRL_MANU_410_OG [0x19a]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/03	21:34:00.0	XRT_Custom_411_OG [0x19b]							
2010/03/03	21:35:00.0	XRT_CTRL_AUTO_412_OG [0x19c]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/03	22:48:30.0	XRT_CTRL_MANU_410_OG [0x19a]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/03	23:09:00.0	XRT_Custom_411_OG [0x19b]							
2010/03/03	23:10:00.0	XRT_CTRL_AUTO_412_OG [0x19c]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/04	00:25:30.0	XRT_CTRL_MANU_410_OG [0x19a]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/04	00:33:30.0	XRT_Custom_411_OG [0x19b]							
2010/03/04	00:34:30.0	XRT_CTRL_AUTO_412_OG [0x19c]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/04	01:49:30.0	XRT_CTRL_MANU_410_OG [0x19a]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/04	02:08:30.0	XRT_Custom_411_OG [0x19b]							
2010/03/04	02:09:30.0	XRT_CTRL_AUTO_412_OG [0x19c]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/04	03:24:30.0	XRT_CTRL_MANU_410_OG [0x19a]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/04	03:45:30.0	XRT_Custom_411_OG [0x19b]							
2010/03/04	03:46:30.0	XRT_CTRL_AUTO_412_OG [0x19c]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/04	04:55:00.0	XRT_CTRL_MANU_410_OG [0x19a]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/04	05:23:00.0	XRT_Custom_411_OG [0x19b]							
2010/03/04	05:24:00.0	XRT_CTRL_AUTO_412_OG [0x19c]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/04	06:35:30.0	XRT_CTRL_MANU_410_OG [0x19a]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/04	07:00:30.0	XRT_Custom_411_OG [0x19b]							
2010/03/04	07:01:30.0	XRT_CTRL_AUTO_412_OG [0x19c]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/04	08:15:30.0	XRT_CTRL_MANU_410_OG [0x19a]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/04	08:37:30.0	XRT_Custom_411_OG [0x19b]							
2010/03/04	08:38:30.0	XRT_CTRL_AUTO_412_OG [0x19c]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/04	09:55:30.0	XRT_CTRL_MANU_410_OG [0x19a]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/04	10:11:30.0	XRT_Custom_411_OG [0x19b]							
2010/03/04	10:12:30.0	XRT_CTRL_AUTO_412_OG [0x19c]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/04	10:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/04	10:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/03/04	11:00:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2010/03/04	11:00:16.0	XRT_FLD_ENA_402_OG [0x192]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/03/04	11:00:18.0	XRT_FLD_RESET_403_OG [0x193]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/03/04	11:00:20.0	XRT_FLRCTRL_ENA_404_OG [0x194]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/03/04	11:00:22.0	XRT_ARS_DIS_418_OG [0x1a2]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/03/04	11:02:56.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2010/03/04	11:02:58.0	XRT_FL_PROG_SET_407_OG [0x197]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2010/03/04	11:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/04	14:59:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/04	14:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/03/04	15:00:00.0	AOCS_OrE-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2010/03/04	15:00:16.0	XRT_FLD_ENA_402_OG [0x192]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/03/04	15:00:18.0	XRT_FLD_RESET_403_OG [0x193]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/03/04	15:00:20.0	XRT_FLRCTRL_ENA_404_OG [0x194]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/03/04	15:00:22.0	XRT_ARS_DIS_418_OG [0x1a2]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/03/04	15:02:56.0	XRT_QT_PROG_SET_409_OG [0x199]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2010/03/04	15:02:58.0	XRT_FL_PROG_SET_407_OG [0x197]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2010/03/04	15:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/04	15:20:30.0	XRT_CTRL_MANU_410_OG [0x19a]							

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2010/03/04	15:34:00.0	XRT_Custom_411_OG [0x19b]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/04	15:35:00.0	XRT_CTRL_AUTO_412_OG [0x19c]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/04	15:40:00.0	XRT_CTRL_MANU_410_OG [0x19a]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/04	15:43:00.0	XRT_Custom_411_OG [0x19b]								
2010/03/04	15:44:00.0	XRT_CTRL_AUTO_412_OG [0x19c]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/04	16:56:00.0	XRT_CTRL_MANU_410_OG [0x19a]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/04	17:20:00.0	XRT_Custom_411_OG [0x19b]								
2010/03/04	17:21:00.0	XRT_CTRL_AUTO_412_OG [0x19c]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/04	18:33:30.0	XRT_CTRL_MANU_410_OG [0x19a]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/04	18:56:30.0	XRT_Custom_411_OG [0x19b]								
2010/03/04	18:57:30.0	XRT_CTRL_AUTO_412_OG [0x19c]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/04	18:59:54.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/04	18:59:56.0	XRT_FOCUS_POSITION_413_OG [0x19d]								
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/03/04	19:00:00.0	AOCs_OrE-point_Start_2_OG [0x098]								
			AOCU_NM	5	02-76	00 00 00 00 00				
2010/03/04	19:00:16.0	XRT_FLD_DIS_414_OG [0x19e]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/03/04	19:00:18.0	XRT_FLRCTRL_DIS_415_OG [0x19f]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/03/04	19:00:20.0	XRT_ARS_DIS_416_OG [0x1a0]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/03/04	19:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2010/03/04	19:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/04	19:09:54.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/04	19:09:56.0	XRT_FOCUS_POSITION_401_OG [0x191]								
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/03/04	19:10:00.0	AOCs_OrE-point_Start_1_OG [0x097]								
			AOCU_NM	5	02-76	03 00 00 00 00				
2010/03/04	19:10:16.0	XRT_FLD_ENA_402_OG [0x192]								
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/03/04	19:10:18.0	XRT_FLD_RESET_403_OG [0x193]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2010/03/04	19:10:20.0	XRT_FLRCTRL_ENA_404_OG [0x194]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/03/04	19:10:22.0	XRT_ARS_DIS_418_OG [0x1a2]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/03/04	19:12:56.0	XRT_QT_PROG_SET_406_OG [0x196]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2010/03/04	19:12:58.0	XRT_FL_PROG_SET_407_OG [0x197]								
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2010/03/04	19:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/04	20:10:30.0	XRT_CTRL_MANU_410_OG [0x19a]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/04	20:34:00.0	XRT_Custom_411_OG [0x19b]								
2010/03/04	20:35:00.0	XRT_CTRL_AUTO_412_OG [0x19c]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/04	21:48:00.0	XRT_CTRL_MANU_410_OG [0x19a]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/04	22:10:30.0	XRT_Custom_411_OG [0x19b]								
2010/03/04	22:11:30.0	XRT_CTRL_AUTO_412_OG [0x19c]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/04	23:25:30.0	XRT_CTRL_MANU_410_OG [0x19a]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/04	23:40:30.0	XRT_Custom_411_OG [0x19b]								
2010/03/04	23:41:30.0	XRT_CTRL_AUTO_412_OG [0x19c]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/05	00:57:30.0	XRT_CTRL_MANU_410_OG [0x19a]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/05	01:02:30.0	XRT_CTRL_MANU_410_OG [0x19a]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/05	01:09:00.5	XRT_Custom_411_OG [0x19b]								
2010/03/05	01:10:00.5	XRT_CTRL_AUTO_412_OG [0x19c]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/05	02:25:00.0	XRT_CTRL_MANU_410_OG [0x19a]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/05	02:45:00.0	XRT_Custom_411_OG [0x19b]								
2010/03/05	02:46:00.0	XRT_CTRL_AUTO_412_OG [0x19c]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/05	03:53:00.0	XRT_CTRL_MANU_410_OG [0x19a]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/05	04:22:30.0	XRT_Custom_411_OG [0x19b]								
2010/03/05	04:23:30.0	XRT_CTRL_AUTO_412_OG [0x19c]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/05	05:33:00.0	XRT_CTRL_MANU_410_OG [0x19a]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/05	06:00:00.0	XRT_Custom_411_OG [0x19b]								
2010/03/05	06:01:00.0	XRT_CTRL_AUTO_412_OG [0x19c]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/05	06:09:24.0	XRT_CTRL_MANU_400_OG [0x190]								

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2010/03/05	06:09:26.0	XRT_FOCUS_POSITION_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/05	06:09:30.0	AOCs_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/03/05	06:09:46.0	XRT_FLD_DIS_414_OG [0x19e]	AOCU_NM	5	02-76	00 00 00 00 00
2010/03/05	06:09:48.0	XRT_FLRCTRL_DIS_415_OG [0x19f]	MDP_XRT_FLD_DIS	1	07-F0	d9
2010/03/05	06:09:50.0	XRT_ARS_DIS_416_OG [0x1a0]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2010/03/05	06:12:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/03/05	06:12:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2010/03/05	06:18:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/03/05	06:18:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/05	06:19:16.0	XRT_FLD_ENA_402_OG [0x192]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/03/05	06:19:18.0	XRT_FLD_RESET_403_OG [0x193]	MDP_XRT_FLD_ENA	1	07-F0	d8
2010/03/05	06:19:20.0	XRT_FLRCTRL_ENA_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/03/05	06:19:22.0	XRT_ARS_DIS_418_OG [0x1a2]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/03/05	06:19:30.0	AOCs_OrE-point_Start_1_OG [0x097]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/03/05	06:21:56.0	XRT_QT_PROG_SET_406_OG [0x196]	AOCU_NM	5	02-76	03 00 00 00 00
2010/03/05	06:21:58.0	XRT_FL_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2010/03/05	06:22:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2010/03/05	07:13:30.0	XRT_CTRL_MANU_410_OG [0x19a]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/03/05	07:37:30.0	XRT_Custom_411_OG [0x19b]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/05	07:38:30.0	XRT_CTRL_AUTO_412_OG [0x19c]	XRT_Custom_411_OG [0x19b]	1	07-F0	c0
2010/03/05	08:53:00.0	XRT_CTRL_MANU_410_OG [0x19a]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/03/05	09:13:30.0	XRT_Custom_411_OG [0x19b]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/05	09:14:30.0	XRT_CTRL_AUTO_412_OG [0x19c]	XRT_Custom_411_OG [0x19b]	1	07-F0	c0
2010/03/05	10:17:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/03/05	10:47:00.0	AOCs_OrE-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM	AOCs_OrE-point_Start_2_OG [0x098]	5	02-76	00 00 00 00 00