

XRT Timeline to be uploaded on 2010/03/05

Period: 2010/03/05 10:37:00 - 2010/03/09 09:37:00

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Normal mode

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XOB #1781: HOP 144 - Ti/Poly - 384x384 - AEC 1 - 40s - G-band Co-align												
Term	Pointing (x, y)	Comment										
03/05 10:44:30 - 03/05 10:46:54	Fixed (,)											
03/05 15:03:00 - 03/05 19:34:54	Track (638.0, 332.9) @ 03/05 15:00:00	* HOP 144, spicules.										
PROG= 01 Inf.-time(s)												
Subr= 1	1-time(s)	2.0sec										
Seqn= 3	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
Seqn= 9	20-time(s)	40.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1786: AR Dynamics - Ti/Poly - 512x512 - AEC 1 - Q92 - G-band Co-Align - Thick/Al-2												
Term	Pointing (x, y)	Comment										
03/05 10:50:00 - 03/05 14:59:54	Track (867.2, -257.9) @ 03/05 10:47:00	# OP start + 10min, track AR 11052.										
03/05 19:48:00 - 03/05 23:59:54	Track (890.3, -267.2) @ 03/05 19:45:00	# Resume tracking AR 11052.										
03/06 00:03:00 - 03/06 05:43:24	Fixed (899.0, -272.0)	* AR 11052, fixed pointing at W limb.										
03/06 05:56:30 - 03/06 08:55:15	Fixed (899.0, -272.0)	# Cont.										
PROG= 02 Inf.-time(s)												
Subr= 1	1-time(s)	2.0sec										
Seqn= 6	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 4	7-time(s)	120.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	512x512 (1024, 1024)	Q=92	1	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1772: Synoptic Q95 2x2 - Al/mesh(33/512) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1024) + G-band(16)-2												
Term	Pointing (x, y)	Comment										
03/05 19:38:00 - 03/05 19:44:54	Fixed (0.0, 0.0)	synoptic, shifted -2.0 min										
03/06 05:46:30 - 03/06 05:53:24	Fixed (0.0, 0.0)	synoptic, shifted -16.5 min										
PROG= 03 1-time(s)												
Subr= 1	1-time(s)	12.0sec										
Seqn= 5	1-time(s)	4.0sec										
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 10	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 1	1-time(s)	4.0sec										
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 8	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1776: Flare Response short exp at first- Dynamics - Thick-Al - Thick-Be - AEC 1 - 384x384 - Q95												
Term	Pointing (x, y)	Comment										
03/05 10:50:00 - 03/05 14:59:54	Track (867.2, -257.9) @ 03/05 10:47:00	# OP start + 10min, track AR 11052.										
03/05 15:03:00 - 03/05 19:34:54	Track (638.0, 332.9) @ 03/05 15:00:00	* HOP 144, spicules.										
03/05 19:48:00 - 03/05 23:59:54	Track (890.3, -267.2) @ 03/05 19:45:00	# Resume tracking AR 11052.										
03/06 00:03:00 - 03/06 05:43:24	Fixed (899.0, -272.0)	* AR 11052, fixed pointing at W limb.										
03/06 05:56:30 - 03/06 08:55:15	Fixed (899.0, -272.0)	# Cont.										
PROG= 04 1-time(s)												
Subr= 1	2-time(s)	2.0sec										
Seqn= 7	15-time(s)	20.0sec										
Open/thick-Al	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Seqn= 2	1-time(s)	4.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 2	8-time(s)	2.0sec										
Seqn= 7	15-time(s)	60.0sec										
Open/thick-Al	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Seqn= 2	1-time(s)	4.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3	25-time(s)	2.0sec										
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

Seqn= 7		1-time(s)		600.0sec											
Open/thick-Al	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)		Q=95	1	0	2.0sec		
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)		Q=95	1	0	2.0sec		
Seqn= 2		1-time(s)		4.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

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Active Region Search

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NOT USED

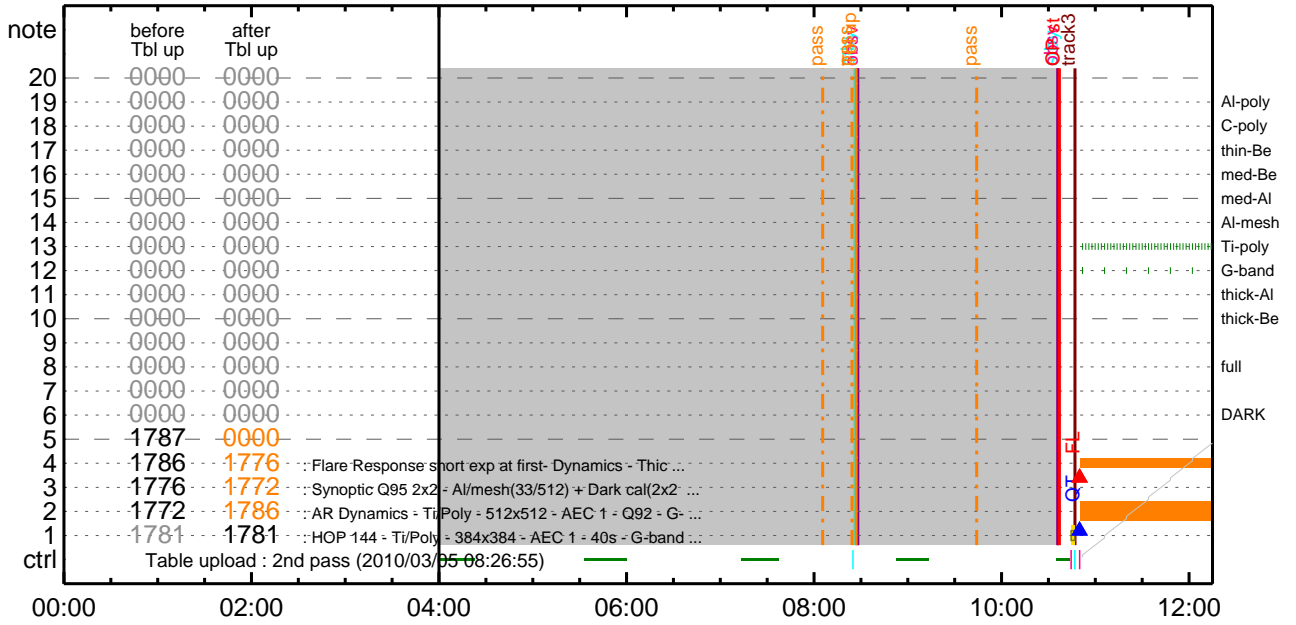
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Flare Detection

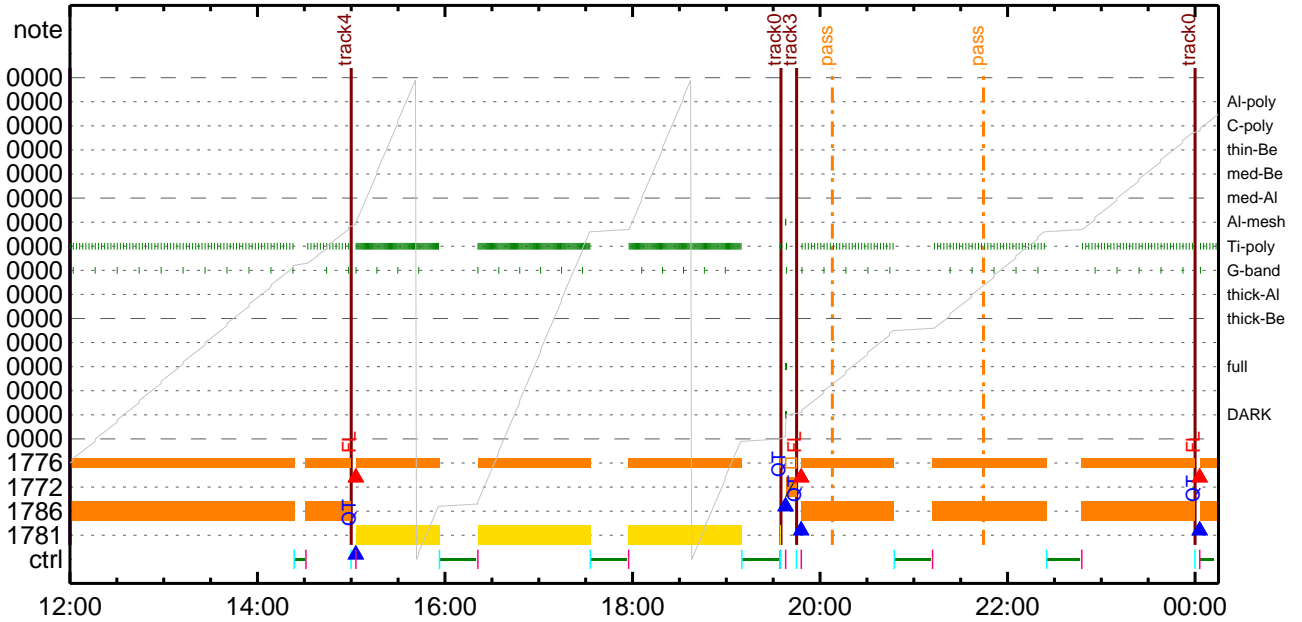
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FLD Patrol												
Term		Pointing (x, y)					Comment					
03/05 10:47:16 - 03/05 19:35:16	Track (867.2, -257.9)	@ 03/05 10:47:00					# OP start + 10min, track AR 11052.					
03/05 19:45:16 - 03/06 05:43:46	Track (890.3, -267.2)	@ 03/05 19:45:00					# Resume tracking AR 11052.					
03/06 05:53:46 - 03/09 09:37:00	Fixed (899.0, -272.0)						# Cont.					
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50		120sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

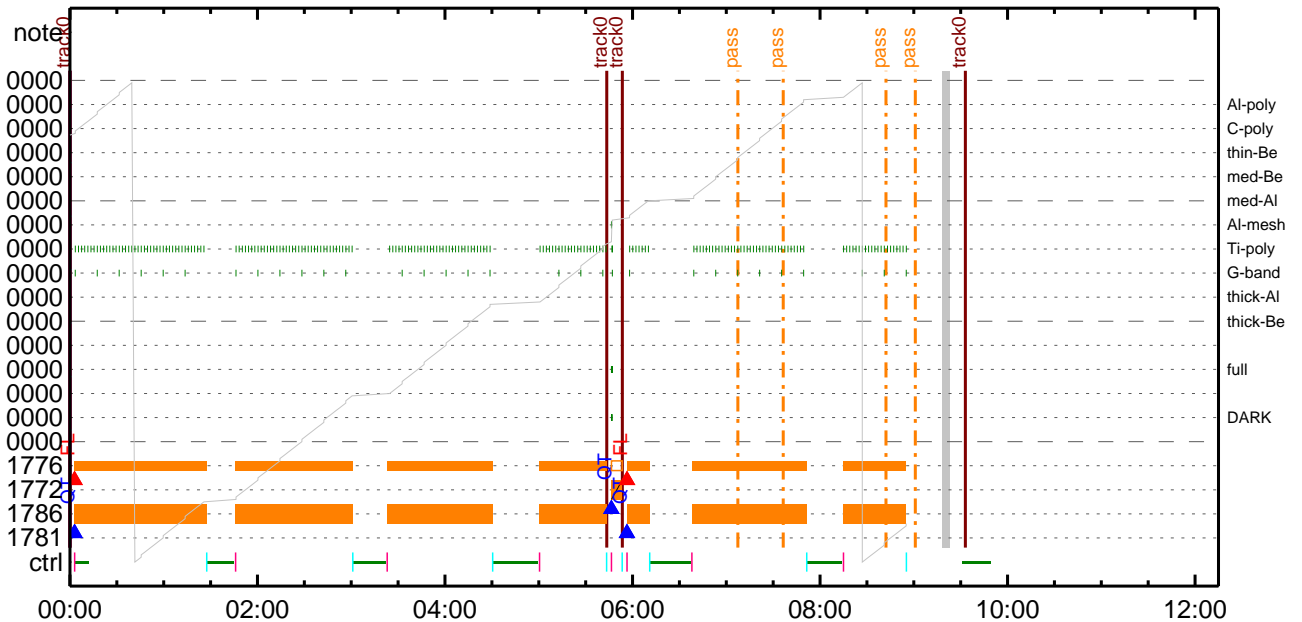
CMDI #0130 2010/03/05



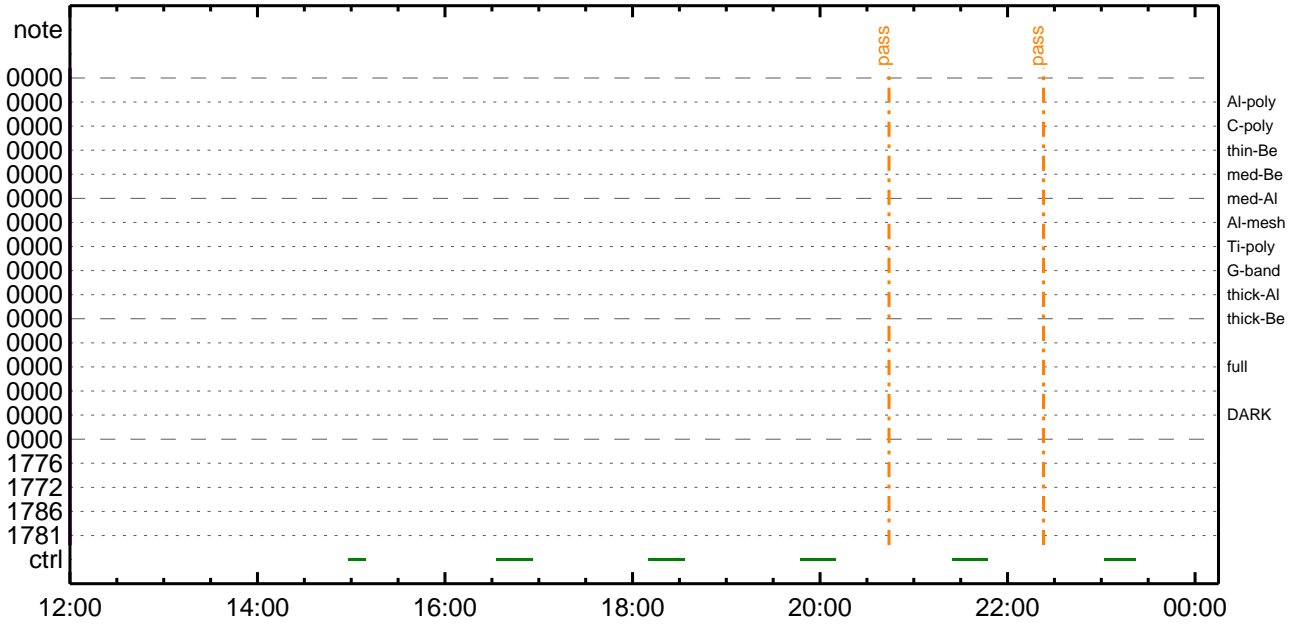
CMDI #0130 2010/03/05



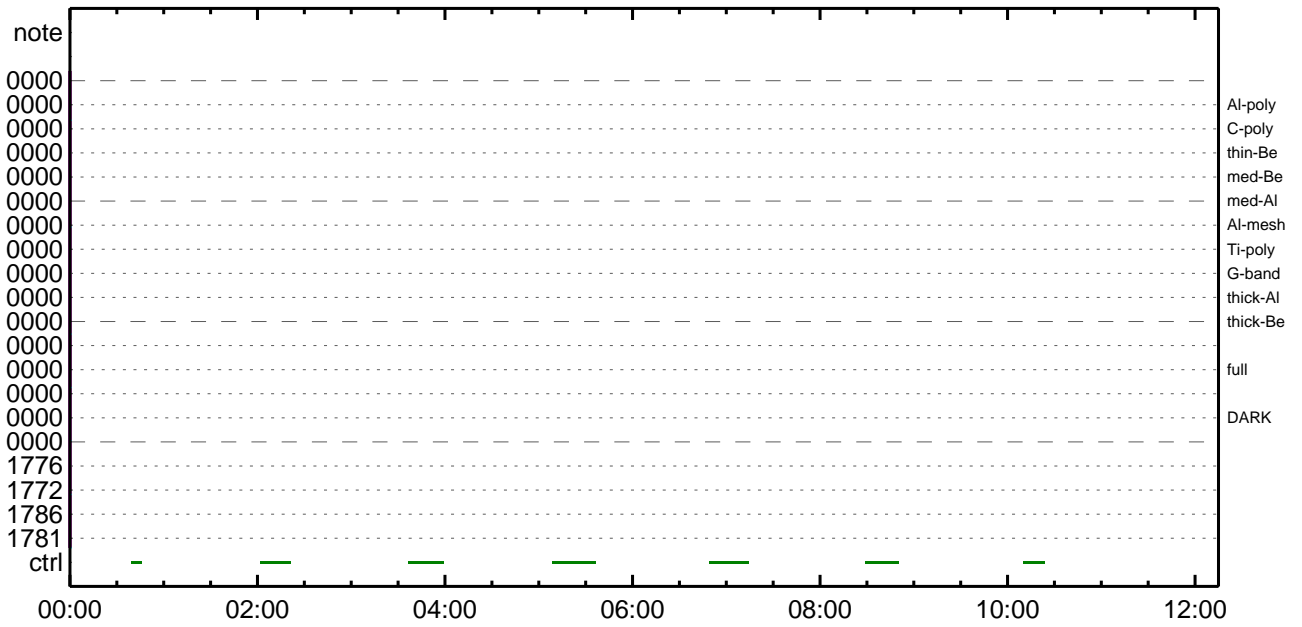
CMDI #0130 2010/03/06



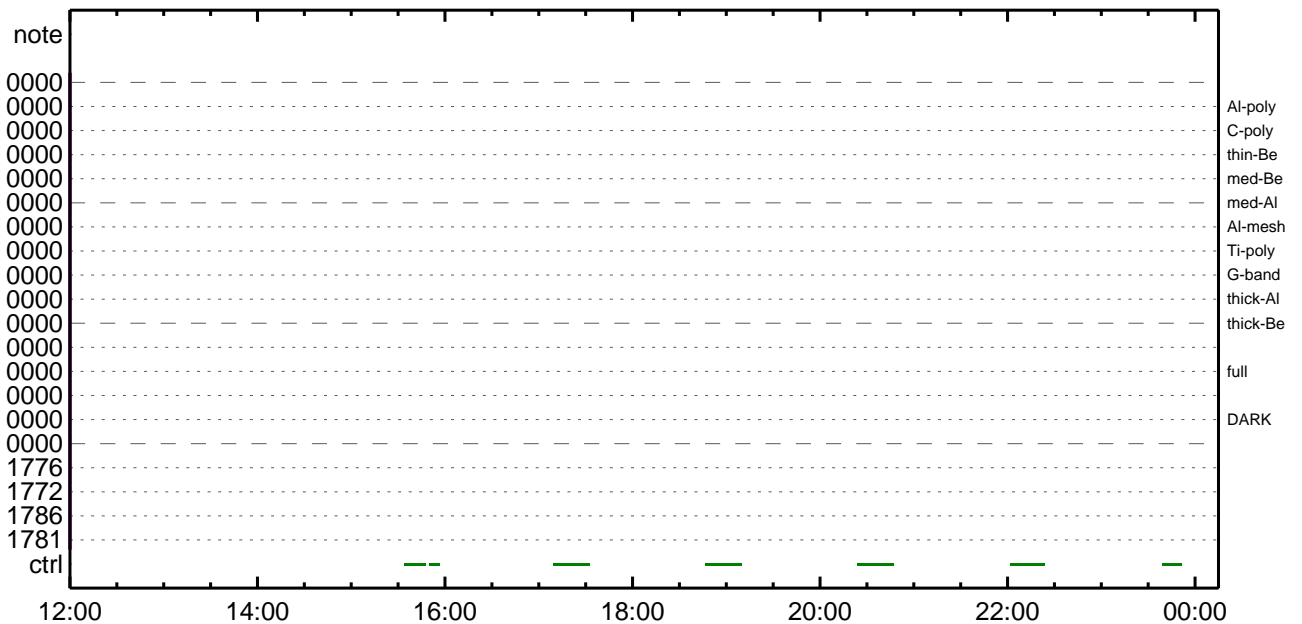
CMDI #0130 2010/03/06



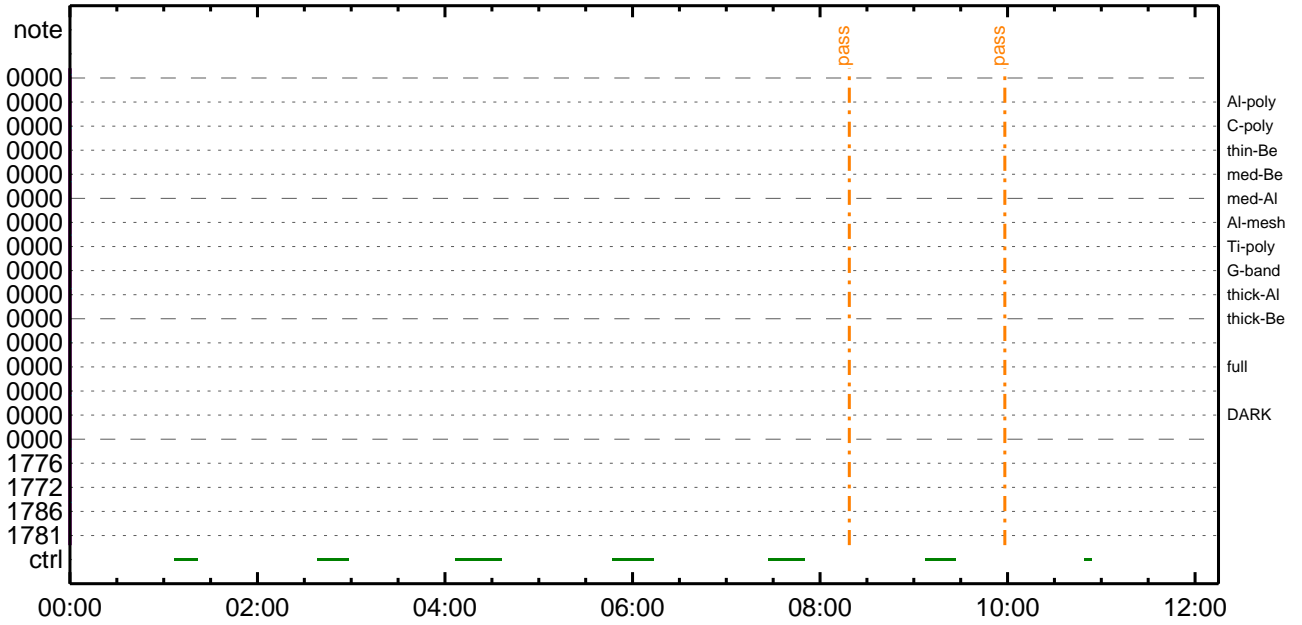
CMDI #0130 2010/03/07



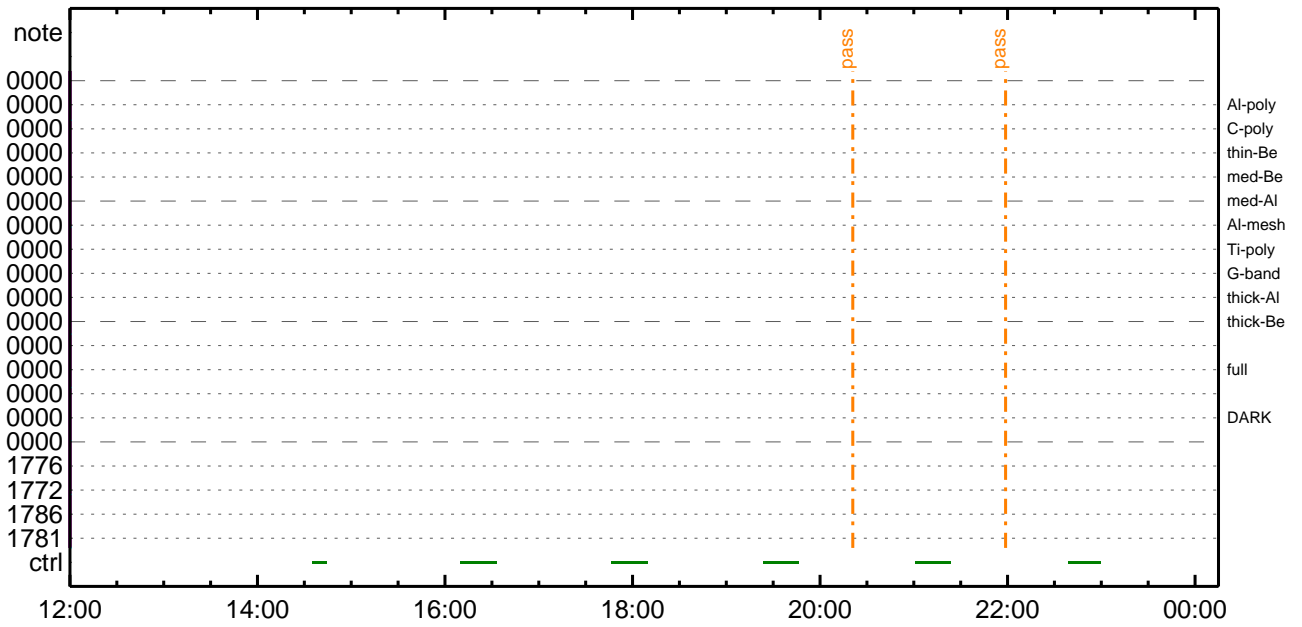
CMDI #0130 2010/03/07



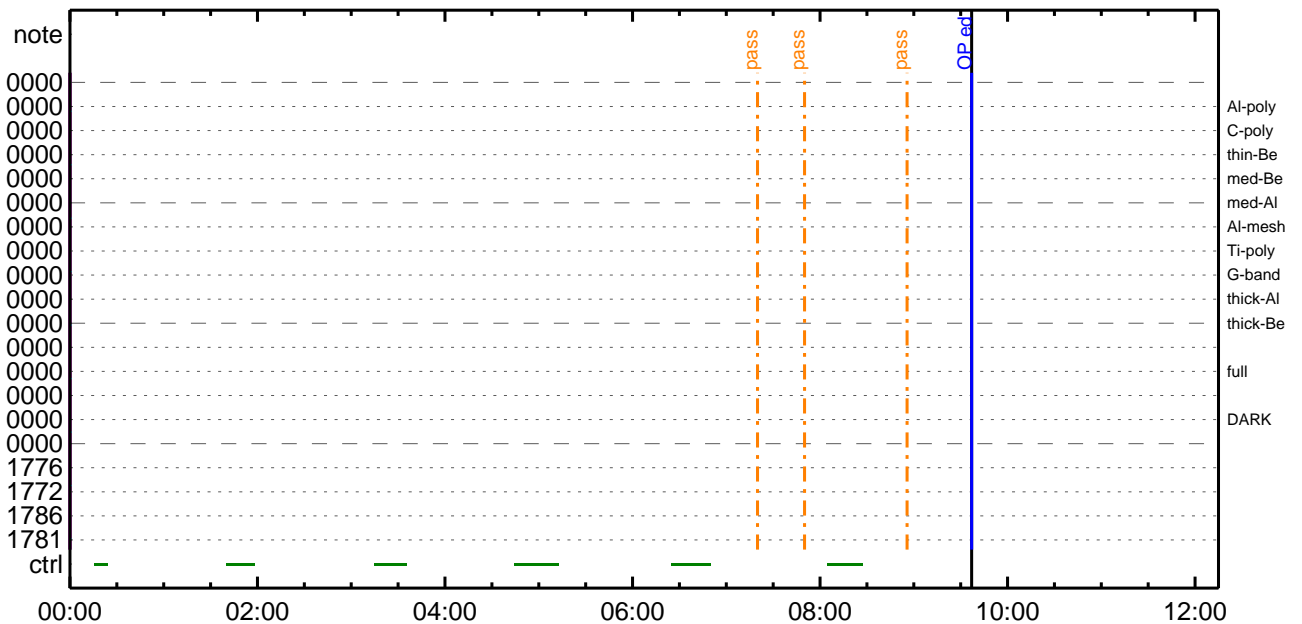
CMDI #0130 2010/03/08



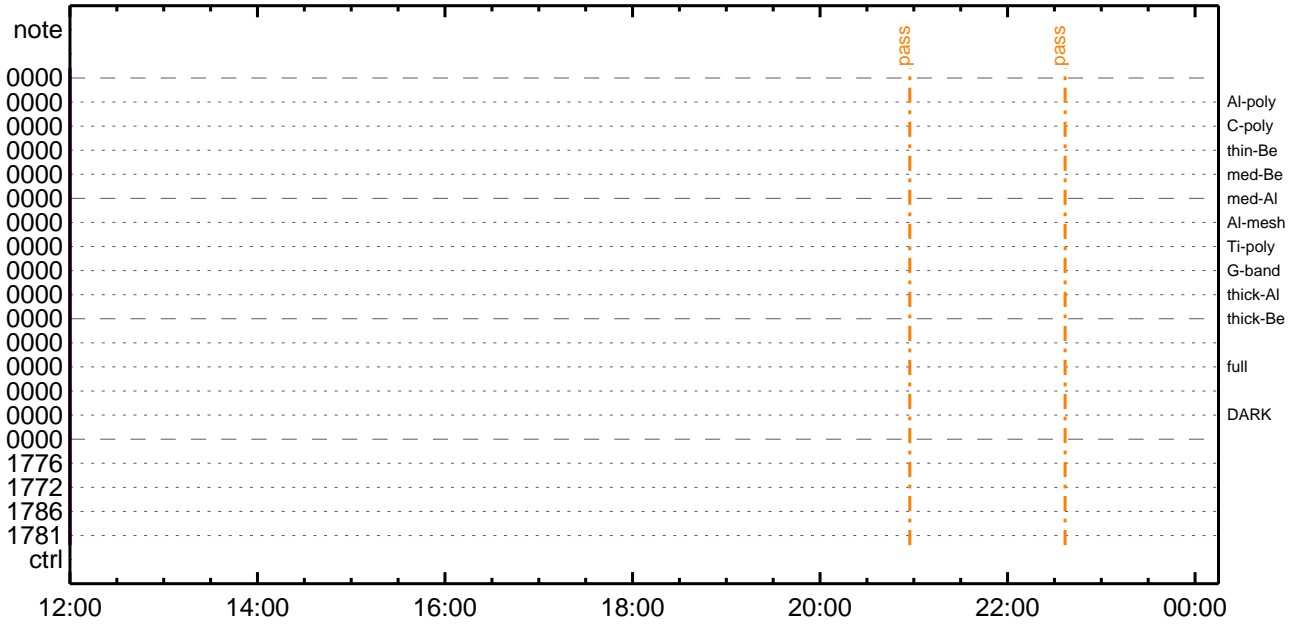
CMDI #0130 2010/03/08



CMDI #0130 2010/03/09



CMDI #0130 2010/03/09




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-214:OP
0104 ( )
0105 S. OG og-214:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ã´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òî½TI-CMDÁ÷¿@²î½Á¹Ôª°¬E²ò³òE;f
0180 C. ²²ò¿;çSET²EEDUMP²î½±°îYÑY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²Y½YÉòðÁDî¿(UT)
0183 +. TI 2010-03-05 10:32:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2010-03-05 10:32:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2010-03-05 10:32:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2010-03-05 10:36:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.      çç[HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0198 C.
0199 C.      °Ê²¼αîÄê%îíñαîî¥Ä¥§¥Ä¥-¹àîÛ
0200 C.      çç[HK1_TI_CMD_ENA/DIS]                    EQ      ENA
0201 C.      çç[HK1_TI_CMD_NUM]                        EQ      4
0202 C.      çç[HK1_NEXT_EXEC_PIM]                     EQ      DHU
0203 C.      çç[HK1_NEXT_EXEC_DC]                      EQ      0xB3
0204 C.
0205 C.      *****
0206 C.      Tîîî°è¥Ä¥Ö¥×
0207 C.      *****
0208 C.
0209 C.      TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.      çç[HK1_DMP_TOP_ADRS_1]                    EQ      07
0213 C.      çç[HK1_DMP_TOP_ADRS_0]                    EQ      2B
0214 C.      çç[HK1_DMP_BLOCK_NUM]                     EQ      3
0215 C.      çç[HK1_DMP_REPEAT_NUM]                    EQ      0
0216 C.      çç[HK1_DMA_DMP_PIM]                       EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.      çç[HK1_PKT_FORM_NO]                       EQ      7
0220 C.      çç[HK1_PKT_GEN_TIME]                       EQ      0.25 s
0221 C.      çç[HK1_S_TLM_BIT_RATE]                    EQ      32k
0222 C.      çç[HK1_X_TLM_BIT_RATE]                    EQ      4M
0223 C.      çç[HK1_DMP_CHK_FLG]                       EQ      EXEC
0224 C.
0225 C.      ¥Ä¥Ö¥×½ª î»αð³îÇ§
0226 C.      çç[HK1_DMP_CHK_FLG]                       EQ      NON
0227 C.
0228 C.      RAM ID=TI_TBLαîî¾È¹ç•è²îOKαð³îÇ§
0229 C.
0230 C.      DHU¥â;¼¥É;È¼¥¼. ¥î;¼¥È;Èαðîäα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.      çç[HK1_PKT_FORM_NO]                       EQ      2
0234 C.      çç[HK1_PKT_GEN_TIME]                       EQ      0.5S
0235 C.      çç[HK1_S_TLM_BIT_RATE]                    EQ      32K
0236 C.      çç[HK1_X_TLM_BIT_RATE]                    EQ      4M
0237 C.
0238 C.      *****
0239 C.      SOT TI command set
0240 C.      *****
0241 C.      Execute, after the success of OP upload.
0242 +. TI 2010-03-05 10:36:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C.      -----
0246 C.      HK1_TI_CMD_NUM      = 1 CNTUP [ ]
0247 C.      -----
0248 C.      ***** SOT END *****
0249 C.      Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C.      ***** Start EIS operation (TI set) *****
0253 C.      Execute, after the success of OP upload.
0254 C.      Set EIS TI-commands
0255 +. TI 2010-03-05 10:36:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2010-03-05 10:36:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.      [ ] [HK1_TI_CMD_NUM]                      EQ      2 COUNTUP
0262 C.      ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C.      ***** XRT START *****
0267 C.      Execute, after the success of OP upload.
0268 +. TI 2010-03-05 10:36:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.      [ ] [HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0272 C.
0273 C.      ***** XRT END *****
0274 C.
0275 C.      ***** MDP `ûÃîαî»ö¼ÝαÈÄα¹αèDCBC•x²è *****
0276 C.      (¼á°î¥Ö¥Ä¥È¥¥¥È¥á¥ç¥èèÈ¼αα¼Ä»Ûα¹αè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C.      ***** ¥Ð¥¹•î Daily±çîñαè'Øα¹αèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C.      ;ãLOS¥Ä¥§¥Ä¥-¼Ä»Û;ã
0287 C.
0288 C.      ***** LOS *****
0289 C.

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(a) Spacecraft Operation Procedure (real-commands)

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main-215 2010-03-05 12:57:46 137 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY~¼Á»Ü;ã
0005 C.
0006 C. YÀYB;¼Y³YFÿÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿øÁø•µ°È»Í×ÁÇøÍY¿YÁY×Yí;¼YÉ;ÈÈèµ•íÉ;ÈøÈ¼°ÇÖø•ø¿¼í¹¿øÍ;çÁ®, ùø¹øÈøøøÇÁ+¿®ø•øÈøøøøøøøÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047 BC (21 02)
0048 . C. Verify EIS in MANUAL mode
0049 . C. Estimated OBSTBL upload time is 2s
0050 C. *****
0051 C. EIS START OBSTBL LOAD
0052 C. *****
0053 . S. RAM ram-820:EIS_OBSTBL
0054 ( )
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056 BC (07 07 07 00 00 70 00)
0057 C.
0058 C. Execute, after the success of OBSTBL upload.
0059 C. Set EIS TI-commands
0060 +. TI 2010-03-05 10:36:50.0
0061 DC 07-FC EIS_MODE_CHG_ENA
0062 BC (20)
0063 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064 C. *****
0065 C. EIS END OBSTBL LOAD
0066 C. *****
0067 C.
0068 C. ***** XRT START *****
0069 C.
0070 +. DC 07-F0 MDP_XRT_CTRL_MANU
0071 BC (c1)
0072 + DC 07-F0 MDP_XRT_MODE_STBY
0073 BC (c3)
0074 . C. ----- Success Verify ? OK / NG_____
0075 C.
0076 C. XRT Obs. Table Upload
0077 . S. RAM ram-291:MDP_OBS_X
0078 ( )
0079 C.
0080 +. DC 07-F0 MDP_DUMP_XRTTBL
0081 BC (84 07 00 00 00 3a d4)
0082 . C. ----- Comparison Check ? OK / ERR _____
0083 C.
0084 C.
0085 +. DC 07-F0 MDP_XRT_ROI_SET
0086 BC (cd 01 b1 b1 04 04)
0087 + DC 07-F0 MDP_XRT_ROI_SET
0088 BC (cd 02 b1 b1 08 08)
0089 + DC 07-F0 MDP_XRT_ROI_SET
0090 BC (cd 03 b1 b1 08 08)
0091 + DC 07-F0 MDP_XRT_ROI_SET
0092 BC (cd 04 b1 b1 06 06)
0093 + DC 07-F0 MDP_XRT_ROI_SET
0094 BC (cd 06 80 80 08 08)
0095 + DC 07-F0 MDP_XRT_ROI_SET
```

```
0096 BC (cd 07 80 60 20 18)
0097 + DC 07-F0 MDP_XRT_ROI_SET
0098 BC (cd 08 85 83 06 06)
0099 + DC 07-F0 MDP_XRT_ROI_SET
0100 BC (cd 09 80 80 20 20)
0101 + DC 07-F0 MDP_XRT_ROI_SET
0102 BC (cd 0a 80 80 20 08)
0103 + DC 07-F0 MDP_XRT_ROI_SET
0104 BC (cd 0b 80 80 08 20)
0105 + DC 07-F0 MDP_XRT_ROI_SET
0106 BC (cd 0f 80 80 06 06)
0107 + DC 07-F0 MDP_XRT_AEC_RESET
0108 BC (d0)
0109 . C. ----- Success Verify ? OK / NG ____
0110 C.
0111 C.
0112 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0113 C.
0114 +. DC 07-F0 MDP_XRT_MODE_OBSV
0115 BC (c2)
0116 +. TI 2010-03-05 10:36:02.0
0117 DC 07-F0 MDP_XRT_MODE_OBSV
0118 BC (c2)
0119 . C. ----- Success Verify ? OK / NG ____
0120 C.
0121 C. ***** XRT END *****
0122 C.
0123 . C. ***** MDP `úÃîñî»ö%ÝðÊÃð¹ñèDCBC•x²è *****
0124 C. (%Á°îÝÓÝÃÝÈÝÞÝËÝáÝçÝèñÈ½¼ñ¼Ã»Ûñ¹ñè)
0125 . S. DC-BC dcbc-402:DCBC
0126 (MDP_known_event)
0127 C.
0128 C.
0129 . C. ***** ÝÐÝ¹•Ï Daily±¿îÑñÈ´Øñ¹ñèDCBC•x²è *****
0130 . S. DC-BC dcbc-153:DCBC
0131 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0132 C.
0133 C.
0134 . C. ¡ãLOSÝÁÝ$ÝÃÝ¬¼Ã»Ûñ¹ñè
0135 C.
0136 . C. ***** LOS *****
0137 C.
```


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*** OP Sequence for XRT ***

2010/03/05	10:43:30.0	XRT_Custom_400_OG [0x190]			
2010/03/05	10:44:30.0	XRT_CTRL_AUTO_401_OG [0x191]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/03/05	10:46:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/05	10:46:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/03/05	10:47:00.0	AOCS_Orе-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2010/03/05	10:47:16.0	XRT_FLD_ENA_404_OG [0x194]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2010/03/05	10:47:18.0	XRT_FLD_RESET_405_OG [0x195]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/03/05	10:47:20.0	XRT_FLRCTRL_ENA_406_OG [0x196]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/03/05	10:47:22.0	XRT_ARS_DIS_407_OG [0x197]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/03/05	10:49:56.0	XRT_QT_PROG_SET_408_OG [0x198]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2010/03/05	10:49:58.0	XRT_FL_PROG_SET_409_OG [0x199]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04
2010/03/05	10:50:00.0	XRT_CTRL_AUTO_410_OG [0x19a]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/03/05	14:23:30.0	XRT_CTRL_MANU_411_OG [0x19b]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/05	14:30:00.0	XRT_Custom_400_OG [0x190]			
2010/03/05	14:31:00.0	XRT_CTRL_AUTO_401_OG [0x191]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/03/05	14:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/05	14:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/03/05	15:00:00.0	AOCS_Orе-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	04 00 00 00 00
2010/03/05	15:00:16.0	XRT_FLD_ENA_404_OG [0x194]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2010/03/05	15:00:18.0	XRT_FLD_RESET_405_OG [0x195]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/03/05	15:00:20.0	XRT_FLRCTRL_ENA_406_OG [0x196]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/03/05	15:00:22.0	XRT_ARS_DIS_407_OG [0x197]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/03/05	15:02:56.0	XRT_QT_PROG_SET_412_OG [0x19c]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2010/03/05	15:02:58.0	XRT_FL_PROG_SET_409_OG [0x199]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04
2010/03/05	15:03:00.0	XRT_CTRL_AUTO_410_OG [0x19a]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/03/05	15:56:30.0	XRT_CTRL_MANU_411_OG [0x19b]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/05	16:20:00.0	XRT_Custom_400_OG [0x190]			
2010/03/05	16:21:00.0	XRT_CTRL_AUTO_401_OG [0x191]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/03/05	17:33:00.0	XRT_CTRL_MANU_411_OG [0x19b]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/05	17:56:30.0	XRT_Custom_400_OG [0x190]			
2010/03/05	17:57:30.0	XRT_CTRL_AUTO_401_OG [0x191]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/03/05	19:10:00.0	XRT_CTRL_MANU_411_OG [0x19b]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/05	19:33:30.0	XRT_Custom_400_OG [0x190]			
2010/03/05	19:34:30.0	XRT_CTRL_AUTO_401_OG [0x191]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/03/05	19:34:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/05	19:34:56.0	XRT_FOCUS_POSITION_413_OG [0x19d]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/03/05	19:35:00.0	AOCS_Orе-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2010/03/05	19:35:16.0	XRT_FLD_DIS_414_OG [0x19e]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2010/03/05	19:35:18.0	XRT_FLRCTRL_DIS_415_OG [0x19f]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2010/03/05	19:35:20.0	XRT_ARS_DIS_416_OG [0x1a0]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/03/05	19:37:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2010/03/05	19:38:00.0	XRT_CTRL_AUTO_410_OG [0x19a]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/03/05	19:44:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/05	19:44:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/03/05	19:45:00.5	AOCS_Orе-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2010/03/05	19:45:16.0	XRT_FLD_ENA_404_OG [0x194]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2010/03/05	19:45:18.0	XRT_FLD_RESET_405_OG [0x195]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/03/05	19:45:20.0	XRT_FLRCTRL_ENA_406_OG [0x196]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8

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2010/03/05	19:45:22.0	XRT_ARS_DIS_407_OG [0x197]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/03/05	19:47:56.0	XRT_QT_PROG_SET_408_OG [0x198]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02			
2010/03/05	19:47:58.0	XRT_FL_PROG_SET_409_OG [0x199]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04			
2010/03/05	19:48:00.0	XRT_CTRL_AUTO_410_OG [0x19a]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/03/05	20:47:30.0	XRT_CTRL_MANU_411_OG [0x19b]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/03/05	21:11:00.0	XRT_Custom_400_OG [0x190]						
2010/03/05	21:12:00.0	XRT_CTRL_AUTO_401_OG [0x191]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/03/05	22:25:00.0	XRT_CTRL_MANU_411_OG [0x19b]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/03/05	22:46:30.0	XRT_Custom_400_OG [0x190]						
2010/03/05	22:47:30.0	XRT_CTRL_AUTO_401_OG [0x191]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/03/05	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/03/05	23:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2010/03/06	00:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]						
		AOCU_NM	5	02-76	00 18 31 b0 19			
2010/03/06	00:00:16.0	XRT_FLD_ENA_404_OG [0x194]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2010/03/06	00:00:18.0	XRT_FLD_RESET_405_OG [0x195]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2010/03/06	00:00:20.0	XRT_FLRCTRL_ENA_406_OG [0x196]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2010/03/06	00:00:22.0	XRT_ARS_DIS_407_OG [0x197]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/03/06	00:02:56.0	XRT_QT_PROG_SET_408_OG [0x198]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02			
2010/03/06	00:02:58.0	XRT_FL_PROG_SET_409_OG [0x199]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04			
2010/03/06	00:03:00.0	XRT_CTRL_AUTO_410_OG [0x19a]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/03/06	01:27:30.0	XRT_CTRL_MANU_411_OG [0x19b]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/03/06	01:45:00.0	XRT_Custom_400_OG [0x190]						
2010/03/06	01:46:00.0	XRT_CTRL_AUTO_401_OG [0x191]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/03/06	03:01:00.0	XRT_CTRL_MANU_411_OG [0x19b]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/03/06	03:22:00.0	XRT_Custom_400_OG [0x190]						
2010/03/06	03:23:00.0	XRT_CTRL_AUTO_401_OG [0x191]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/03/06	04:30:30.0	XRT_CTRL_MANU_411_OG [0x19b]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/03/06	04:59:30.0	XRT_Custom_400_OG [0x190]						
2010/03/06	05:00:30.0	XRT_CTRL_AUTO_401_OG [0x191]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/03/06	05:43:24.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/03/06	05:43:26.0	XRT_FOCUS_POSITION_413_OG [0x19d]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2010/03/06	05:43:30.0	AOCS_ORe-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2010/03/06	05:43:46.0	XRT_FLD_DIS_414_OG [0x19e]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2010/03/06	05:43:48.0	XRT_FLRCTRL_DIS_415_OG [0x19f]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2010/03/06	05:43:50.0	XRT_ARS_DIS_416_OG [0x1a0]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/03/06	05:46:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03			
2010/03/06	05:46:30.0	XRT_CTRL_AUTO_410_OG [0x19a]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/03/06	05:53:24.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/03/06	05:53:26.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2010/03/06	05:53:30.0	AOCS_ORe-point_Start_4_OG [0x09a]						
		AOCU_NM	5	02-76	00 18 31 b0 19			
2010/03/06	05:53:46.0	XRT_FLD_ENA_404_OG [0x194]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2010/03/06	05:53:48.0	XRT_FLD_RESET_405_OG [0x195]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2010/03/06	05:53:50.0	XRT_FLRCTRL_ENA_406_OG [0x196]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2010/03/06	05:53:52.0	XRT_ARS_DIS_407_OG [0x197]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/03/06	05:56:26.0	XRT_QT_PROG_SET_408_OG [0x198]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02			
2010/03/06	05:56:28.0	XRT_FL_PROG_SET_409_OG [0x199]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04			
2010/03/06	05:56:30.0	XRT_CTRL_AUTO_410_OG [0x19a]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/03/06	06:11:00.0	XRT_CTRL_MANU_411_OG [0x19b]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/03/06	06:37:00.0	XRT_Custom_400_OG [0x190]						
2010/03/06	06:38:00.0	XRT_CTRL_AUTO_401_OG [0x191]						

2010/03/06	07:51:30.0	XRT_CTRL_MANU_411_OG [0x19b]	MDP_XRT_CTRL_AUTO	1	07-F0	c0					
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2010/03/06	08:14:00.0	XRT_Custom_400_OG [0x190]									
2010/03/06	08:15:00.0	XRT_CTRL_AUTO_401_OG [0x191]									
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2010/03/06	08:55:15.0	XRT_CTRL_MANU_402_OG [0x192]									
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2010/03/06	09:33:00.0	AOCS_ORe-point_Start_3_OG [0x099]									
			AOCU_NM	5	02-76	00	00	00	00	00	00