

# XRT Timeline to be uploaded on 2010/03/09

Period: 2010/03/09 09:37:00 - 2010/03/13 10:27:00

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Normal mode

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XOB #1775: HOP130 Al/mesh short(128ms)/long(2048ms) + Ti/poly short(256ms)/long(4096ms) FOV 512x512 - bin 1x1												
Term	Pointing (x, y)				Comment							
03/09 10:11:00 - 03/09 10:23:54	Fixed ( 936.0, 0.0)				# OP start + 10min HOP130 full disk slot scan 1/15							
03/09 10:27:00 - 03/09 10:39:54	Fixed ( 468.0, 0.0)				2/15							
03/09 10:43:00 - 03/09 10:55:54	Fixed ( 0.0, 0.0)				3/15							
03/09 10:59:00 - 03/09 11:11:54	Fixed (-468.0, 0.0)				4/15							
03/09 11:15:00 - 03/09 11:27:54	Fixed (-936.0, 0.0)				5/15							
03/09 11:31:00 - 03/09 11:37:54	Fixed ( 815.0, 470.0)				6/15							
03/09 11:41:00 - 03/09 11:53:54	Fixed ( 411.0, 847.0)				7/15							
03/09 11:57:00 - 03/09 12:09:54	Fixed ( 0.0, 941.0)				8/15							
03/09 12:13:00 - 03/09 12:25:54	Fixed (-411.0, 847.0)				9/15							
03/09 12:29:00 - 03/09 12:35:54	Fixed (-815.0, 470.0)				10/15							
03/09 12:39:00 - 03/09 12:45:54	Fixed ( 815.0, -470.0)				11/15							
03/09 12:49:00 - 03/09 13:01:54	Fixed ( 411.0, -847.0)				12/15							
03/09 13:05:00 - 03/09 13:17:54	Fixed ( 0.0, -941.0)				13/15							
03/09 13:21:00 - 03/09 13:33:54	Fixed (-411.0, -847.0)				14/15							
03/09 13:37:00 - 03/09 13:49:54	Fixed (-815.0, -471.0)				15/15							
<b>PROG= 09 1-time(s)</b>												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 20 1-time(s) 2.0sec												
└─ Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	125ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 21 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	4.00s	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 22 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	512x512 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #178C: HOP 152 - Ti/Poly - 512x512 - AEC 1 - 40s - G-band Co-align												
Term	Pointing (x, y)				Comment							
03/09 13:53:00 - 03/09 17:59:54	Track ( 371.0, 455.5)				<small>© 03/09 13:50:00</small>	HOP152 with sac peak						
03/09 18:13:00 - 03/10 03:46:00	Track ( 404.4, 453.5)				<small>© 03/09 18:10:00</small>	Track bright reasion and filament						
<b>PROG= 14 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 13 1-time(s) 2.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 4 20-time(s) 40.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	512x512 (1024, 1024)	Q=92	1	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1772: Synoptic Q95 2x2 - Al/mesh(33/512) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1024) + G-band(16)-2												
Term	Pointing (x, y)				Comment							
03/09 18:03:00 - 03/09 18:09:54	Fixed ( 0.0, 0.0)				synoptic							
03/10 06:03:00 - 03/10 07:03:00	Fixed ( 0.0, 0.0)				synoptic							
03/11 08:33:00 - 03/11 08:39:54	Fixed ( 0.0, 0.0)				synoptic, shifted -27.0 min							
<b>PROG= 03 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 5 1-time(s) 4.0sec												
└─ Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 10 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 1 1-time(s) 4.0sec												
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 8 1-time(s) 2.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1563: CCD Monitor During Bakeout - G-band + dark - wide FOV												
Term	Pointing (x, y)				Comment							
03/10 19:57:10 - 03/11 02:00:00	Fixed ( -16.0, -921.0)				HOP101 helioseismology on S-pole							
<b>PROG= 08 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 600.0sec												
└─ Seqn= 14 1-time(s) 4.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec

Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1778: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long**

Term	Pointing (x, y)	Comment
03/11 08:43:00 - 03/11 08:49:54	Fixed ( -528.4, -528.4)	XRT quadrant pointing #1
<b>PROG= 10 1-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 12.0sec</b>		
└─ <b>Seqn= 15 1-time(s) 12.0sec</b>		
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 23 2-time(s) 2.0sec</b>		
Open/Al-mesh	Open/Ti-poly close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #1779: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh, Ti/Poly -long**

Term	Pointing (x, y)	Comment
03/11 08:53:00 - 03/11 08:59:54	Fixed ( 528.4, -528.4)	XRT quadrant pointing #2
<b>PROG= 11 1-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 12.0sec</b>		
└─ <b>Seqn= 16 1-time(s) 12.0sec</b>		
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 23 2-time(s) 2.0sec</b>		
Open/Al-mesh	Open/Ti-poly close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #177A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant- Al/mesh, Ti/Poly-long**

Term	Pointing (x, y)	Comment
03/11 09:03:00 - 03/11 09:09:54	Fixed ( 528.4, 528.4)	XRT quadrant pointing #3
<b>PROG= 12 1-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 12.0sec</b>		
└─ <b>Seqn= 17 1-time(s) 12.0sec</b>		
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 23 2-time(s) 2.0sec</b>		
Open/Al-mesh	Open/Ti-poly close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #177B: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long**

Term	Pointing (x, y)	Comment
03/11 09:13:00 - 03/13 10:27:00	Fixed ( -528.4, 528.4)	XRT quadrant pointing #4
<b>PROG= 13 1-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 12.0sec</b>		
└─ <b>Seqn= 18 1-time(s) 12.0sec</b>		
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 23 2-time(s) 2.0sec</b>		
Open/Al-mesh	Open/Ti-poly close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Flare mode**

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**XOB #1776: Flare Response short exp at first- Dynamics - Thick-Al - Thick-Be - AEC 1 - 384x384 - Q95**

Term	Pointing (x, y)	Comment
03/09 10:11:00 - 03/09 10:23:54	Fixed ( 936.0, 0.0)	# OP start + 10min HOP130 full disk slot scan 1/15
03/09 10:27:00 - 03/09 10:39:54	Fixed ( 468.0, 0.0)	2/15
03/09 10:43:00 - 03/09 10:55:54	Fixed ( 0.0, 0.0)	3/15
03/09 10:59:00 - 03/09 11:11:54	Fixed ( -468.0, 0.0)	4/15

03/09 11:15:00 - 03/09 11:27:54	Fixed ( -936.0, 0.0)	5/15
03/09 11:31:00 - 03/09 11:37:54	Fixed ( 815.0, 470.0)	6/15
03/09 11:41:00 - 03/09 11:53:54	Fixed ( 411.0, 847.0)	7/15
03/09 11:57:00 - 03/09 12:09:54	Fixed ( 0.0, 941.0)	8/15
03/09 12:13:00 - 03/09 12:25:54	Fixed ( -411.0, 847.0)	9/15
03/09 12:29:00 - 03/09 12:35:54	Fixed ( -815.0, 470.0)	10/15
03/09 12:39:00 - 03/09 12:45:54	Fixed ( 815.0, -470.0)	11/15
03/09 12:49:00 - 03/09 13:01:54	Fixed ( 411.0, -847.0)	12/15
03/09 13:05:00 - 03/09 13:17:54	Fixed ( 0.0, -941.0)	13/15
03/09 13:21:00 - 03/09 13:33:54	Fixed ( -411.0, -847.0)	14/15
03/09 13:37:00 - 03/09 13:49:54	Fixed ( -815.0, -471.0)	15/15
03/09 13:53:00 - 03/09 17:59:54	Track ( 371.0, 455.5) <sup>® 03/09 13:50:00</sup>	HOP152 with sac peak
03/09 18:13:00 - 03/10 03:46:00	Track ( 404.4, 453.5) <sup>® 03/09 18:10:00</sup>	Track bright reasion and filament

**PROG= 04 1-time(s)**

<b>Subr= 1 2-time(s) 2.0sec</b>	
└─ <b>Seqn= 7 15-time(s) 20.0sec</b>	
└─ Open/thick-Al	Open/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
└─ <b>Seqn= 2 1-time(s) 4.0sec</b>	
└─ Open/G-band	Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
<b>Subr= 2 8-time(s) 2.0sec</b>	
└─ <b>Seqn= 7 15-time(s) 60.0sec</b>	
└─ Open/thick-Al	Open/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
└─ <b>Seqn= 2 1-time(s) 4.0sec</b>	
└─ Open/G-band	Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
<b>Subr= 3 25-time(s) 2.0sec</b>	
└─ <b>Seqn= 7 1-time(s) 600.0sec</b>	
└─ Open/thick-Al	Open/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 1 0 2.0sec
└─ <b>Seqn= 2 1-time(s) 4.0sec</b>	
└─ Open/G-band	Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Active Region Search**

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NOT USED

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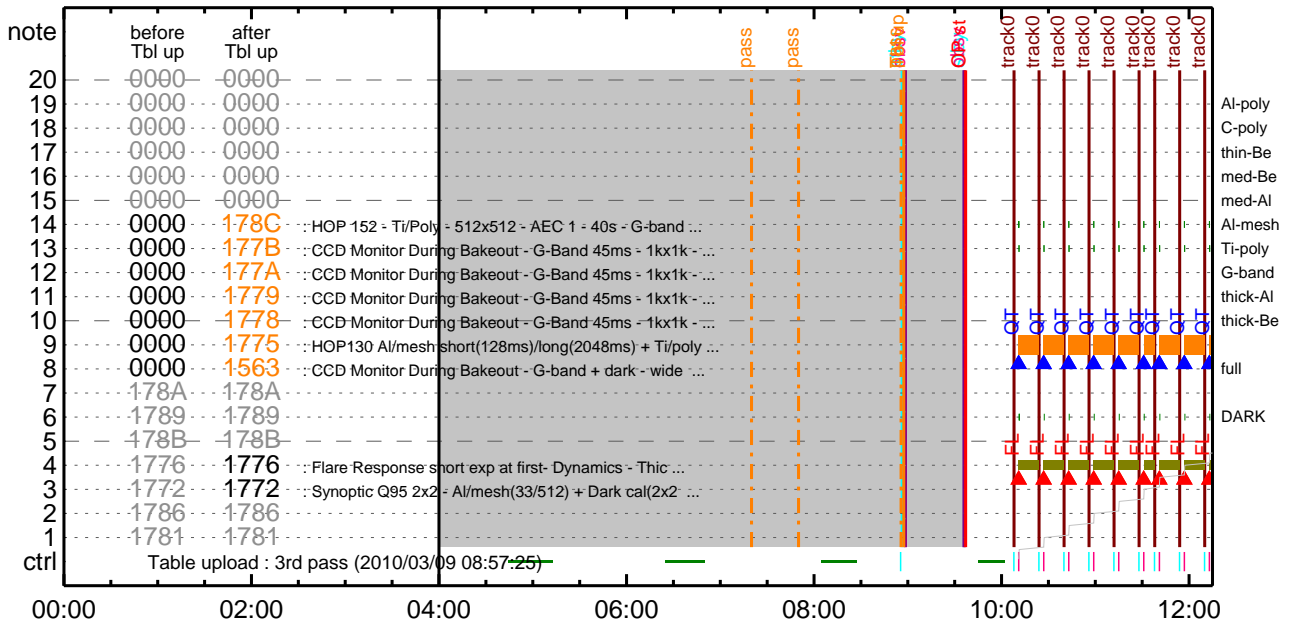
**Flare Detection**

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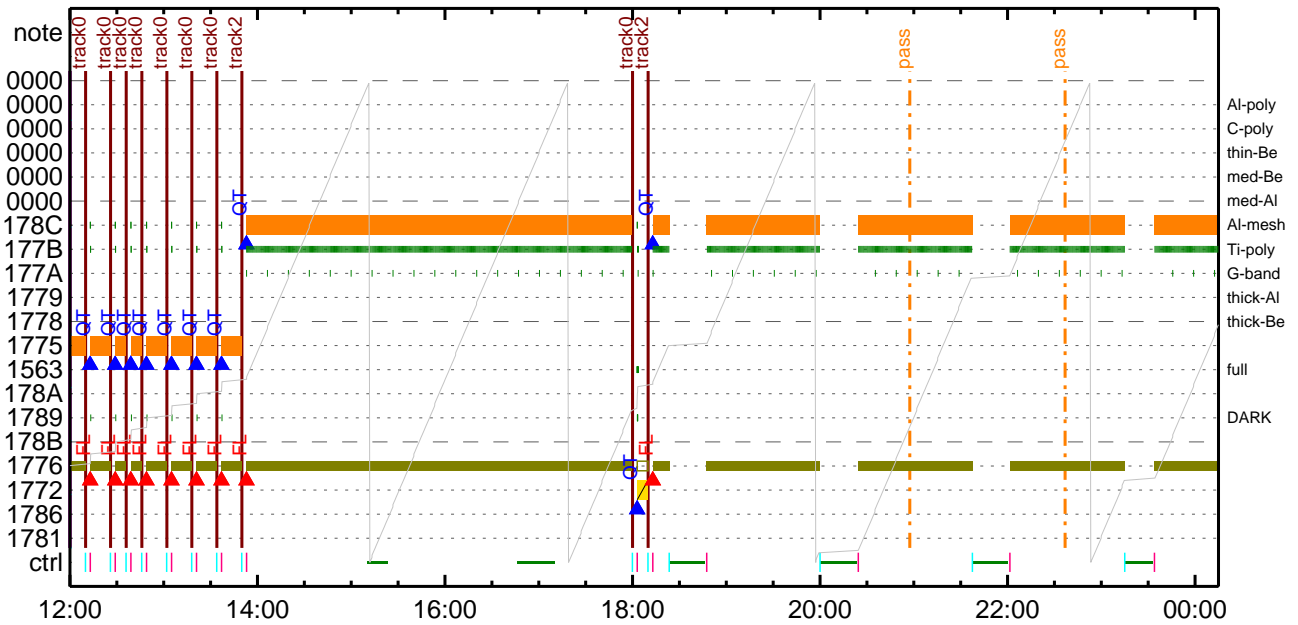
**FLD Patrol**

Term	Pointing (x, y)	Comment
03/09 10:08:16 - 03/09 18:00:16	Fixed ( 936.0, 0.0)	# OP start + 10min HOP130 full disk slot scan 1/15
03/09 18:10:16 - 03/10 06:00:16	Track ( 404.4, 453.5) <sup>® 03/09 18:10:00</sup>	Track bright reasion and filament
Open/Ti-poly	Open/thick-Al close Safe Norm 8ms Obs 8x8	Q=50 120sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

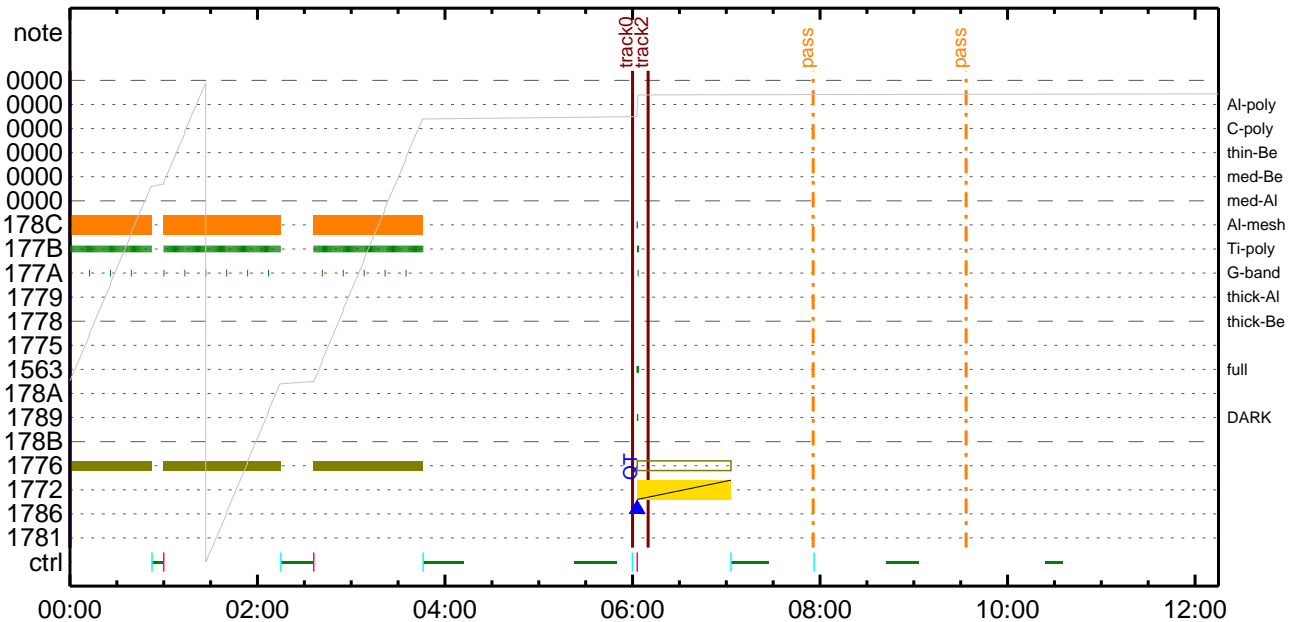
### CMDI #0143 2010/03/09



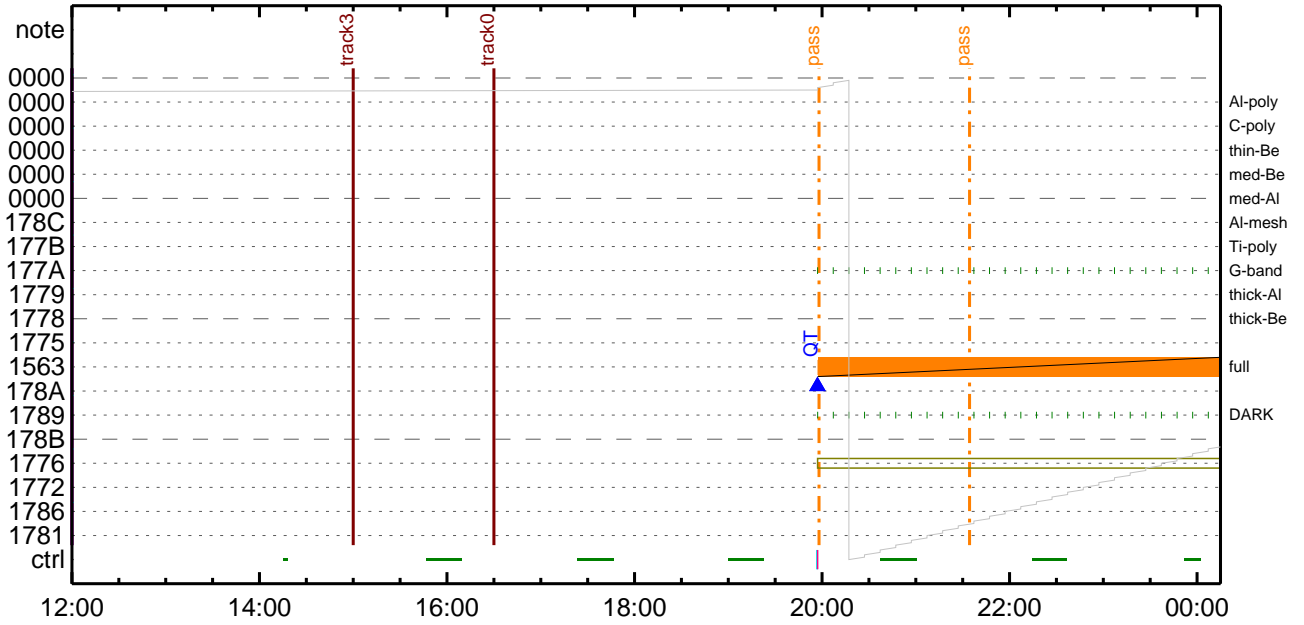
### CMDI #0143 2010/03/09



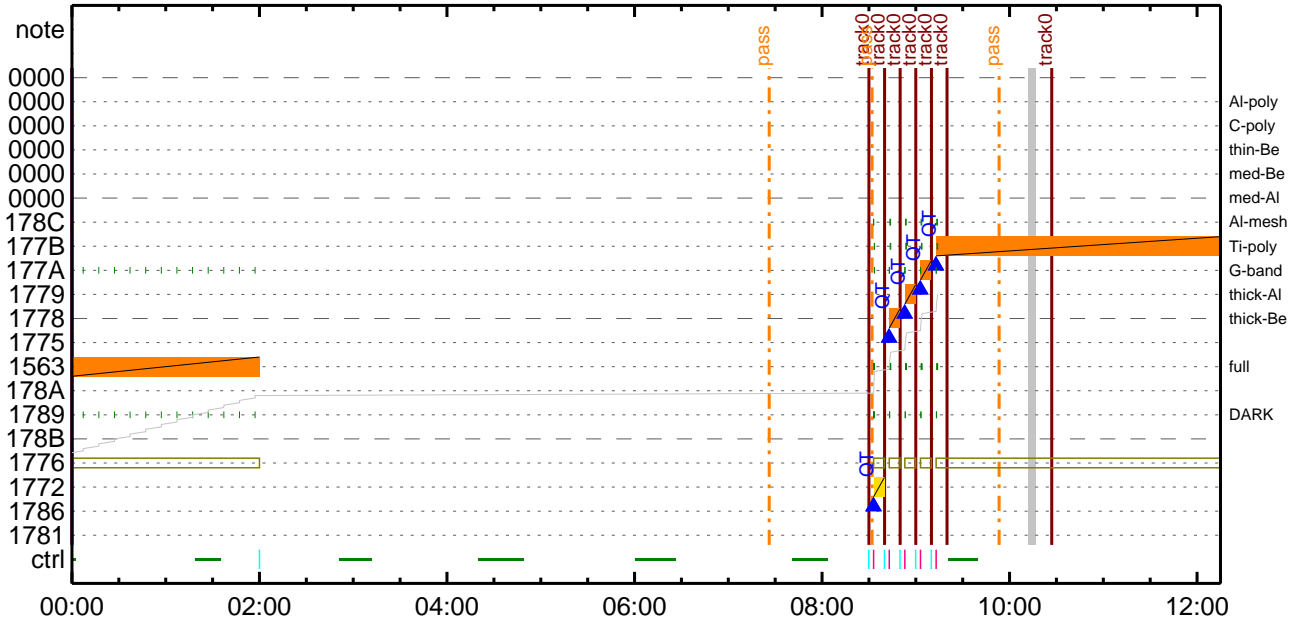
### CMDI #0143 2010/03/10



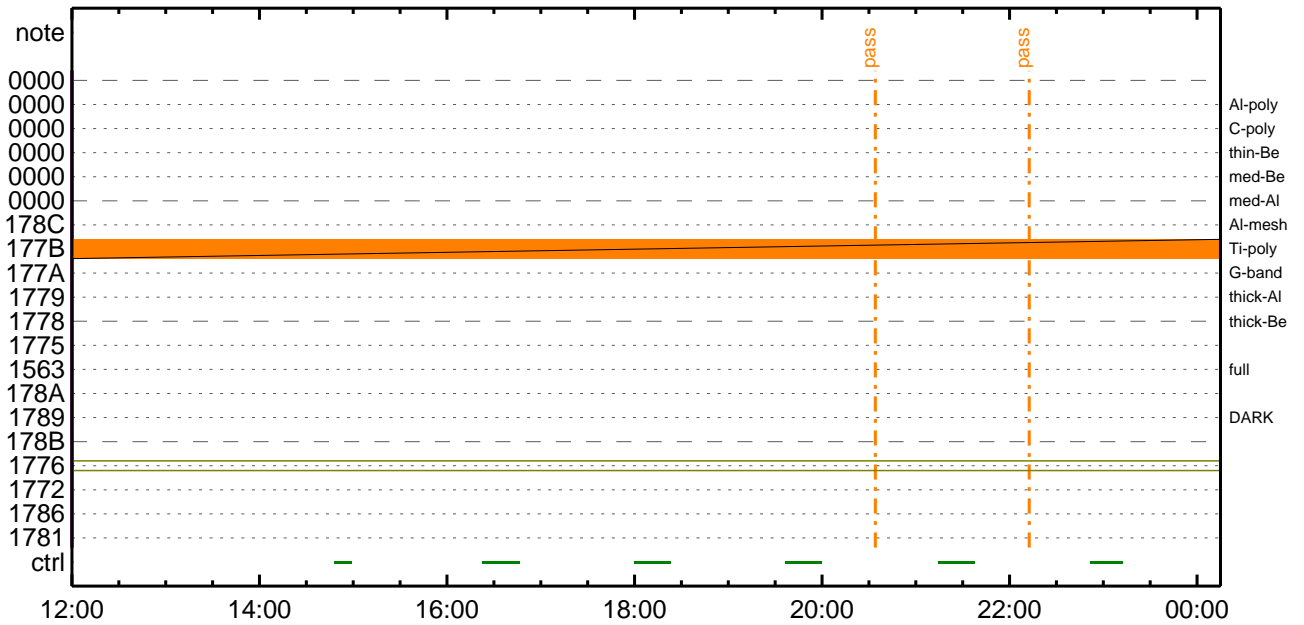
CMDI #0143 2010/03/10



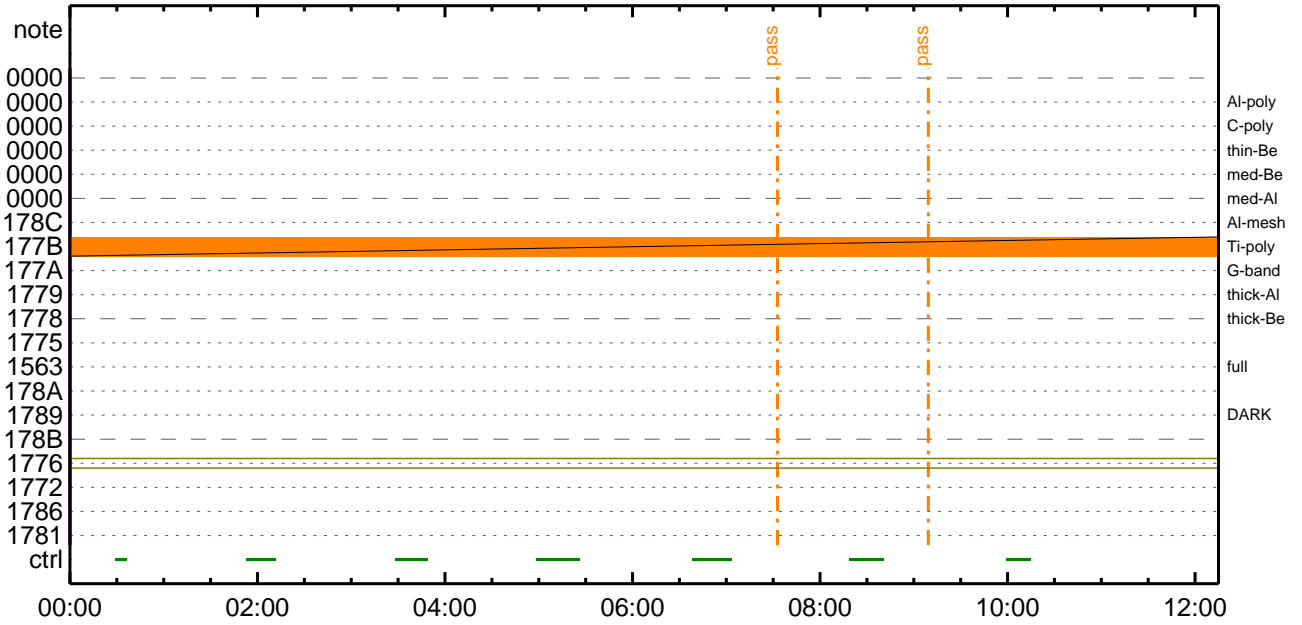
CMDI #0143 2010/03/11



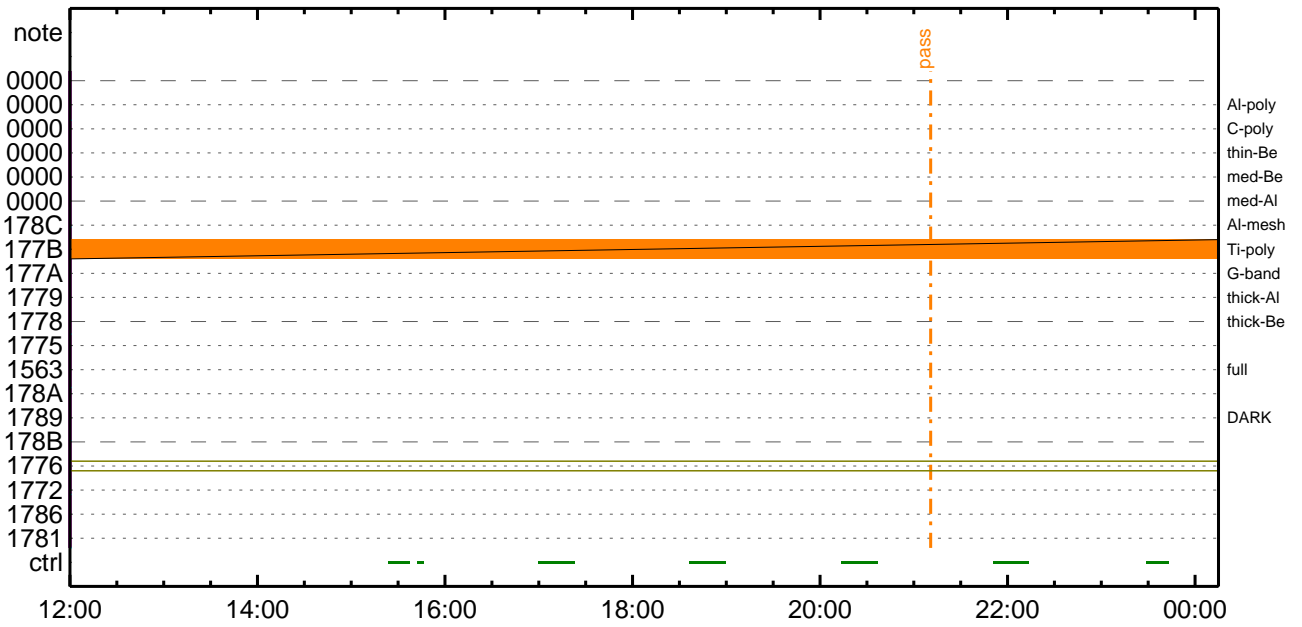
CMDI #0143 2010/03/11



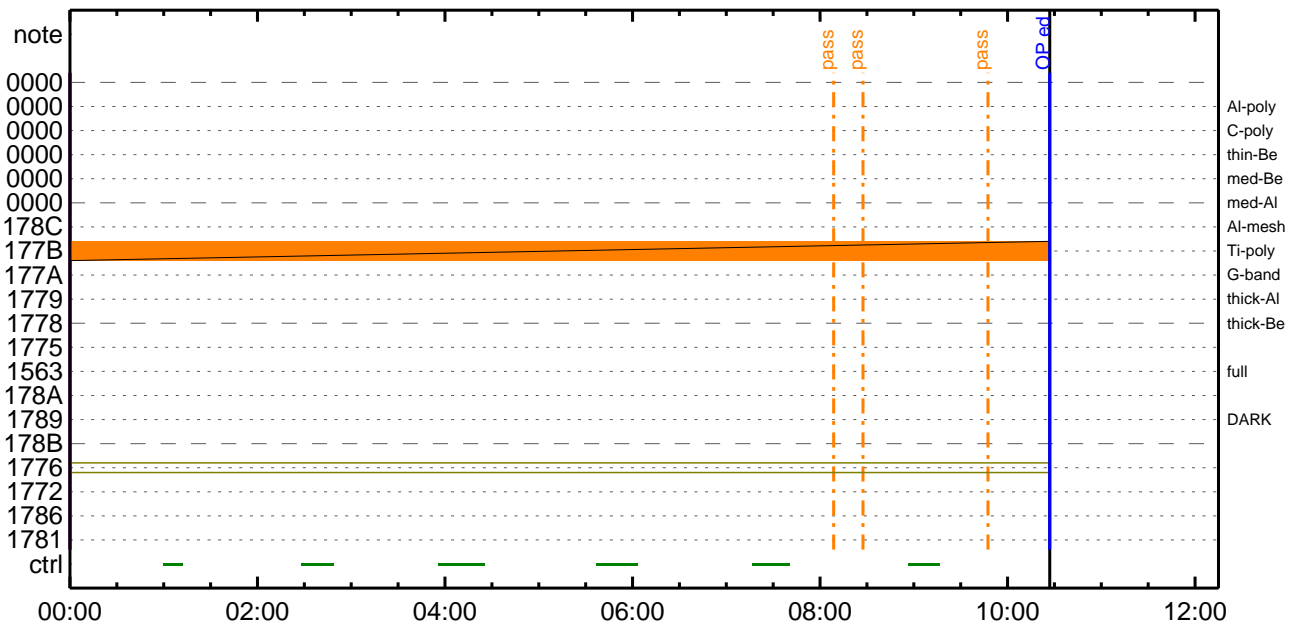
### CMDI #0143 2010/03/12



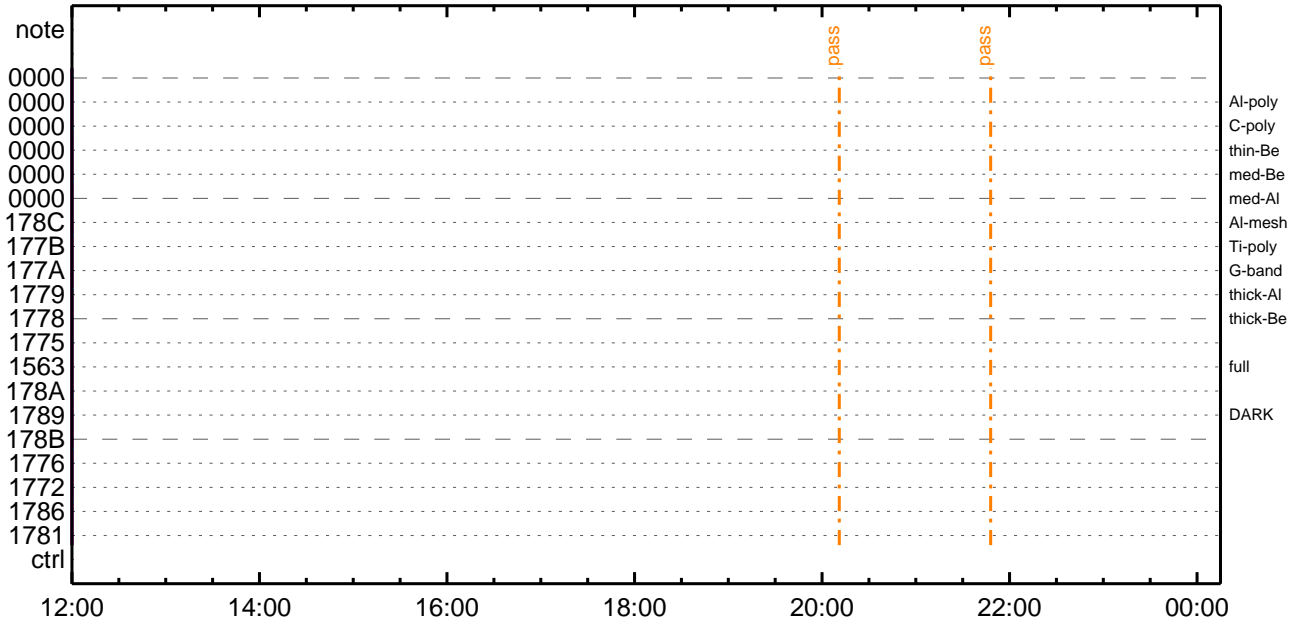
### CMDI #0143 2010/03/12



### CMDI #0143 2010/03/13



CMDI #0143 2010/03/13







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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-222:OP
0104 ( )
0105 S. OG og-222:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ä´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòÇòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½î¹ç°²E¼î¹çòÇòâ) *****
0180 C. çç[HK1_DMP_CHK_FLG] EQ NON
0181 C.
0182 C. TIY³YpYóYÉòðÁDî¿(UT)
0183 +. TI 2010-03-09 09:32:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2010-03-09 09:32:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2010-03-09 09:32:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2010-03-09 09:36:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄê%îîñαîŷÄŷ§ŷÄŷ-¹àîÛ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]                    EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]                        EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]                     EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]                      EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîŷ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]                    EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]                    EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]                     EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]                   EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]                      EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]                      EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]                     EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]                   EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]                   EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]                      EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»αò³îÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]                      EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLαîŷÊ¹ç•è²îOKαò³îÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÊ;Êŷŷ¼.ŷî;¼ŷÊ;Êαòîáα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]                      EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]                     EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]                   EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]                   EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2010-03-09 09:36:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2010-03-09 09:36:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]                      EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C. *****
0255 C. * IRU-B1/B2 HIŷâ;¼ŷÊÄÛαèÄÖα" *
0256 C. *****
0257 C.
0258 C. ***** ²¼µ-CMDαòÄ÷ç@α¹αè *****
0259 C.
0260 +. DC 02-AD AOCU_IRU-B1_HI
0261 + DC 02-B2 AOCU_IRU-B2_HI
0262 C.
0263 C.          [ ] <A_STS2> [IRU-B1] RMS = HI
0264 C.          [ ] <A_STS2> [IRU-B2] RMS = HI
0265 C.
0266 C.
0267 C. ***** End of AOCs SPECIAL CMD *****
0268 C.
0269 C. *****
0270 C. SOT TI command set
0271 C. *****
0272 C. Execute, after the success of OP upload.
0273 +. TI 2010-03-09 09:36:16.0
0274 DC 07-F0 MDP_SOT_MODE_STBY
0275 BC      (41)
0276 C. -----
0277 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0278 C. -----
0279 C. ***** SOT END *****
0280 C.
0281 C. ***** XRT START *****
0282 C. Execute, after the success of OP upload.
0283 +. TI 2010-03-09 09:36:00.0
0284 DC 07-F0 MDP_XRT_MODE_STBY
0285 BC      (c3)
0286 C.          [ ] [HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0287 C.
0288 C. ***** XRT END *****
0289 C.
0290 C. ***** MDP ´úÄîαî»ò¼ŷαèÄöα¹αèDCBC•x²è *****
0291 C. (%â°îŷÖŷÄŷÊŷpŷÊŷáŷçŷèèÊ¼αα¼Ä»Ûα¹αè)

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```
0292 . S. DC-BC dcbc-402:DCBC
0293 (MDP_known_event)
0294 C.
0295 C.
0296 . C. ***** ¥D¥1•İ Daily±;İÑñĒ'Øα¹αēDCBC•x²è *****
0297 . S. DC-BC dcbc-153:DCBC
0298 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0299 C.
0300 C.
0301 . C. ;äLOS¥Á¥S¥Ä¥-¼Ä»Û;ä
0302 C.
0303 . C. ***** LOS *****
0304 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-223 2010-03-09 14:45:28 91 33 SOLAR-B MAIN //  
0001 C.  
0002 . C. ***** AOS *****  
0003 C.  
0004 . C. ;ãAOSYÄYŠYÄY-¼Ä»Û;ä  
0005 C.  
0006 C. YÄYB;¼Y³YFÝÓYÉÄ+¿®  
0007 +. DC 00-00 NULL_DUMMY_CMD  
0008 C.  
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****  
0010 C. ÄíÈ¿¿Àá•µ°È»Í×ÁÇ¿ÍYÇYÄY×Yí;¼YÉ;ÈÈè%µ•íÉ;ÈáÈ¼°ÇÓ••¿¼i¹ç¿Í;çÄ®, ùã¹ãèã¿ãÇÄ+¿®••ëãã³ãÈ;f  
0011 +. DC 02-8E AOCU_ORB_UPD  
0012 C.  
0013 C.  
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****  
0015 C. Upload the Orbit Element and the Target Attitude  
0016 C. RAM-ID:TARGET_ATT  
0017 . S. RAM ram-150:TARGET_ATT  
0018 ( )  
0019 C.  
0020 C.  
0021 C. Set the dump memory area of TARGET_ATT  
0022 +. DC 02-48 AOCU_DUMP_SET  
0023 BC (07 00 00 00 18 00)  
0024 C.  
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]  
0026 C.  
0027 C.  
0028 C. Change the TLMFormatNo for the AOCS Dump Format  
0029 +. DC 01-22 DHU_MODE_CHNG  
0030 BC (04 0b f8)  
0031 C.  
0032 C. Wait for AOCSDUMP to end  
0033 C.  
0034 . C. Check the dump memory  
0035 C.  
0036 C. Result = OK [ ]  
0037 C.  
0038 +. DC 01-22 DHU_MODE_CHNG  
0039 BC (02 0a f8)  
0040 C.  
0041 C. <A_***>[TLM STS] FMT = 2 [ ]  
0042 C.  
0043 +. DC 02-8E AOCU_ORB_UPD  
0044 . C.  
0045 . C. ***** AOCS Commands (Orbital Element Update) *****  
0046 C. Update the orbital element  
0047 +. DC 02-50 AOCU_ORB_PRPGT_START  
0048 BC (16)  
0049 +. DC 02-8E AOCU_ORB_UPD  
0050 C.  
0051 C. <A_ORB>[ORBIT] EPC = 8133735.8 +- 1.0 (s) [ ]  
0052 C.  
0053 . C.  
0054 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes  
0055 +. DC 07-FC EIS_MODE_MANU  
0056 BC (21 02)  
0057 . C. Verify EIS in MANUAL mode  
0058 . C. Estimated OBSTBL upload time is 55s  
0059 C. *****  
0060 C. EIS START OBSTBL LOAD  
0061 C. *****  
0062 . S. RAM ram-820:EIS_OBSTBL  
0063 ( )  
0064 +. DC 07-FC EIS_DUMP_OBSTBL  
0065 BC (07 07 07 00 00 70 00)  
0066 C.  
0067 C. Execute, after the success of OBSTBL upload.  
0068 C. Set EIS TI-commands  
0069 +. TI 2010-03-09 09:36:50.0  
0070 DC 07-FC EIS_MODE_CHG_ENA  
0071 BC (20)  
0072 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP  
0073 C. *****  
0074 C. EIS END OBSTBL LOAD  
0075 C. *****  
0076 C.  
0077 . C. ***** MDP ´âÄÎâÍ»ò%Y¿ÈÄã¹ãéDCBC•x²è *****  
0078 C. (%ã°íYÓYÄYÈY¿YÈYáYçYèãE%¾ã¼Ä»Ûã¹ãè)  
0079 . S. DC-BC dcbbc-402:DCBC  
0080 (MDP_known_event)  
0081 C.  
0082 C.  
0083 . C. ***** YDÝ¹!Ï Daily±¿ÍÑãÈ´Øã¹ãéDCBC•x²è *****  
0084 . S. DC-BC dcbbc-153:DCBC  
0085 (SPECIAL-CMD_DAILY_OPERATIN_DCB)  
0086 C.  
0087 C.  
0088 . C. ;ãLOSÄYŠYÄY-¼Ä»Û;ä  
0089 C.  
0090 . C. ***** LOS *****  
0091 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```

main-224 2010-03-09 14:45:28 250 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSÝÄÝŠÝÄÝ-¼Ä»Û;ã
0005 C.
0006 C. ÝÄÝß;¼Ý³ÝÞÝÓÝÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Äí;È¿¿ðÄð•µ°È»Í×ÄÇ¿ÍÝÇÝÄÝ×ÝÍ;¼ÝÉ;ÈÈÈ¼µ•íÉ;ÈðÈ¼°ÇÓð•¿¿¼í¹¿ðí;çÄ®, ù¹ðÈðð¿çÄ+¿®ð•ðÈðð¿ðÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ+¿@µ;ON
0016 C. *****
0017 C. ç °ÄÄ, Í×ÈÝðÄLOSð¿ç¿Í»p´Öðð¹íí, ð•; çÉÖÍ×ðÈXÄÓONðí¹ÖðÈíðÈðð¿ðÈ;f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XÝDÝÓÝÉÝÍÝÄÝ-¾ÖÄÖð-°ÄÄêð•¿¿ðé; ç°È²¼ðí°ÈÄ, ¼è¿çðð¼Ä¹Öð¹ðé;f
0030 C.
0031 . C. *****
0032 C. DR PT1 Äí¼í°ÈÄ,
0033 C. *****
0034 C. ç ° RESTART;ÈPT1;Èð•¿¿ð¼¼í¹¿ðí; ç°È²¼ðí°ÈÄ¹Öð»°; çDCBC-150ð¿¿Èð¿;f
0035 C.
0036 . C. ;ãPT1°ÈÄ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãÝçÝÓÝÉÝÈÄÙÄØ;ÈÄ•Ä°²óÈð;È, áðí°ÈÄ, °È³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÈÄ, ð-¼«È°Ä»ßð•¿¿, á; ç°È²¼ð¼Ä¹Öð¹ðé;f
0055 C. ÝçÝÓÝÉÝÈÄÙÄØðÄÄ•Ä°²óÈðð-¼¼ð¼¼í¹¿ðí´°í»ð¹ðÈðð¿çÄÖðÄ;f
0056 C.
0057 . C. *****
0058 C. DR PT2 Äí¼í°ÈÄ,
0059 C. *****
0060 C. ç ° RESTART;ÈPT2;Èð•¿¿ð¼¼í¹¿ðí; ç°È²¼ðí°ÈÄ¹Öð»°; çDCBC-151ð¿¿Èð¿;f
0061 C.
0062 . C. ;ãPT2°ÈÄ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãÝçÝÓÝÉÝÈÄÙÄØ;ÈÄ•Ä°²óÈð;È, áðí°ÈÄ, °È³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÈÄ, ÄÄ»ß; çXÄ+¿@µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÈÄ, ÄÄ»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ+¿@µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 06 80 80 20 20)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 07 80 80 20 08)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 08 80 80 08 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 09 80 80 08 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 0a 80 80 20 04)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0b c0 c0 10 10)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0c 40 c0 10 10)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0d 40 40 10 10)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0e c0 40 10 10)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 0f 80 80 06 06)
0144 + DC 07-F0 MDP_XRT_ROI_SET
0145 BC (cd 10 80 80 04 04)
0146 + DC 07-F0 MDP_XRT_AEC_RESET
0147 BC (d0)
0148 . C. ----- Success Verify ? OK / NG ____
0149 C.
0150 C.
0151 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0152 C.
0153 +. DC 07-F0 MDP_XRT_MODE_OBSV
0154 BC (c2)
0155 +. TI 2010-03-09 09:36:02.0
0156 DC 07-F0 MDP_XRT_MODE_OBSV
0157 BC (c2)
0158 . C. ----- Success Verify ? OK / NG ____
0159 C.
0160 C. ***** XRT END *****
0161 . C. *****
0162 C. SOT table upload
0163 C. *****
0164 . C. < Stop FG table >
0165 +. DC 07-F0 MDP_FG_CTRL_MANU
0166 BC (51)
0167 . C. -----
0168 C. MDP_FG_CTRL_MODE = MANU [ ]
0169 C. -----
0170 C.
0171 . C. <Upload FG Observation Table>
0172 . S. RAM ram-267:MDP_OBS_F
0173 ( )
0174 C.
0175 . C. < Dump RAMID=MDP_OBS_F >
0176 +. DC 07-F0 MDP_DUMP_FGTBL
0177 BC (82 07 00 00 00 38 b8)
0178 C. -----
0179 C. MDP_OBS_F verify = OK/NG [ ]
0180 C. -----
0181 C.
0182 . C. < Stop SP table >
0183 +. DC 07-F0 MDP_SP_CTRL_MANU
0184 BC (61)
0185 C. -----
0186 C. MDP_SP_CTRL_MODE = MANU [ ]
0187 C. -----
0188 C.
0189 . C. <Upload SP Observation Table>
0190 . S. RAM ram-285:MDP_OBS_S
0191 ( )
0192 C.
0193 . C. < Dump RAMID=MDP_OBS_S >

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0194 +. DC 07-F0 MDP_DUMP_SPTBL
0195 BC (83 07 00 00 00 38 b8)
0196 C. -----
0197 C. MDP_OBS_S verify = OK/NG [ ]
0198 C. -----
0199 C.
0200 . C. < Upload DPL table >
0201 C.
0202 C. MDP_OBS_S verify = OK/NG [ ]
0203 C.
0204 . S. RAM ram-271:MDP_DPL
0205 ( )
0206 C.
0207 . C. < Dump RAMID=MDP_DPL >
0208 +. DC 07-F0 MDP_DUMP_FGTBL
0209 BC (82 07 00 38 b8 00 40)
0210 C. -----
0211 C. MDP_DPL verify = OK [ ]
0212 C. -----
0213 C.
0214 C. STS_CHK ON
0215 C.
0216 . C. < Update MDP DSC PAR1 >
0217 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0218 BC (4c)
0219 C. MDP_CMD_CODE = F04C0700 [ ]
0220 C. MDP_CMD_CNT (count-up 1) [ ]
0221 C. -----
0222 C.
0223 C.
0224 C. *****
0225 C. SOT TI command set
0226 C. *****
0227 C. Execute, after the success of TBL upload.
0228 +. TI 2010-03-09 09:36:18.0
0229 DC 07-F0 MDP_SOT_MODE_OBSV
0230 BC (40)
0231 C. -----
0232 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0233 C. -----
0234 C.
0235 C.
0236 . C. ***** MDP DCBC-402:DCBC *****
0237 C. (MDP_known_event)
0238 . S. DC-BC dcbc-402:DCBC
0239 (MDP_known_event)
0240 C.
0241 C.
0242 . C. ***** MDP DCBC-153:DCBC *****
0243 . S. DC-BC dcbc-153:DCBC
0244 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0245 C.
0246 C.
0247 . C. MDP DCBC-153:DCBC *****
0248 C.
0249 . C. ***** LOS *****
0250 C.

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\*\*\* OP Sequence for XRT \*\*\*

2010/03/09	10:07:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/03/09	10:07:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/03/09	10:08:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 00 00 ac cd		
2010/03/09	10:08:16.0	XRT_FLD_ENA_404_OG [0x194]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/03/09	10:08:18.0	XRT_FLD_RESET_405_OG [0x195]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/03/09	10:08:20.0	XRT_FLRCTRL_ENA_406_OG [0x196]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/03/09	10:08:22.0	XRT_ARS_DIS_407_OG [0x197]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/03/09	10:10:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09		
2010/03/09	10:10:58.0	XRT_FL_PROG_SET_409_OG [0x199]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04		
2010/03/09	10:11:00.0	XRT_CTRL_AUTO_410_OG [0x19a]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/03/09	10:23:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/03/09	10:23:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/03/09	10:24:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 d6 67		
2010/03/09	10:24:16.0	XRT_FLD_ENA_404_OG [0x194]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/03/09	10:24:18.0	XRT_FLD_RESET_405_OG [0x195]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/03/09	10:24:20.0	XRT_FLRCTRL_ENA_406_OG [0x196]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/03/09	10:24:22.0	XRT_ARS_DIS_407_OG [0x197]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/03/09	10:26:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09		
2010/03/09	10:26:58.0	XRT_FL_PROG_SET_409_OG [0x199]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04		
2010/03/09	10:27:00.0	XRT_CTRL_AUTO_410_OG [0x19a]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/03/09	10:39:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/03/09	10:39:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/03/09	10:40:00.0	AOCS_Ore-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2010/03/09	10:40:16.0	XRT_FLD_ENA_404_OG [0x194]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/03/09	10:40:18.0	XRT_FLD_RESET_405_OG [0x195]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/03/09	10:40:20.0	XRT_FLRCTRL_ENA_406_OG [0x196]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/03/09	10:40:22.0	XRT_ARS_DIS_407_OG [0x197]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/03/09	10:42:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09		
2010/03/09	10:42:58.0	XRT_FL_PROG_SET_409_OG [0x199]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04		
2010/03/09	10:43:00.0	XRT_CTRL_AUTO_410_OG [0x19a]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/03/09	10:55:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/03/09	10:55:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/03/09	10:56:00.0	AOCS_Ore-point_Start_4_OG [0x09a]					
		AOCU_NM	5	02-76	00 00 00 29 99		
2010/03/09	10:56:16.0	XRT_FLD_ENA_404_OG [0x194]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/03/09	10:56:18.0	XRT_FLD_RESET_405_OG [0x195]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/03/09	10:56:20.0	XRT_FLRCTRL_ENA_406_OG [0x196]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/03/09	10:56:22.0	XRT_ARS_DIS_407_OG [0x197]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/03/09	10:58:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09		
2010/03/09	10:58:58.0	XRT_FL_PROG_SET_409_OG [0x199]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04		
2010/03/09	10:59:00.0	XRT_CTRL_AUTO_410_OG [0x19a]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/03/09	11:11:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/03/09	11:11:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/03/09	11:12:00.0	AOCS_Ore-point_Start_5_OG [0x09b]					
		AOCU_NM	5	02-76	00 00 00 53 33		
2010/03/09	11:12:16.0	XRT_FLD_ENA_404_OG [0x194]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/03/09	11:12:18.0	XRT_FLD_RESET_405_OG [0x195]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/03/09	11:12:20.0	XRT_FLRCTRL_ENA_406_OG [0x196]					



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2010/03/09	11:12:22.0	XRT_ARS_DIS_407_OG [0x197]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/03/09	11:14:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09	
2010/03/09	11:14:58.0	XRT_FL_PROG_SET_409_OG [0x199]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	04	
2010/03/09	11:15:00.0	XRT_CTRL_AUTO_410_OG [0x19a]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/03/09	11:27:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/03/09	11:27:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97 00
2010/03/09	11:28:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00	d6	36 b7 8e
2010/03/09	11:28:16.0	XRT_FLD_ENA_404_OG [0x194]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/03/09	11:28:18.0	XRT_FLD_RESET_405_OG [0x195]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/03/09	11:28:20.0	XRT_FLRCTRL_ENA_406_OG [0x196]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/03/09	11:28:22.0	XRT_ARS_DIS_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/03/09	11:30:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09	
2010/03/09	11:30:58.0	XRT_FL_PROG_SET_409_OG [0x199]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	04	
2010/03/09	11:31:00.0	XRT_CTRL_AUTO_410_OG [0x19a]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/03/09	11:37:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/03/09	11:37:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97 00
2010/03/09	11:38:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00	b4	b5 db 75
2010/03/09	11:38:16.0	XRT_FLD_ENA_404_OG [0x194]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/03/09	11:38:18.0	XRT_FLD_RESET_405_OG [0x195]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/03/09	11:38:20.0	XRT_FLRCTRL_ENA_406_OG [0x196]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/03/09	11:38:22.0	XRT_ARS_DIS_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/03/09	11:40:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09	
2010/03/09	11:40:58.0	XRT_FL_PROG_SET_409_OG [0x199]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	04	
2010/03/09	11:41:00.0	XRT_CTRL_AUTO_410_OG [0x19a]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/03/09	11:53:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/03/09	11:53:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97 00
2010/03/09	11:54:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00	ac	5b 00 00
2010/03/09	11:54:16.0	XRT_FLD_ENA_404_OG [0x194]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/03/09	11:54:18.0	XRT_FLD_RESET_405_OG [0x195]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/03/09	11:54:20.0	XRT_FLRCTRL_ENA_406_OG [0x196]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/03/09	11:54:22.0	XRT_ARS_DIS_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/03/09	11:56:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09	
2010/03/09	11:56:58.0	XRT_FL_PROG_SET_409_OG [0x199]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	04	
2010/03/09	11:57:00.0	XRT_CTRL_AUTO_410_OG [0x19a]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/03/09	12:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/03/09	12:09:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97 00
2010/03/09	12:10:00.0	AOCS_ORe-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00	b4	b5 24 8b
2010/03/09	12:10:16.0	XRT_FLD_ENA_404_OG [0x194]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/03/09	12:10:18.0	XRT_FLD_RESET_405_OG [0x195]	MDP_XRT_FLD_RESET	1	07-F0	da		
2010/03/09	12:10:20.0	XRT_FLRCTRL_ENA_406_OG [0x196]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/03/09	12:10:22.0	XRT_ARS_DIS_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/03/09	12:12:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09	
2010/03/09	12:12:58.0	XRT_FL_PROG_SET_409_OG [0x199]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	04	
2010/03/09	12:13:00.0	XRT_CTRL_AUTO_410_OG [0x19a]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/03/09	12:25:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/03/09	12:25:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97 00

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2010/03/09	12:26:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	d6	36	48	72
2010/03/09	12:26:16.0	XRT_FLD_ENA_404_OG [0x194]							
		MDP_XRT_FLD_ENA	1	07-F0		d8			
2010/03/09	12:26:18.0	XRT_FLD_RESET_405_OG [0x195]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2010/03/09	12:26:20.0	XRT_FLRCTRL_ENA_406_OG [0x196]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2010/03/09	12:26:22.0	XRT_ARS_DIS_407_OG [0x197]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2010/03/09	12:28:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4	09		
2010/03/09	12:28:58.0	XRT_FL_PROG_SET_409_OG [0x199]							
		MDP_XRT_FL_PROG_SET	2	07-F0		c5	04		
2010/03/09	12:29:00.0	XRT_CTRL_AUTO_410_OG [0x19a]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2010/03/09	12:35:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2010/03/09	12:35:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/03/09	12:36:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00	29	ca	b7	8e
2010/03/09	12:36:16.0	XRT_FLD_ENA_404_OG [0x194]							
		MDP_XRT_FLD_ENA	1	07-F0		d8			
2010/03/09	12:36:18.0	XRT_FLD_RESET_405_OG [0x195]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2010/03/09	12:36:20.0	XRT_FLRCTRL_ENA_406_OG [0x196]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2010/03/09	12:36:22.0	XRT_ARS_DIS_407_OG [0x197]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2010/03/09	12:38:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4	09		
2010/03/09	12:38:58.0	XRT_FL_PROG_SET_409_OG [0x199]							
		MDP_XRT_FL_PROG_SET	2	07-F0		c5	04		
2010/03/09	12:39:00.0	XRT_CTRL_AUTO_410_OG [0x19a]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2010/03/09	12:45:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2010/03/09	12:45:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/03/09	12:46:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00	4b	4b	db	75
2010/03/09	12:46:16.0	XRT_FLD_ENA_404_OG [0x194]							
		MDP_XRT_FLD_ENA	1	07-F0		d8			
2010/03/09	12:46:18.0	XRT_FLD_RESET_405_OG [0x195]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2010/03/09	12:46:20.0	XRT_FLRCTRL_ENA_406_OG [0x196]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2010/03/09	12:46:22.0	XRT_ARS_DIS_407_OG [0x197]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2010/03/09	12:48:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4	09		
2010/03/09	12:48:58.0	XRT_FL_PROG_SET_409_OG [0x199]							
		MDP_XRT_FL_PROG_SET	2	07-F0		c5	04		
2010/03/09	12:49:00.0	XRT_CTRL_AUTO_410_OG [0x19a]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2010/03/09	13:01:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2010/03/09	13:01:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/03/09	13:02:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00	53	a5	00	00
2010/03/09	13:02:16.0	XRT_FLD_ENA_404_OG [0x194]							
		MDP_XRT_FLD_ENA	1	07-F0		d8			
2010/03/09	13:02:18.0	XRT_FLD_RESET_405_OG [0x195]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2010/03/09	13:02:20.0	XRT_FLRCTRL_ENA_406_OG [0x196]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2010/03/09	13:02:22.0	XRT_ARS_DIS_407_OG [0x197]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2010/03/09	13:04:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4	09		
2010/03/09	13:04:58.0	XRT_FL_PROG_SET_409_OG [0x199]							
		MDP_XRT_FL_PROG_SET	2	07-F0		c5	04		
2010/03/09	13:05:00.0	XRT_CTRL_AUTO_410_OG [0x19a]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2010/03/09	13:17:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2010/03/09	13:17:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/03/09	13:18:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00	4b	4b	24	8b
2010/03/09	13:18:16.0	XRT_FLD_ENA_404_OG [0x194]							
		MDP_XRT_FLD_ENA	1	07-F0		d8			
2010/03/09	13:18:18.0	XRT_FLD_RESET_405_OG [0x195]							
		MDP_XRT_FLD_RESET	1	07-F0		da			
2010/03/09	13:18:20.0	XRT_FLRCTRL_ENA_406_OG [0x196]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2010/03/09	13:18:22.0	XRT_ARS_DIS_407_OG [0x197]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2010/03/09	13:20:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4	09		
2010/03/09	13:20:58.0	XRT_FL_PROG_SET_409_OG [0x199]							

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2010/03/09	13:21:00.0	XRT_CTRL_AUTO_410_OG [0x19a]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	04
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/03/09	13:33:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/03/09	13:33:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2010/03/09	13:34:00.0	AOCS_Ore-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00 29 db 48 72	
2010/03/09	13:34:16.0	XRT_FLD_ENA_404_OG [0x194]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2010/03/09	13:34:18.0	XRT_FLD_RESET_405_OG [0x195]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/03/09	13:34:20.0	XRT_FLRCTRL_ENA_406_OG [0x196]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2010/03/09	13:34:22.0	XRT_ARS_DIS_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/03/09	13:36:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2010/03/09	13:36:58.0	XRT_FL_PROG_SET_409_OG [0x199]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04	
2010/03/09	13:37:00.0	XRT_CTRL_AUTO_410_OG [0x19a]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/03/09	13:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/03/09	13:49:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2010/03/09	13:50:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	02 00 00 00 00	
2010/03/09	13:50:16.0	XRT_FLD_ENA_404_OG [0x194]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2010/03/09	13:50:18.0	XRT_FLD_RESET_405_OG [0x195]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/03/09	13:50:20.0	XRT_FLRCTRL_ENA_406_OG [0x196]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2010/03/09	13:50:22.0	XRT_ARS_DIS_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/03/09	13:52:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2010/03/09	13:52:58.0	XRT_FL_PROG_SET_409_OG [0x199]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04	
2010/03/09	13:53:00.0	XRT_CTRL_AUTO_410_OG [0x19a]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/03/09	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/03/09	17:59:56.0	XRT_FOCUS_POSITION_413_OG [0x19d]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2010/03/09	18:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00	
2010/03/09	18:00:16.0	XRT_FLD_DIS_414_OG [0x19e]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2010/03/09	18:00:18.0	XRT_FLRCTRL_DIS_415_OG [0x19f]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2010/03/09	18:00:20.0	XRT_ARS_DIS_416_OG [0x1a0]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/03/09	18:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2010/03/09	18:03:00.0	XRT_CTRL_AUTO_410_OG [0x19a]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/03/09	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/03/09	18:09:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2010/03/09	18:10:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	02 00 00 00 00	
2010/03/09	18:10:16.0	XRT_FLD_ENA_404_OG [0x194]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2010/03/09	18:10:18.0	XRT_FLD_RESET_405_OG [0x195]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/03/09	18:10:20.0	XRT_FLRCTRL_ENA_406_OG [0x196]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2010/03/09	18:10:22.0	XRT_ARS_DIS_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/03/09	18:12:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2010/03/09	18:12:58.0	XRT_FL_PROG_SET_409_OG [0x199]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04	
2010/03/09	18:13:00.0	XRT_CTRL_AUTO_410_OG [0x19a]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/03/09	18:23:30.0	XRT_CTRL_MANU_411_OG [0x19b]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/03/09	18:46:30.0	XRT_Custom_400_OG [0x190]					
2010/03/09	18:47:30.0	XRT_CTRL_AUTO_401_OG [0x191]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/03/09	20:00:00.0	XRT_CTRL_MANU_411_OG [0x19b]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/03/09	20:23:30.0	XRT_Custom_400_OG [0x190]					
2010/03/09	20:24:30.0	XRT_CTRL_AUTO_401_OG [0x191]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/03/09	21:37:30.0	XRT_CTRL_MANU_411_OG [0x19b]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/03/09	22:00:30.0	XRT_Custom_400_OG [0x190]					
2010/03/09	22:01:30.0	XRT_CTRL_AUTO_401_OG [0x191]					

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2010/03/09	23:15:00.0	XRT_CTRL_MANU_411_OG [0x19b]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/09	23:33:00.0	XRT_Custom_400_OG [0x190]				
2010/03/09	23:34:00.0	XRT_CTRL_AUTO_401_OG [0x191]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/03/10	00:52:30.0	XRT_CTRL_MANU_411_OG [0x19b]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/10	00:59:00.0	XRT_Custom_400_OG [0x190]				
2010/03/10	01:00:00.0	XRT_CTRL_AUTO_401_OG [0x191]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/03/10	02:15:00.0	XRT_CTRL_MANU_411_OG [0x19b]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/10	02:35:00.0	XRT_Custom_400_OG [0x190]				
2010/03/10	02:36:00.0	XRT_CTRL_AUTO_401_OG [0x191]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/03/10	03:46:00.0	XRT_CTRL_MANU_411_OG [0x19b]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/10	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/10	05:59:56.0	XRT_FOCUS_POSITION_413_OG [0x19d]				
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/03/10	06:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]				
			AOCU_NM	5	02-76	00 00 00 00 00
2010/03/10	06:00:16.0	XRT_FLD_DIS_414_OG [0x19e]				
			MDP_XRT_FLD_DIS	1	07-F0	d9
2010/03/10	06:00:18.0	XRT_FLRCTRL_DIS_415_OG [0x19f]				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2010/03/10	06:00:20.0	XRT_ARS_DIS_416_OG [0x1a0]				
			MDP_XRT_ARS_DIS	1	07-F0	d5
2010/03/10	06:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2010/03/10	06:03:00.0	XRT_CTRL_AUTO_410_OG [0x19a]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/03/10	06:10:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]				
			AOCU_NM	5	02-76	02 00 00 00 00
2010/03/10	07:03:00.0	XRT_CTRL_MANU_411_OG [0x19b]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/10	07:56:10.5	XRT_CTRL_MANU_423_OG [0x1a7]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/10	07:56:40.5	XRT_TCIB_XRT_S_HTR_A_ENA_424_OG [0x1a8]				
			TCIB_XRT_S_HTR_A_ENA	0	04-BC	
2010/03/10	09:56:40.5	XRT_Custom_425_OG [0x1a9]				
2010/03/10	11:56:40.5	XRT_Custom_425_OG [0x1a9]				
2010/03/10	13:56:40.5	XRT_Custom_425_OG [0x1a9]				
2010/03/10	15:00:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]				
			AOCU_NM	5	02-76	03 00 00 00 00
2010/03/10	15:56:40.5	XRT_Custom_426_OG [0x1aa]				
2010/03/10	15:56:50.5	XRT_Custom_425_OG [0x1a9]				
2010/03/10	16:30:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]				
			AOCU_NM	5	02-76	00 51 db 01 68
2010/03/10	17:56:50.5	XRT_Custom_427_OG [0x1ab]				
2010/03/10	19:56:28.5	XRT_Custom_426_OG [0x1aa]				
2010/03/10	19:56:38.5	XRT_CTRL_MANU_411_OG [0x19b]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/10	19:56:40.5	XRT_TCIB_XRT_S_HTR_A_DIS_428_OG [0x1ac]				
			TCIB_XRT_S_HTR_A_DIS	0	04-C0	
2010/03/10	19:57:00.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/10	19:57:02.0	XRT_QT_PROG_SET_429_OG [0x1ad]				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2010/03/10	19:57:04.0	XRT_ARS_DIS_430_OG [0x1ae]				
			MDP_XRT_ARS_DIS	1	07-F0	d5
2010/03/10	19:57:06.0	XRT_FLD_DIS_431_OG [0x1af]				
			MDP_XRT_FLD_DIS	1	07-F0	d9
2010/03/10	19:57:08.0	XRT_FLRCTRL_DIS_432_OG [0x1b0]				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2010/03/10	19:57:10.5	XRT_CTRL_AUTO_410_OG [0x19a]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/03/11	02:00:00.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/11	08:29:54.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/11	08:29:56.0	XRT_FOCUS_POSITION_413_OG [0x19d]				
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/03/11	08:30:00.0	AOCS_ORe-point_Start_3_OG [0x099]				
			AOCU_NM	5	02-76	00 00 00 00 00
2010/03/11	08:30:16.0	XRT_FLD_DIS_414_OG [0x19e]				
			MDP_XRT_FLD_DIS	1	07-F0	d9
2010/03/11	08:30:18.0	XRT_FLRCTRL_DIS_415_OG [0x19f]				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2010/03/11	08:30:20.0	XRT_ARS_DIS_416_OG [0x1a0]				
			MDP_XRT_ARS_DIS	1	07-F0	d5
2010/03/11	08:32:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2010/03/11	08:33:00.0	XRT_CTRL_AUTO_410_OG [0x19a]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/03/11	08:39:54.0	XRT_CTRL_MANU_433_OG [0x1b1]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/03/11	08:40:00.0	AOCS_ORe-point_Start_19_OG [0x0a9]				
			AOCU_NM	5	02-76	00 2e f9 2e f9
2010/03/11	08:42:32.0	XRT_FOCUS_POSITION_413_OG [0x19d]				
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00

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2010/03/11	08:42:52.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2010/03/11	08:42:54.0	XRT_FLD_DIS_414_OG [0x19e]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/03/11	08:42:56.0	XRT_FLRCTRL_DIS_415_OG [0x19f]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/03/11	08:42:58.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/03/11	08:43:00.0	XRT_CTRL_AUTO_401_OG [0x191]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/11	08:49:54.0	XRT_CTRL_MANU_433_OG [0x1b1]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/11	08:50:00.0	AOCS_ORe-point_Start_20_OG [0x0aa]							
		AOCU_NM	5	02-76	00	2e f9 d1 07			
2010/03/11	08:52:32.0	XRT_FOCUS_POSITION_413_OG [0x19d]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00			
2010/03/11	08:52:52.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2010/03/11	08:52:54.0	XRT_FLD_DIS_414_OG [0x19e]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/03/11	08:52:56.0	XRT_FLRCTRL_DIS_415_OG [0x19f]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/03/11	08:52:58.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/03/11	08:53:00.0	XRT_CTRL_AUTO_401_OG [0x191]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/11	08:59:54.0	XRT_CTRL_MANU_433_OG [0x1b1]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/11	09:00:00.0	AOCS_ORe-point_Start_21_OG [0x0ab]							
		AOCU_NM	5	02-76	00	d1 07 d1 07			
2010/03/11	09:02:32.0	XRT_FOCUS_POSITION_413_OG [0x19d]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00			
2010/03/11	09:02:52.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2010/03/11	09:02:54.0	XRT_FLD_DIS_414_OG [0x19e]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/03/11	09:02:56.0	XRT_FLRCTRL_DIS_415_OG [0x19f]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/03/11	09:02:58.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/03/11	09:03:00.0	XRT_CTRL_AUTO_401_OG [0x191]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/11	09:09:54.0	XRT_CTRL_MANU_433_OG [0x1b1]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/03/11	09:10:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	00	d1 07 2e f9			
2010/03/11	09:12:32.0	XRT_FOCUS_POSITION_413_OG [0x19d]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00			
2010/03/11	09:12:52.0	XRT_QT_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2010/03/11	09:12:54.0	XRT_FLD_DIS_414_OG [0x19e]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/03/11	09:12:56.0	XRT_FLRCTRL_DIS_415_OG [0x19f]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/03/11	09:12:58.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/03/11	09:13:00.0	XRT_CTRL_AUTO_401_OG [0x191]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/03/11	09:20:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	00	55 58 01 68			
2010/03/11	10:27:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00 00 00 00			