XRT Timeline to be uploaded on 2010/04/12

Period: 2010/04/12 10:31:00 - 2010/04/15 10:44:00

Normal mode

XOB #17A8: AR - TI/poly, Al/mesh with multi-FoV - G-Band Co-align

Term	Pointing (x, y)	Comment
04/12 10:48:00 - 04/12 17:48:56	Track (544.6, 456.9) @ 04/12 10:41:00	# OP start + 10min, track AR 11060.
04/12 18:02:00 - 04/13 05:59:54	Track (590.4, 452.6) @ 04/12 17:59:00	# Cont.

PROG= 17 Inf.-time(s)

Subr= 2 1-time(s) 2.0sec

Seqn= 58 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	1024x1024 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 47 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/G-band	close	Safe	Dark	4.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	500ms	Obs	2x2	512x512 (1024, 1024)	DPCM	0	0	2.0sec

Subr= 1 21-time(s) 180.0sec

Seqn= 48 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

XOB #1772: Synoptic Q95 2x2 - Al/mesh(33/512) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(64/1024) + G-band(16)-2

Term	Pointing (x, y)	Comment
04/12 17:52:02 - 04/12 17:58:54	Fixed (0.0, 0.0)	synoptic, shifted -11.0 min
04/13 06:03:00 - 04/13 06:05:00	Fixed (0.0, 0.0)	synoptic, extended for joint XRT-AIA observations.

PROG= 09 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 44 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 5 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 45 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 4 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #17A9: Full-disk Full-Res Al/Mesh, Ti/Poly, Thick/Al - 2 loops

Term	Pointing (x, y)	Comment
04/13 06:08:08 - 04/15 10:44:00	Fixed (0.0, 0.0)	synoptic, extended for joint XRT-AIA observations.

PROG= 11 1-time(s)

Subr= 1 2-time(s) 300.0sec

Seqn= 57 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/thick-Al	Open/thick-Al	close	Safe	Norm	22.6s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 53 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 54 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 56 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Dark	1.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Flare mode

NOT USED

Active Region Search

NOT USED

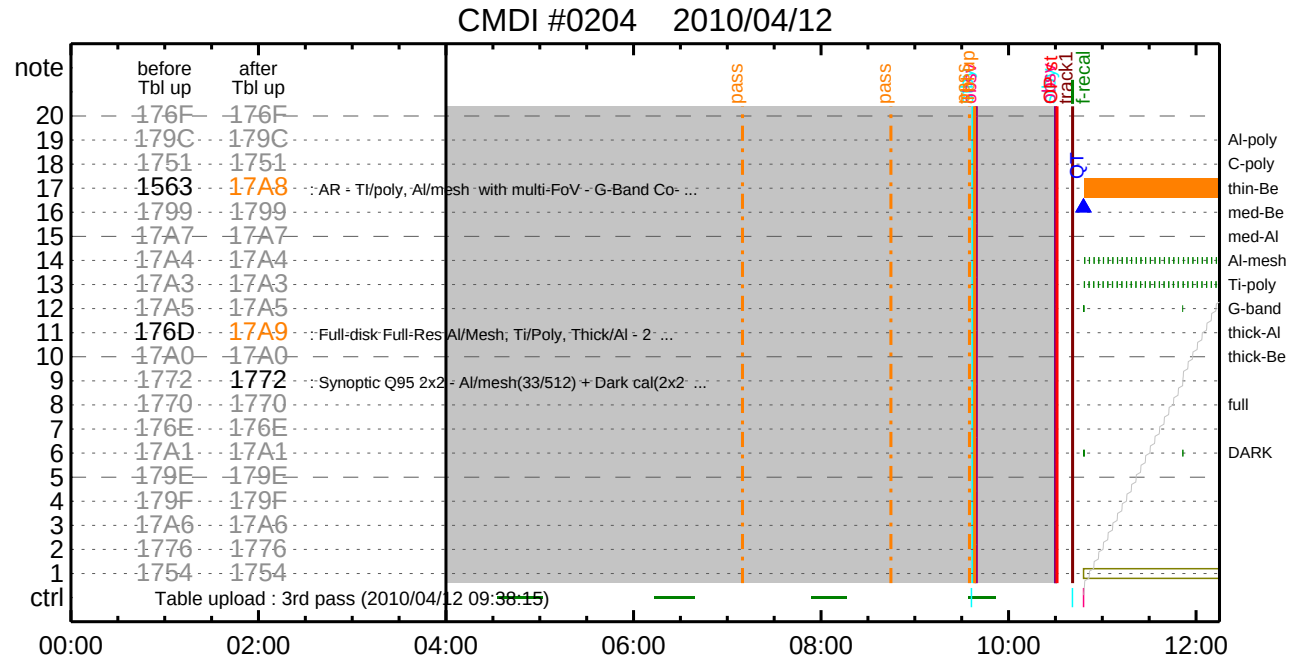
Flare Detection

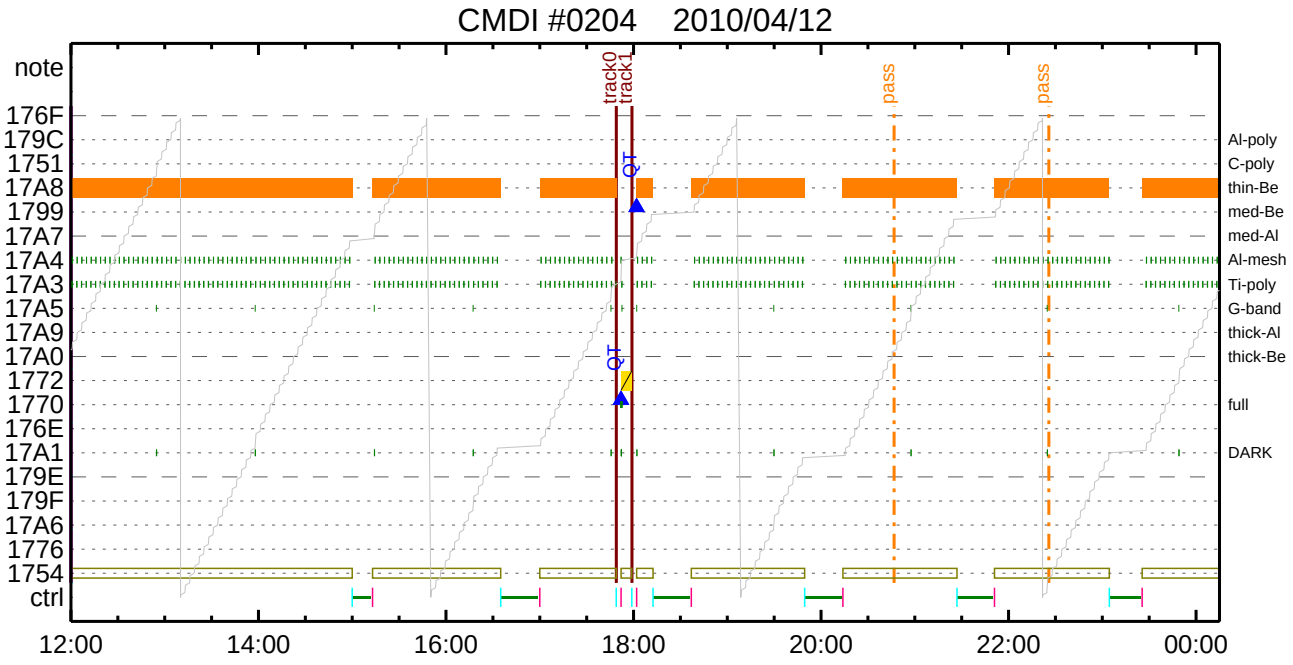
FLD Patrol

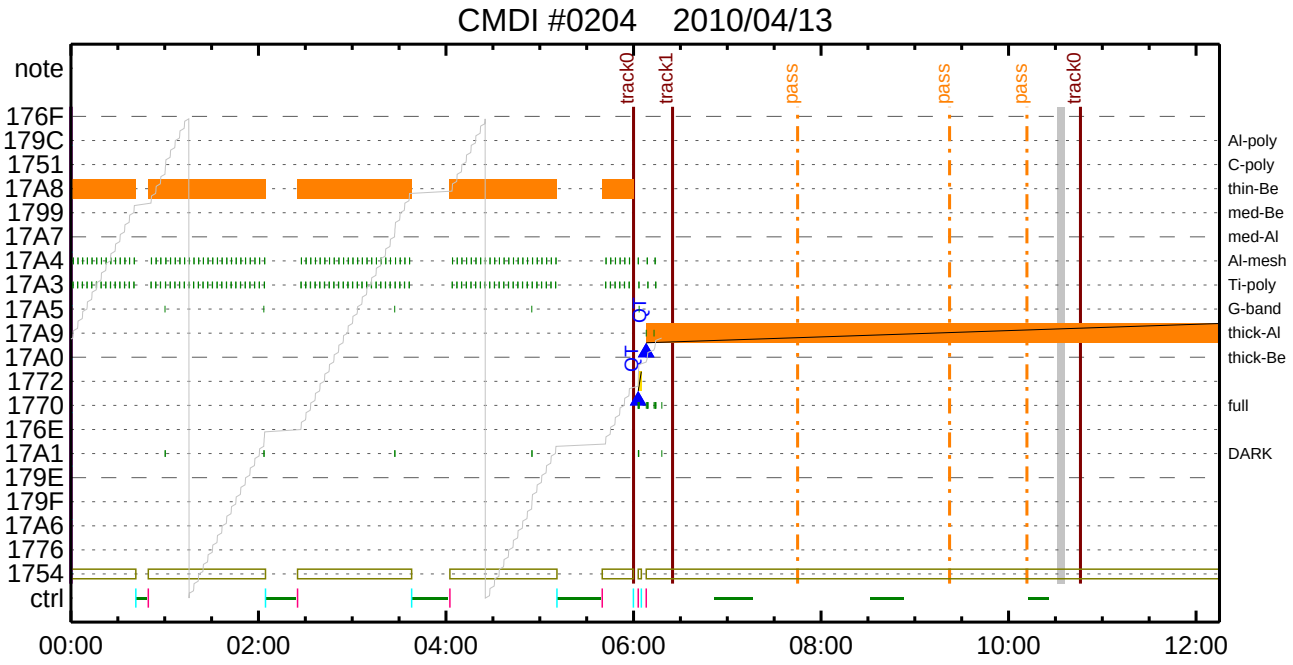
Term	Pointing (x, y)	Comment

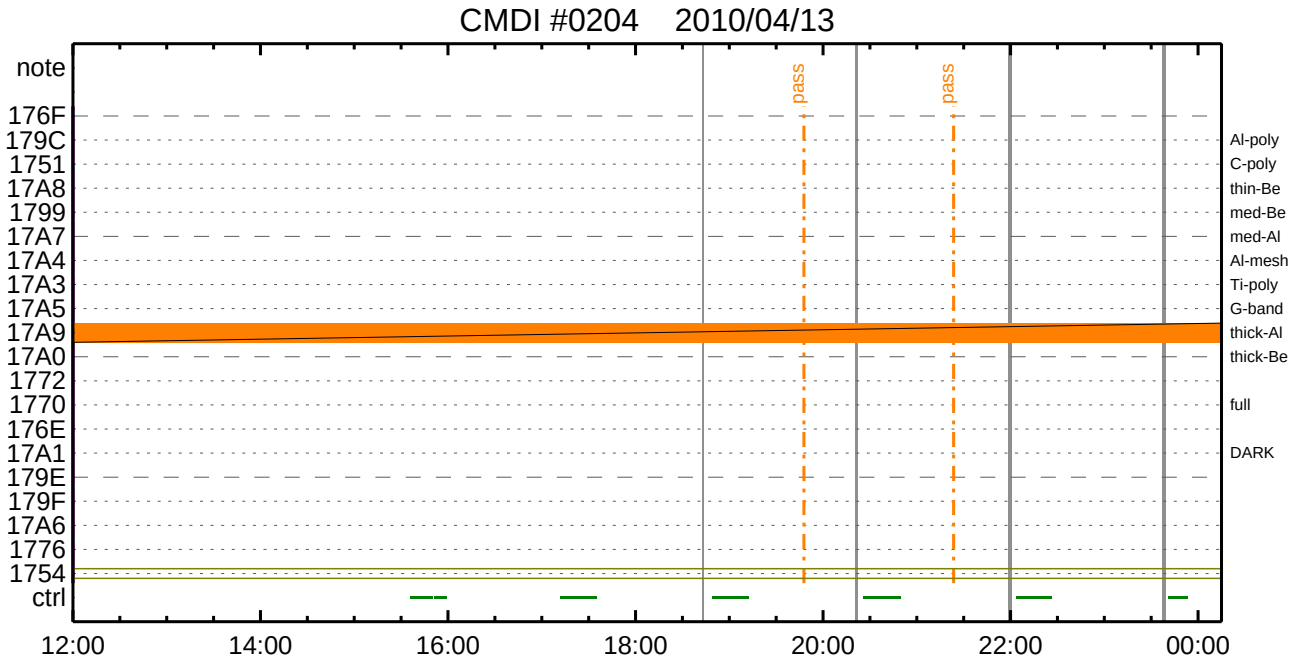
04/12 10:45:18 - 04/12 17:49:18 Track (544.6, 456.9) @ 04/12 10:41:00 # OP start + 10min, track AR 11060.
04/12 17:59:18 - 04/13 06:00:16 Track (590.4, 452.6) @ 04/12 17:59:00 # Cont.

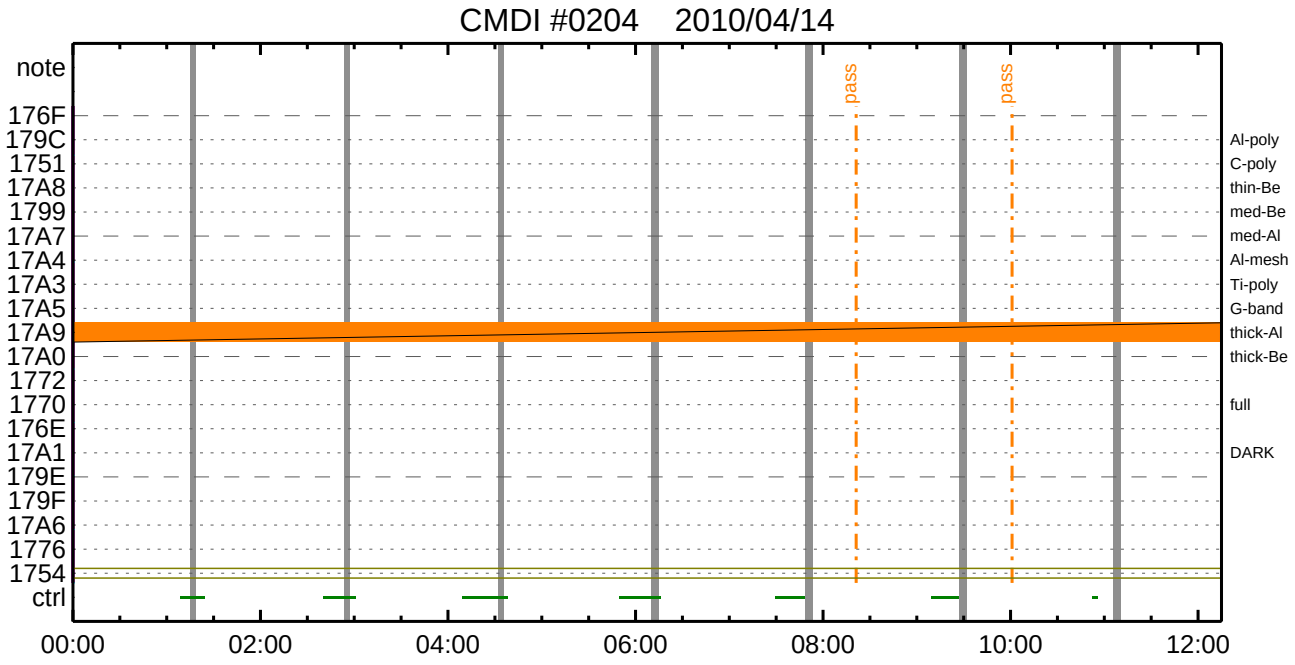
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

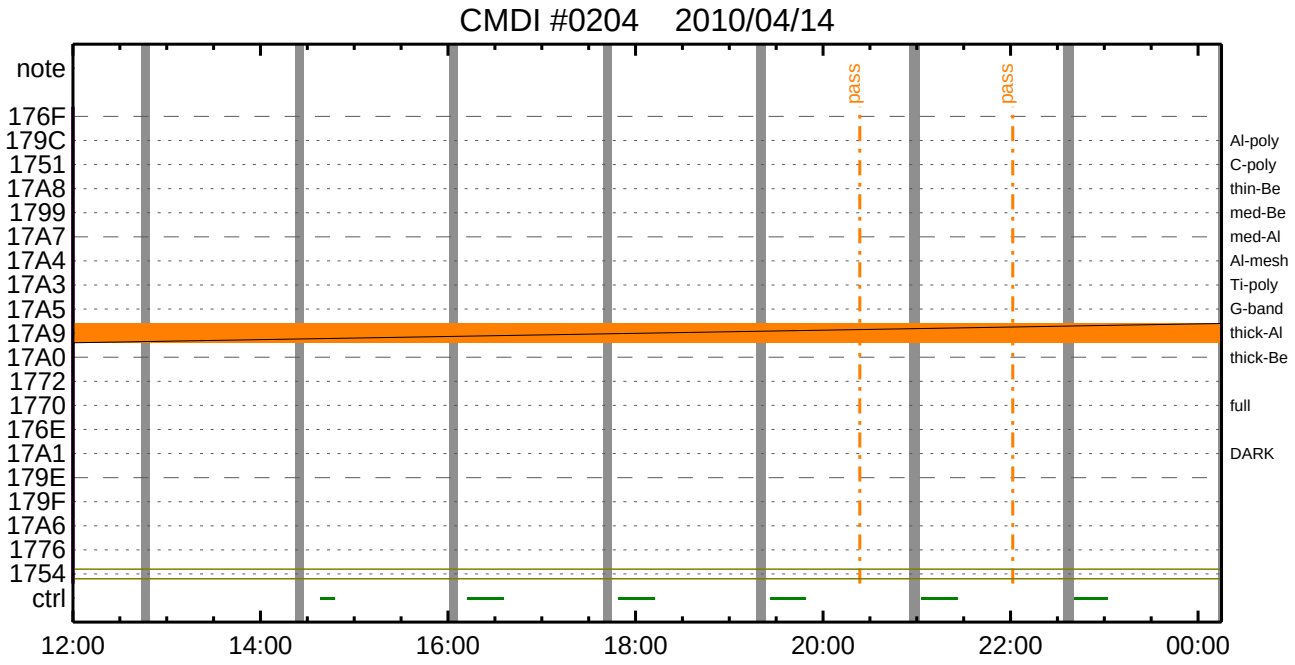

CMDI #0204 2010/04/12
note
before Tbl up
after Tbl up
176F 176F
179C 179C
1751 1751
1563 17A8
1799 1799
17A7 17A7
17A4 17A4
17A3 17A3
17A5 17A5
176D 17A9
17A0 17A0
1772 1772
1770 1770
176E 176E
17A1 17A1
179E 179E
179F 179F
17A6 17A6
1776 1776
1754 1754
ctrl
: AR - TI/poly, Al/mesh with multi-FoV - G-Band Co- ...
: Full-disk Full-Res Al/Mesh; Ti/Poly, Thick/Al - 2 ...
: Synoptic Q95 2x2 - Al/mesh(33/512) + Dark cal(2x2 ...
Table upload : 3rd pass (2010/04/12 09:38:15)
pass
pass
track0
track1
f-recal
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

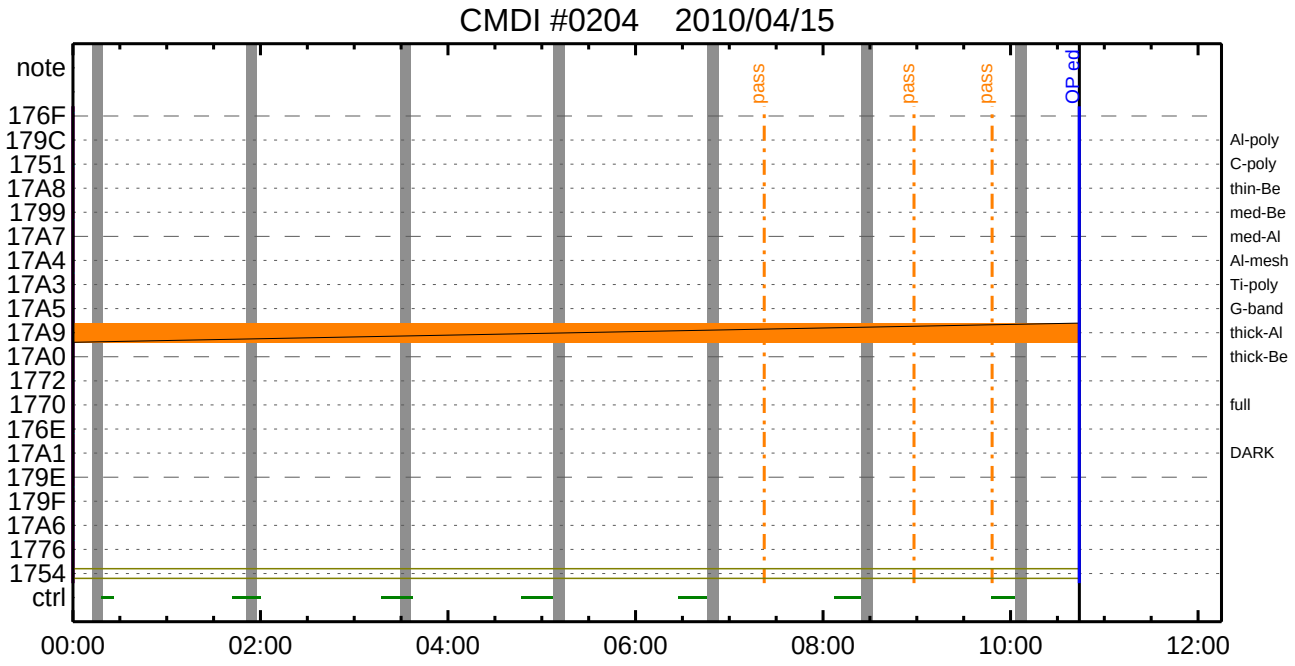

CMDI #0204 2010/04/12
note
track0
track1
pass
pass
QT
QT
176F
179C
1751
17A8
1799
17A7
17A4
17A3
17A5
17A9
17A0
1772
1770
176E
17A1
179E
179F
17A6
1776
1754
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

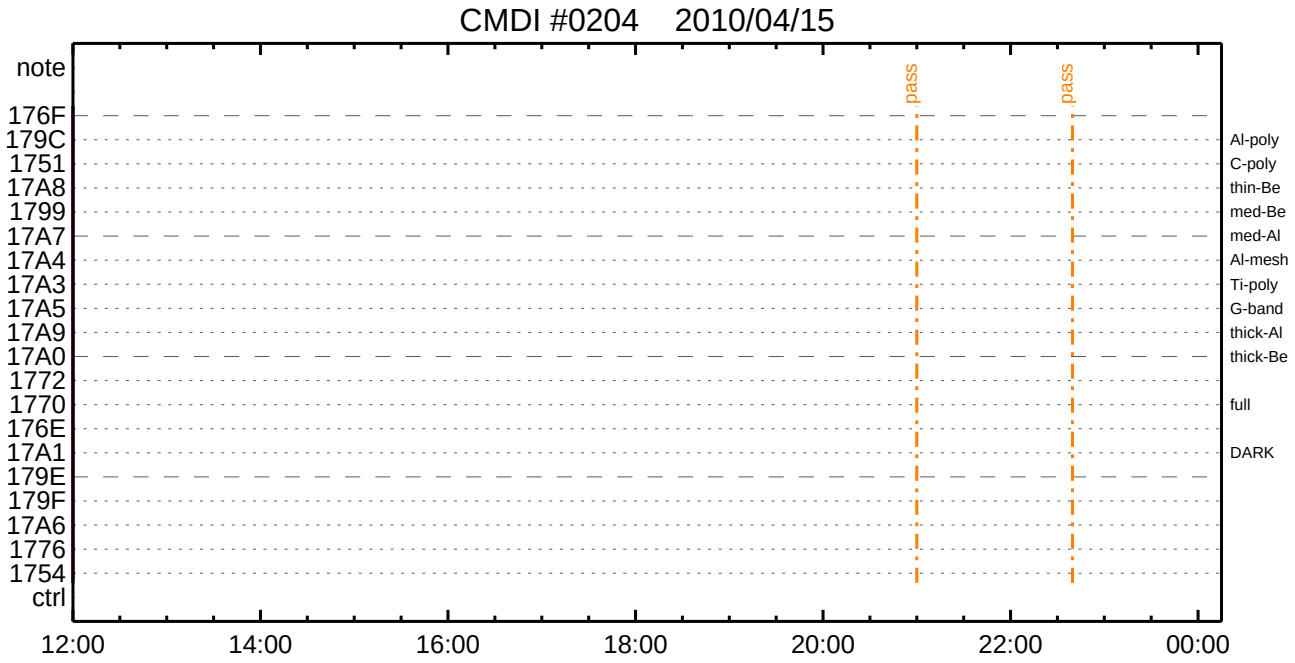

CMDI #0204 2010/04/13
note
track0
track1
pass
pass
pass
track0
QT
QT
176F
179C
1751
17A8
1799
17A7
17A4
17A3
17A5
17A9
17A0
1772
1770
176E
17A1
179E
179F
17A6
1776
1754
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0204 2010/04/13
note
176F
179C
1751
17A8
1799
17A7
17A4
17A3
17A5
17A9
17A0
1772
1770
176E
17A1
179E
179F
17A6
1776
1754
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0204 2010/04/14
note
176F
179C
1751
17A8
1799
17A7
17A4
17A3
17A5
17A9
17A0
1772
1770
176E
17A1
179E
179F
17A6
1776
1754
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0204 2010/04/14
note
176F
179C
1751
17A8
1799
17A7
17A4
17A3
17A5
17A9
17A0
1772
1770
176E
17A1
179E
179F
17A6
1776
1754
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0204 2010/04/15
note
176F
179C
1751
17A8
1799
17A7
17A4
17A3
17A5
17A9
17A0
1772
1770
176E
17A1
179E
179F
17A6
1776
1754
ctrl
pass
pass
pass
OP end
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0204 2010/04/15
note
176F
179C
1751
17A8
1799
17A7
17A4
17A3
17A5
17A9
17A0
1772
1770
176E
17A1
179E
179F
17A6
1776
1754
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-292 2010-04-12 12:38:08 289 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ****************************
0099     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0100     C. ****************************
0101     C.
0102   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0103   . S. OP    op-292:OP
0104     ()
0105   . S. OG    og-292:OG
0106     ()
0107     C.
0108   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0109     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0110   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111     BC       (20 00 7f 01 02)
0112     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0113     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0114     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0115     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0116     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0117   +. DC 01-22 DHU_MODE_CHNG
0118     BC       (07 0b f8)
0119     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0120     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0122     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0123     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0124   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0125     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0126   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0127     C.
0128     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0129   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130     BC       (20 80 7f 01 02)
0131     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0132     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0133     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0134     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0135     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0136   +. DC 01-22 DHU_MODE_CHNG
0137     BC       (07 0b f8)
0138     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0139     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0141     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0142     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0143   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0144     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0145   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0146     C.
0147     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149     BC       (21 00 41 01 02)
0150     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0151     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0152     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0153     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0154     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0155   +. DC 01-22 DHU_MODE_CHNG
0156     BC       (07 0b f8)
0157     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0158     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0160     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0161     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0162   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0163     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0164   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0165     C.
0166   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0167     C. DHU¥â¡¼¥É¡Ê¼ý¸¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0168   +. DC 01-22 DHU_MODE_CHNG
0169     BC       (02 0a f8)
0170     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0171     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0172     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0173     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0174     C.
0175   . C. ********************************
0176     C. TI-CMD SET (OPOG STOP/COPY/START)
0177     C. ********************************
0178     C.
0179   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
0180     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¿¼Ç¹Ô¤¦¤³¤È¡£
0181     C.
0182   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0183   +. TI 2010-04-12 10:26:00.0
0184     DC 01-B3 DHU_OP_STOP
0185     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186     C.
0187   +. TI 2010-04-12 10:26:01.0
0188     DC 01-B4 DHU_OP_COPY
0189     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190     C.
0191   +. TI 2010-04-12 10:26:01.0
0192     DC 01-B5 DHU_OPOG_COPY
0193     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
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0194     C.
0195   +. TI 2010-04-12 10:30:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238     C. ********************************
0239     C.  SOT TI command set
0240     C. ********************************
0241     C. Execute, after the success of OP upload.
0242   +. TI 2010-04-12 10:30:16.0
0243     DC 07-F0 MDP_SOT_MODE_STBY
0244     BC       (41)
0245    . C. ------------------------------------
0246     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0247     C. ------------------------------------
0248     C. ************ SOT END ***********
0249    . C. Stop EIS observation and temporarily disable EIS mode changes
0250     C.
0251     C.
0252     C. ********** Start EIS operation (TI set) **********
0253     C. Execute, after the success of OP upload.
0254     C. Set EIS TI-commands
0255   +. TI 2010-04-12 10:30:30.0
0256     DC 07-FC EIS_MODE_MANU
0257     BC       (21 02)
0258   +. TI 2010-04-12 10:30:40.0
0259     DC 07-FC EIS_MODE_CHG_DIS
0260     BC       (22)
0261    . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0262     C. ************ End EIS operation (TI set) ***********
0263     C.
0264     C.
0265     C.
0266     C. ************ XRT START ***********
0267     C. Execute, after the success of OP upload.
0268   +. TI 2010-04-12 10:30:00.0
0269     DC 07-F0 MDP_XRT_MODE_STBY
0270     BC       (c3)
0271    . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0272     C.
0273     C. ************ XRT END ***********
0274     C.
0275    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0276     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0277    . S. DC-BC dcbc-402:DCBC
0278     (MDP_known_event)
0279     C.
0280     C.
0281    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0282    . S. DC-BC dcbc-153:DCBC
0283     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284     C.
0285     C.
0286    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0287     C.
0288    . C. ********** LOS **********
0289     C.
```

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(a) Spacecraft Operation Procedure (real-commands)

main-293 2010-04-12 12:38:08 175 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106   +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113   +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                      Result = OK [ ]
0121     C.
0122   +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                         <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127   +. DC 02-8E AOCU_ORB_UPD
0128   . C.
0129   . C. ***** AOCS Commands (Orbital Element Update) *****
0130     C. Update the orbital element
0131   +. DC 02-50 AOCU_ORB_PRPGT_START
0132     BC       (16)
0133   +  DC 02-8E AOCU_ORB_UPD
0134     C.
0135     C.       <A_ORB>[ORBIT] EPC = 2765549.8 +- 1.0 (s) [ ]
0136     C.
0137   . C.
0138   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139   +. DC 07-FC EIS_MODE_MANU
0140     BC       (21 02)
0141   . C. Verify EIS in MANUAL mode
0142   . C. Estimated OBSTBL upload time is 17s
0143     C. ********************************
0144     C. EIS START OBSTBL LOAD
0145     C. ********************************
0146   . S. RAM   ram-820:EIS_OBSTBL
0147     ()
0148   +. DC 07-FC EIS_DUMP_OBSTBL
0149     BC       (07 07 07 00 00 70 00)
0150     C.
0151     C. Execute, after the success of OBSTBL upload.
0152     C. Set EIS TI-commands
0153   +. TI 2010-04-12 10:30:50.0
0154     DC 07-FC EIS_MODE_CHG_ENA
0155     BC       (20)
0156   . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0157     C. ********************************
0158     C. EIS END OBSTBL LOAD
0159     C. ********************************
0160     C.
0161   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0162     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0163   . S. DC-BC dcbc-402:DCBC
0164     (MDP_known_event)
0165     C.
0166     C.
0167   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0168   . S. DC-BC dcbc-153:DCBC
0169     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0170     C.
0171     C.
0172   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0173     C.
0174   . C. ********** LOS **********
0175     C.
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(a) Spacecraft Operation Procedure (real-commands)

main-294 2010-04-12 12:38:08 153 33 SOLAR-B MAIN //
0001     C.
0002    . C. *********** AOS ***********
0003     C.
0004    . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009    . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014    . C. ********************************
0015     C.  SOT table upload
0016     C. ********************************
0017    . C. < Stop FG table >
0018   +. DC 07-F0 MDP_FG_CTRL_MANU
0019     BC       (51)
0020    . C. ------------------------------------
0021     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0022     C. ------------------------------------
0023     C.
0024    . C. <Upload FG Observation Table>
0025    . S. RAM   ram-263:MDP_OBS_F
0026     ()
0027     C.
0028    . C. < Dump RAMID=MDP_OBS_F >
0029   +. DC 07-F0 MDP_DUMP_FGTBL
0030     BC       (82 07 00 00 00 38 b8)
0031     C. ------------------------------------
0032     C.  MDP_OBS_F verify = OK/NG        [ ]
0033     C. ------------------------------------
0034     C.
0035    . C. < Upload DPL table >
0036     C.
0037     C. ¥¢¥Ã¥×¥í¡¼¥É¤ÎÁ°¤ËSTS_CHK¤òOFF¤Ë¤¹¤ë
0038     C.
0039    . S. RAM   ram-271:MDP_DPL
0040     ()
0041     C.
0042    . C. < Dump RAMID=MDP_DPL >
0043   +. DC 07-F0 MDP_DUMP_FGTBL
0044     BC       (82 07 00 38 b8 00 40)
0045     C. ------------------------------------
0046     C.  MDP_DPL verify          = OK    [ ]
0047     C. ------------------------------------
0048     C.
0049     C. STS_CHK¤òON¤Ë¤¹¤ë
0050     C.
0051    . C. < Update MDP DSC PAR1 >
0052   +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0053     BC       (4c)
0054     C.  MDP_CMD_CODE          = F04C0700[ ]
0055     C.  MDP_CMD_CNT        (count-up 1) [ ]
0056     C. ------------------------------------
0057     C.
0058    . C.
0059    . C. < Resume FG table (auto mode) >
0060   +. DC 07-F0 MDP_FG_CTRL_AUTO
0061     BC       (50)
0062    . C. ------------------------------------
0063     C.  MDP_FG_CTRL_MODE      = AUTO    [ ]
0064     C. ------------------------------------
0065     C.
0066     C. ********************************
0067     C.  SOT TI command set
0068     C. ********************************
0069     C. Execute, after the success of TBL upload.
0070   +. TI 2010-04-12 10:30:18.0
0071     DC 07-F0 MDP_SOT_MODE_OBSV
0072     BC       (40)
0073    . C. ------------------------------------
0074     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0075     C. ------------------------------------
0076     C.
0077     C. Only when FG_CTRL_AUTO is used in RT.
0078   +. TI 2010-04-12 10:30:20.0
0079     DC 07-F0 MDP_FG_CTRL_AUTO
0080     BC       (50)
0081    . C. ------------------------------------
0082     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0083     C. ------------------------------------
0084     C. ************ SOT END ************
0085     C.
0086     C. ************ XRT START ************
0087     C.
0088   +. DC 07-F0 MDP_XRT_CTRL_MANU
0089     BC       (c1)
0090   +  DC 07-F0 MDP_XRT_MODE_STBY
0091     BC       (c3)
0092    . C. ----------- Success Verify ?             OK / NG____
0093     C.
0094     C. XRT Obs. Table Upload
0095    . S. RAM   ram-291:MDP_OBS_X
```

```
0096     ()
0097     C.
0098  +. DC 07-F0 MDP_DUMP_XRTTBL
0099     BC       (84 07 00 00 00 3a d4)
0100   . C. ---------- Comparison Check ?                      OK / ERR ____
0101     C.
0102     C.
0103  +. DC 07-F0 MDP_XRT_ROI_SET
0104     BC       (cd 01 b1 b1 04 04)
0105  +  DC 07-F0 MDP_XRT_ROI_SET
0106     BC       (cd 02 b1 b1 08 08)
0107  +  DC 07-F0 MDP_XRT_ROI_SET
0108     BC       (cd 03 b1 b1 08 08)
0109  +  DC 07-F0 MDP_XRT_ROI_SET
0110     BC       (cd 04 b1 b1 06 06)
0111  +  DC 07-F0 MDP_XRT_ROI_SET
0112     BC       (cd 06 80 80 06 06)
0113  +  DC 07-F0 MDP_XRT_ROI_SET
0114     BC       (cd 07 80 80 08 08)
0115  +  DC 07-F0 MDP_XRT_ROI_SET
0116     BC       (cd 08 80 80 10 10)
0117  +  DC 07-F0 MDP_XRT_ROI_SET
0118     BC       (cd 09 80 80 20 20)
0119  +  DC 07-F0 MDP_XRT_ROI_SET
0120     BC       (cd 0a 80 80 20 08)
0121  +  DC 07-F0 MDP_XRT_ROI_SET
0122     BC       (cd 0b 80 80 08 20)
0123  +  DC 07-F0 MDP_XRT_AEC_RESET
0124     BC       (d0)
0125   . C. ---------- Success Verify ?           OK / NG ____
0126     C.
0127     C.
0128   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0129     C.
0130  +. DC 07-F0 MDP_XRT_MODE_OBSV
0131     BC       (c2)
0132  +. TI 2010-04-12 10:30:02.0
0133     DC 07-F0 MDP_XRT_MODE_OBSV
0134     BC       (c2)
0135   . C. ---------- Success Verify ?           OK / NG ____
0136     C.
0137     C. ************ XRT END ***********
0138     C.
0139   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0140     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0141   . S. DC-BC dcbc-402:DCBC
0142     (MDP_known_event)
0143     C.
0144     C.
0145   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0146   . S. DC-BC dcbc-153:DCBC
0147     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0148     C.
0149     C.
0150   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0151     C.
0152   . C. *********** LOS ***********
0153     C.
```

```
*** OP Sequence for XRT ***

2010/04/12  10:40:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/04/12  10:40:56.0  XRT_FOCUS_RECALIBRATE_444_OG [0x1bc]
                                XRT_FOCUS_RECAL      2 07-F8 78 00
2010/04/12  10:41:00.5  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 01 00 00 00 00
2010/04/12  10:44:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/04/12  10:45:16.0  XRT_AEC_RESET_428_OG [0x1ac]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2010/04/12  10:45:18.0  XRT_FLD_ENA_402_OG [0x192]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/04/12  10:45:20.0  XRT_FLD_RESET_403_OG [0x193]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/04/12  10:45:22.0  XRT_FLRCTRL_DIS_414_OG [0x19e]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/04/12  10:45:24.0  XRT_ARS_DIS_405_OG [0x195]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/04/12  10:47:58.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2010/04/12  10:48:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/04/12  15:00:00.5  XRT_CTRL_MANU_409_OG [0x199]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/04/12  15:12:00.0  XRT_Custom_410_OG [0x19a]
2010/04/12  15:13:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/04/12  16:35:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/04/12  16:59:00.0  XRT_Custom_410_OG [0x19a]
2010/04/12  17:00:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/04/12  17:48:56.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/04/12  17:48:58.0  XRT_FOCUS_POSITION_412_OG [0x19c]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/04/12  17:49:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2010/04/12  17:49:18.0  XRT_FLD_DIS_413_OG [0x19d]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2010/04/12  17:49:20.0  XRT_FLRCTRL_DIS_414_OG [0x19e]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/04/12  17:49:22.0  XRT_ARS_DIS_415_OG [0x19f]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/04/12  17:52:00.0  XRT_QT_PROG_SET_430_OG [0x1ae]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 09
2010/04/12  17:52:02.5  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/04/12  17:58:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/04/12  17:58:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/04/12  17:59:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 01 00 00 00 00
2010/04/12  17:59:16.0  XRT_AEC_RESET_428_OG [0x1ac]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2010/04/12  17:59:18.0  XRT_FLD_ENA_402_OG [0x192]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/04/12  17:59:20.0  XRT_FLD_RESET_403_OG [0x193]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/04/12  17:59:22.0  XRT_FLRCTRL_DIS_414_OG [0x19e]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/04/12  17:59:24.0  XRT_ARS_DIS_405_OG [0x195]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/04/12  18:01:58.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2010/04/12  18:02:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/04/12  18:12:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/04/12  18:36:00.0  XRT_Custom_410_OG [0x19a]
2010/04/12  18:37:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/04/12  19:49:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/04/12  20:13:00.0  XRT_Custom_410_OG [0x19a]
2010/04/12  20:14:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/04/12  21:27:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/04/12  21:50:00.0  XRT_Custom_410_OG [0x19a]
2010/04/12  21:51:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/04/12  23:04:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/04/12  23:24:30.0  XRT_Custom_410_OG [0x19a]
2010/04/12  23:25:30.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/04/13  00:41:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/04/13  00:48:30.0  XRT_Custom_410_OG [0x19a]
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```
2010/04/13  00:49:30.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/04/13  02:04:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/04/13  02:24:00.0  XRT_Custom_410_OG [0x19a]
2010/04/13  02:25:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/04/13  03:38:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/04/13  04:01:30.0  XRT_Custom_410_OG [0x19a]
2010/04/13  04:02:30.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/04/13  05:11:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/04/13  05:39:00.0  XRT_Custom_410_OG [0x19a]
2010/04/13  05:40:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/04/13  05:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/04/13  05:59:56.0  XRT_FOCUS_POSITION_412_OG [0x19c]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/04/13  06:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2010/04/13  06:00:16.0  XRT_FLD_DIS_413_OG [0x19d]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2010/04/13  06:00:18.0  XRT_FLRCTRL_DIS_414_OG [0x19e]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/04/13  06:00:20.0  XRT_ARS_DIS_415_OG [0x19f]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/04/13  06:02:58.0  XRT_QT_PROG_SET_430_OG [0x1ae]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 09
2010/04/13  06:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/04/13  06:05:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/04/13  06:05:02.0  XRT_FOCUS_POSITION_412_OG [0x19c]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/04/13  06:05:22.0  XRT_AEC_RESET_428_OG [0x1ac]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2010/04/13  06:05:24.0  XRT_FLD_DIS_413_OG [0x19d]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2010/04/13  06:05:26.0  XRT_FLRCTRL_DIS_414_OG [0x19e]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/04/13  06:05:28.0  XRT_ARS_DIS_415_OG [0x19f]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/04/13  06:08:06.0  XRT_QT_PROG_SET_432_OG [0x1b0]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2010/04/13  06:08:08.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/04/13  06:25:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 01 00 00 00 00
2010/04/13  10:46:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
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