

# XRT Timeline to be uploaded on 2010/04/20

Period: 2010/04/20 09:29:00 - 2010/04/24 10:52:00

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## Normal mode

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XOB #1754: Synoptic Q95 2x2 - Al/mesh(64/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(128/2048) + G-band(16												
Term	Pointing (x, y)						Comment					
04/21 09:59:32 - 04/21 10:02:00	Track ( -223.4, -0.9) @ 04/20 09:39:00						# OP start + 10min HOP151 QS near disk center.					
<b>PROG= 01 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 6 1-time(s) 4.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─ Seqn= 7 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 4 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #17AD: Full-disk Full-Res Al/Mesh (256/4096), Ti/Poly (512/8192), Thick/Al (46s) - 2 loops												
Term	Pointing (x, y)						Comment					
04/21 10:02:32 - 04/21 10:15:30	Track ( -223.4, -0.9) @ 04/20 09:39:00						# OP start + 10min HOP151 QS near disk center.					
<b>PROG= 16 1-time(s)</b>												
└─ Subr= 1 2-time(s) 300.0sec												
└─ Seqn= 68 1-time(s) 2.0sec												
└─ Open/thick-Al Open/thick-Al close Safe Norm 45.2s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 67 1-time(s) 2.0sec												
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 250ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 4.00s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 66 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 0.5sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 8.00s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 56 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Dark 1.00s Obs 1x1 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #17B2: HOP 151 Al/mesh - G Band alignment - 1x1 - 384FOV - 2048FOV context (2x2) - 20s cadence - AEC4 - Q95												
Term	Pointing (x, y)						Comment					
04/21 10:43:00 - 04/21 16:17:30	Track ( -223.4, -0.9) @ 04/20 09:39:00						# OP start + 10min HOP151 QS near disk center.					
<b>PROG= 18 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 74 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Subr= 2 135-time(s) 2.0sec												
└─ Seqn= 75 1-time(s) 20.0sec												
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 4 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

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## Flare mode

\* \* \* \* \*

NOT USED

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## Active Region Search

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NOT USED

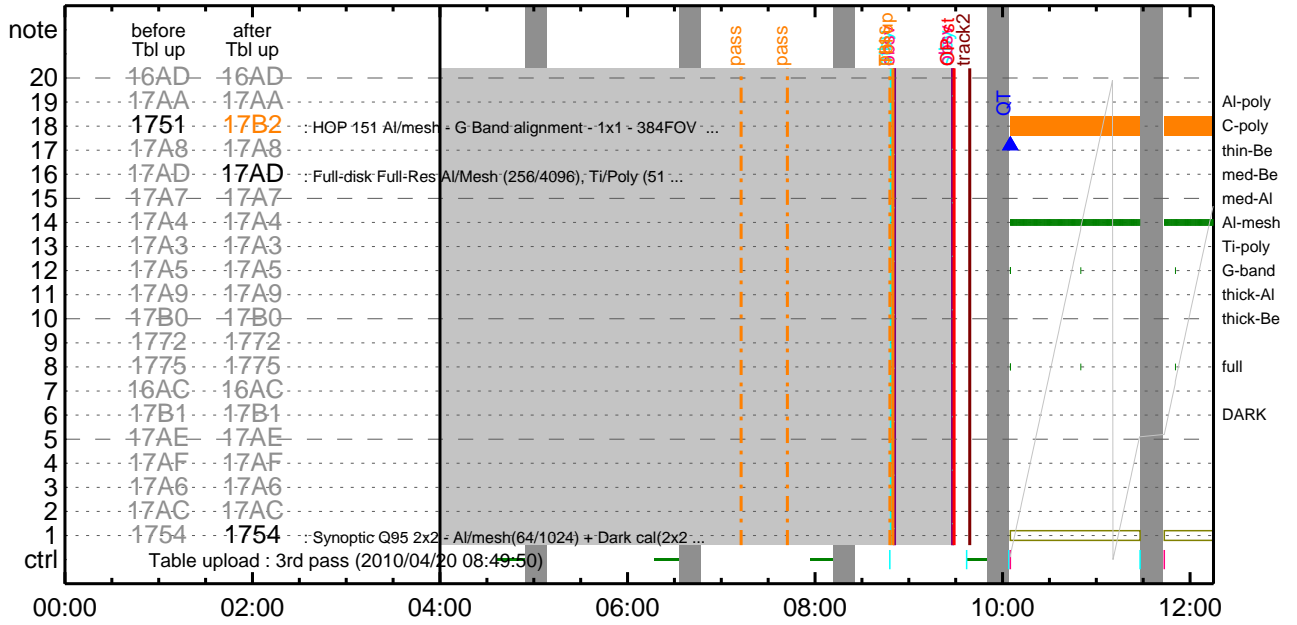
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## Flare Detection

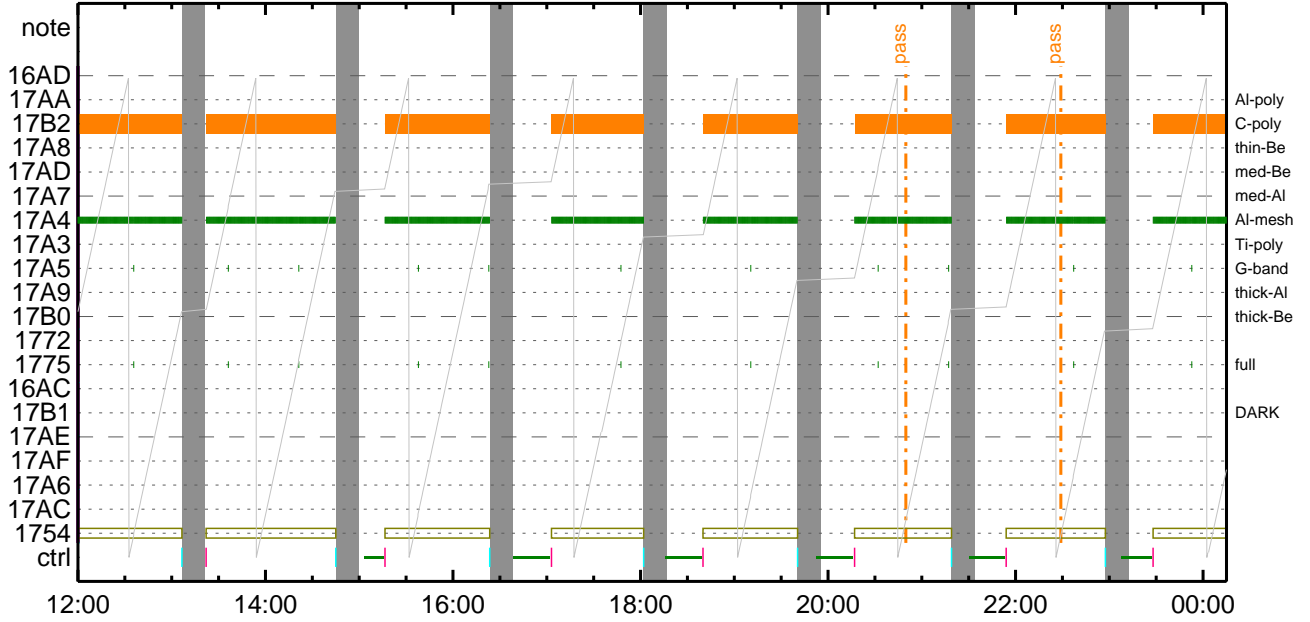
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FLD Patrol												
Term	Pointing (x, y)						Comment					
04/20 10:04:26 - 04/21 09:59:24	Track ( -223.4, -0.9) @ 04/20 09:39:00						# OP start + 10min HOP151 QS near disk center.					
04/21 10:42:24 - 04/24 10:52:00	Track ( -223.4, -0.9) @ 04/20 09:39:00						# OP start + 10min HOP151 QS near disk center.					
Open/Ti-poly Open/thick-Al close Safe Norm 8ms Obs 8x8 Q=50 30sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

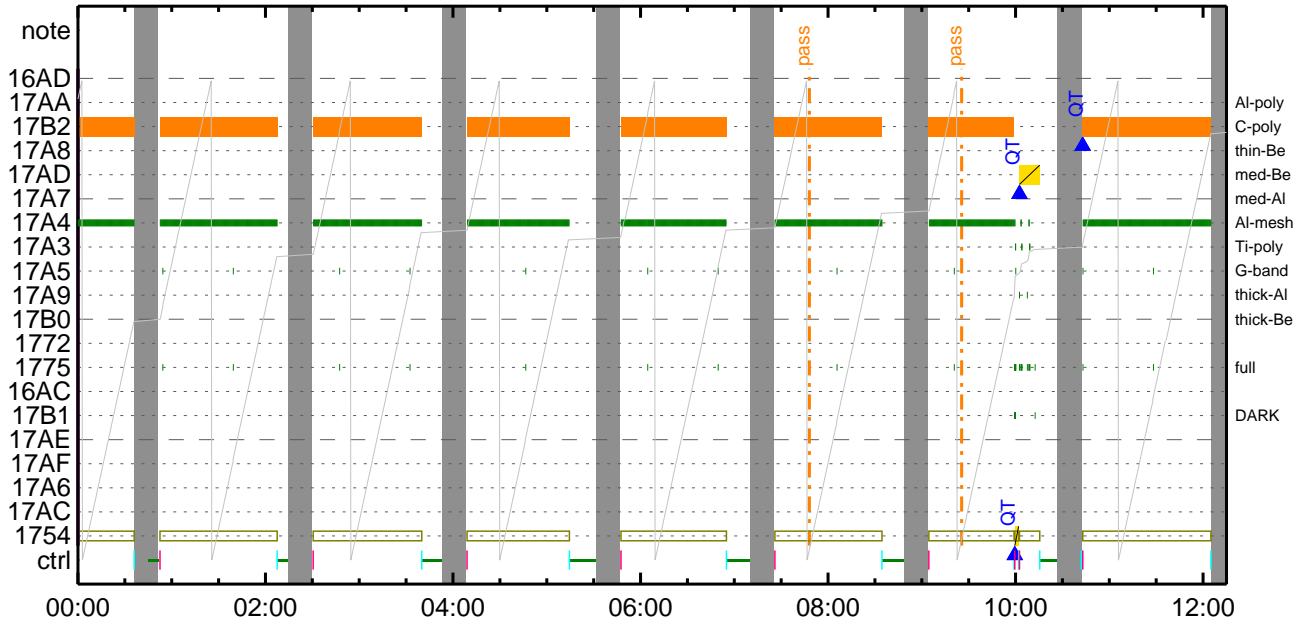
### CMDI #0219 2010/04/20



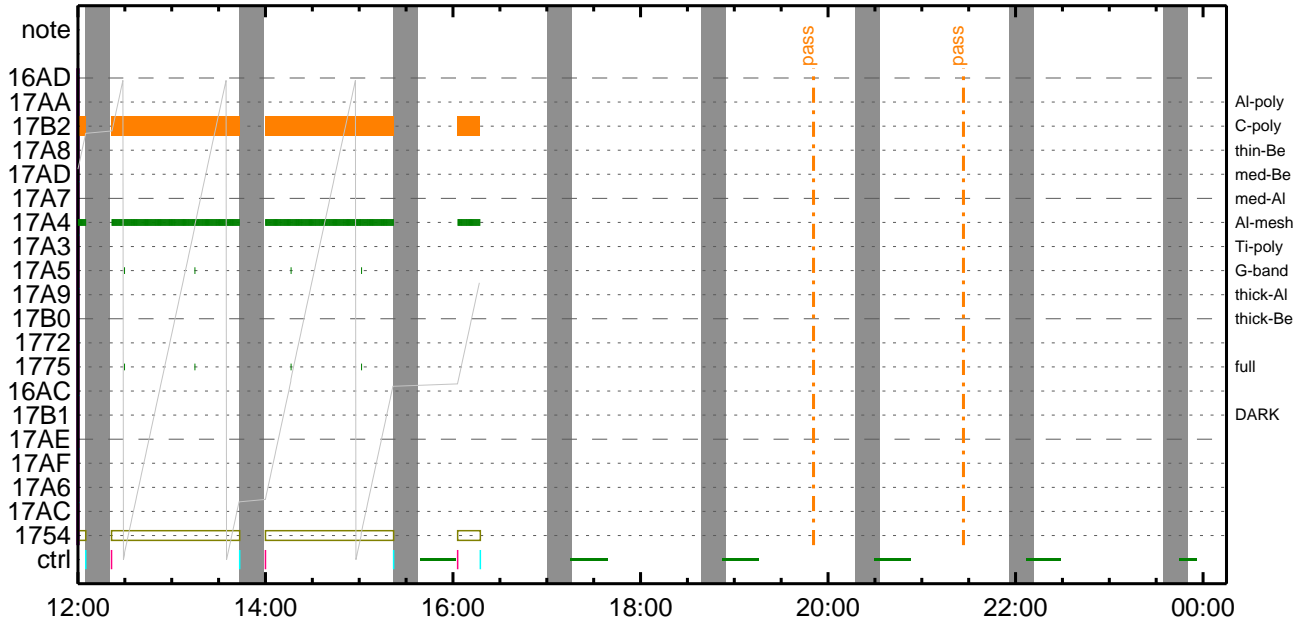
### CMDI #0219 2010/04/20



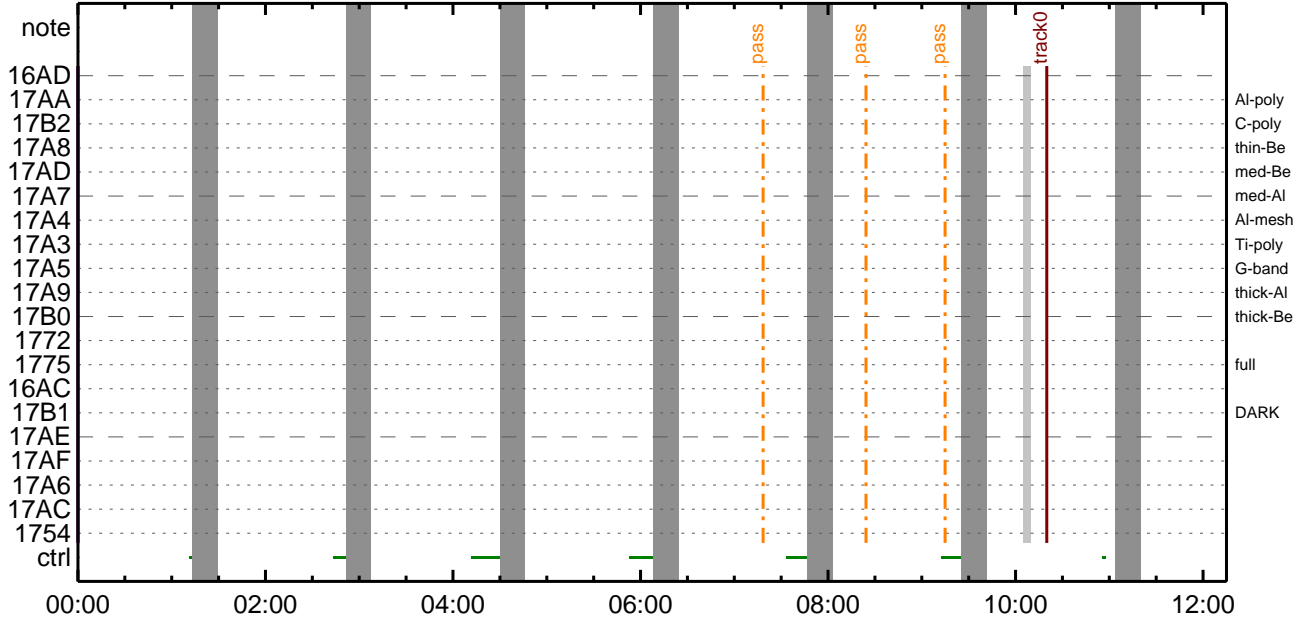
### CMDI #0219 2010/04/21



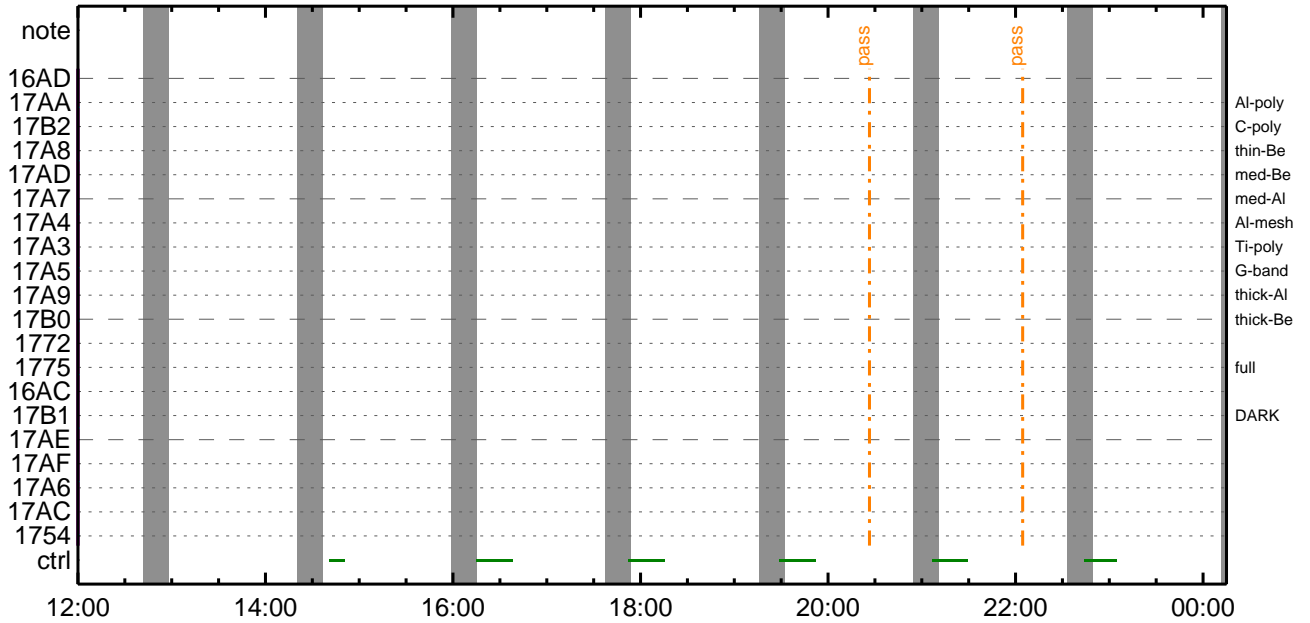
CMDI #0219 2010/04/21



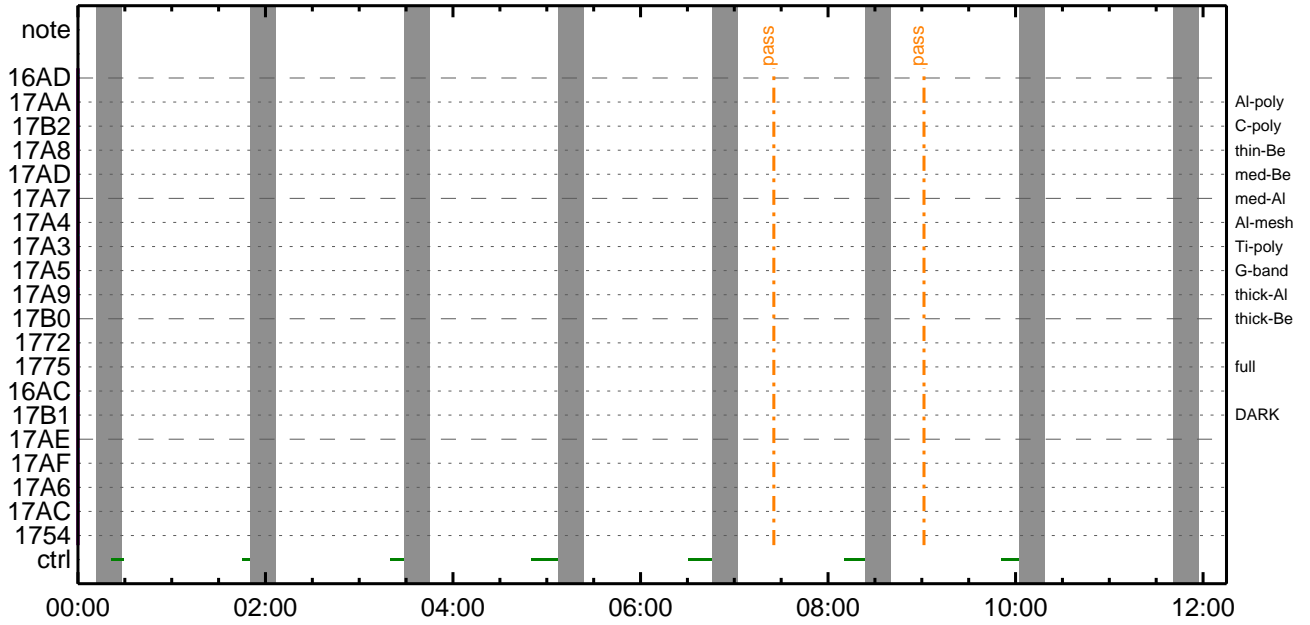
CMDI #0219 2010/04/22



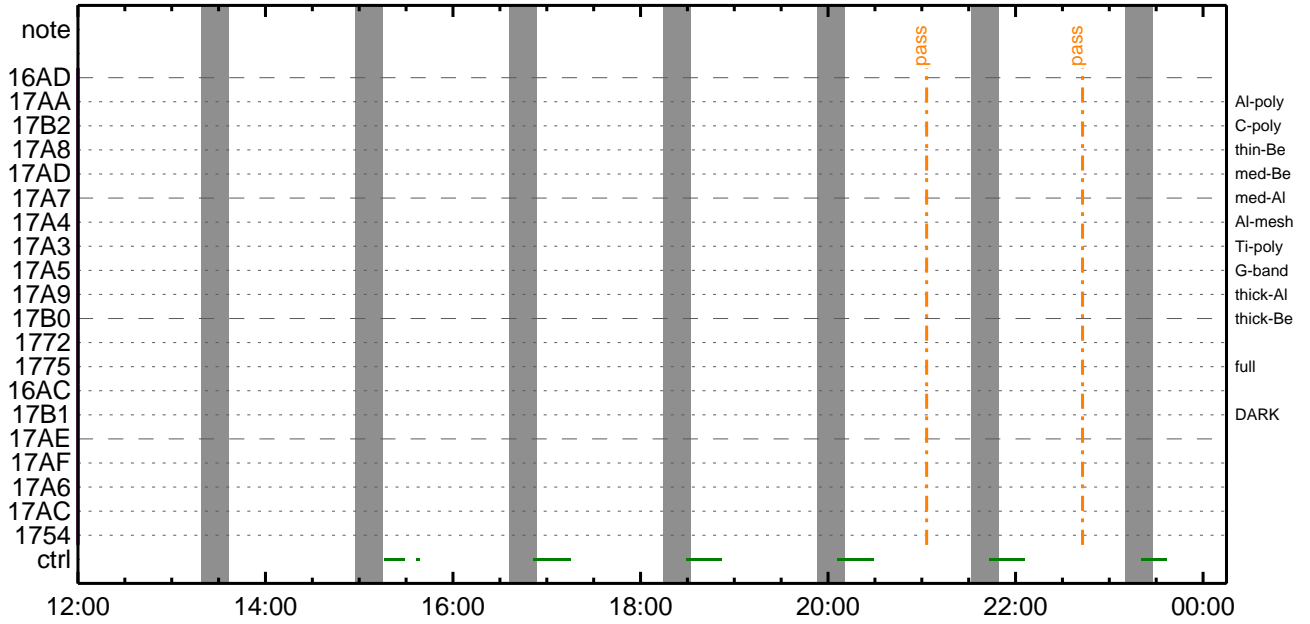
CMDI #0219 2010/04/22



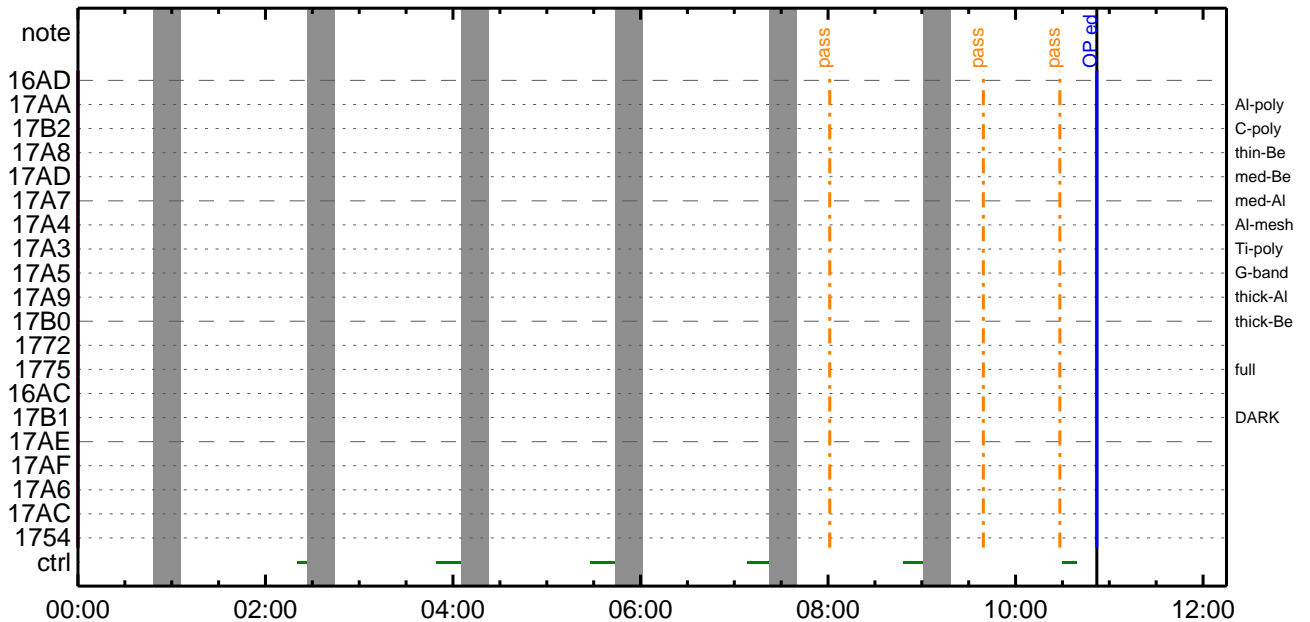
CMDI #0219 2010/04/23



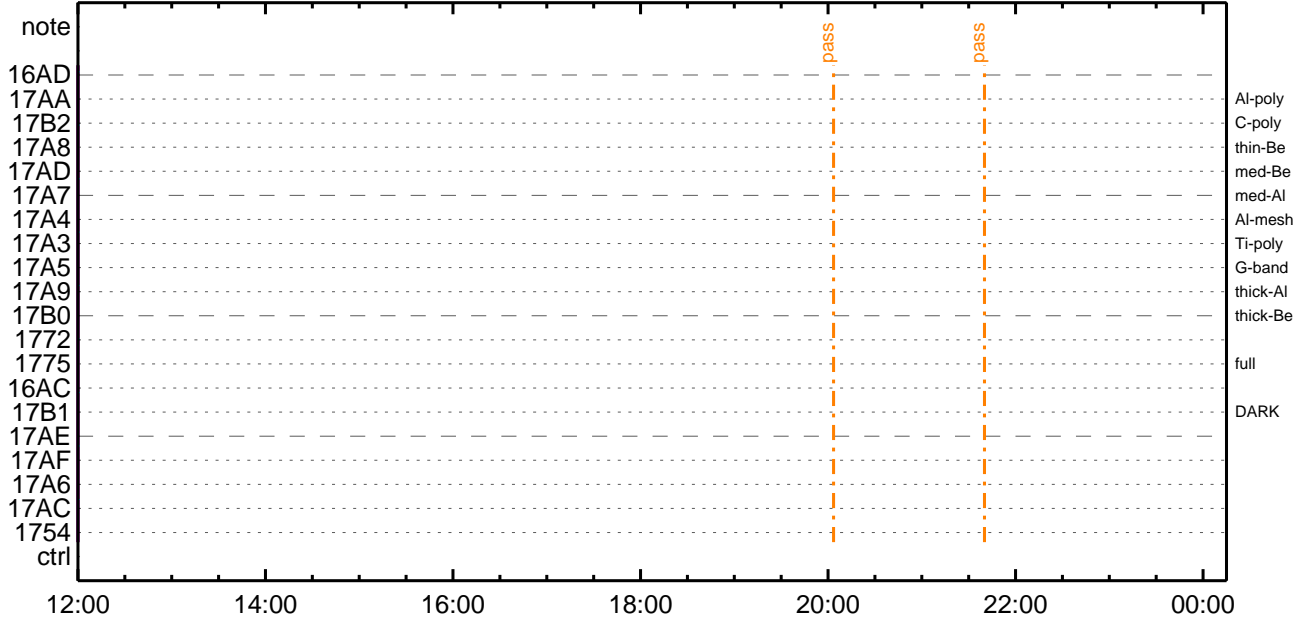
CMDI #0219 2010/04/23



CMDI #0219 2010/04/24



CMDI #0219 2010/04/24





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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-311:OP
0104 ( )
0105 S. OG og-311:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYôYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYôYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYôYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½A´¶Á°òEÉ-ò°Á÷¿@ (¼âµ-YAYôYx½ê½çòðÁÓÆòÇ¼ª°"òE¼î¹çòçòâ) *****
0167 C. DHUYâ;½YE;E½Y½, Yî;½YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½Á¹Ôª°²E²ò³òE;f
0180 C. ²²ò¿;çSET²EEDUMP²î½±°îYÑY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²YôYÉòðÁDî¿(UT)
0183 +. TI 2010-04-20 09:24:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2010-04-20 09:24:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2010-04-20 09:24:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 06 80 80 20 20)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 07 80 80 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 08 80 80 20 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 09 80 80 08 20)
0132 + DC 07-F0 MDP_XRT_AEC_RESET
0133 BC (d0)
0134 . C. ----- Success Verify ? OK / NG ____
0135 C.
0136 C.
0137 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0138 C.
0139 +. DC 07-F0 MDP_XRT_MODE_OBSV
0140 BC (c2)
0141 +. TI 2010-04-20 09:28:02.0
0142 DC 07-F0 MDP_XRT_MODE_OBSV
0143 BC (c2)
0144 . C. ----- Success Verify ? OK / NG ____
0145 C.
0146 C. ***** XRT END *****
0147 . C. *****
0148 C. SOT table upload
0149 C. *****
0150 . C. < Stop FG table >
0151 +. DC 07-F0 MDP_FG_CTRL_MANU
0152 BC (51)
0153 . C. -----
0154 C. MDP_FG_CTRL_MODE = MANU [ ]
0155 C. -----
0156 C.
0157 . C. <Upload FG Observation Table>
0158 . S. RAM ram-266:MDP_OBS_F
0159 ( )
0160 C.
0161 . C. < Dump RAMID=MDP_OBS_F >
0162 +. DC 07-F0 MDP_DUMP_FGTBL
0163 BC (82 07 00 00 00 38 b8)
0164 C. -----
0165 C. MDP_OBS_F verify = OK/NG [ ]
0166 C. -----
0167 C.
0168 . C. < Upload DPL table >
0169 C.
0170 C. ¥ç¥Ã¥×¥í;¼¥É¼îÁº¼ÈSTS_CHK¼ðOFF¼È¼¼ë
0171 C.
0172 . S. RAM ram-271:MDP_DPL
0173 ( )
0174 C.
0175 . C. < Dump RAMID=MDP_DPL >
0176 +. DC 07-F0 MDP_DUMP_FGTBL
0177 BC (82 07 00 38 b8 00 40)
0178 C. -----
0179 C. MDP_DPL verify = OK [ ]
0180 C. -----
0181 C.
0182 C. STS_CHK¼ðON¼È¼¼ë
0183 C.
0184 . C. < Update MDP DSC PAR1 >
0185 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0186 BC (4c)
0187 C. MDP_CMD_CODE = F04C0700[ ]
0188 C. MDP_CMD_CNT (count-up 1) [ ]
0189 C. -----
0190 C.
0191 . C.
0192 C. *****
0193 C. SOT TI command set

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0194 C. *****
0195 C. Execute, after the success of TBL upload.
0196 +. TI 2010-04-20 09:28:18.0
0197 DC 07-F0 MDP_SOT_MODE_OBSV
0198 BC (40)
0199 . C. -----
0200 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0201 C. -----
0202 C.
0203 C.
0204 . C. ***** MDP 'uAiAi»öYöEÄDq¹æDCBC•x²è *****
0205 C. (ª°iYÓYÄYÈYpYÈYáYçYèEª¼q¼Ä»Ûq¹æ)
0206 . S. DC-BC dcbc-402:DCBC
0207 (MDP_known_event)
0208 C.
0209 C.
0210 . C. ***** YDY¹•i Daily±;iNqÈ'øq¹æDCBC•x²è *****
0211 . S. DC-BC dcbc-153:DCBC
0212 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0213 C.
0214 C.
0215 . C. ;äLOSÄYÁYÄY-¼Ä»Û;ä
0216 C.
0217 . C. ***** LOS *****
0218 C.
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Apr 20, 10 11:27

## XRT\_OGLIST\_0219.chk

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\*\*\* OP Sequence for XRT \*\*\*

2010/04/20	09:37:00.0	XRT_CTRL_MANU_409_OG [0x199]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/04/20	09:39:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 00 00 00 00
2010/04/20	10:04:02.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/04/20	10:04:04.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/04/20	10:04:24.0	XRT_AEC_RESET_428_OG [0x1ac]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2010/04/20	10:04:26.0	XRT_FLD_ENA_402_OG [0x192]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2010/04/20	10:04:28.0	XRT_FLD_RESET_403_OG [0x193]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2010/04/20	10:04:30.0	XRT_FLRCTRL_DIS_414_OG [0x19e]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2010/04/20	10:04:32.0	XRT_ARS_DIS_421_OG [0x1a5]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2010/04/20	10:04:58.0	XRT_QT_PROG_SET_422_OG [0x1a6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2010/04/20	10:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/04/20	11:28:00.0	XRT_CTRL_MANU_409_OG [0x199]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/04/20	11:42:30.0	XRT_Custom_410_OG [0x19a]			
2010/04/20	11:43:30.0	XRT_CTRL_AUTO_411_OG [0x19b]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/04/20	13:06:30.0	XRT_CTRL_MANU_409_OG [0x199]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/04/20	13:21:00.0	XRT_Custom_410_OG [0x19a]			
2010/04/20	13:22:00.0	XRT_CTRL_AUTO_411_OG [0x19b]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/04/20	14:45:00.0	XRT_CTRL_MANU_409_OG [0x199]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/04/20	15:15:30.0	XRT_Custom_410_OG [0x19a]			
2010/04/20	15:16:30.0	XRT_CTRL_AUTO_411_OG [0x19b]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/04/20	16:23:30.0	XRT_CTRL_MANU_409_OG [0x199]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/04/20	17:02:00.0	XRT_Custom_410_OG [0x19a]			
2010/04/20	17:03:00.0	XRT_CTRL_AUTO_411_OG [0x19b]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/04/20	18:02:00.0	XRT_CTRL_MANU_409_OG [0x199]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/04/20	18:39:00.0	XRT_Custom_410_OG [0x19a]			
2010/04/20	18:40:00.0	XRT_CTRL_AUTO_411_OG [0x19b]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/04/20	19:40:30.0	XRT_CTRL_MANU_409_OG [0x199]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/04/20	20:16:00.0	XRT_Custom_410_OG [0x19a]			
2010/04/20	20:17:00.0	XRT_CTRL_AUTO_411_OG [0x19b]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/04/20	21:19:00.0	XRT_CTRL_MANU_409_OG [0x199]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/04/20	21:53:00.0	XRT_Custom_410_OG [0x19a]			
2010/04/20	21:54:00.0	XRT_CTRL_AUTO_411_OG [0x19b]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/04/20	22:57:30.0	XRT_CTRL_MANU_409_OG [0x199]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/04/20	23:27:00.0	XRT_Custom_410_OG [0x19a]			
2010/04/20	23:28:00.0	XRT_CTRL_AUTO_411_OG [0x19b]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/04/21	00:36:00.0	XRT_CTRL_MANU_409_OG [0x199]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/04/21	00:51:30.0	XRT_Custom_410_OG [0x19a]			
2010/04/21	00:52:30.0	XRT_CTRL_AUTO_411_OG [0x19b]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/04/21	02:07:30.0	XRT_CTRL_MANU_409_OG [0x199]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/04/21	02:29:30.0	XRT_Custom_410_OG [0x19a]			
2010/04/21	02:30:30.0	XRT_CTRL_AUTO_411_OG [0x19b]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/04/21	03:40:00.0	XRT_CTRL_MANU_409_OG [0x199]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/04/21	04:08:00.0	XRT_Custom_410_OG [0x19a]			
2010/04/21	04:09:00.0	XRT_CTRL_AUTO_411_OG [0x19b]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/04/21	05:14:30.0	XRT_CTRL_MANU_409_OG [0x199]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/04/21	05:46:30.0	XRT_Custom_410_OG [0x19a]			
2010/04/21	05:47:30.0	XRT_CTRL_AUTO_411_OG [0x19b]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/04/21	06:55:00.0	XRT_CTRL_MANU_409_OG [0x199]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/04/21	07:25:00.0	XRT_Custom_410_OG [0x19a]			
2010/04/21	07:26:00.0	XRT_CTRL_AUTO_411_OG [0x19b]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/04/21	08:34:30.0	XRT_CTRL_MANU_409_OG [0x199]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/04/21	09:03:30.0	XRT_Custom_410_OG [0x19a]			
2010/04/21	09:04:30.0	XRT_CTRL_AUTO_411_OG [0x19b]			

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2010/04/21	09:59:02.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/04/21	09:59:04.0	XRT_FOCUS_POSITION_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/04/21	09:59:24.0	XRT_FLD_DIS_413_OG [0x19d]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2010/04/21	09:59:26.0	XRT_FLRCTRL_DIS_414_OG [0x19e]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2010/04/21	09:59:28.0	XRT_ARS_DIS_446_OG [0x1be]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2010/04/21	09:59:30.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/04/21	09:59:32.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2010/04/21	10:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/04/21	10:02:02.0	XRT_FOCUS_POSITION_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/04/21	10:02:22.0	XRT_AEC_RESET_428_OG [0x1ac]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2010/04/21	10:02:24.0	XRT_FLD_DIS_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2010/04/21	10:02:26.0	XRT_FLRCTRL_DIS_414_OG [0x19e]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2010/04/21	10:02:28.0	XRT_ARS_DIS_446_OG [0x1be]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2010/04/21	10:02:30.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/04/21	10:02:32.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2010/04/21	10:15:30.0	XRT_CTRL_MANU_409_OG [0x199]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/04/21	10:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/04/21	10:42:02.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/04/21	10:42:22.0	XRT_AEC_RESET_428_OG [0x1ac]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2010/04/21	10:42:24.0	XRT_FLD_ENA_402_OG [0x192]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2010/04/21	10:42:26.0	XRT_FLD_RESET_403_OG [0x193]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2010/04/21	10:42:28.0	XRT_FLRCTRL_DIS_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/04/21	10:42:30.0	XRT_ARS_DIS_418_OG [0x1a2]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2010/04/21	10:42:58.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/04/21	10:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2010/04/21	12:05:00.0	XRT_CTRL_MANU_409_OG [0x199]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/04/21	12:20:30.0	XRT_Custom_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/04/21	12:21:30.0	XRT_CTRL_AUTO_411_OG [0x19b]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/04/21	13:43:30.0	XRT_CTRL_MANU_409_OG [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/04/21	13:59:00.0	XRT_Custom_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/04/21	14:00:00.0	XRT_CTRL_AUTO_411_OG [0x19b]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/04/21	15:22:00.0	XRT_CTRL_MANU_409_OG [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/04/21	16:02:00.0	XRT_Custom_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/04/21	16:03:00.0	XRT_CTRL_AUTO_411_OG [0x19b]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/04/21	16:17:30.0	XRT_CTRL_MANU_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/04/21	16:18:00.0	XRT_TCIB_XRT_S_HTR_A_ENA_448_OG [0x1c0]	TCIB_XRT_S_HTR_A_ENA	0	04-BC		
2010/04/22	10:20:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	