XRT Timeline to be uploaded on 2010/05/05

Period: 2010/05/05 09:31:00 - 2010/05/10 10:24:00

* * * * * **Normal mode** * * * * *

XOB #17B6: AR multifilter - Al/mesh,Ti/Poly, G-bandFOV 384 AEC2 Q95 (5min)

Term	Pointing (x, y)	Comment
05/05 09:44:02 - 05/05 12:32:30	Track (665.1, -563.9) @ 05/05 09:41:00	# OP start + 10min, track filament with Meudon, HOP 139.
05/06 06:18:32 - 05/06 11:59:54	Track (716.8, -572.1) @ 05/06 06:15:30	# Track filament for HOP 139, with Meudon over 8 - 12 UT.

PROG= 03 Inf.-time(s)
- **Subr= 2 1-time(s) 2.0sec**
  - **Seqn= 1 1-time(s) 2.0sec**
    - Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 512x512 (1024, 1024) Q=98 0 0 2.0sec
- **Subr= 1 9-time(s) 300.0sec**
  - **Seqn= 84 1-time(s) 4.0sec**
    - Open/Al-mesh Open/Ti-poly close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #17BB: Coronal cavity Al/mesh, Ti/poly, G-band-DPCM 2x2-768X768 -AEC0 - 3min cadence

Term	Pointing (x, y)	Comment
05/05 12:58:38 - 05/05 17:28:00	Fixed (-550.0, 770.0)	* HOP 73 on cavity in NE.
05/07 08:03:02 - 05/07 08:27:00	Fixed (727.0, -574.0)	* HOP 73 with Meudon, prominence at W limb.

PROG= 06 Inf.-time(s)
- **Subr= 2 1-time(s) 2.0sec**
  - **Seqn= 88 1-time(s) 2.0sec**
    - Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec
- **Subr= 1 20-time(s) 180.0sec**
  - **Seqn= 89 1-time(s) 2.0sec**
    - Open/Al-mesh Open/Ti-poly close Safe Norm 2.00s Obs 2x2 768x768 (1024, 1024) DPCM 0 0 2.0sec
  - **Seqn= 90 1-time(s) 2.0sec**
    - Open/Ti-poly Open/thick-Al close Safe Norm 4.00s Obs 2x2 768x768 (1024, 1024) DPCM 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)

Term	Pointing (x, y)	Comment
05/05 18:11:30 - 05/05 18:18:24	Fixed (0.0, 0.0)	synoptic, shifted 8.5 min
05/06 05:58:30 - 05/06 06:01:30	Fixed (0.0, 0.0)	synoptic, shifted -4.5 min, extended for joint XRT-AIA observations.
05/06 17:58:00 - 05/06 18:05:00	Fixed (0.0, 0.0)	synoptic, shifted -5.0 min
05/07 06:03:00 - 05/07 06:06:00	Fixed (0.0, 0.0)	synoptic, extended for joint XRT-AIA observations.

PROG= 15 1-time(s)
- **Subr= 1 1-time(s) 12.0sec**
  - **Seqn= 80 1-time(s) 4.0sec**
    - Open/Al-mesh Open/Al-mesh close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - **Seqn= 5 1-time(s) 2.0sec**
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
  - **Seqn= 81 1-time(s) 4.0sec**
    - Open/Ti-poly Open/Ti-poly close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - **Seqn= 4 1-time(s) 2.0sec**
    - Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #16AC: G-Band Alignment with North Pole Q90 2x2(G-band only) - 5min cadence - Partial Sun-wNGT

Term	Pointing (x, y)	Comment
05/05 18:33:30 - 05/05 20:33:24	Fixed (0.0, 945.0)	# Alignment offset N.

PROG= 09 1-time(s)
- **Subr= 1 1-time(s) 360.0sec**
  - **Seqn= 3 24-time(s) 300.0sec**
    - Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x1536 (1024, 768) Q=90 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #16AD: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8 min cadence-wNGT

Term	Pointing (x, y)	Comment
05/05 20:48:30 - 05/05 22:58:00	Fixed (-945.0, 0.0)	* Alignment offset, E.

PROG= 20 1-time(s)
- **Subr= 1 1-time(s) 360.0sec**
  - **Seqn= 28 15-time(s) 480.0sec**
    - Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 1536x2048 (1280, 1024) Q=90 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #17AC: HOP146 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - 512FOV - Q90

Term	Pointing (x, y)	Comment
05/05 23:01:12 - 05/06 05:55:24	Track (663.7, 432.4) @ 05/05 22:40:00	* EIS interchange reconnection study.

PROG= 02 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 64 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec

Subr= 2 12-time(s) 300.0sec

Seqn= 65 2-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	8.00s	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec

XOB #17A9: Full-disk Full-Res Al/Mesh, Ti/Poly, Thick/Al - 2 loops

Term	Pointing (x, y)	Comment
05/06 06:02:00 - 05/06 06:15:24	Fixed (0.0, 0.0)	synoptic, shifted -4.5 min, extended for joint XRT-AIA observations.
05/07 06:06:30 - 05/07 06:24:54	Fixed (0.0, 0.0)	synoptic, extended for joint XRT-AIA observations.

PROG= 14 1-time(s)

Subr= 1 2-time(s) 300.0sec

Seqn= 57 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/thick-Al	Open/thick-Al	close	Safe	Norm	22.6s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 53 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 54 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 56 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Dark	1.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

XOB #179F: AR Dynamics- Ti/Poly(384FOV) AEC2_3 Q95, Al-mesh, Ti-poly AEC1, Thick-Al fixed 32, G-band(512FOV) - 30s cadence

Term	Pointing (x, y)	Comment
05/06 12:03:02 - 05/06 17:54:54	Track (514.4, 636.9) @ 05/06 12:00:00	* Track AR 11069.
05/07 06:28:02 - 05/07 07:59:54	Track (593.1, 630.2) @ 05/07 06:25:00	# Cont. tracking AR 11069.

PROG= 12 Inf.-time(s)

Subr= 1 1-time(s) 30.0sec

Seqn= 1 1-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 42 1-time(s) 40.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	125ms	Obs	1x1	512x512 (1024, 1024)	Q=92	1	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	512x512 (1024, 1024)	Q=92	1	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	32.0s	Obs	1x1	512x512 (1024, 1024)	Q=92	0	0	2.0sec

Subr= 2 30-time(s) 30.0sec

Seqn= 18 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Seqn= 43 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec

XOB #17BA: AR Dynamics- Ti/Poly(384FOV) AEC2_3 Q95, Al-mesh, Ti-poly AEC1, Thick-Al fixed 32, G-band(512FOV) - 1min cadence

Term	Pointing (x, y)	Comment
05/06 18:48:08 - 05/07 05:21:00	Track (542.0, 634.7) @ 05/06 18:05:00	# Cont.

PROG= 17 Inf.-time(s)

Subr= 1 1-time(s) 30.0sec

Seqn= 1 1-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 42 1-time(s) 40.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	125ms	Obs	1x1	512x512 (1024, 1024)	Q=92	1	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	512x512 (1024, 1024)	Q=92	1	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	32.0s	Obs	1x1	512x512 (1024, 1024)	Q=92	0	0	2.0sec

Subr= 2 15-time(s) 60.0sec

Seqn= 18 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Seqn= 43 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec

* * * * * **Flare mode** * * * * *

XOB #1776: Flare Response short exp at first- Dynamics - Thick-Al - Thick-Be - AEC 1 - 384x384 - Q95

Term	Pointing (x, y)	Comment
05/05 09:44:02 - 05/05 12:32:30	Track (665.1, -563.9) @ 05/05 09:41:00	# OP start + 10min, track filament with Meudon, HOP 139.

Term	Pointing (x, y)	Comment
05/05 12:58:38 - 05/05 17:28:00	Fixed (-550.0, 770.0)	* HOP 73 on cavity in NE.
05/05 23:01:12 - 05/06 05:55:24	Track (663.7, 432.4) @ 05/05 22:40:00	* EIS interchange reconnection study.
05/06 06:18:32 - 05/06 11:59:54	Track (716.8, -572.1) @ 05/06 06:15:30	# Track filament for HOP 139, with Meudon over 8 - 12 UT.
05/06 12:03:02 - 05/06 17:54:54	Track (514.4, 636.9) @ 05/06 12:00:00	* Track AR 11069.
05/06 18:48:08 - 05/07 05:21:00	Track (542.0, 634.7) @ 05/06 18:05:00	# Cont.
05/07 06:28:02 - 05/07 07:59:54	Track (593.1, 630.2) @ 05/07 06:25:00	# Cont. tracking AR 11069.
05/07 08:03:02 - 05/07 08:27:00	Fixed (727.0, -574.0)	* HOP 73 with Meudon, prominence at W limb.

PROG= 13 1-time(s)

Subr= 1 2-time(s) 2.0sec

Seqn= 9 15-time(s) 20.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/thick-Al	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec

Seqn= 2 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec

Subr= 2 8-time(s) 2.0sec

Seqn= 9 15-time(s) 60.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/thick-Al	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec

Seqn= 2 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec

Subr= 3 25-time(s) 2.0sec

Seqn= 9 1-time(s) 600.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/thick-Al	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec

Seqn= 2 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec

* * * * * Active Region Search * * * * *

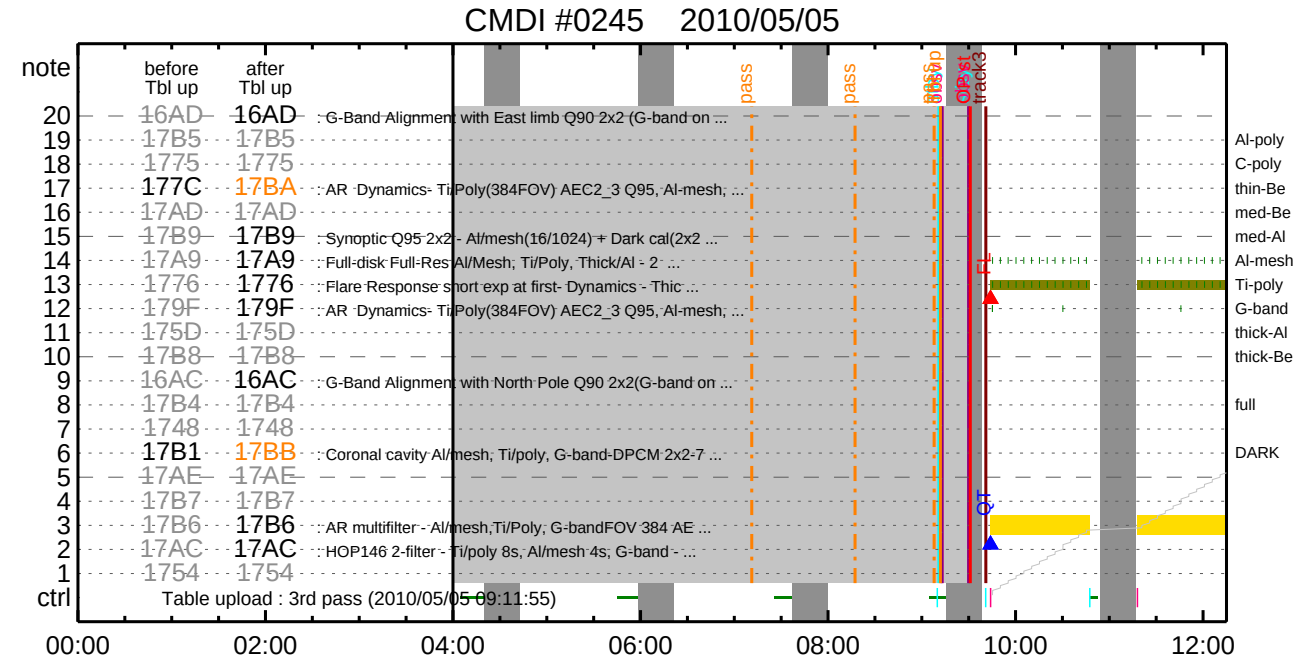
NOT USED

* * * * * Flare Detection * * * * *

FLD Patrol

Term	Pointing (x, y)	Comment
05/05 09:41:18 - 05/05 18:08:46	Track (665.1, -563.9) @ 05/05 09:41:00	# OP start + 10min, track filament with Meudon, HOP 139.
05/05 22:58:26 - 05/06 05:55:46	Track (663.7, 432.4) @ 05/05 22:40:00	* EIS interchange reconnection study.
05/06 06:15:48 - 05/06 17:55:16	Track (716.8, -572.1) @ 05/06 06:15:30	# Track filament for HOP 139, with Meudon over 8 - 12 UT.
05/06 18:45:24 - 05/07 06:00:16	Track (542.0, 634.7) @ 05/06 18:05:00	# Cont.
05/07 06:25:18 - 05/10 10:24:00	Track (593.1, 630.2) @ 05/07 06:25:00	# Cont. tracking AR 11069.

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec


CMDI #0245 2010/05/05
note
before Tbl up
after Tbl up
20 16AD 16AD : G-Band Alignment with East limb Q90 2x2 (G-band on ...
19 17B5 17B5
18 1775 1775
17 177C 17BA : AR Dynamics- Ti/Poly(384FOV) AEC2_3 Q95, Al-mesh, ...
16 17AD 17AD
15 17B9 17B9 : Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 ...
14 17A9 17A9 : Full-disk Full-Res Al/Mesh, Ti/Poly, Thick/Al - 2 ...
13 1776 1776 : Flare Response short exp at first- Dynamics - Thic ...
12 179F 179F : AR Dynamics- Ti/Poly(384FOV) AEC2_3 Q95, Al-mesh, ...
11 175D 175D
10 17B8 17B8
9 16AC 16AC : G-Band Alignment with North Pole Q90 2x2(G-band on ...
8 17B4 17B4
7 1748 1748
6 17B1 17BB : Coronal cavity Al/mesh, Ti/poly, G-band-DPCM 2x2-7 ...
5 17AE 17AE
4 17B7 17B7
3 17B6 17B6 : AR multifilter - Al/mesh,Ti/Poly, G-bandFOV 384 AE ...
2 17AC 17AC : HOP146 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - ...
1 1754 1754
ctrl Table upload : 3rd pass (2010/05/05 09:11:55)
pass
pass
track3
FL
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
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06:00
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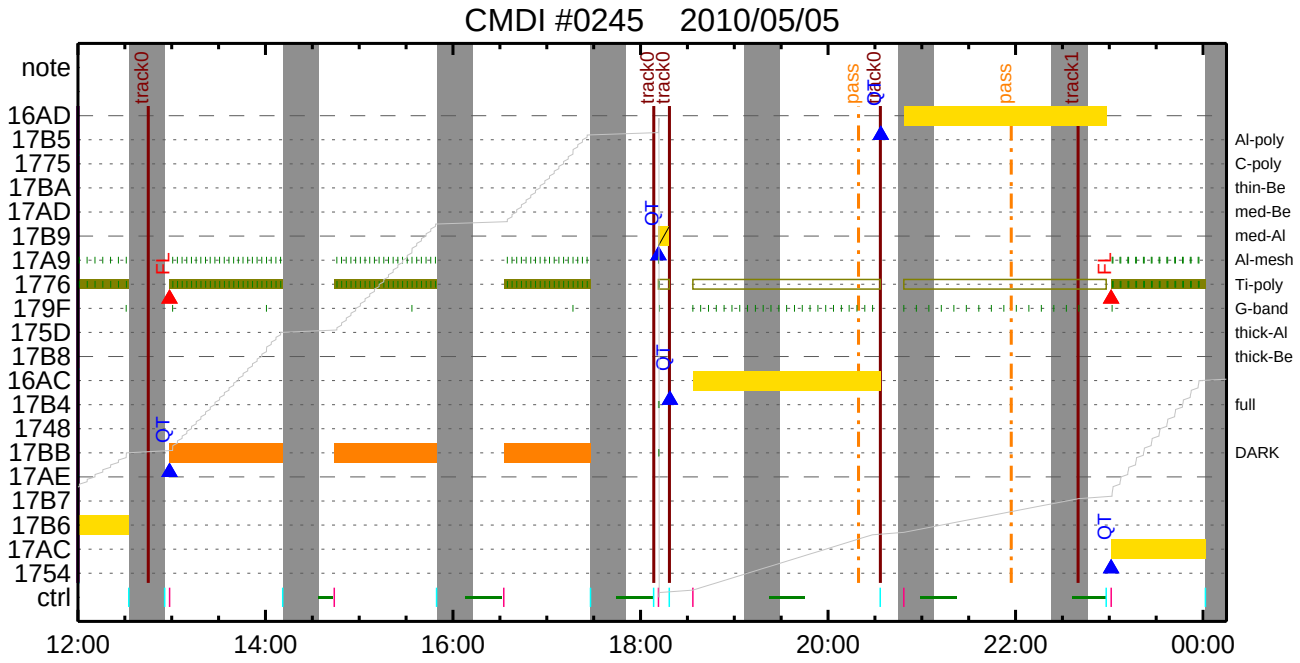
10:00
12:00


CMDI #0245 2010/05/05
note
16AD
17B5
1775
17BA
17AD
17B9
17A9
1776
179F
175D
17B8
16AC
17B4
1748
17BB
17AE
17B7
17B6
17AC
1754
ctrl
track0
track0
track0
pass
pass
track1
FL
FL
QT
QT
QT
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
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14:00
16:00
18:00
20:00
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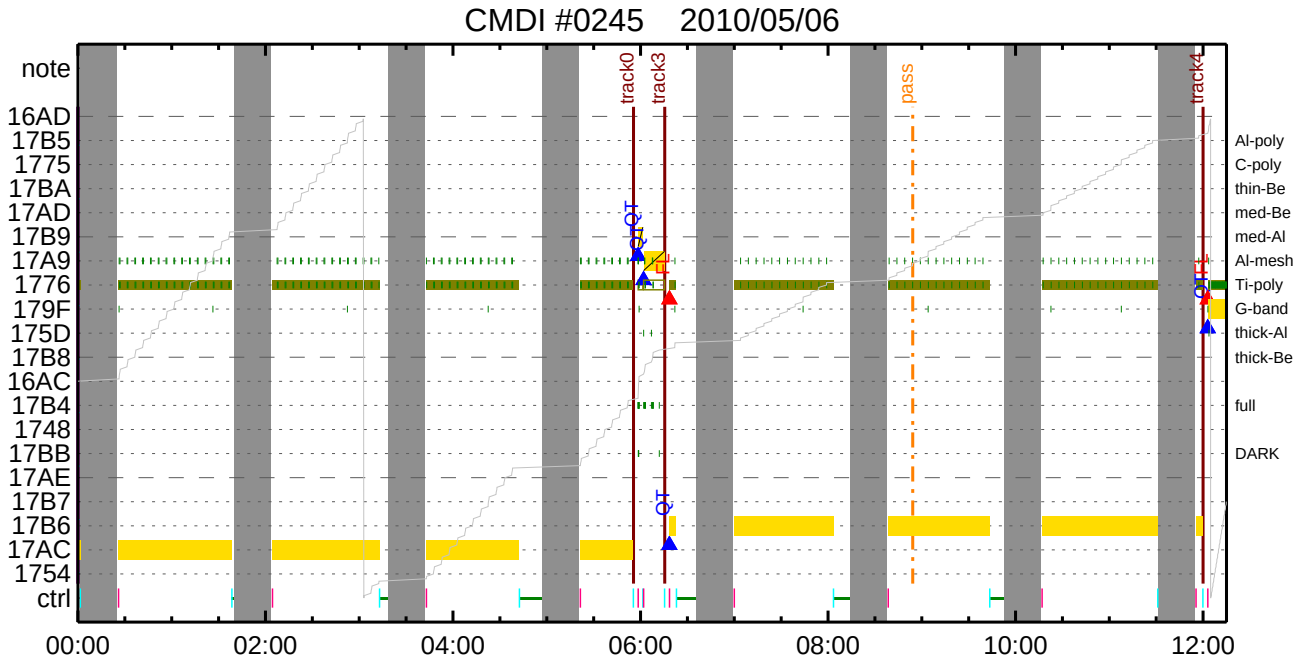
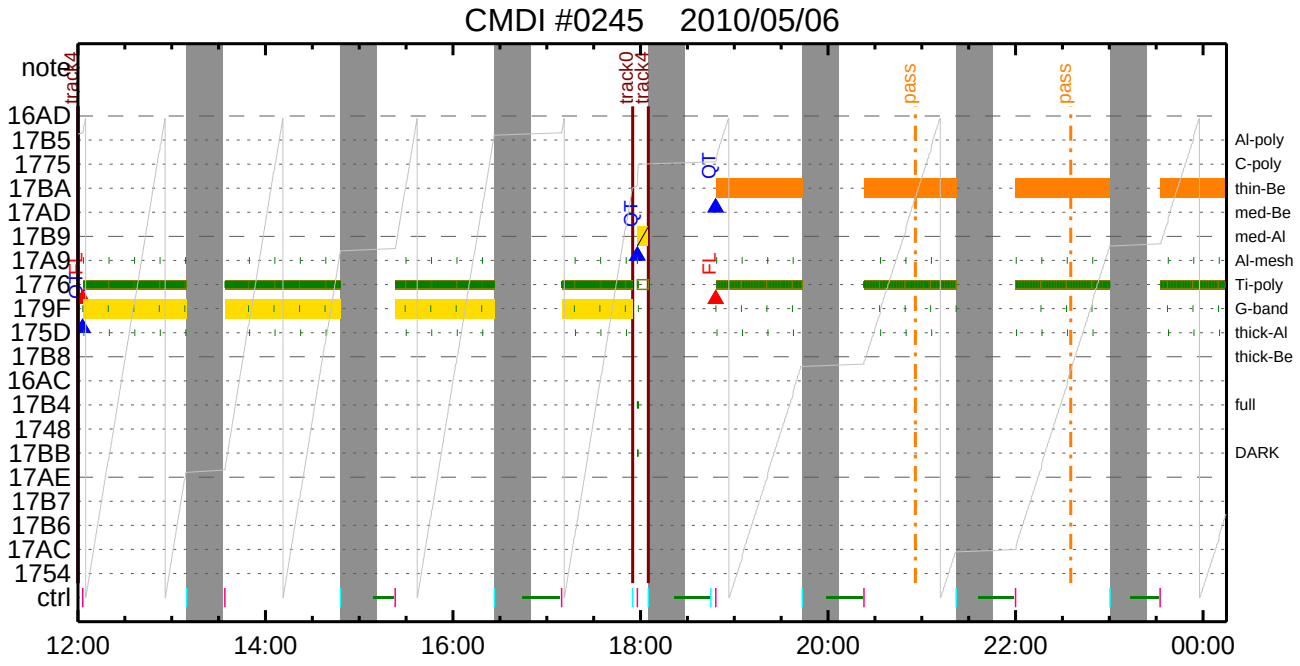

CMDI #0245 2010/05/06
note
16AD
17B5
1775
17BA
17AD
17B9
17A9
1776
179F
175D
17B8
16AC
17B4
1748
17BB
17AE
17B7
17B6
17AC
1754
ctrl
track0
track3
pass
track4
QT
QT
FL
FL
QT
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0245 2010/05/06
note
16AD
17B5
1775
17BA
17AD
17B9
17A9
1776
179F
175D
17B8
16AC
17B4
1748
17BB
17AE
17B7
17B6
17AC
1754
ctrl
track4
track0
track4
pass
pass
QT
QT
FL
FL
12:00
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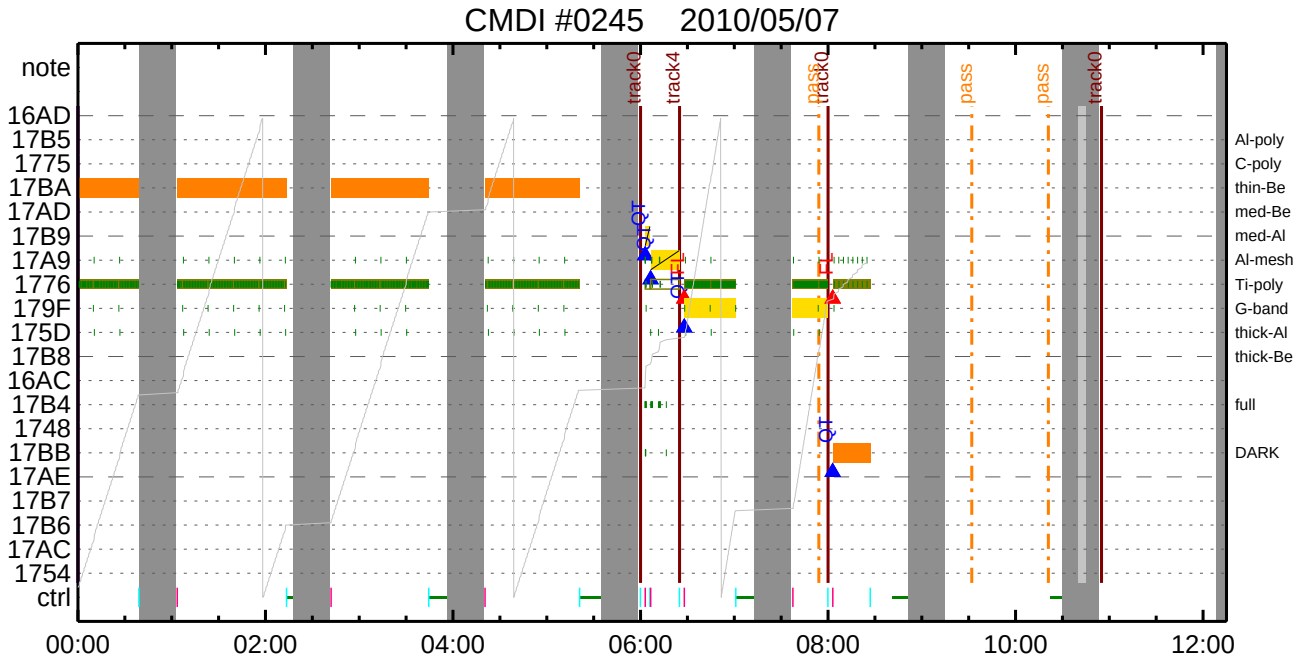
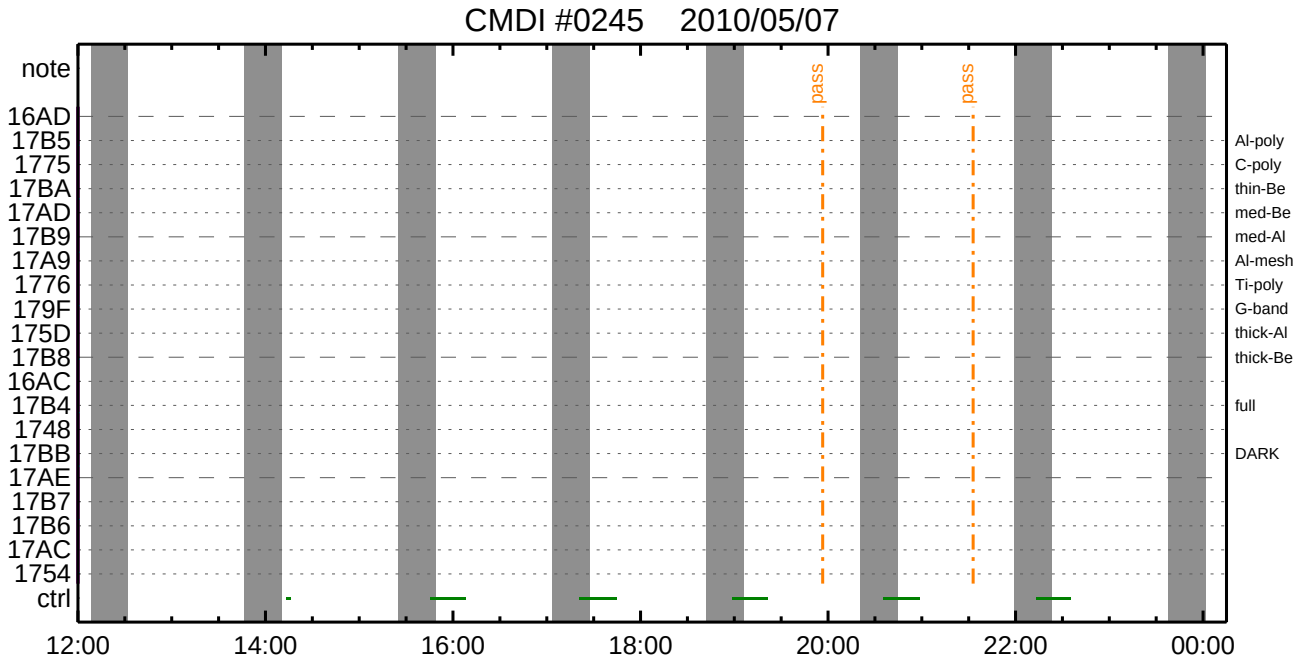
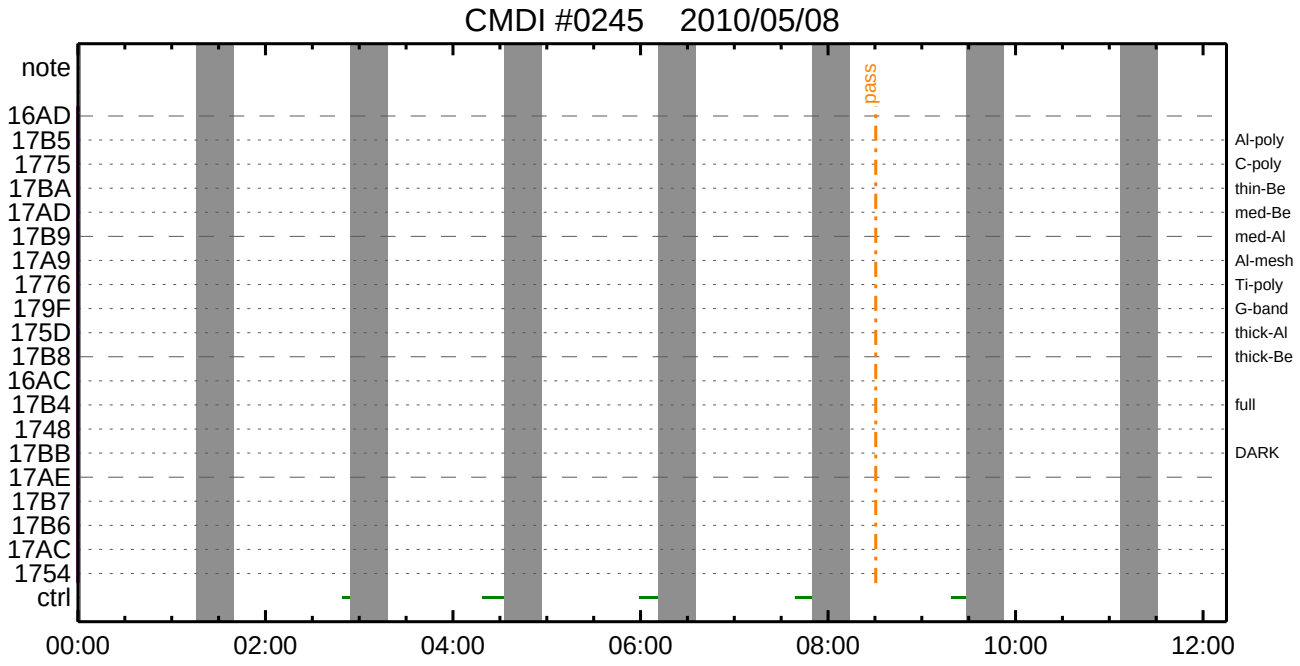
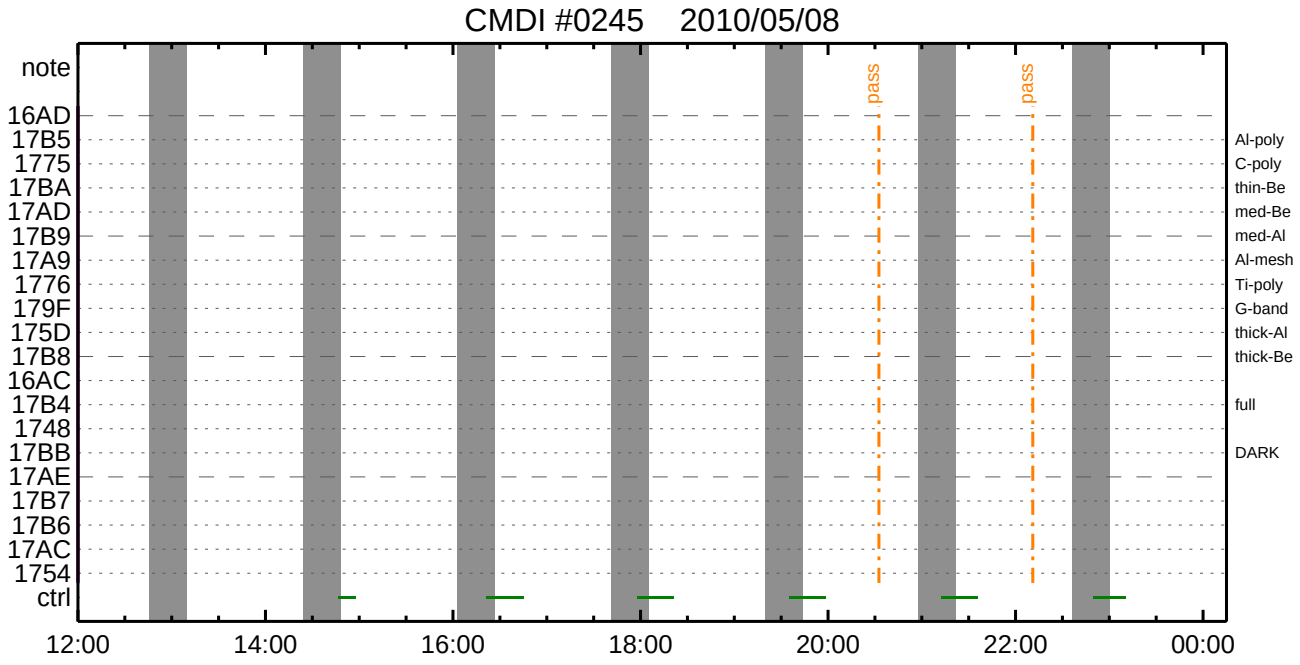
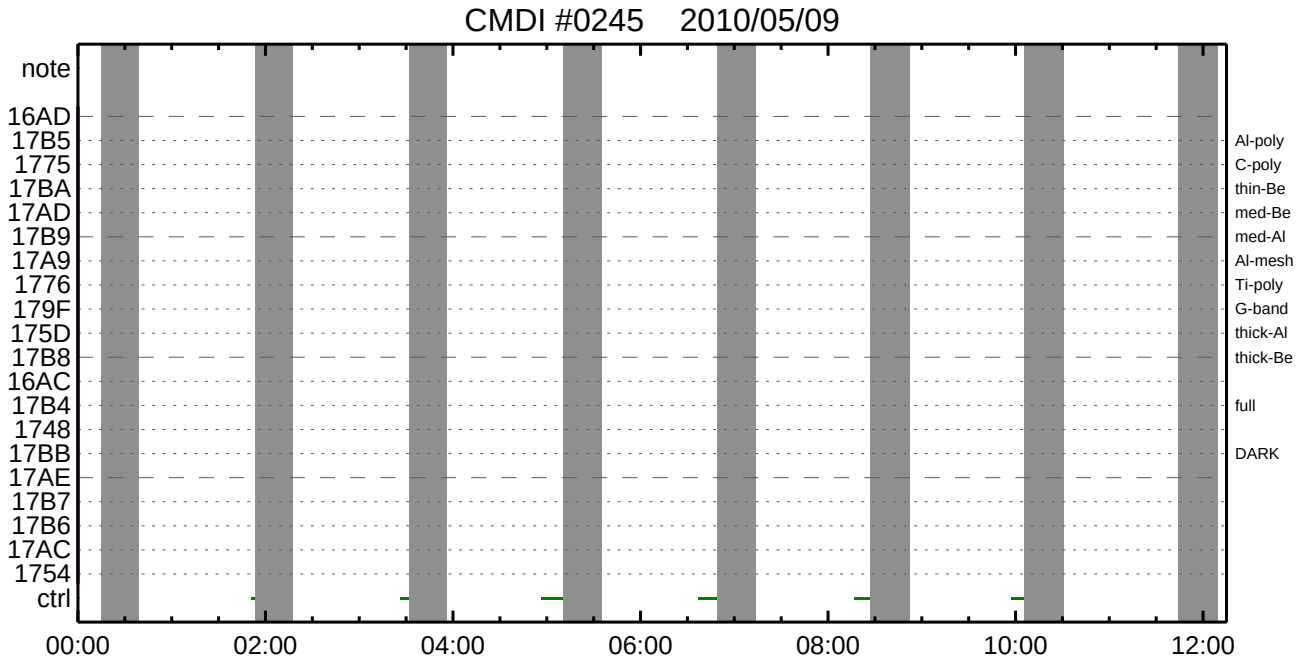
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK

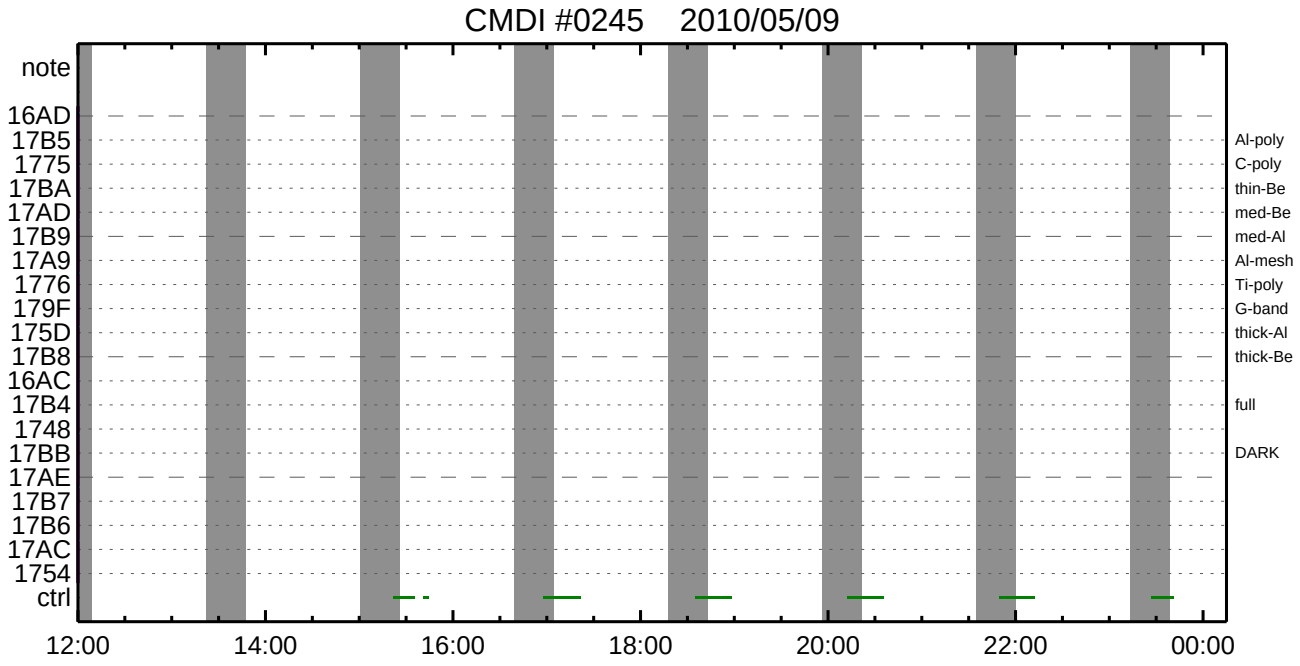

CMDI #0245 2010/05/07
note
16AD
17B5
1775
17BA
17AD
17B9
17A9
1776
179F
175D
17B8
16AC
17B4
1748
17BB
17AE
17B7
17B6
17AC
1754
ctrl
track0
track4
pass
track0
pass
pass
track0
QT
QT
FL
FL
00:00
02:00
04:00
06:00
08:00
10:00
12:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK

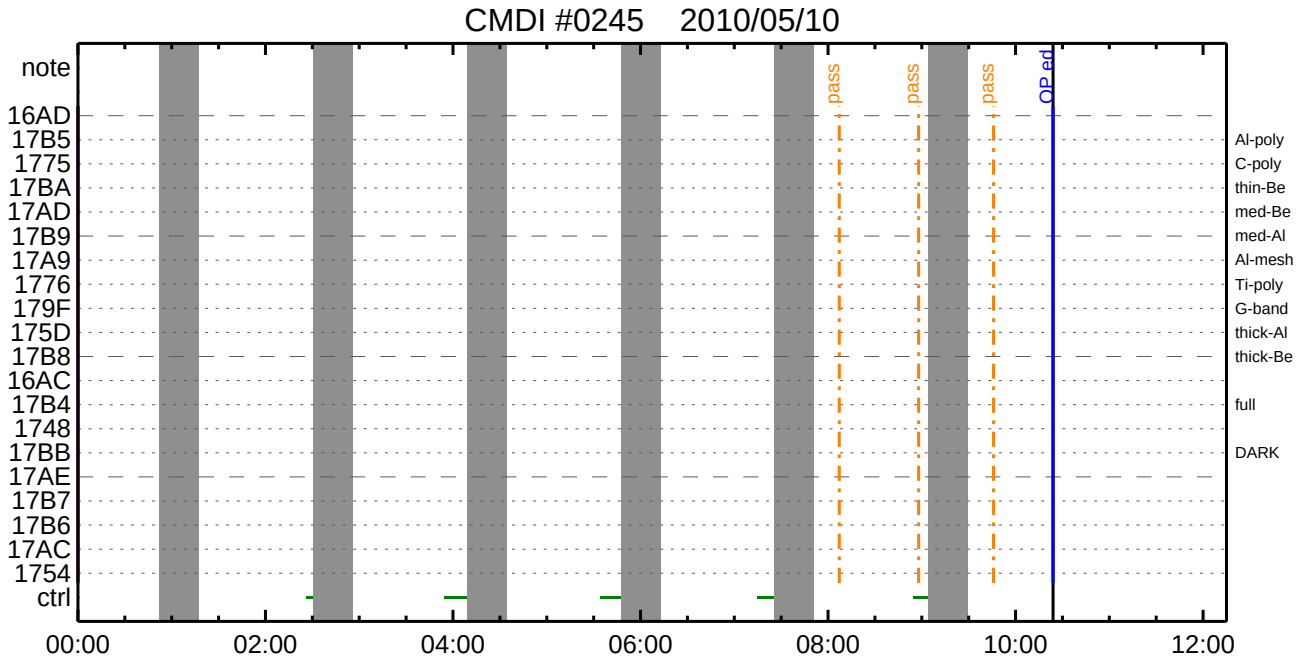

CMDI #0245 2010/05/07
note
16AD
17B5
1775
17BA
17AD
17B9
17A9
1776
179F
175D
17B8
16AC
17B4
1748
17BB
17AE
17B7
17B6
17AC
1754
ctrl
pass
pass
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK

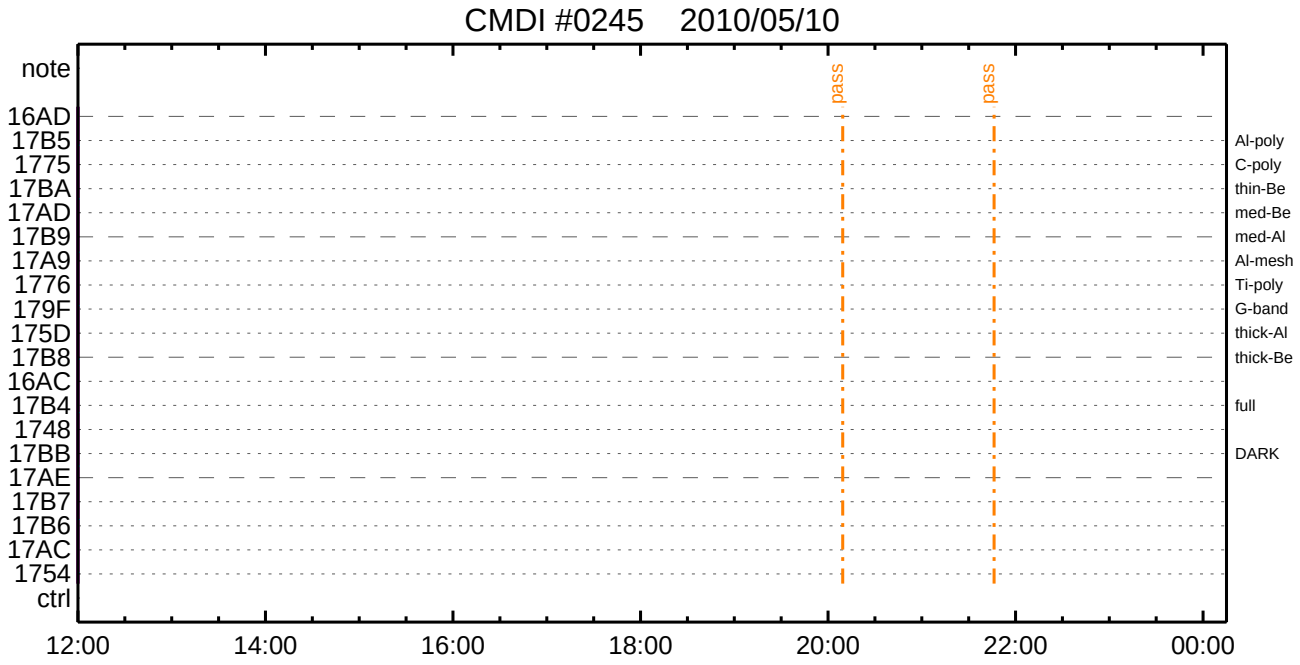

CMDI #0245 2010/05/08
note
16AD
17B5
1775
17BA
17AD
17B9
17A9
1776
179F
175D
17B8
16AC
17B4
1748
17BB
17AE
17B7
17B6
17AC
1754
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0245 2010/05/08
note
16AD
17B5
1775
17BA
17AD
17B9
17A9
1776
179F
175D
17B8
16AC
17B4
1748
17BB
17AE
17B7
17B6
17AC
1754
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0245 2010/05/09
note
16AD
17B5
1775
17BA
17AD
17B9
17A9
1776
179F
175D
17B8
16AC
17B4
1748
17BB
17AE
17B7
17B6
17AC
1754
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0245 2010/05/09
note
16AD
17B5
1775
17BA
17AD
17B9
17A9
1776
179F
175D
17B8
16AC
17B4
1748
17BB
17AE
17B7
17B6
17AC
1754
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0245 2010/05/10
note
16AD
17B5
1775
17BA
17AD
17B9
17A9
1776
179F
175D
17B8
16AC
17B4
1748
17BB
17AE
17B7
17B6
17AC
1754
ctrl
pass
pass
pass
OP end
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0245 2010/05/10
note
16AD
17B5
1775
17BA
17AD
17B9
17A9
1776
179F
175D
17B8
16AC
17B4
1748
17BB
17AE
17B7
17B6
17AC
1754
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-339 2010-05-05 12:59:57 205 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0016     C. ****************************
0017     C.
0018   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0019   . S. OP    op-339:OP
0020     ()
0021   . S. OG    og-339:OG
0022     ()
0023     C.
0024   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0025     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0026   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027     BC       (20 00 7f 01 02)
0028     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0029     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0030     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0031     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0032     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0033   +. DC 01-22 DHU_MODE_CHNG
0034     BC       (07 0b f8)
0035     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0036     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0037     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0038     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0039     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0040   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0041     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0042   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0043     C.
0044     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0045   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046     BC       (20 80 7f 01 02)
0047     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0048     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0049     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0050     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0051     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0052   +. DC 01-22 DHU_MODE_CHNG
0053     BC       (07 0b f8)
0054     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0055     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0056     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0057     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0058     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0059   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0060     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0061   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0062     C.
0063     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065     BC       (21 00 41 01 02)
0066     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0067     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0068     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0069     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0070     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0071   +. DC 01-22 DHU_MODE_CHNG
0072     BC       (07 0b f8)
0073     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0074     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0075     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0076     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0077     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0078   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0079     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0080   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0081     C.
0082   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0083     C. DHU¥â¡¼¥É¡Ê¼ý¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0084   +. DC 01-22 DHU_MODE_CHNG
0085     BC       (02 0a f8)
0086     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0087     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0088     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0089     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0090     C.
0091   . C. ********************************
0092     C. TI-CMD SET (OPOG STOP/COPY/START)
0093     C. ********************************
0094     C.
0095   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
```

```
0096     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Î¤¦¤³¤È¡£
0097     C.
0098   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0099   +. TI 2010-05-05 09:26:00.0
0100     DC 01-B3 DHU_OP_STOP
0101     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0102     C.
0103   +. TI 2010-05-05 09:26:01.0
0104     DC 01-B4 DHU_OP_COPY
0105     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0106     C.
0107   +. TI 2010-05-05 09:26:01.0
0108     DC 01-B5 DHU_OPOG_COPY
0109     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0110     C.
0111   +. TI 2010-05-05 09:30:59.5
0112     DC 01-B2 DHU_OP_START
0113     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0114     C.
0115     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0116     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0117     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0118     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0119     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0120     C.
0121   . C. ****************************
0122     C. TIÎÎ°è¥À¥ó¥×
0123     C. ****************************
0124     C.
0125     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0126   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127     BC       (03 ab 03 01 02)
0128     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0129     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0130     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0131     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0132     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0133   +. DC 01-22 DHU_MODE_CHNG
0134     BC       (07 0b f8)
0135     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0136     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0137     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0138     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0139     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0140     C.
0141   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0142     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0143     C.
0144   . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0145     C.
0146   . C. DHU¥â¡¼¥É¡Ê¼ý¸¸¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0147   +. DC 01-22 DHU_MODE_CHNG
0148     BC       (02 0a f8)
0149     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0150     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0151     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0152     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0153     C.
0154     C. ******************************
0155     C.  SOT TI command set
0156     C. ******************************
0157     C. Execute, after the success of OP upload.
0158   +. TI 2010-05-05 09:30:16.0
0159     DC 07-F0 MDP_SOT_MODE_STBY
0160     BC       (41)
0161   . C. ------------------------------------
0162     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0163     C. ------------------------------------
0164     C. ************ SOT END ***********
0165   . C. Stop EIS observation and temporarily disable EIS mode changes
0166     C.
0167     C.
0168     C. ********** Start EIS operation (TI set) **********
0169     C. Execute, after the success of OP upload.
0170     C. Set EIS TI-commands
0171   +. TI 2010-05-05 09:30:30.0
0172     DC 07-FC EIS_MODE_MANU
0173     BC       (21 02)
0174   +. TI 2010-05-05 09:30:40.0
0175     DC 07-FC EIS_MODE_CHG_DIS
0176     BC       (22)
0177   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0178     C. ************ End EIS operation (TI set) ***********
0179     C.
0180     C.
0181     C.
0182     C. ************ XRT START ***********
0183     C. Execute, after the success of OP upload.
0184   +. TI 2010-05-05 09:30:00.0
0185     DC 07-F0 MDP_XRT_MODE_STBY
0186     BC       (c3)
0187   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0188     C.
0189     C. ************ XRT END ***********
0190     C.
0191   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0192     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0193   . S. DC-BC dcbc-402:DCBC
```

```
0194     (MDP_known_event)
0195     C.
0196     C.
0197   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0198   . S. DC-BC dcbc-153:DCBC
0199     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200     C.
0201     C.
0202   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0203     C.
0204   . C. ********** LOS **********
0205     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-340 2010-05-05 12:59:57 175 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106   +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113   +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                      Result = OK [ ]
0121     C.
0122   +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                         <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127   +. DC 02-8E AOCU_ORB_UPD
0128   . C.
0129   . C. ***** AOCS Commands (Orbital Element Update) *****
0130     C. Update the orbital element
0131   +. DC 02-50 AOCU_ORB_PRPGT_START
0132     BC       (16)
0133   +  DC 02-8E AOCU_ORB_UPD
0134     C.
0135     C.       <A_ORB>[ORBIT] EPC = 4579962.6 +- 1.0 (s) [ ]
0136     C.
0137   . C.
0138   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139   +. DC 07-FC EIS_MODE_MANU
0140     BC       (21 02)
0141   . C. Verify EIS in MANUAL mode
0142   . C. Estimated OBSTBL upload time is 1m14s
0143     C. ********************************
0144     C. EIS START OBSTBL LOAD
0145     C. ********************************
0146   . S. RAM   ram-820:EIS_OBSTBL
0147     ()
0148   +. DC 07-FC EIS_DUMP_OBSTBL
0149     BC       (07 07 07 00 00 70 00)
0150     C.
0151     C. Execute, after the success of OBSTBL upload.
0152     C. Set EIS TI-commands
0153   +. TI 2010-05-05 09:30:50.0
0154     DC 07-FC EIS_MODE_CHG_ENA
0155     BC       (20)
0156   . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0157     C. ********************************
0158     C. EIS END OBSTBL LOAD
0159     C. ********************************
0160     C.
0161   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0162     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0163   . S. DC-BC dcbc-402:DCBC
0164     (MDP_known_event)
0165     C.
0166     C.
0167   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0168   . S. DC-BC dcbc-153:DCBC
0169     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0170     C.
0171     C.
0172   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0173     C.
0174   . C. ********** LOS **********
0175     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-341 2010-05-05 12:59:57 164 33 SOLAR-B MAIN //
0001     C.
0002    . C. *********** AOS ***********
0003     C.
0004    . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007    +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009    . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011    +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014    . C. ********************************
0015     C.  SOT table upload
0016     C. ********************************
0017    . C. < Stop FG table >
0018    +. DC 07-F0 MDP_FG_CTRL_MANU
0019     BC       (51)
0020    . C. ------------------------------------
0021     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0022     C. ------------------------------------
0023     C.
0024    . C. <Upload FG Observation Table>
0025    . S. RAM   ram-262:MDP_OBS_F
0026     ()
0027     C.
0028    . C. < Dump RAMID=MDP_OBS_F >
0029    +. DC 07-F0 MDP_DUMP_FGTBL
0030     BC       (82 07 00 00 00 38 b8)
0031     C. ------------------------------------
0032     C.  MDP_OBS_F verify = OK/NG        [ ]
0033     C. ------------------------------------
0034     C.
0035    . C. < Stop SP table >
0036    +. DC 07-F0 MDP_SP_CTRL_MANU
0037     BC       (61)
0038     C. ------------------------------------
0039     C.  MDP_SP_CTRL_MODE      = MANU    [ ]
0040     C. ------------------------------------
0041     C.
0042    . C. <Upload SP Observation Table>
0043    . S. RAM   ram-285:MDP_OBS_S
0044     ()
0045     C.
0046    . C. < Dump RAMID=MDP_OBS_S >
0047    +. DC 07-F0 MDP_DUMP_SPTBL
0048     BC       (83 07 00 00 00 38 b8)
0049     C. ------------------------------------
0050     C.  MDP_OBS_S verify = OK/NG        [ ]
0051     C. ------------------------------------
0052     C.
0053    . C. < Upload DPL table >
0054     C.
0055     C. ¥¢¥Ã¥×¥í¡¼¥É¤ÎÁ°¤ËSTS_CHK¤òOFF¤Ë¤¹¤ë
0056     C.
0057    . S. RAM   ram-271:MDP_DPL
0058     ()
0059     C.
0060    . C. < Dump RAMID=MDP_DPL >
0061    +. DC 07-F0 MDP_DUMP_FGTBL
0062     BC       (82 07 00 38 b8 00 40)
0063     C. ------------------------------------
0064     C.  MDP_DPL verify          = OK    [ ]
0065     C. ------------------------------------
0066     C.
0067     C. STS_CHK¤òON¤Ë¤¹¤ë
0068     C.
0069    . C. < Update MDP DSC PAR1 >
0070    +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0071     BC       (4c)
0072     C.  MDP_CMD_CODE          = F04C0700[ ]
0073     C.  MDP_CMD_CNT        (count-up 1) [ ]
0074     C. ------------------------------------
0075     C.
0076    . C.
0077     C. ********************************
0078     C.  SOT TI command set
0079     C. ********************************
0080     C. Execute, after the success of TBL upload.
0081    +. TI 2010-05-05 09:30:18.0
0082     DC 07-F0 MDP_SOT_MODE_OBSV
0083     BC       (40)
0084    . C. ------------------------------------
0085     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0086     C. ------------------------------------
0087     C.
0088     C.
0089     C. ************ XRT START ************
0090     C.
0091    +. DC 07-F0 MDP_XRT_CTRL_MANU
0092     BC       (c1)
0093    +  DC 07-F0 MDP_XRT_MODE_STBY
0094     BC       (c3)
0095    . C. ----------- Success Verify ?             OK / NG____
```

```
0096     C.
0097     C. XRT Obs. Table Upload
0098   . S. RAM   ram-291:MDP_OBS_X
0099     ()
0100     C.
0101  +. DC 07-F0 MDP_DUMP_XRTTBL
0102     BC       (84 07 00 00 00 3a d4)
0103   . C. ---------- Comparison Check ?                        OK / ERR ____
0104     C.
0105     C.
0106  +. DC 07-F0 MDP_XRT_ROI_SET
0107     BC       (cd 01 b1 b1 04 04)
0108  +  DC 07-F0 MDP_XRT_ROI_SET
0109     BC       (cd 02 b1 b1 08 08)
0110  +  DC 07-F0 MDP_XRT_ROI_SET
0111     BC       (cd 03 b1 b1 08 08)
0112  +  DC 07-F0 MDP_XRT_ROI_SET
0113     BC       (cd 04 b1 b1 06 06)
0114  +  DC 07-F0 MDP_XRT_ROI_SET
0115     BC       (cd 06 80 80 06 06)
0116  +  DC 07-F0 MDP_XRT_ROI_SET
0117     BC       (cd 07 80 80 08 08)
0118  +  DC 07-F0 MDP_XRT_ROI_SET
0119     BC       (cd 08 80 80 0c 0c)
0120  +  DC 07-F0 MDP_XRT_ROI_SET
0121     BC       (cd 09 80 80 20 20)
0122  +  DC 07-F0 MDP_XRT_ROI_SET
0123     BC       (cd 0a 80 80 20 08)
0124  +  DC 07-F0 MDP_XRT_ROI_SET
0125     BC       (cd 0b 80 80 08 20)
0126  +  DC 07-F0 MDP_XRT_ROI_SET
0127     BC       (cd 0c 80 60 20 18)
0128  +  DC 07-F0 MDP_XRT_ROI_SET
0129     BC       (cd 0d a0 80 18 20)
0130  +  DC 07-F0 MDP_XRT_ROI_SET
0131     BC       (cd 0e 85 83 06 06)
0132  +  DC 07-F0 MDP_XRT_ROI_SET
0133     BC       (cd 0f 80 80 06 06)
0134  +  DC 07-F0 MDP_XRT_AEC_RESET
0135     BC       (d0)
0136   . C. ---------- Success Verify ?            OK / NG ____
0137     C.
0138     C.
0139   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0140     C.
0141  +. DC 07-F0 MDP_XRT_MODE_OBSV
0142     BC       (c2)
0143  +. TI 2010-05-05 09:30:02.0
0144     DC 07-F0 MDP_XRT_MODE_OBSV
0145     BC       (c2)
0146   . C. ---------- Success Verify ?            OK / NG ____
0147     C.
0148     C. ************ XRT END ***********
0149     C.
0150   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0151     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0152   . S. DC-BC dcbc-402:DCBC
0153     (MDP_known_event)
0154     C.
0155     C.
0156   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0157   . S. DC-BC dcbc-153:DCBC
0158     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0159     C.
0160     C.
0161   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0162     C.
0163   . C. *********** LOS ***********
0164     C.
```

```
*** OP Sequence for XRT ***

2010/05/05  09:40:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/05  09:40:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/05/05  09:41:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 03 00 00 00 00
2010/05/05  09:41:16.0  XRT_AEC_RESET_428_OG [0x1ac]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2010/05/05  09:41:18.0  XRT_FLD_ENA_402_OG [0x192]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/05/05  09:41:20.0  XRT_FLD_RESET_403_OG [0x193]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/05/05  09:41:22.0  XRT_FLRCTRL_ENA_418_OG [0x1a2]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/05/05  09:41:24.0  XRT_ARS_DIS_405_OG [0x195]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/05  09:43:58.0  XRT_QT_PROG_SET_429_OG [0x1ad]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2010/05/05  09:44:00.0  XRT_FL_PROG_SET_442_OG [0x1ba]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0d
2010/05/05  09:44:02.5  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/05  10:47:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/05  11:17:00.0  XRT_Custom_410_OG [0x19a]
2010/05/05  11:18:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/05  12:32:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/05  12:45:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 bb 8e 30 e5
2010/05/05  12:55:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/05  12:55:32.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/05/05  12:55:52.0  XRT_AEC_RESET_428_OG [0x1ac]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2010/05/05  12:55:54.0  XRT_FLD_ENA_402_OG [0x192]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/05/05  12:55:56.0  XRT_FLD_RESET_403_OG [0x193]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/05/05  12:55:58.0  XRT_FLRCTRL_ENA_418_OG [0x1a2]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/05/05  12:56:00.0  XRT_ARS_DIS_405_OG [0x195]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/05  12:58:34.0  XRT_QT_PROG_SET_419_OG [0x1a3]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 06
2010/05/05  12:58:36.0  XRT_FL_PROG_SET_442_OG [0x1ba]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0d
2010/05/05  12:58:38.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/05  14:11:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/05  14:43:00.0  XRT_Custom_410_OG [0x19a]
2010/05/05  14:44:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/05  15:49:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/05  16:31:30.0  XRT_Custom_410_OG [0x19a]
2010/05/05  16:32:30.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/05  17:28:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/05  18:08:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/05  18:08:26.0  XRT_FOCUS_POSITION_412_OG [0x19c]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/05/05  18:08:30.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 00 00 00 00
2010/05/05  18:08:46.0  XRT_FLD_DIS_413_OG [0x19d]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2010/05/05  18:08:48.0  XRT_FLRCTRL_DIS_414_OG [0x19e]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/05/05  18:08:50.0  XRT_ARS_DIS_415_OG [0x19f]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/05  18:11:28.0  XRT_QT_PROG_SET_438_OG [0x1b6]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2010/05/05  18:11:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/05  18:18:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/05  18:18:26.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/05/05  18:18:30.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 ac 00 00 00
2010/05/05  18:18:46.0  XRT_QT_PROG_SET_430_OG [0x1ae]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 09
2010/05/05  18:18:48.0  XRT_ARS_DIS_425_OG [0x1a9]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/05  18:18:50.0  XRT_FLD_DIS_426_OG [0x1aa]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
```

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2010/05/05  18:18:52.0  XRT_FLRCTRL_DIS_407_OG [0x197]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/05/05  18:33:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/05  20:33:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/05  20:33:26.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/05/05  20:33:30.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM              5 02-76 00 00 00 54 00
2010/05/05  20:33:46.0  XRT_QT_PROG_SET_424_OG [0x1a8]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 14
2010/05/05  20:33:48.0  XRT_ARS_DIS_425_OG [0x1a9]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/05  20:33:50.0  XRT_FLD_DIS_426_OG [0x1aa]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2010/05/05  20:33:52.0  XRT_FLRCTRL_DIS_407_OG [0x197]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/05/05  20:48:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/05  22:40:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM              5 02-76 01 00 00 00 00
2010/05/05  22:58:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/05  22:58:02.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/05/05  22:58:22.0  XRT_AEC_RESET_428_OG [0x1ac]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2010/05/05  22:58:24.0  XRT_FLD_RESET_403_OG [0x193]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/05/05  22:58:26.0  XRT_FLD_ENA_449_OG [0x1c1]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/05/05  22:58:28.0  XRT_FLRCTRL_ENA_427_OG [0x1ab]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/05/05  22:58:30.0  XRT_ARS_DIS_415_OG [0x19f]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/05  23:01:08.0  XRT_QT_PROG_SET_433_OG [0x1b1]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 02
2010/05/05  23:01:10.0  XRT_FL_PROG_SET_442_OG [0x1ba]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0d
2010/05/05  23:01:12.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/06  00:01:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/06  00:25:00.0  XRT_Custom_410_OG [0x19a]
2010/05/06  00:26:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/06  01:38:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/06  02:03:30.0  XRT_Custom_410_OG [0x19a]
2010/05/06  02:04:30.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/06  03:13:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/06  03:42:00.0  XRT_Custom_410_OG [0x19a]
2010/05/06  03:43:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/06  04:42:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/06  05:20:30.0  XRT_Custom_410_OG [0x19a]
2010/05/06  05:21:30.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/06  05:55:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/06  05:55:26.0  XRT_FOCUS_POSITION_412_OG [0x19c]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/05/06  05:55:30.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 00 00 00 00
2010/05/06  05:55:46.0  XRT_FLD_DIS_413_OG [0x19d]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2010/05/06  05:55:48.0  XRT_FLRCTRL_DIS_414_OG [0x19e]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/05/06  05:55:50.0  XRT_ARS_DIS_415_OG [0x19f]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/06  05:58:28.0  XRT_QT_PROG_SET_438_OG [0x1b6]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2010/05/06  05:58:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/06  06:01:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/06  06:01:32.0  XRT_FOCUS_POSITION_412_OG [0x19c]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/05/06  06:01:52.0  XRT_FLD_DIS_413_OG [0x19d]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2010/05/06  06:01:54.0  XRT_FLRCTRL_DIS_414_OG [0x19e]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/05/06  06:01:56.0  XRT_ARS_DIS_446_OG [0x1be]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/06  06:01:58.0  XRT_QT_PROG_SET_443_OG [0x1bb]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0e
2010/05/06  06:02:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/06  06:15:24.0  XRT_CTRL_MANU_400_OG [0x190]
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                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/06  06:15:26.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/05/06  06:15:30.0  AOCS_ORe-point_Start_1_OG [0x097]
                                        AOCU_NM              5 02-76 03 00 00 00 00
2010/05/06  06:15:46.0  XRT_AEC_RESET_428_OG [0x1ac]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2010/05/06  06:15:48.0  XRT_FLD_ENA_402_OG [0x192]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2010/05/06  06:15:50.0  XRT_FLD_RESET_403_OG [0x193]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/05/06  06:15:52.0  XRT_FLRCTRL_ENA_418_OG [0x1a2]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/05/06  06:15:54.0  XRT_ARS_DIS_405_OG [0x195]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/06  06:18:28.0  XRT_QT_PROG_SET_429_OG [0x1ad]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2010/05/06  06:18:30.0  XRT_FL_PROG_SET_442_OG [0x1ba]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0d
2010/05/06  06:18:32.5  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/06  06:23:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/06  06:59:00.0  XRT_Custom_410_OG [0x19a]
2010/05/06  07:00:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/06  08:03:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/06  08:37:30.0  XRT_Custom_410_OG [0x19a]
2010/05/06  08:38:30.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/06  09:43:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/06  10:16:00.0  XRT_Custom_410_OG [0x19a]
2010/05/06  10:17:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/06  11:31:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/06  11:54:30.0  XRT_Custom_410_OG [0x19a]
2010/05/06  11:55:30.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/06  11:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/06  11:59:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/05/06  12:00:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                        AOCU_NM              5 02-76 04 00 00 00 00
2010/05/06  12:00:16.0  XRT_AEC_RESET_428_OG [0x1ac]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2010/05/06  12:00:18.0  XRT_FLD_ENA_402_OG [0x192]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2010/05/06  12:00:20.0  XRT_FLRCTRL_ENA_418_OG [0x1a2]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/05/06  12:00:22.0  XRT_FLD_RESET_403_OG [0x193]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/05/06  12:00:24.0  XRT_ARS_DIS_405_OG [0x195]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/06  12:02:58.0  XRT_QT_PROG_SET_431_OG [0x1af]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2010/05/06  12:03:00.0  XRT_FL_PROG_SET_442_OG [0x1ba]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0d
2010/05/06  12:03:02.5  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/06  13:09:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/06  13:33:00.0  XRT_Custom_410_OG [0x19a]
2010/05/06  13:34:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/06  14:48:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/06  15:22:00.0  XRT_Custom_410_OG [0x19a]
2010/05/06  15:23:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/06  16:26:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/06  17:08:30.0  XRT_Custom_410_OG [0x19a]
2010/05/06  17:09:30.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/06  17:54:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/06  17:54:56.0  XRT_FOCUS_POSITION_412_OG [0x19c]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/05/06  17:55:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2010/05/06  17:55:16.0  XRT_FLD_DIS_413_OG [0x19d]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2010/05/06  17:55:18.0  XRT_FLRCTRL_DIS_414_OG [0x19e]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/05/06  17:55:20.0  XRT_ARS_DIS_415_OG [0x19f]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/06  17:57:58.0  XRT_QT_PROG_SET_438_OG [0x1b6]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2010/05/06  17:58:00.0  XRT_CTRL_AUTO_408_OG [0x198]
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                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/06  18:05:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                        AOCU_NM              5 02-76 04 00 00 00 00
2010/05/06  18:05:00.5  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/06  18:45:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/06  18:45:02.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/05/06  18:45:22.0  XRT_AEC_RESET_428_OG [0x1ac]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2010/05/06  18:45:24.0  XRT_FLD_ENA_402_OG [0x192]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2010/05/06  18:45:26.0  XRT_FLRCTRL_ENA_418_OG [0x1a2]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/05/06  18:45:28.0  XRT_FLD_RESET_403_OG [0x193]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/05/06  18:45:30.0  XRT_ARS_DIS_405_OG [0x195]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/06  18:48:04.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2010/05/06  18:48:06.0  XRT_FL_PROG_SET_442_OG [0x1ba]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0d
2010/05/06  18:48:08.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/06  19:43:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/06  20:22:00.5  XRT_Custom_410_OG [0x19a]
2010/05/06  20:23:00.5  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/06  21:22:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/06  21:59:00.0  XRT_Custom_410_OG [0x19a]
2010/05/06  22:00:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/06  23:00:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/06  23:31:30.0  XRT_Custom_410_OG [0x19a]
2010/05/06  23:32:30.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/07  00:39:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/07  01:02:30.0  XRT_Custom_410_OG [0x19a]
2010/05/07  01:03:30.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/07  02:13:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/07  02:41:00.0  XRT_Custom_410_OG [0x19a]
2010/05/07  02:42:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/07  03:44:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/07  04:19:30.0  XRT_Custom_410_OG [0x19a]
2010/05/07  04:20:30.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/07  05:21:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/07  05:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/07  05:59:56.0  XRT_FOCUS_POSITION_412_OG [0x19c]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/05/07  06:00:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2010/05/07  06:00:16.0  XRT_FLD_DIS_413_OG [0x19d]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2010/05/07  06:00:18.0  XRT_FLRCTRL_DIS_414_OG [0x19e]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/05/07  06:00:20.0  XRT_ARS_DIS_415_OG [0x19f]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/07  06:02:58.0  XRT_QT_PROG_SET_438_OG [0x1b6]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2010/05/07  06:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/07  06:06:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/07  06:06:02.0  XRT_FOCUS_POSITION_412_OG [0x19c]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/05/07  06:06:22.0  XRT_FLD_DIS_413_OG [0x19d]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2010/05/07  06:06:24.0  XRT_FLRCTRL_DIS_414_OG [0x19e]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/05/07  06:06:26.0  XRT_ARS_DIS_446_OG [0x1be]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/07  06:06:28.0  XRT_QT_PROG_SET_443_OG [0x1bb]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0e
2010/05/07  06:06:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/07  06:24:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/07  06:24:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/05/07  06:25:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                        AOCU_NM              5 02-76 04 00 00 00 00
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2010/05/07  06:25:16.0  XRT_AEC_RESET_428_OG [0x1ac]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2010/05/07  06:25:18.0  XRT_FLD_ENA_402_OG [0x192]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/05/07  06:25:20.0  XRT_FLRCTRL_ENA_418_OG [0x1a2]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/05/07  06:25:22.0  XRT_FLD_RESET_403_OG [0x193]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/05/07  06:25:24.0  XRT_ARS_DIS_405_OG [0x195]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/07  06:27:58.0  XRT_QT_PROG_SET_431_OG [0x1af]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2010/05/07  06:28:00.0  XRT_FL_PROG_SET_442_OG [0x1ba]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0d
2010/05/07  06:28:02.5  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/07  07:01:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/07  07:36:30.0  XRT_Custom_410_OG [0x19a]
2010/05/07  07:37:30.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/07  07:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/07  07:59:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/05/07  08:00:00.0  AOCS_ORe-point_Start_8_OG [0x09e]
                                AOCU_NM              5 02-76 00 33 02 bf 65
2010/05/07  08:00:16.0  XRT_AEC_RESET_428_OG [0x1ac]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2010/05/07  08:00:18.0  XRT_FLD_ENA_402_OG [0x192]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/05/07  08:00:20.0  XRT_FLD_RESET_403_OG [0x193]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/05/07  08:00:22.0  XRT_FLRCTRL_ENA_418_OG [0x1a2]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/05/07  08:00:24.0  XRT_ARS_DIS_405_OG [0x195]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/07  08:02:58.0  XRT_QT_PROG_SET_419_OG [0x1a3]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 06
2010/05/07  08:03:00.0  XRT_FL_PROG_SET_442_OG [0x1ba]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0d
2010/05/07  08:03:02.5  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/07  08:27:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/07  10:55:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 00 00 00 00
```