XRT Timeline to be uploaded on 2010/05/11

Period: 2010/05/11 10:30:00 - 2010/05/15 10:48:00

* * * * * **Normal mode** * * * * *

XOB #179A: Full Sun - Al/Mesh - Ti/Poly - 4x4 - AEC1 - 5 min cadence

Term	Pointing (x, y)	Comment
05/11 10:43:00 - 05/11 15:59:54	Fixed (0.0, -940.0)	# OP start + 10min | HOP81 South Pole (Day 2/2)

PROG= 11 Inf.-time(s)
Subr= 1 1-time(s) 2.0sec
Seqn= 31 1-time(s) 300.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	16ms	Obs	4x4	2048x2048 (1024, 1024)	Q=95	1	0 2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	63ms	Obs	4x4	2048x2048 (1024, 1024)	Q=95	1	0 2.0sec

XOB #179F: AR Dynamics- Ti/Poly(384FOV) AEC2_3 Q95, Al-mesh, Ti-poly AEC1, Thick-Al fixed 32, G-band(512FOV) - 30s cadence

Term	Pointing (x, y)	Comment
05/11 16:03:02 - 05/11 17:43:24	Track (339.0, -279.2) @ 05/11 16:00:00	Track AR 11068
05/13 07:21:32 - 05/13 10:51:00	Track (618.1, -290.7) @ 05/13 07:18:30	Track AR 11068

PROG= 12 Inf.-time(s)
Subr= 1 1-time(s) 30.0sec
Seqn= 1 1-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0 2.0sec

Seqn= 42 1-time(s) 40.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	125ms	Obs	1x1	512x512 (1024, 1024)	Q=92	1	0 2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	512x512 (1024, 1024)	Q=92	1	0 2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	32.0s	Obs	1x1	512x512 (1024, 1024)	Q=92	0	0 2.0sec

Subr= 2 30-time(s) 30.0sec
Seqn= 18 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0 2.0sec

Seqn= 43 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0 2.0sec

XOB #175D: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)

Term	Pointing (x, y)	Comment
05/11 17:46:30 - 05/11 17:53:24	Fixed (0.0, 0.0)	synoptic, shifted -16.5 min
05/12 06:03:00 - 05/12 06:09:54	Fixed (0.0, 0.0)	synoptic
05/13 06:31:30 - 05/13 06:38:24	Fixed (0.0, 0.0)	synoptic, shifted 28.5 min

PROG= 19 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 80 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec

Seqn= 82 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0 2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0 2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0 2.0sec

Seqn= 81 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec

Seqn= 4 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec

XOB #17BA: AR Dynamics- Ti/Poly(384FOV) AEC2_3 Q95, Al-mesh, Ti-poly AEC1, Thick-Al fixed 32, G-band(512FOV) - 1min cadence

Term	Pointing (x, y)	Comment
05/11 18:38:02 - 05/12 05:59:54	Track (353.9, -279.7) @ 05/11 17:53:30	# Track AR 11068

PROG= 17 Inf.-time(s)
Subr= 1 1-time(s) 30.0sec
Seqn= 1 1-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	Q=98	0	0 2.0sec

Seqn= 42 1-time(s) 40.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	125ms	Obs	1x1	512x512 (1024, 1024)	Q=92	1	0 2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	512x512 (1024, 1024)	Q=92	1	0 2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	32.0s	Obs	1x1	512x512 (1024, 1024)	Q=92	0	0 2.0sec

Subr= 2 15-time(s) 60.0sec
Seqn= 18 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0 2.0sec

Seqn= 43 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0 2.0sec

XOB #17AC: HOP146 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - 512FOV - Q90

Term	Pointing (x, y)	Comment

05/12 06:13:02 - 05/12 11:50:30 Track (137.8, 200.9) @ 05/12 06:10:00 # Start HOP146, SW source region study - Track North CH

PROG= 02 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 64 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec

Subr= 2 12-time(s) 300.0sec

Seqn= 65 2-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	8.00s	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec

XOB #1563: CCD Monitor During Bakeout - G-band + dark - wide FOV

Term	Pointing (x, y)	Comment
05/12 23:55:40 - 05/13 01:05:00	Track (-0.2, -820.0) @ 05/12 18:16:00	# Start HOP146 - Track edge of South Polar CH
05/13 01:31:30 - 05/13 05:48:00	Track (578.2, -288.6) @ 05/13 01:00:00	Track AR 11068

PROG= 08 Inf.-time(s)

Subr= 1 1-time(s) 600.0sec

Seqn= 39 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	2048x256 (1024, 1024)	DPCM	0	0	2.0sec

XOB #176E: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly

Term	Pointing (x, y)	Comment
05/13 06:41:30 - 05/13 06:48:24	Fixed (-528.4, -528.4)	# XRT Quadra Exp. #1/4

PROG= 16 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 22 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 40 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #176D: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh,Ti/Poly

Term	Pointing (x, y)	Comment
05/13 06:51:30 - 05/13 06:58:24	Fixed (528.4, -528.4)	# XRT Quadra Exp. #2/4

PROG= 01 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 23 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 40 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #176F: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant- Al/mesh, Ti/Poly

Term	Pointing (x, y)	Comment
05/13 07:01:30 - 05/13 07:08:24	Fixed (528.4, 528.4)	# XRT Quadra Exp. #3/4

PROG= 10 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 24 1-time(s) 12.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 40 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1770: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly

Term	Pointing (x, y)	Comment
05/13 07:11:30 - 05/13 07:18:24	Fixed (-528.4, 528.4)	# XRT Quadra Exp. #4/4

PROG= 18 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 25 1-time(s) 12.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec	
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec	
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec	
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec	
Subr= 2 1-time(s) 2.0sec													
Seqn= 40 1-time(s) 2.0sec													
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval	

* * * * * Flare mode * * * * *

XOB #1776: Flare Response short exp at first- Dynamics - Thick-Al - Thick-Be - AEC 1 - 384x384 - Q95

Term	Pointing (x, y)	Comment
05/11 10:43:00 - 05/11 15:59:54	Fixed (0.0, -940.0)	# OP start + 10min | HOP81 South Pole (Day 2/2)
05/11 16:03:02 - 05/11 17:43:24	Track (339.0, -279.2) @ 05/11 16:00:00	Track AR 11068
05/11 18:38:02 - 05/12 05:59:54	Track (353.9, -279.7) @ 05/11 17:53:30	# Track AR 11068
05/13 07:21:32 - 05/13 10:51:00	Track (618.1, -290.7) @ 05/13 07:18:30	Track AR 11068

PROG= 13 1-time(s)													
Subr= 1 2-time(s) 2.0sec													
Seqn= 9 15-time(s) 20.0sec													
Open/thick-Al	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec	
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec	
Seqn= 2 1-time(s) 4.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Subr= 2 8-time(s) 2.0sec													
Seqn= 9 15-time(s) 60.0sec													
Open/thick-Al	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec	
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec	
Seqn= 2 1-time(s) 4.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Subr= 3 25-time(s) 2.0sec													
Seqn= 9 1-time(s) 600.0sec													
Open/thick-Al	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec	
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec	
Seqn= 2 1-time(s) 4.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval	

* * * * * Active Region Search * * * * *

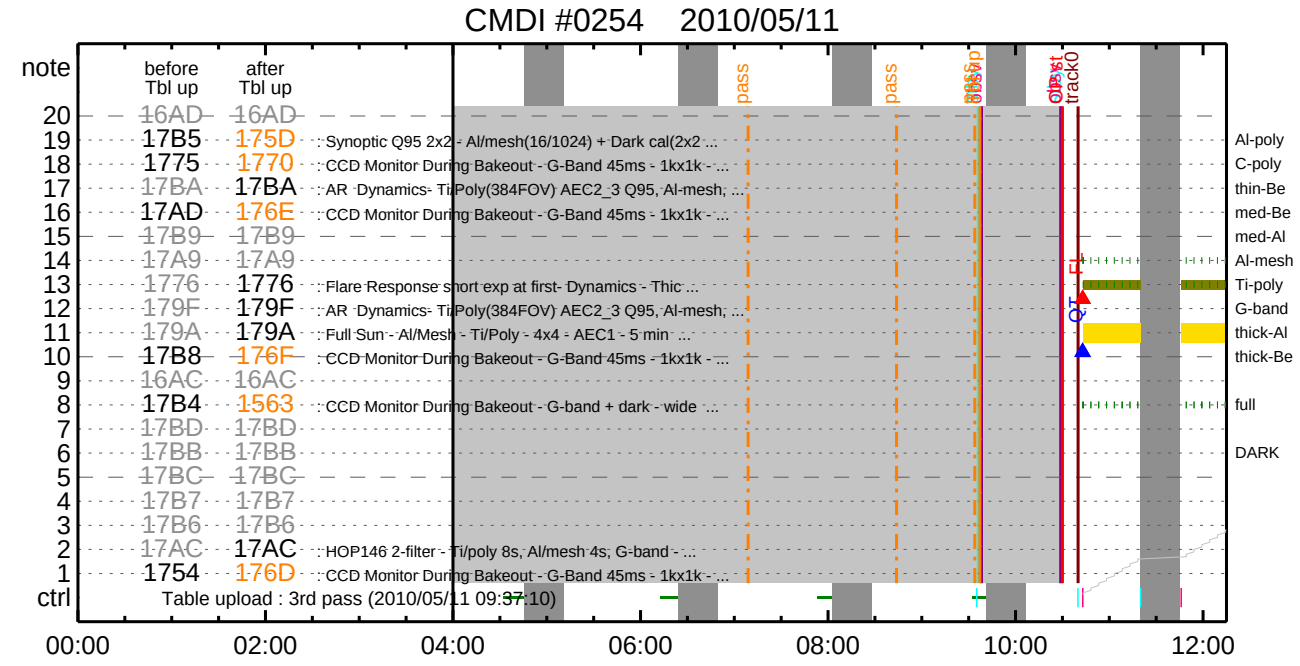
NOT USED

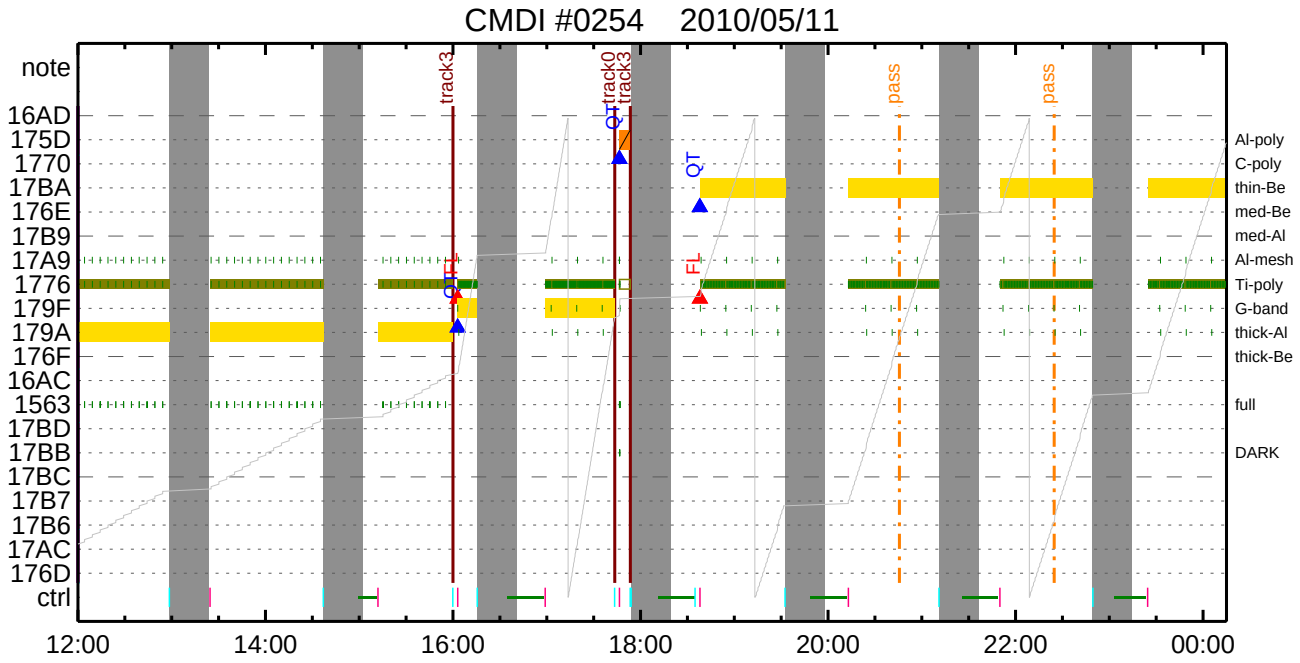
* * * * * Flare Detection * * * * *

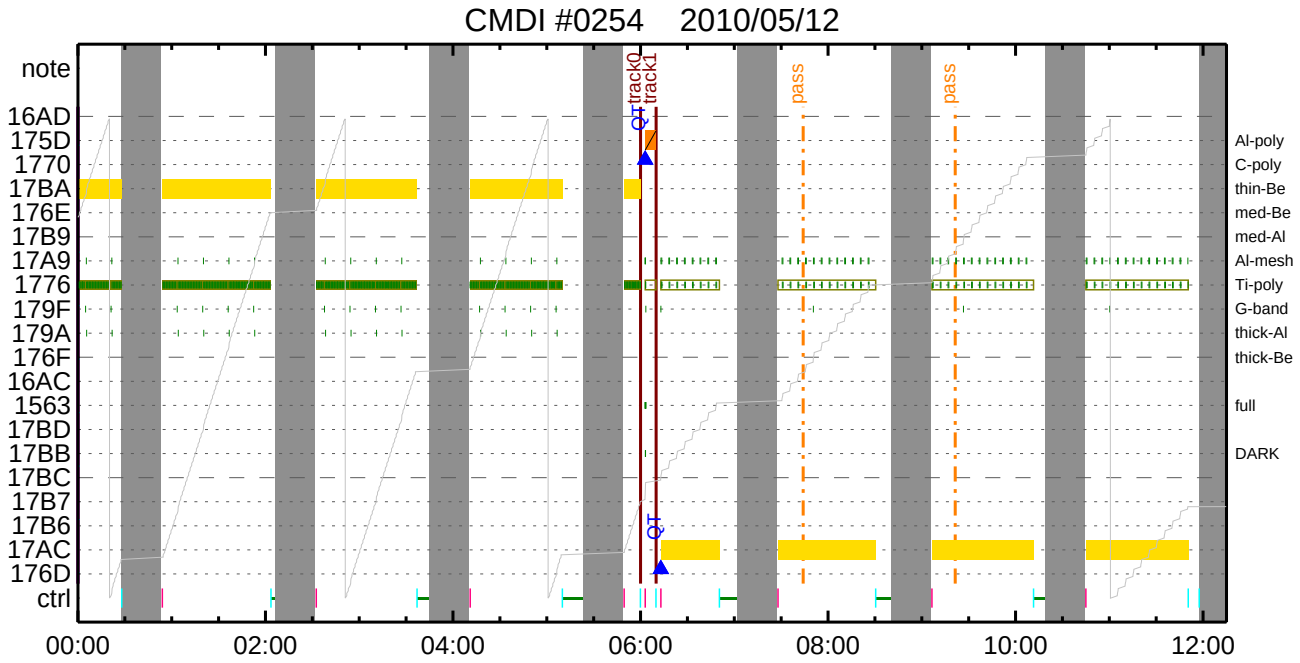
FLD Patrol

Term	Pointing (x, y)	Comment
05/11 10:40:16 - 05/11 17:43:46	Fixed (0.0, -940.0)	# OP start + 10min | HOP81 South Pole (Day 2/2)
05/11 18:35:18 - 05/12 06:00:16	Track (353.9, -279.7) @ 05/11 17:53:30	# Track AR 11068
05/12 06:10:18 - 05/12 23:55:36	Track (137.8, 200.9) @ 05/12 06:10:00	# Start HOP146, SW source region study - Track North CH
05/13 07:18:48 - 05/15 10:48:00	Track (618.1, -290.7) @ 05/13 07:18:30	Track AR 11068

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval	


CMDI #0254 2010/05/11
note
before Tbl up
after Tbl up
16AD 16AD
17B5 175D : Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 ...
1775 1770 : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
17BA 17BA : AR Dynamics- Ti/Poly(384FOV) AEC2_3 Q95, Al-mesh, ...
17AD 176E : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
17B9 17B9
17A9 17A9
1776 1776 : Flare Response short exp at first- Dynamics - Thic ...
179F 179F : AR Dynamics- Ti/Poly(384FOV) AEC2_3 Q95, Al-mesh, ...
179A 179A : Full Sun - Al/Mesh - Ti/Poly - 4x4 - AEC1 - 5 min ...
17B8 176F : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
16AC 16AC
17B4 1563 : CCD Monitor During Bakeout - G-band + dark - wide ...
17BD 17BD
17BB 17BB
17BC 17BC
17B7 17B7
17B6 17B6
17AC 17AC : HOP146 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - ...
1754 176D : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
ctrl
Table upload : 3rd pass (2010/05/11 09:37:10)
pass
pass
track0
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0254 2010/05/11
note
16AD
175D
1770
17BA
176E
17B9
17A9
1776
179F
179A
176F
16AC
1563
17BD
17BB
17BC
17B7
17B6
17AC
176D
ctrl
track3
QT
track0
track3
pass
pass
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0254 2010/05/12
note
16AD
175D
1770
17BA
176E
17B9
17A9
1776
179F
179A
176F
16AC
1563
17BD
17BB
17BC
17B7
17B6
17AC
176D
ctrl
QT
track0
track1
pass
pass
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
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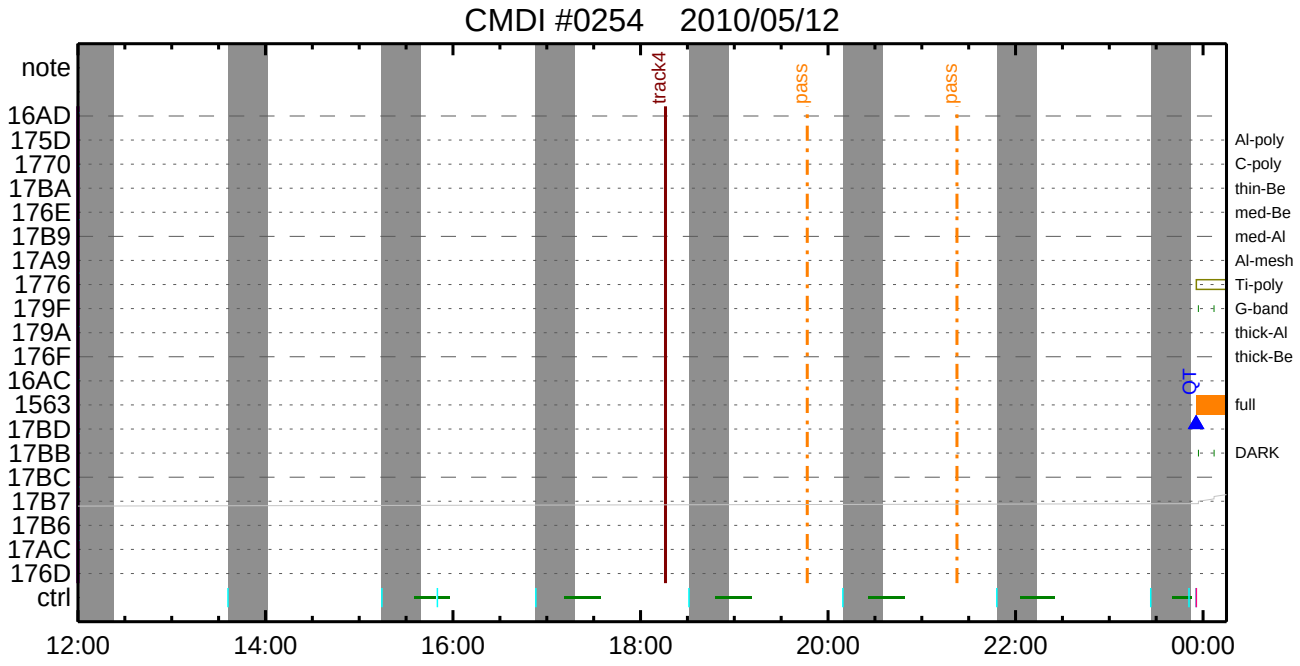
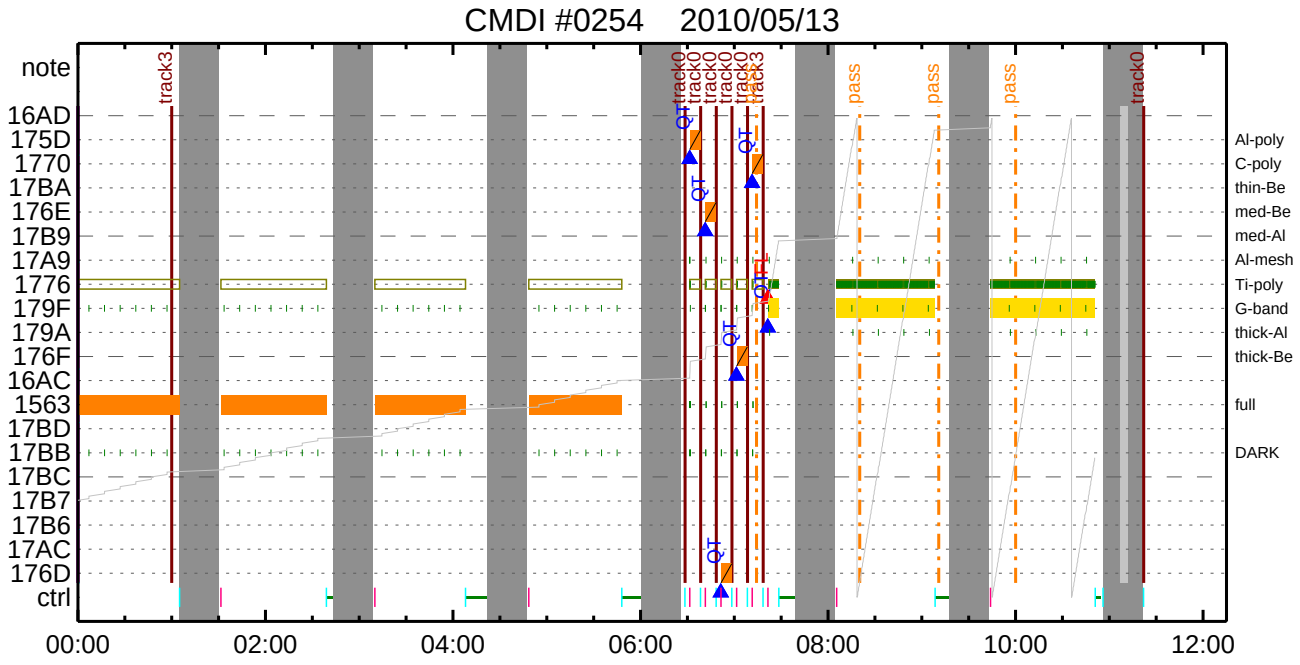
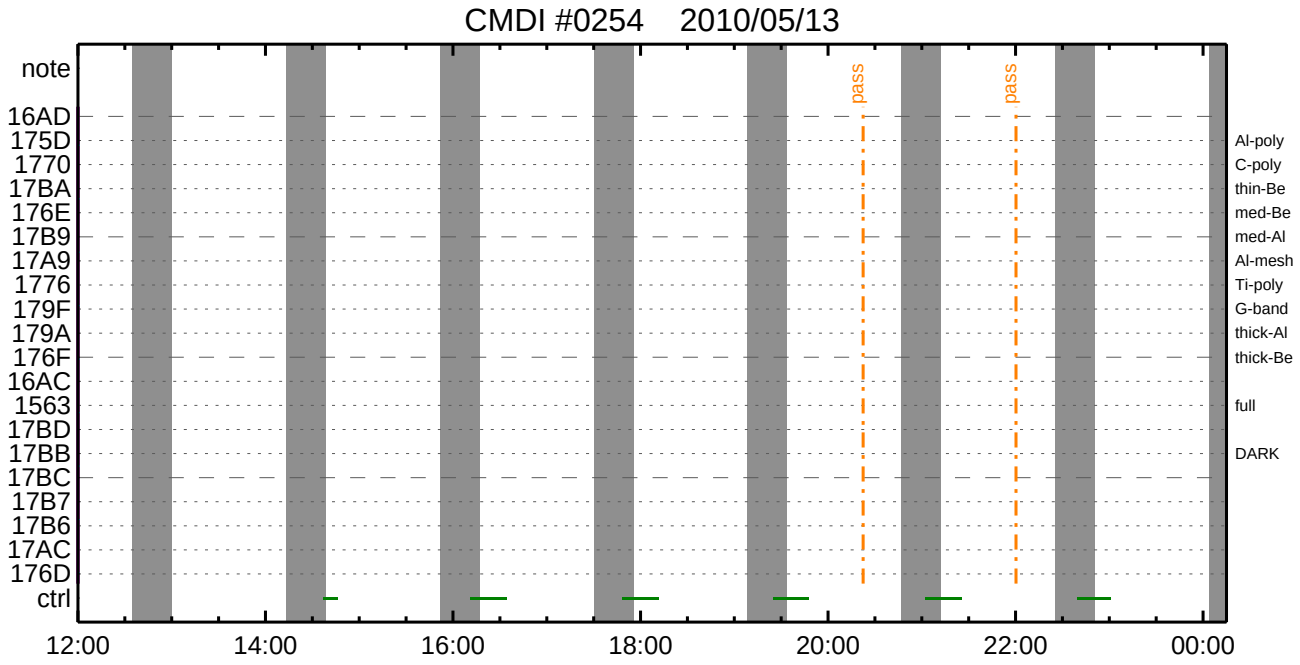
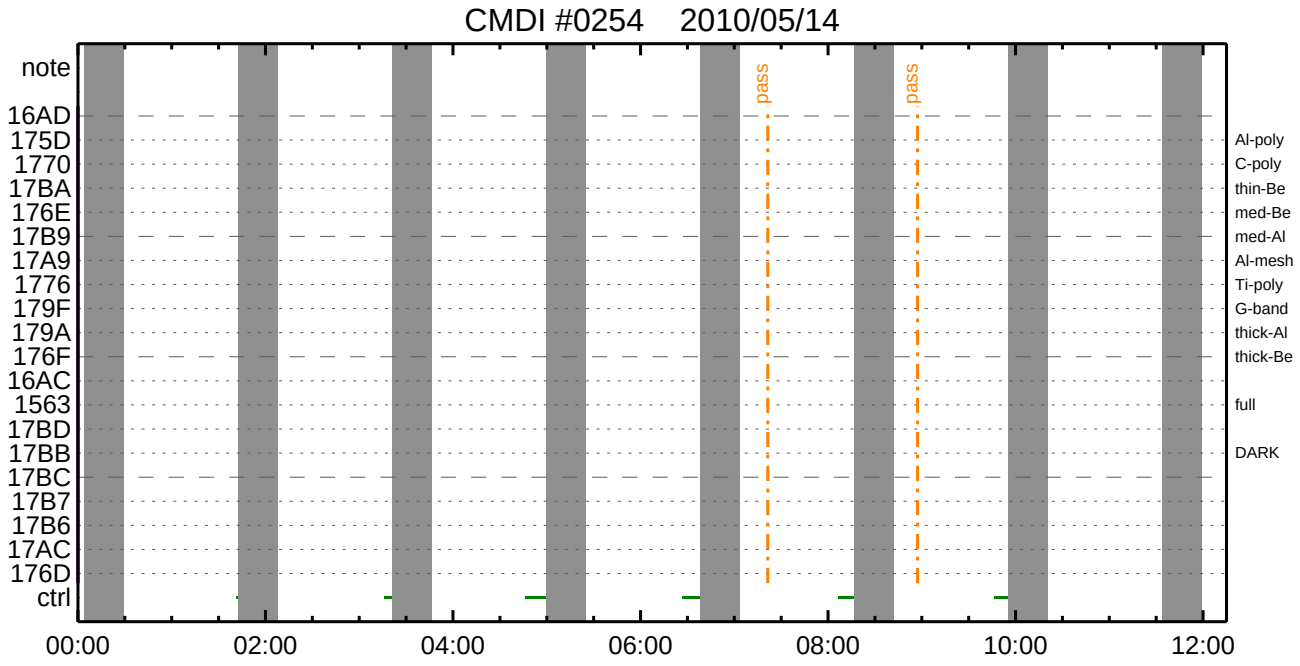
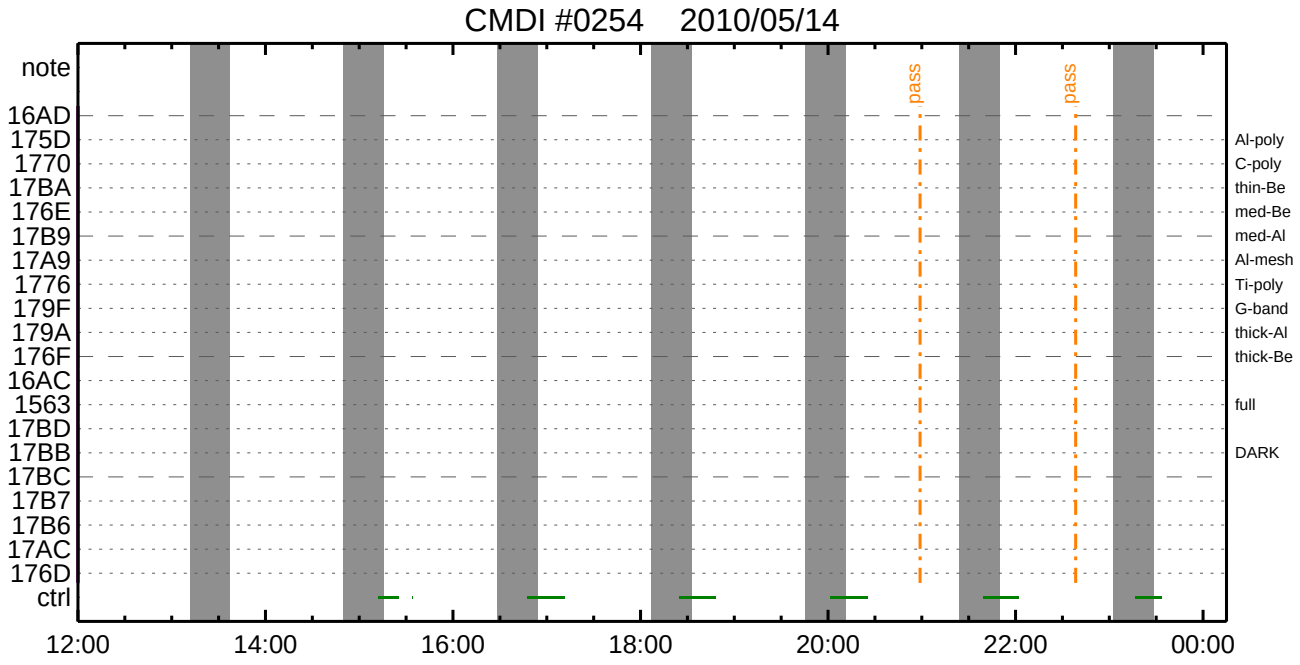
06:00
08:00
10:00
12:00

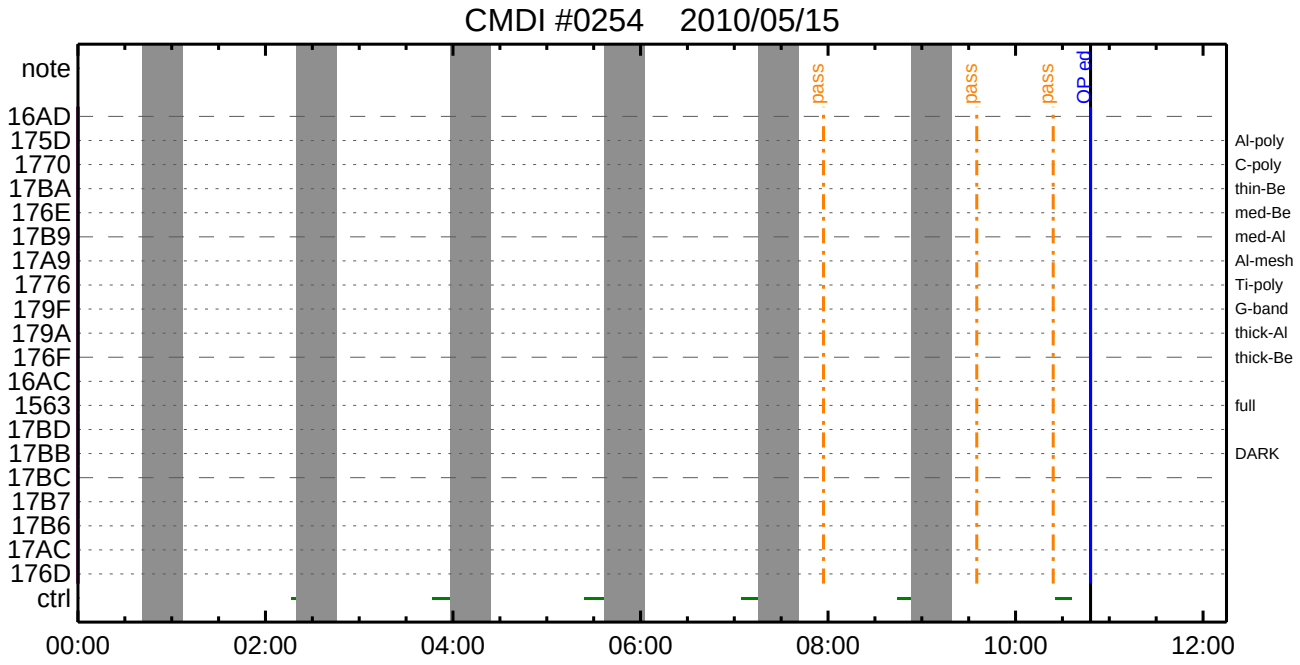

CMDI #0254 2010/05/12
note
16AD
175D
1770
17BA
176E
17B9
17A9
1776
179F
179A
176F
16AC
1563
17BD
17BB
17BC
17B7
17B6
17AC
176D
ctrl
track4
pass
pass
QT
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK

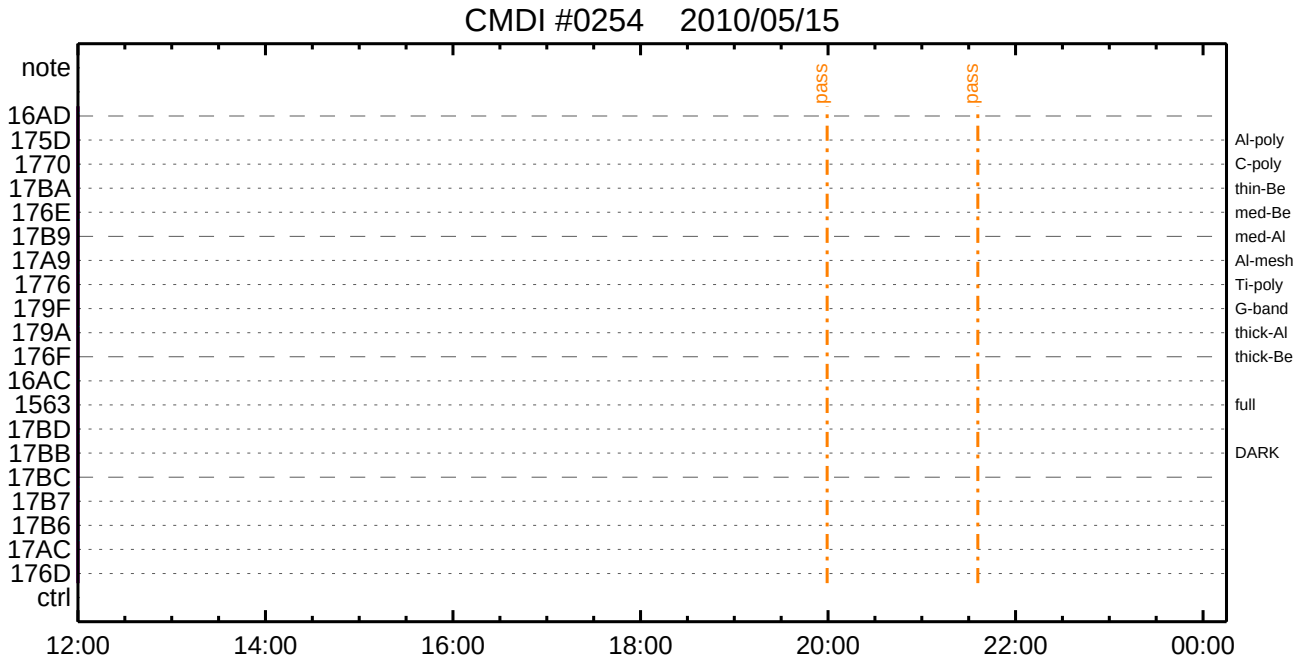

CMDI #0254 2010/05/13
note
16AD
175D
1770
17BA
176E
17B9
17A9
1776
179F
179A
176F
16AC
1563
17BD
17BB
17BC
17B7
17B6
17AC
176D
ctrl
track3
track0
track0
track0
track0
track3
track0
QT
QT
QT
QT
QT
QT
pass
pass
pass
00:00
02:00
04:00
06:00
08:00
10:00
12:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0254 2010/05/13
note
16AD
175D
1770
17BA
176E
17B9
17A9
1776
179F
179A
176F
16AC
1563
17BD
17BB
17BC
17B7
17B6
17AC
176D
ctrl
pass
pass
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0254 2010/05/14
note
16AD
175D
1770
17BA
176E
17B9
17A9
1776
179F
179A
176F
16AC
1563
17BD
17BB
17BC
17B7
17B6
17AC
176D
ctrl
pass
pass
00:00
02:00
04:00
06:00
08:00
10:00
12:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0254 2010/05/14
note
16AD
175D
1770
17BA
176E
17B9
17A9
1776
179F
179A
176F
16AC
1563
17BD
17BB
17BC
17B7
17B6
17AC
176D
ctrl
pass
pass
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0254 2010/05/15
note
16AD
175D
1770
17BA
176E
17B9
17A9
1776
179F
179A
176F
16AC
1563
17BD
17BB
17BC
17B7
17B6
17AC
176D
ctrl
pass
pass
pass
OP end
00:00
02:00
04:00
06:00
08:00
10:00
12:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0254 2010/05/15
note
16AD
175D
1770
17BA
176E
17B9
17A9
1776
179F
179A
176F
16AC
1563
17BD
17BB
17BC
17B7
17B6
17AC
176D
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-350 2010-05-11 13:36:32 289 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸¸å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ****************************
0099     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0100     C. ****************************
0101     C.
0102   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0103   . S. OP    op-350:OP
0104     ()
0105   . S. OG    og-350:OG
0106     ()
0107     C.
0108   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0109     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0110   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111     BC       (20 00 7f 01 02)
0112     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0113     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0114     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0115     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0116     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0117   +. DC 01-22 DHU_MODE_CHNG
0118     BC       (07 0b f8)
0119     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0120     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0122     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0123     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0124   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0125     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0126   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0127     C.
0128     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0129   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130     BC       (20 80 7f 01 02)
0131     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0132     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0133     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0134     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0135     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0136   +. DC 01-22 DHU_MODE_CHNG
0137     BC       (07 0b f8)
0138     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0139     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0141     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0142     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0143   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0144     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0145   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0146     C.
0147     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149     BC       (21 00 41 01 02)
0150     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0151     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0152     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0153     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0154     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0155   +. DC 01-22 DHU_MODE_CHNG
0156     BC       (07 0b f8)
0157     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0158     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0160     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0161     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0162   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0163     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0164   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0165     C.
0166   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0167     C. DHU¥â¡¼¥É¡Ê¼ý¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0168   +. DC 01-22 DHU_MODE_CHNG
0169     BC       (02 0a f8)
0170     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0171     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0172     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0173     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0174     C.
0175   . C. ********************************
0176     C. TI-CMD SET (OPOG STOP/COPY/START)
0177     C. ********************************
0178     C.
0179   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
0180     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¿¼Ç¹Ô¤¦¤³¤È¡£
0181     C.
0182   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0183   +. TI 2010-05-11 10:25:00.0
0184     DC 01-B3 DHU_OP_STOP
0185     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186     C.
0187   +. TI 2010-05-11 10:25:01.0
0188     DC 01-B4 DHU_OP_COPY
0189     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190     C.
0191   +. TI 2010-05-11 10:25:01.0
0192     DC 01-B5 DHU_OPOG_COPY
0193     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195   +. TI 2010-05-11 10:29:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                        ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                        ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                        ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                        ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                        ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                        ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                        ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                        ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                        ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                        ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238     C. ********************************
0239     C.  SOT TI command set
0240     C. ********************************
0241     C. Execute, after the success of OP upload.
0242   +. TI 2010-05-11 10:29:16.0
0243     DC 07-F0 MDP_SOT_MODE_STBY
0244     BC       (41)
0245    . C. ------------------------------------
0246     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0247     C. ------------------------------------
0248     C. ************ SOT END ***********
0249     C.
0250     C. ************ XRT START ***********
0251     C. Execute, after the success of OP upload.
0252   +. TI 2010-05-11 10:29:00.0
0253     DC 07-F0 MDP_XRT_MODE_STBY
0254     BC       (c3)
0255    . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0256     C.
0257     C. ************ XRT END ***********
0258    . C. Stop EIS observation and temporarily disable EIS mode changes
0259     C.
0260     C.
0261     C. ********** Start EIS operation (TI set) **********
0262     C. Execute, after the success of OP upload.
0263     C. Set EIS TI-commands
0264   +. TI 2010-05-11 10:29:30.0
0265     DC 07-FC EIS_MODE_MANU
0266     BC       (21 02)
0267   +. TI 2010-05-11 10:29:40.0
0268     DC 07-FC EIS_MODE_CHG_DIS
0269     BC       (22)
0270    . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0271     C. *********** End EIS operation (TI set) ***********
0272     C.
0273     C.
0274     C.
0275    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0276     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0277    . S. DC-BC dcbc-402:DCBC
0278     (MDP_known_event)
0279     C.
0280     C.
0281    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0282    . S. DC-BC dcbc-153:DCBC
0283     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284     C.
0285     C.
0286    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0287     C.
0288    . C. *********** LOS ***********
0289     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-351 2010-05-11 13:36:32 166 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Äº²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106   +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113   +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                      Result = OK [ ]
0121     C.
0122   +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                         <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127   +. DC 02-8E AOCU_ORB_UPD
0128    . C.
0129    . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130   +. DC 07-FC EIS_MODE_MANU
0131     BC       (21 02)
0132    . C. Verify EIS in MANUAL mode
0133    . C. Estimated OBSTBL upload time is 27s
0134     C. ********************************
0135     C. EIS START OBSTBL LOAD
0136     C. ********************************
0137    . S. RAM   ram-820:EIS_OBSTBL
0138     ()
0139   +. DC 07-FC EIS_DUMP_OBSTBL
0140     BC       (07 07 07 00 00 70 00)
0141     C.
0142     C. Execute, after the success of OBSTBL upload.
0143     C. Set EIS TI-commands
0144   +. TI 2010-05-11 10:29:50.0
0145     DC 07-FC EIS_MODE_CHG_ENA
0146     BC       (20)
0147    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0148     C. ********************************
0149     C. EIS END OBSTBL LOAD
0150     C. ********************************
0151     C.
0152    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0153     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0154    . S. DC-BC dcbc-402:DCBC
0155     (MDP_known_event)
0156     C.
0157     C.
0158    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0159    . S. DC-BC dcbc-153:DCBC
0160     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161     C.
0162     C.
0163    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0164     C.
0165    . C. ********** LOS **********
0166     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-352 2010-05-11 13:36:32 120 33 SOLAR-B MAIN //
0001     C.
0002    . C. *********** AOS ***********
0003     C.
0004    . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009    . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014     C.
0015     C. ************ XRT START ************
0016     C.
0017   +. DC 07-F0 MDP_XRT_CTRL_MANU
0018     BC       (c1)
0019   +  DC 07-F0 MDP_XRT_MODE_STBY
0020     BC       (c3)
0021    . C. ----------- Success Verify ?              OK / NG____
0022     C.
0023     C. XRT Obs. Table Upload
0024    . S. RAM   ram-291:MDP_OBS_X
0025     ()
0026     C.
0027   +. DC 07-F0 MDP_DUMP_XRTTBL
0028     BC       (84 07 00 00 00 3a d4)
0029    . C. ----------- Comparison Check ?                        OK / ERR ____
0030     C.
0031     C.
0032   +. DC 07-F0 MDP_XRT_ROI_SET
0033     BC       (cd 01 b1 b1 04 04)
0034   +  DC 07-F0 MDP_XRT_ROI_SET
0035     BC       (cd 02 b1 b1 08 08)
0036   +  DC 07-F0 MDP_XRT_ROI_SET
0037     BC       (cd 03 b1 b1 08 08)
0038   +  DC 07-F0 MDP_XRT_ROI_SET
0039     BC       (cd 04 b1 b1 06 06)
0040   +  DC 07-F0 MDP_XRT_ROI_SET
0041     BC       (cd 06 80 80 20 20)
0042   +  DC 07-F0 MDP_XRT_ROI_SET
0043     BC       (cd 07 80 80 08 08)
0044   +  DC 07-F0 MDP_XRT_ROI_SET
0045     BC       (cd 08 85 83 06 06)
0046   +  DC 07-F0 MDP_XRT_ROI_SET
0047     BC       (cd 09 80 80 20 04)
0048   +  DC 07-F0 MDP_XRT_ROI_SET
0049     BC       (cd 0a c0 c0 10 10)
0050   +  DC 07-F0 MDP_XRT_ROI_SET
0051     BC       (cd 0b 40 c0 10 10)
0052   +  DC 07-F0 MDP_XRT_ROI_SET
0053     BC       (cd 0c 40 40 10 10)
0054   +  DC 07-F0 MDP_XRT_ROI_SET
0055     BC       (cd 0d c0 40 10 10)
0056   +  DC 07-F0 MDP_XRT_ROI_SET
0057     BC       (cd 0f 80 80 06 06)
0058   +  DC 07-F0 MDP_XRT_AEC_RESET
0059     BC       (d0)
0060    . C. ---------- Success Verify ?            OK / NG ____
0061     C.
0062     C.
0063    . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0064     C.
0065   +. DC 07-F0 MDP_XRT_MODE_OBSV
0066     BC       (c2)
0067   +. TI 2010-05-11 10:29:02.0
0068     DC 07-F0 MDP_XRT_MODE_OBSV
0069     BC       (c2)
0070    . C. ---------- Success Verify ?            OK / NG ____
0071     C.
0072     C. ************ XRT END ***********
0073    . C. ********************************
0074     C.  SOT table upload
0075     C. ********************************
0076    . C. < Stop FG table >
0077   +. DC 07-F0 MDP_FG_CTRL_MANU
0078     BC       (51)
0079    . C. ------------------------------------
0080     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0081     C. ------------------------------------
0082     C.
0083    . C. <Upload FG Observation Table>
0084    . S. RAM   ram-264:MDP_OBS_F
0085     ()
0086     C.
0087    . C. < Dump RAMID=MDP_OBS_F >
0088   +. DC 07-F0 MDP_DUMP_FGTBL
0089     BC       (82 07 00 00 00 38 b8)
0090     C. ------------------------------------
0091     C.  MDP_OBS_F verify = OK/NG        [ ]
0092     C. ------------------------------------
0093     C.
0094     C. ********************************
0095     C.  SOT TI command set
```

```
0096     C. ******************************
0097     C. Execute, after the success of TBL upload.
0098  +. TI 2010-05-11 10:29:18.0
0099     DC 07-F0 MDP_SOT_MODE_OBSV
0100     BC       (40)
0101   . C. ------------------------------------
0102     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0103     C. ------------------------------------
0104     C.
0105     C.
0106   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0107     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0108   . S. DC-BC dcbc-402:DCBC
0109     (MDP_known_event)
0110     C.
0111     C.
0112   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0113   . S. DC-BC dcbc-153:DCBC
0114     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0115     C.
0116     C.
0117   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0118     C.
0119   . C. ********** LOS **********
0120     C.
```

```
*** OP Sequence for XRT ***

2010/05/11  10:39:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/11  10:39:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/05/11  10:40:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 53 8d 00 00
2010/05/11  10:40:16.0  XRT_FLD_ENA_402_OG [0x192]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/05/11  10:40:18.0  XRT_FLD_RESET_403_OG [0x193]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/05/11  10:40:20.0  XRT_FLRCTRL_ENA_418_OG [0x1a2]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/05/11  10:40:22.0  XRT_ARS_DIS_405_OG [0x195]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/11  10:42:56.0  XRT_QT_PROG_SET_434_OG [0x1b2]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2010/05/11  10:42:58.0  XRT_FL_PROG_SET_442_OG [0x1ba]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0d
2010/05/11  10:43:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/11  11:20:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/11  11:45:00.0  XRT_Custom_410_OG [0x19a]
2010/05/11  11:46:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/11  12:58:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/11  13:23:30.0  XRT_Custom_410_OG [0x19a]
2010/05/11  13:24:30.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/11  14:37:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/11  15:11:00.0  XRT_Custom_410_OG [0x19a]
2010/05/11  15:12:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/11  15:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/11  15:59:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/05/11  16:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 03 00 00 00 00
2010/05/11  16:00:16.0  XRT_AEC_RESET_428_OG [0x1ac]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2010/05/11  16:00:18.0  XRT_FLD_ENA_402_OG [0x192]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/05/11  16:00:20.0  XRT_FLRCTRL_ENA_418_OG [0x1a2]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/05/11  16:00:22.0  XRT_FLD_RESET_403_OG [0x193]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/05/11  16:00:24.0  XRT_ARS_DIS_405_OG [0x195]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/11  16:02:58.0  XRT_QT_PROG_SET_431_OG [0x1af]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2010/05/11  16:03:00.0  XRT_FL_PROG_SET_442_OG [0x1ba]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0d
2010/05/11  16:03:02.5  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/11  16:15:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/11  16:58:00.0  XRT_Custom_410_OG [0x19a]
2010/05/11  16:59:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/11  17:43:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/11  17:43:26.0  XRT_FOCUS_POSITION_412_OG [0x19c]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/05/11  17:43:30.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 00 00 00 00
2010/05/11  17:43:46.0  XRT_FLD_DIS_413_OG [0x19d]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2010/05/11  17:43:48.0  XRT_FLRCTRL_DIS_414_OG [0x19e]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/05/11  17:43:50.0  XRT_ARS_DIS_415_OG [0x19f]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/11  17:46:28.0  XRT_QT_PROG_SET_435_OG [0x1b3]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2010/05/11  17:46:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/11  17:53:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/11  17:53:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 03 00 00 00 00
2010/05/11  17:54:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/11  18:34:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/11  18:34:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/05/11  18:35:16.0  XRT_AEC_RESET_428_OG [0x1ac]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2010/05/11  18:35:18.0  XRT_FLD_ENA_402_OG [0x192]
```

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                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2010/05/11  18:35:20.0  XRT_FLRCTRL_ENA_418_OG [0x1a2]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/05/11  18:35:22.0  XRT_FLD_RESET_403_OG [0x193]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/05/11  18:35:24.0  XRT_ARS_DIS_405_OG [0x195]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/11  18:37:58.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2010/05/11  18:38:00.0  XRT_FL_PROG_SET_442_OG [0x1ba]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0d
2010/05/11  18:38:02.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/11  19:32:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/11  20:12:00.0  XRT_Custom_410_OG [0x19a]
2010/05/11  20:13:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/11  21:11:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/11  21:49:00.0  XRT_Custom_410_OG [0x19a]
2010/05/11  21:50:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/11  22:49:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/11  23:23:30.0  XRT_Custom_410_OG [0x19a]
2010/05/11  23:24:30.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/12  00:28:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/12  00:53:00.0  XRT_Custom_410_OG [0x19a]
2010/05/12  00:54:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/12  02:03:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/12  02:31:30.0  XRT_Custom_410_OG [0x19a]
2010/05/12  02:32:30.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/12  03:37:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/12  04:10:00.0  XRT_Custom_410_OG [0x19a]
2010/05/12  04:11:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/12  05:10:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/12  05:48:30.0  XRT_Custom_410_OG [0x19a]
2010/05/12  05:49:30.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/12  05:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/12  05:59:56.0  XRT_FOCUS_POSITION_412_OG [0x19c]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/05/12  06:00:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2010/05/12  06:00:16.0  XRT_FLD_DIS_413_OG [0x19d]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2010/05/12  06:00:18.0  XRT_FLRCTRL_DIS_414_OG [0x19e]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/05/12  06:00:20.0  XRT_ARS_DIS_415_OG [0x19f]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/12  06:02:58.0  XRT_QT_PROG_SET_435_OG [0x1b3]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2010/05/12  06:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/12  06:09:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/12  06:09:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/05/12  06:10:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                        AOCU_NM              5 02-76 01 00 00 00 00
2010/05/12  06:10:16.0  XRT_FLD_RESET_403_OG [0x193]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/05/12  06:10:18.0  XRT_FLD_ENA_449_OG [0x1c1]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2010/05/12  06:10:20.0  XRT_FLRCTRL_DIS_414_OG [0x19e]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/05/12  06:10:22.0  XRT_ARS_DIS_415_OG [0x19f]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/12  06:13:00.0  XRT_QT_PROG_SET_433_OG [0x1b1]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 02
2010/05/12  06:13:02.5  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/12  06:50:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/12  07:27:00.0  XRT_Custom_410_OG [0x19a]
2010/05/12  07:28:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/12  08:30:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/12  09:05:30.0  XRT_Custom_410_OG [0x19a]
2010/05/12  09:06:30.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/12  10:11:30.0  XRT_CTRL_MANU_409_OG [0x199]
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                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/12  10:44:00.0  XRT_Custom_410_OG [0x19a]
2010/05/12  10:45:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/12  11:50:30.0  XRT_CTRL_MANU_436_OG [0x1b4]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/12  11:51:00.0  XRT_TCIB_XRT_S_HTR_A_ENA_437_OG [0x1b5]
                                        TCIB_XRT_S_HTR_A_ENA 0 04-BC
2010/05/12  11:57:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/12  13:36:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/12  13:51:00.0  XRT_Custom_441_OG [0x1b9]
2010/05/12  15:14:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/12  15:50:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/12  15:51:00.0  XRT_Custom_441_OG [0x1b9]
2010/05/12  16:53:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/12  17:51:00.0  XRT_Custom_441_OG [0x1b9]
2010/05/12  18:16:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                        AOCU_NM              5 02-76 04 00 00 00 00
2010/05/12  18:31:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/12  19:51:00.0  XRT_Custom_445_OG [0x1bd]
2010/05/12  19:51:10.0  XRT_Custom_441_OG [0x1b9]
2010/05/12  20:09:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/12  21:48:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/12  21:51:10.0  XRT_Custom_447_OG [0x1bf]
2010/05/12  23:26:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/12  23:50:48.0  XRT_Custom_445_OG [0x1bd]
2010/05/12  23:50:58.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/12  23:51:00.5  XRT_TCIB_XRT_S_HTR_A_DIS_404_OG [0x194]
                                        TCIB_XRT_S_HTR_A_DIS 0 04-C0
2010/05/12  23:55:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/12  23:55:32.0  XRT_QT_PROG_SET_448_OG [0x1c0]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 08
2010/05/12  23:55:34.0  XRT_ARS_DIS_425_OG [0x1a9]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/12  23:55:36.0  XRT_FLD_DIS_426_OG [0x1aa]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2010/05/12  23:55:38.0  XRT_FLRCTRL_DIS_416_OG [0x1a0]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/05/12  23:55:40.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/13  01:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 03 00 00 00 00
2010/05/13  01:05:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/13  01:30:30.0  XRT_Custom_410_OG [0x19a]
2010/05/13  01:31:30.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/13  02:39:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/13  03:09:00.0  XRT_Custom_410_OG [0x19a]
2010/05/13  03:10:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/13  04:08:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/13  04:47:30.0  XRT_Custom_410_OG [0x19a]
2010/05/13  04:48:30.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/13  05:48:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/13  06:28:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/13  06:28:26.0  XRT_FOCUS_POSITION_412_OG [0x19c]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/05/13  06:28:30.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2010/05/13  06:28:46.0  XRT_FLD_DIS_413_OG [0x19d]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2010/05/13  06:28:48.0  XRT_FLRCTRL_DIS_414_OG [0x19e]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/05/13  06:28:50.0  XRT_ARS_DIS_415_OG [0x19f]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/13  06:31:28.0  XRT_QT_PROG_SET_435_OG [0x1b3]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2010/05/13  06:31:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/13  06:38:24.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/13  06:38:30.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                        AOCU_NM              5 02-76 00 2e f9 2e f9
2010/05/13  06:41:02.0  XRT_FOCUS_POSITION_444_OG [0x1bc]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/05/13  06:41:22.0  XRT_QT_PROG_SET_420_OG [0x1a4]
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                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 10
2010/05/13  06:41:24.0  XRT_FLD_DIS_413_OG [0x19d]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2010/05/13  06:41:26.0  XRT_FLRCTRL_DIS_414_OG [0x19e]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/05/13  06:41:28.0  XRT_ARS_DIS_421_OG [0x1a5]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/13  06:41:30.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/13  06:48:24.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/13  06:48:30.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                        AOCU_NM              5 02-76 00 2e f9 d1 07
2010/05/13  06:51:02.0  XRT_FOCUS_POSITION_444_OG [0x1bc]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/05/13  06:51:22.0  XRT_QT_PROG_SET_423_OG [0x1a7]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 01
2010/05/13  06:51:24.0  XRT_FLD_DIS_413_OG [0x19d]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2010/05/13  06:51:26.0  XRT_FLRCTRL_DIS_414_OG [0x19e]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/05/13  06:51:28.0  XRT_ARS_DIS_421_OG [0x1a5]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/13  06:51:30.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/13  06:58:24.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/13  06:58:30.0  AOCS_ORe-point_Start_8_OG [0x09e]
                                        AOCU_NM              5 02-76 00 d1 07 d1 07
2010/05/13  07:01:02.0  XRT_FOCUS_POSITION_444_OG [0x1bc]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/05/13  07:01:22.0  XRT_QT_PROG_SET_432_OG [0x1b0]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0a
2010/05/13  07:01:24.0  XRT_FLD_DIS_413_OG [0x19d]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2010/05/13  07:01:26.0  XRT_FLRCTRL_DIS_414_OG [0x19e]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/05/13  07:01:28.0  XRT_ARS_DIS_421_OG [0x1a5]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/13  07:01:30.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/13  07:08:24.0  XRT_CTRL_MANU_439_OG [0x1b7]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/13  07:08:30.0  AOCS_ORe-point_Start_9_OG [0x09f]
                                        AOCU_NM              5 02-76 00 d1 07 2e f9
2010/05/13  07:11:02.0  XRT_FOCUS_POSITION_444_OG [0x1bc]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/05/13  07:11:22.0  XRT_QT_PROG_SET_406_OG [0x196]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 12
2010/05/13  07:11:24.0  XRT_FLD_DIS_413_OG [0x19d]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2010/05/13  07:11:26.0  XRT_FLRCTRL_DIS_414_OG [0x19e]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/05/13  07:11:28.0  XRT_ARS_DIS_421_OG [0x1a5]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/13  07:11:30.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/13  07:18:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/13  07:18:26.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/05/13  07:18:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 03 00 00 00 00
2010/05/13  07:18:46.0  XRT_AEC_RESET_428_OG [0x1ac]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2010/05/13  07:18:48.0  XRT_FLD_ENA_402_OG [0x192]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2010/05/13  07:18:50.0  XRT_FLRCTRL_ENA_418_OG [0x1a2]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/05/13  07:18:52.0  XRT_FLD_RESET_403_OG [0x193]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/05/13  07:18:54.0  XRT_ARS_DIS_405_OG [0x195]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/05/13  07:21:28.0  XRT_QT_PROG_SET_431_OG [0x1af]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2010/05/13  07:21:30.0  XRT_FL_PROG_SET_442_OG [0x1ba]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0d
2010/05/13  07:21:32.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/13  07:28:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/13  08:04:30.0  XRT_Custom_410_OG [0x19a]
2010/05/13  08:05:30.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/13  09:08:30.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/13  09:43:00.0  XRT_Custom_410_OG [0x19a]
2010/05/13  09:44:00.0  XRT_CTRL_AUTO_411_OG [0x19b]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/05/13  10:51:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/13  10:56:00.0  XRT_CTRL_MANU_409_OG [0x199]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
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2010/05/13  11:21:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/05/13  11:22:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 00 00 00 00
```