

# XRT Timeline to be uploaded on 2010/07/14

Period: 2010/07/14 10:22:00 - 2010/07/17 10:14:00

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Normal mode

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XOB #175D: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Ti-poly(33/2048) + G-band(16)												
Term	Pointing (x, y)						Comment					
07/14 10:35:00 - 07/14 10:42:00	Fixed ( 0.0, 0.0)						# OP start + 10min, 6 hours disk center for SOT flat fields, and XRT pre-bake-out synoptic.					
07/15 06:22:00 - 07/15 06:28:54	Fixed ( 0.0, 0.0)						synoptic, shifted 19.0 min					
<b>PROG= 09 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 7 1-time(s) 4.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 48 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 8 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 4 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1563: CCD Monitor During Bakeout - G-band + dark - wide FOV												
Term	Pointing (x, y)						Comment					
07/14 22:43:10 - 07/15 04:44:00	Track ( -139.7, 240.4) <sup>© 07/14 16:32:00</sup>						# Observe AR 11087.					
<b>PROG= 11 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 600.0sec												
└─ Seqn= 37 1-time(s) 4.0sec												
└─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 2048x256 (1024, 1024) DPCM 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 2048x256 (1024, 1024) DPCM 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #17F1: AR Standard-A(Filter-Ratio) for FW1=Open, 512x512 at 1064 1048, 4min-cad												
Term	Pointing (x, y)						Comment					
07/15 04:44:34 - 07/15 06:18:54	Track ( -139.7, 240.4) <sup>© 07/14 16:32:00</sup>						# Observe AR 11087.					
<b>PROG= 18 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 27 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec												
└─ Seqn= 85 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec												
└─ Open/thick-Al Open/thick-Be close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 86 10-time(s) 240.0sec												
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1778: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long												
Term	Pointing (x, y)						Comment					
07/15 06:32:00 - 07/15 06:38:54	Fixed ( -528.4, -528.4)						# XRT 1/4 post bake-out quadrant pointings.					
<b>PROG= 15 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 38 1-time(s) 12.0sec												
└─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 93 2-time(s) 2.0sec												
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1779: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh, Ti/Poly -long												
Term	Pointing (x, y)						Comment					
07/15 06:42:00 - 07/15 06:48:54	Fixed ( 528.4, -528.4)						# 2/4					
<b>PROG= 04 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 36 1-time(s) 12.0sec												
└─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec												

	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
<b>Seqn= 93</b>	<b>2-time(s)</b>	<b>2.0sec</b>											
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #177A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant- Al/mesh, Ti/Poly-long**

Term	Pointing (x, y)		Comment										
07/15 06:52:00 - 07/15 06:58:54	Fixed ( 528.4, 528.4)		# 3/4										
<b>PROG= 12</b>	<b>1-time(s)</b>												
<b>Subr= 1</b>	<b>1-time(s)</b>	<b>12.0sec</b>											
<b>Seqn= 39</b>	<b>1-time(s)</b>	<b>12.0sec</b>											
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
<b>Seqn= 93</b>	<b>2-time(s)</b>	<b>2.0sec</b>											
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #177B: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long**

Term	Pointing (x, y)		Comment										
07/15 07:02:00 - 07/15 07:08:54	Fixed ( -528.4, 528.4)		# 4/4										
<b>PROG= 14</b>	<b>1-time(s)</b>												
<b>Subr= 1</b>	<b>1-time(s)</b>	<b>12.0sec</b>											
<b>Seqn= 40</b>	<b>1-time(s)</b>	<b>12.0sec</b>											
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
<b>Seqn= 93</b>	<b>2-time(s)</b>	<b>2.0sec</b>											
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #17F2: AR Standard-B(Morphology) for FW1=Open, 512x512 at 1064 1048, 20sec-cad**

Term	Pointing (x, y)		Comment										
07/15 07:12:00 - 07/15 10:37:00	Track ( -15.5, 238.6) @ 07/15 07:09:00		# Return to AR. HOP-165 to run during this time-period.										
<b>PROG= 02</b>	<b>Inf.-time(s)</b>												
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
<b>Seqn= 27</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
<b>Seqn= 94</b>	<b>4-time(s)</b>	<b>2.0sec</b>											
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
<b>Subr= 1</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
<b>Seqn= 92</b>	<b>120-time(s)</b>	<b>20.0sec</b>											
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Flare mode**

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**XOB #1776: Flare Response short exp at first- Dynamics - Thick-Al - Thick-Be - AEC 1 - 384x384 - Q95**

Term	Pointing (x, y)		Comment										
07/15 04:44:34 - 07/15 06:18:54	Track ( -139.7, 240.4) @ 07/14 16:32:00		# Observe AR 11087.										
07/15 07:12:00 - 07/15 08:08:00	Track ( -15.5, 238.6) @ 07/15 07:09:00		# Return to AR. HOP-165 to run during this time-period.										
07/15 08:21:34 - 07/15 10:37:00	Track ( -15.5, 238.6) @ 07/15 07:09:00		# Return to AR. HOP-165 to run during this time-period.										
<b>PROG= 01</b>	<b>1-time(s)</b>												
<b>Subr= 1</b>	<b>2-time(s)</b>	<b>2.0sec</b>											
<b>Seqn= 9</b>	<b>15-time(s)</b>	<b>20.0sec</b>											
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
<b>Seqn= 2</b>	<b>1-time(s)</b>	<b>4.0sec</b>											
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 2</b>	<b>8-time(s)</b>	<b>2.0sec</b>											
<b>Seqn= 9</b>	<b>15-time(s)</b>	<b>60.0sec</b>											
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
<b>Seqn= 2</b>	<b>1-time(s)</b>	<b>4.0sec</b>											
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec

Subr= 3		25-time(s)	2.0sec											
Seqn= 9		1-time(s)	600.0sec											
Open/thick-Al	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec		
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec		
Seqn= 2		1-time(s)	4.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval			

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### Active Region Search

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NOT USED

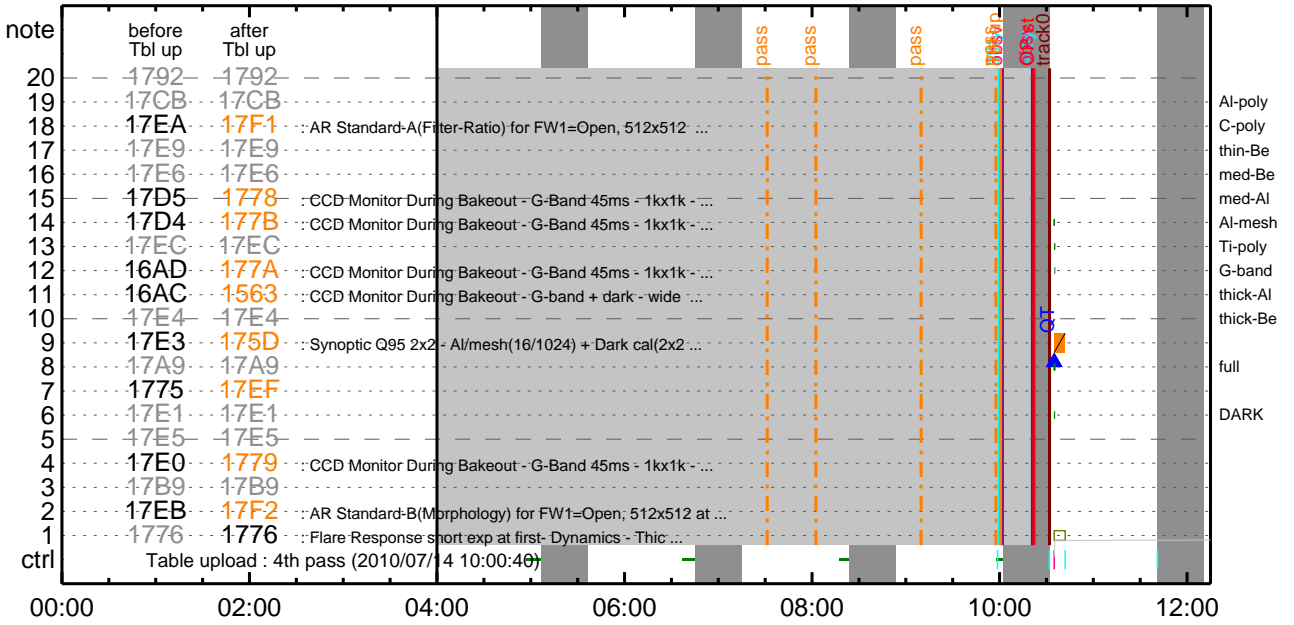
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### Flare Detection

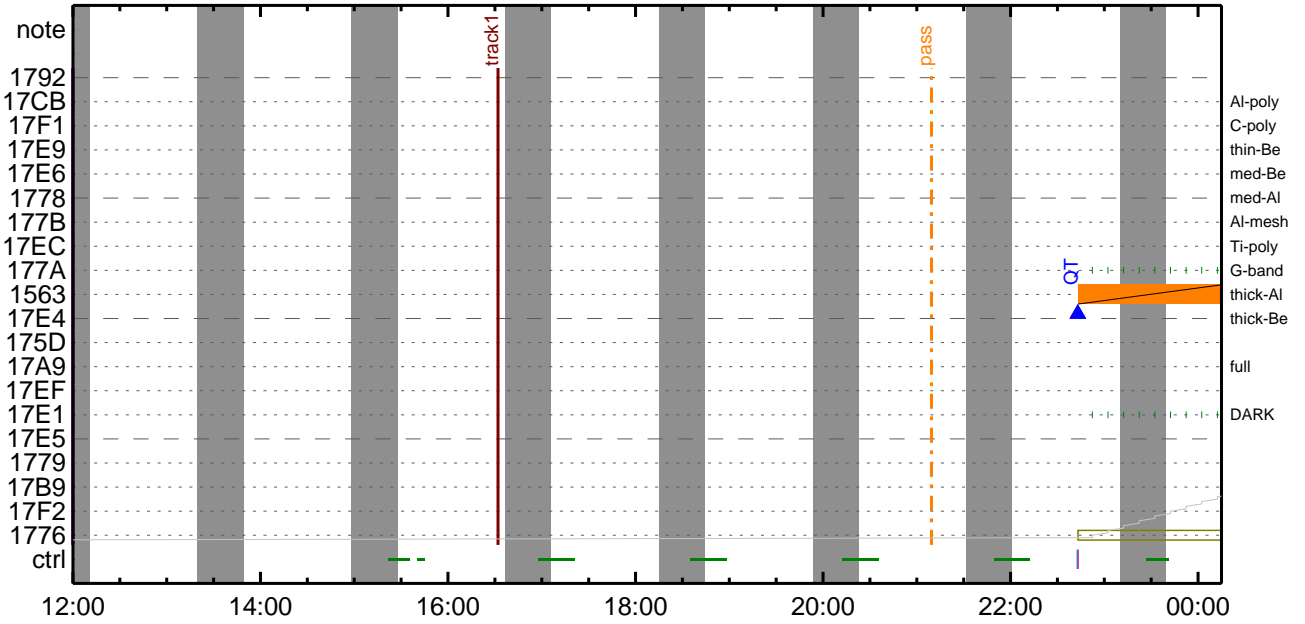
\* \* \* \* \*

FLD Patrol												
Term		Pointing (x, y)				Comment						
07/15 04:44:24 - 07/15 06:19:16		Track ( -139.7, 240.4) <sup>© 07/14 16:32:00</sup>				# Observe AR 11087.						
07/15 07:09:18 - 07/17 10:14:00		Track ( -15.5, 238.6) <sup>© 07/15 07:09:00</sup>				# Return to AR. HOP-165 to run during this time-period.						
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

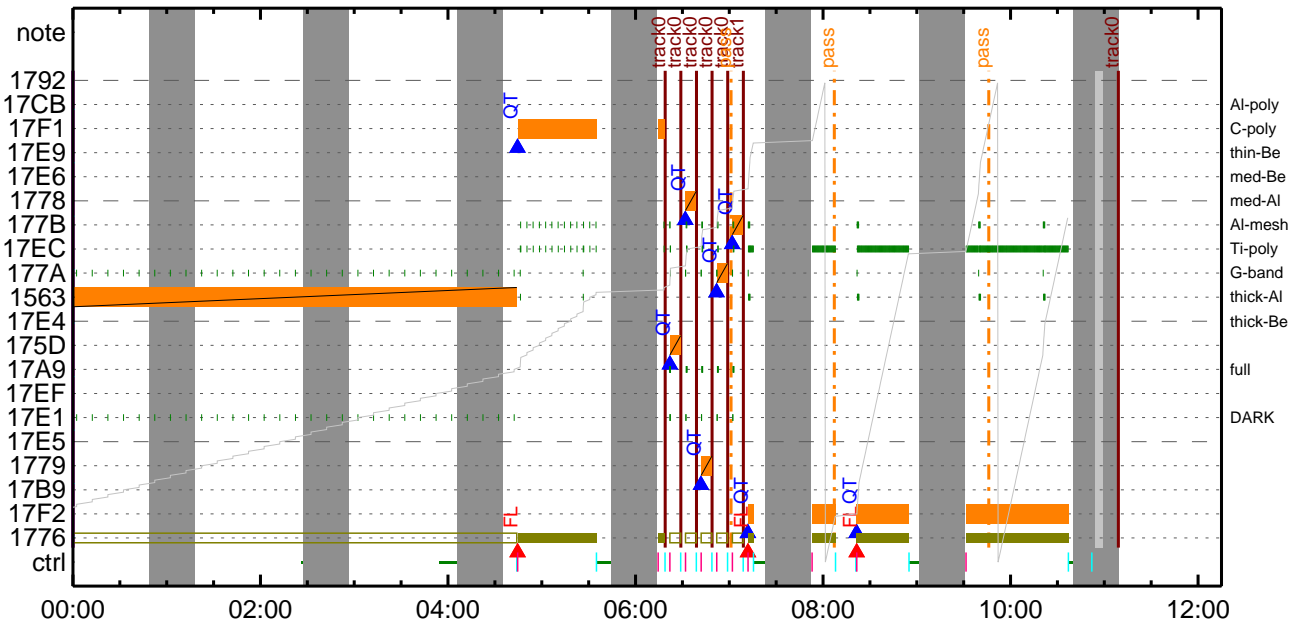
### CMDI #0374 2010/07/14



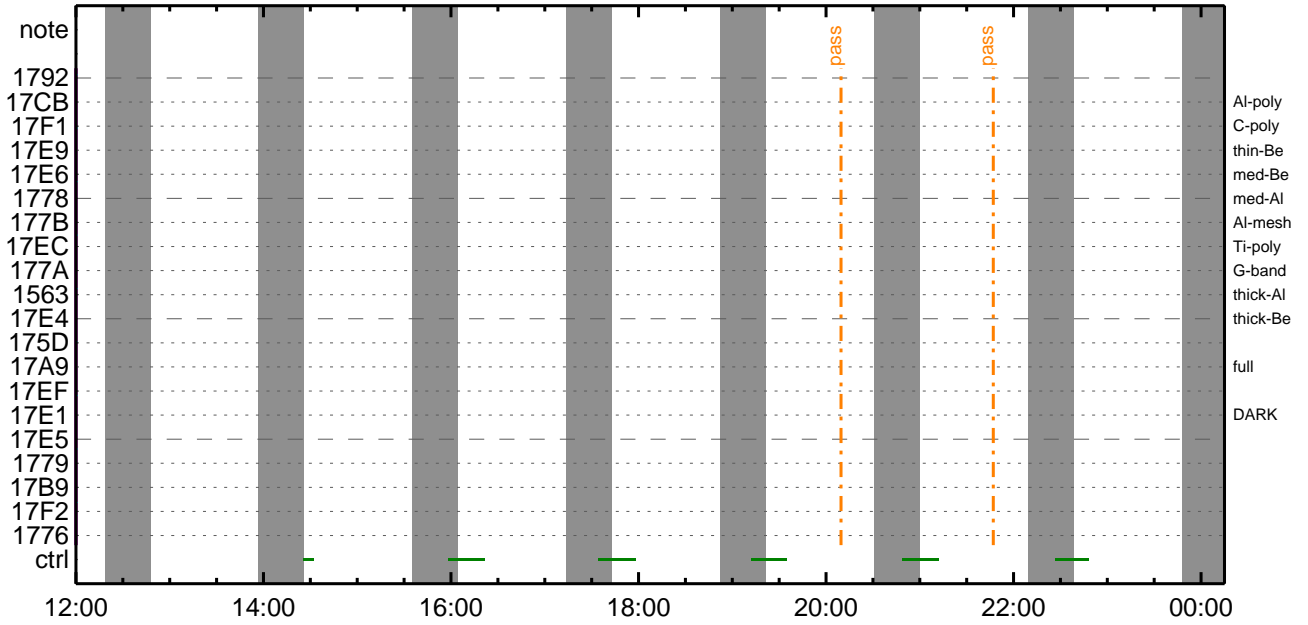
### CMDI #0374 2010/07/14



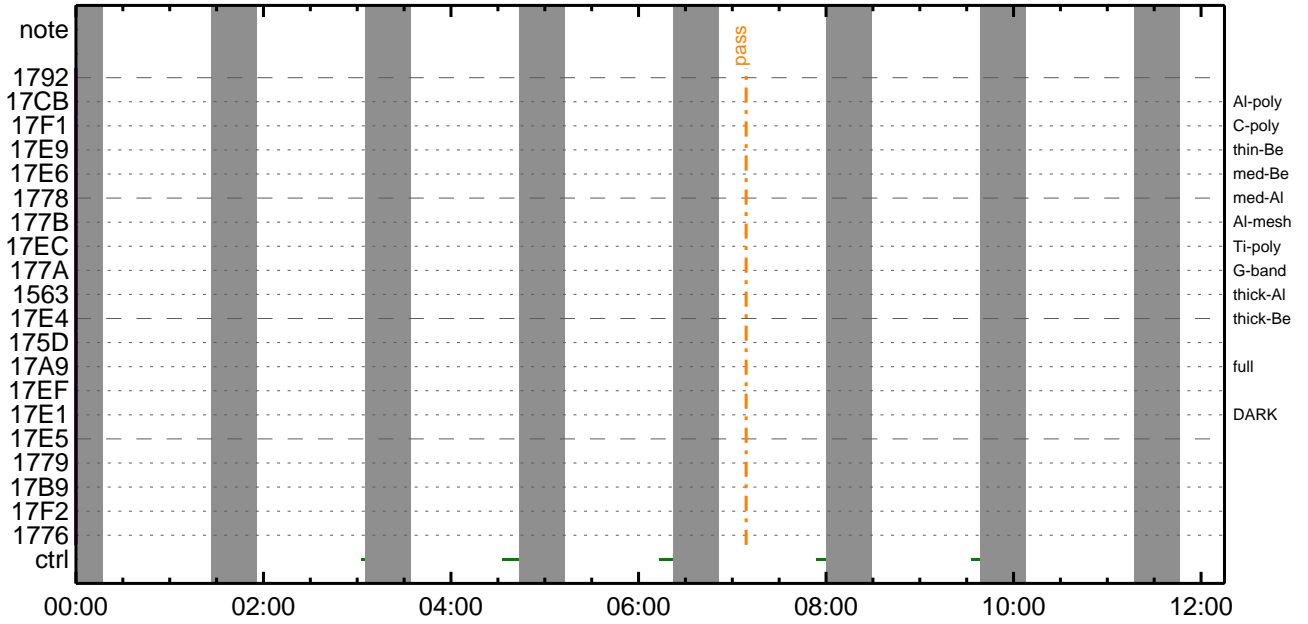
### CMDI #0374 2010/07/15



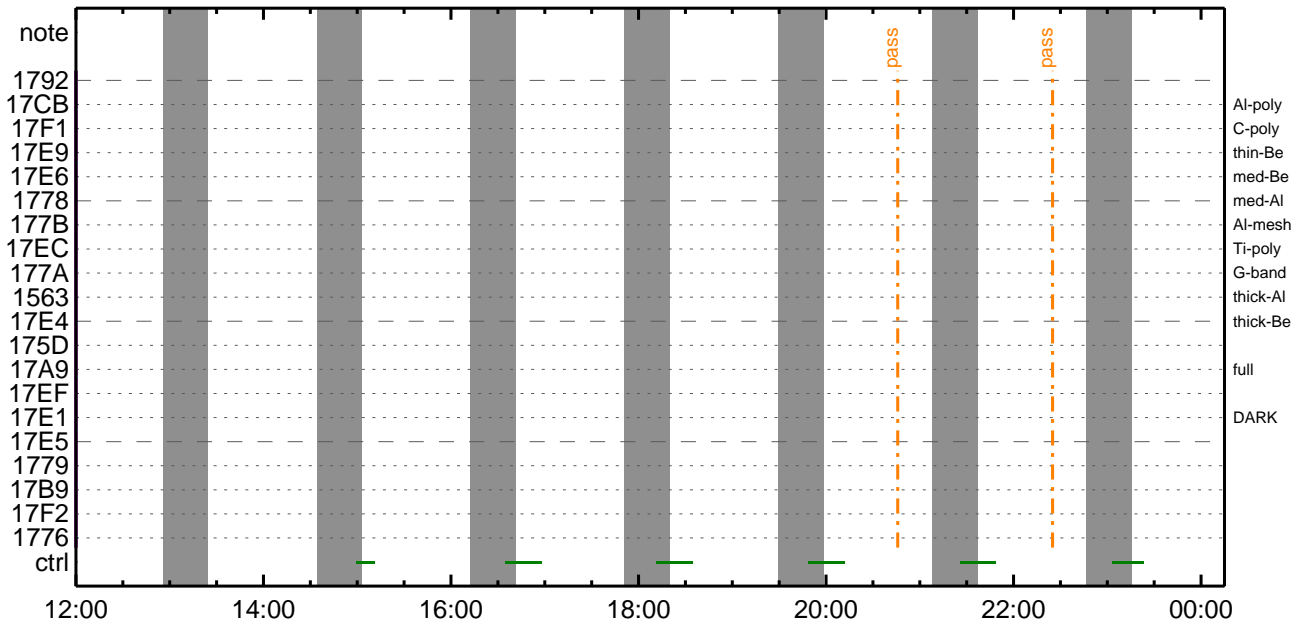
### CMDI #0374 2010/07/15



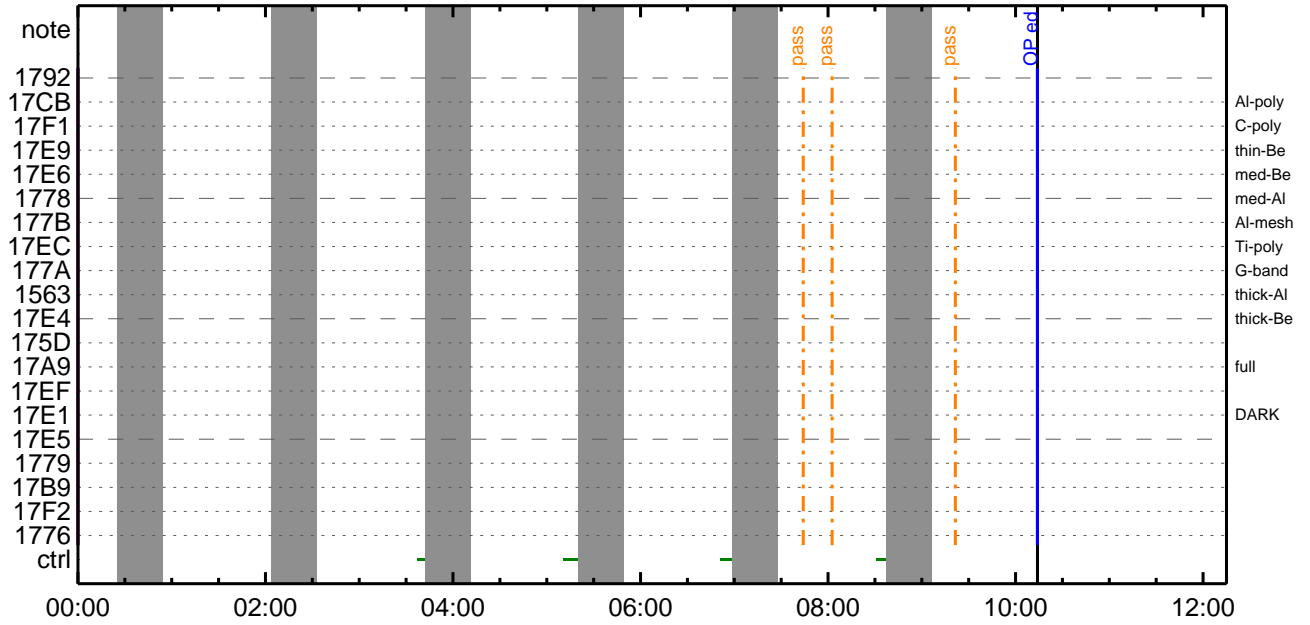
### CMDI #0374 2010/07/16



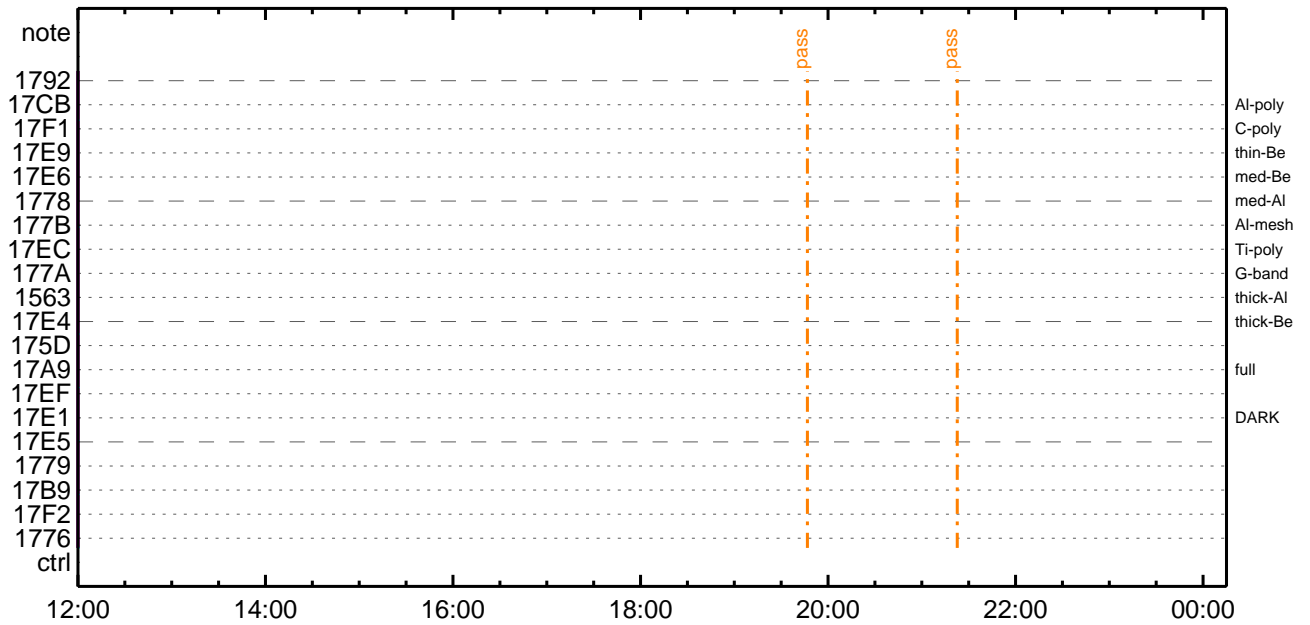
### CMDI #0374 2010/07/16



CMDI #0374 2010/07/17



CMDI #0374 2010/07/17



(a) Spacecraft Operation Procedure (real-commands)

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main-466 2010-07-14 11:59:59 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSÝÁÝÞÝÄÝ¬¼Á»Û;ä
0005 C.
0006 C. ÝÄÝÞ;¼Ý³ÝÞÝÓÝÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Éð¿ðÄð•µºÆ»Í×ÁÇðÍÝÇÝÄÝ×ÝÍ;¼ÝÉ;ÉÈÈ¼µºííÉ;ÈðÈ¼ºÇÓð•µ¿¼l¹Çðí;çÄ®, ùñ¹ðñððÇÄ+¿®ð•ðÉððð³ðÈ;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿µ;ON
0016 C. *****
0017 C. ç °ÆÀ, í×ÈÝðäLOSððÇðí»´ Öðð¹íí, ð, ; çÉÓÍ×ðÈXÁÓONðí¹ÔðÈðíðÈððð³ðÈ;ç
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÝÓÝÉÝÍÝÄÝ¬¼ÖÄÖð-ºÄÄèð•µ¿ðé; çºÈ²¼ðíºÆÀ, ¼è¼çððð¼Á¹Ôð¹ðé;ç
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼íºÆÀ,
0033 C. *****
0034 C. ç ° RESTART;ÈPT1;Èð•µ¿ð¼l¹Çðí; çºÈ²¼ðí¼Á¹Ôð»º; çDCBC-150ðØ¿Èðä;ç
0035 C.
0036 . C. ;ãPT1ºÆÀ, ³«»Í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ô, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ô, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ô, ;¼Ú)
0045 C.
0046 . C. ;ãÝçÝÓÝÉÝÉÄÙÄØ;ÈÄ•ÄººóÈð;È, áðíºÆÀ, °Æ³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ô, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ô, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ô, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1ºÆÀ, ð-¼ºÆºÄ»ßð•µ¿, á; çºÈ²¼ðð¼Á¹Ôð¹ðé;ç
0055 C. ÝçÝÓÝÉÝÉÄÙÄØððÄÄ•ÄººóÈðð-¼áð¼l¹Çðí °í»ð¹ðÈððÇÄÖðÄ;ç
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼íºÆÀ,
0059 C. *****
0060 C. ç ° RESTART;ÈPT2;Èð•µ¿ð¼l¹Çðí; çºÈ²¼ðí¼Á¹Ôð»º; çDCBC-151ðØ¿Èðä;ç
0061 C.
0062 . C. ;ãPT2ºÆÀ, ³«»Í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ô, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ô, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ô, ;¼Ú)
0071 C.
0072 . C. ;ãÝçÝÓÝÉÝÉÄÙÄØ;ÈÄ•ÄººóÈð;È, áðíºÆÀ, °Æ³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ô, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ô, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ô, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DRºÆÀ, Ää»ß;çXÁ+¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDRºÆÀ, Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+¿µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF

```

```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-466:OP
0104 ( )
0105 S. OG og-466:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYôYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOK²³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYôYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOK²³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYôYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOK²³îÇ§
0165 C.
0166 C. ***** °E²¼²î½E¹ç•è²îOK²³îÇ§ *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;E²òîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷ç@NG²î½E¹ç•è²îOK²³îÇ§
0180 C. çç[HK1_DMP_CHK_FLG] EQ NON
0181 C.
0182 C. TIY³YpYôYE²òîã¹ç(UT)
0183 +. TI 2010-07-14 10:17:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2010-07-14 10:17:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2010-07-14 10:17:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```



```
0194 C.
0195 +. TI 2010-07-14 10:21:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          ÷÷[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0198 C.
0199 C. °È²¼oİÄè%İİñoİŷÄŷ§ŷÄŷ¹àİŰ
0200 C.          ÷÷[HK1_TI_CMD_ENA/DIS]      EQ          ENA
0201 C.          ÷÷[HK1_TI_CMD_NUM]          EQ          4
0202 C.          ÷÷[HK1_NEXT_EXEC_PIM]       EQ          DHU
0203 C.          ÷÷[HK1_NEXT_EXEC_DC]       EQ          0xB3
0204 C.
0205 C. *****
0206 C. TIİİ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC          (03 ab 03 01 02)
0212 C.          ÷÷[HK1_DMP_TOP_ADRS_1]     EQ          07
0213 C.          ÷÷[HK1_DMP_TOP_ADRS_0]     EQ          2B
0214 C.          ÷÷[HK1_DMP_BLOCK_NUM]       EQ          3
0215 C.          ÷÷[HK1_DMP_REPEAT_NUM]     EQ          0
0216 C.          ÷÷[HK1_DMA_DMP_PIM]        EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC          (07 0b f8)
0219 C.          ÷÷[HK1_PKT_FORM_NO]        EQ          7
0220 C.          ÷÷[HK1_PKT_GEN_TIME]        EQ          0.25 s
0221 C.          ÷÷[HK1_S_TLM_BIT_RATE]     EQ          32k
0222 C.          ÷÷[HK1_X_TLM_BIT_RATE]     EQ          4M
0223 C.          ÷÷[HK1_DMP_CHK_FLG]        EQ          EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªİ»oð³İÇ§
0226 C.          ÷÷[HK1_DMP_CHK_FLG]        EQ          NON
0227 C.
0228 C. RAM ID=TI_TBLoİ¼È¹ç•è²İOKoð³İÇ§
0229 C.
0230 C. DHUŷã;¼ŷÈ;È¼ŷ¼. ŷİ;¼ŷÈ;Èoðİão¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC          (02 0a f8)
0233 C.          ÷÷[HK1_PKT_FORM_NO]        EQ          2
0234 C.          ÷÷[HK1_PKT_GEN_TIME]        EQ          0.5S
0235 C.          ÷÷[HK1_S_TLM_BIT_RATE]     EQ          32K
0236 C.          ÷÷[HK1_X_TLM_BIT_RATE]     EQ          4M
0237 C.
0238 C.
0239 C. ***** XRT START *****
0240 C. Execute, after the success of OP upload.
0241 +. TI 2010-07-14 10:21:00.0
0242 DC 07-F0 MDP_XRT_MODE_STBY
0243 BC          (c3)
0244 C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0245 C.
0246 C. ***** XRT END *****
0247 C. Stop EIS observation and temporarily disable EIS mode changes
0248 C.
0249 C.
0250 C. ***** Start EIS operation (TI set) *****
0251 C. Execute, after the success of OP upload.
0252 C. Set EIS TI-commands
0253 +. TI 2010-07-14 10:21:30.0
0254 DC 07-FC EIS_MODE_MANU
0255 BC          (21 02)
0256 +. TI 2010-07-14 10:21:40.0
0257 DC 07-FC EIS_MODE_CHG_DIS
0258 BC          (22)
0259 C.          [ ] [HK1_TI_CMD_NUM]      EQ          2 COUNTUP
0260 C. ***** End EIS operation (TI set) *****
0261 C.
0262 C.
0263 C. *****
0264 C. SOT TI command set
0265 C. *****
0266 C. Execute, after the success of OP upload.
0267 +. TI 2010-07-14 10:21:16.0
0268 DC 07-F0 MDP_SOT_MODE_STBY
0269 BC          (41)
0270 C. -----
0271 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0272 C. -----
0273 C. ***** SOT END *****
0274 C.
0275 C. ***** MDP `ûÄİoİ»ö¼ŷoÈÄðo¹oèDCBC•x²è *****
0276 C. (¼ã°İŷÖŷÄŷÈŷpŷÈŷáŷçŷèoÈ¼o¼Ä»Űo¹oè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹•İ Daily±çİñoÈ'Øo¹oèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ¹¼Ä»Ű;ã
0287 C.
0288 C. ***** LOS *****
0289 C.
```



(a) Spacecraft Operation Procedure (real-commands)

```
main-468 2010-07-14 11:59:59 29 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YBYÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿áÁá•µ°Æ»ÍxÁÇáÍYçYÁYxYí;¼YÉ;ÈÈèµ•íÍÉ;ÈÈÈ¼°ÇÔá•á¿¼í¹çáÍ;çÀ®, ùá¹áèáááçÁ+¿®á•áÈááá³áÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 . C. ***** MDP ´úÁÍáÍ»ò¼YáÈÁá¹áèDCBC•x²è *****
0016 C. (¼á°íYÓYÁYÈYBYÉYáYçYèáÈ¼áá¼Á»Ûá¹áè)
0017 . S. DC-BC dcbc-402:DCBC
0018 (MDP_known_event)
0019 C.
0020 C.
0021 . C. ***** YDY¹•İ Daily+¿ÍÑáÈ´Øá¹áèDCBC•x²è *****
0022 . S. DC-BC dcbc-153:DCBC
0023 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0024 C.
0025 C.
0026 . C. ;ãLOSÁY$YÁY-¼Á»Û;ã
0027 C.
0028 . C. ***** LOS *****
0029 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-469 2010-07-14 11:59:59 118 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È□¿□□□•µ°È»Í×ÁÇ□íYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;È□È¼°Ç□□•□¿¼í¹ç□í;çÀ®, ù□¹□È□□□ÇÁ+¿®□•□È□□□□È;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 06 80 80 20 20)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 07 80 80 20 04)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 08 85 83 08 08)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 09 c0 c0 10 10)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 0a 40 c0 10 10)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0b 40 40 10 10)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0c c0 40 10 10)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0f 80 80 06 06)
0056 + DC 07-F0 MDP_XRT_AEC_RESET
0057 BC (d0)
0058 . C. ----- Success Verify ? OK / NG ____
0059 C.
0060 C.
0061 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0062 C.
0063 +. DC 07-F0 MDP_XRT_MODE_OBSV
0064 BC (c2)
0065 +. TI 2010-07-14 10:21:02.0
0066 DC 07-F0 MDP_XRT_MODE_OBSV
0067 BC (c2)
0068 . C. ----- Success Verify ? OK / NG ____
0069 C.
0070 C. ***** XRT END *****
0071 . C. *****
0072 C. SOT table upload
0073 C. *****
0074 . C. < Stop FG table >
0075 +. DC 07-F0 MDP_FG_CTRL_MANU
0076 BC (51)
0077 . C. -----
0078 C. MDP_FG_CTRL_MODE = MANU [ ]
0079 C. -----
0080 C.
0081 . C. <Upload FG Observation Table>
0082 . S. RAM ram-262:MDP_OBS_F
0083 ( )
0084 C.
0085 . C. < Dump RAMID=MDP_OBS_F >
0086 +. DC 07-F0 MDP_DUMP_FGTBL
0087 BC (82 07 00 00 00 38 b8)
0088 C. -----
0089 C. MDP_OBS_F verify = OK/NG [ ]
0090 C. -----
0091 C.
0092 C. *****
0093 C. SOT TI command set
0094 C. *****
0095 C. Execute, after the success of TBL upload.
```

```
0096 +. TI 2010-07-14 10:21:18.0
0097 DC 07-F0 MDP_SOT_MODE_OBSV
0098 BC (40)
0099 . C. -----
0100 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0101 C. -----
0102 C.
0103 C.
0104 . C. ***** MDP 'úÃîñî»ô¼ÝñÊÃðñ¹ñèDCBC•x²è *****
0105 C. (¼ã°îÝÓÝÃÝÊÝÞÝËÝáÝçÝèñ¼ññ¼Ã»Ûñ¹ñè)
0106 . S. DC-BC dcbc-402:DCBC
0107 (MDP_known_event)
0108 C.
0109 C.
0110 . C. ***** ÝÐÝ¹•Ï Daily±¿ÎÑñË´Øñ¹ñèDCBC•x²è *****
0111 . S. DC-BC dcbc-153:DCBC
0112 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0113 C.
0114 C.
0115 . C. ;ãLOSÝÁÝ§ÝÃÝ¬¼Ã»Û;ã
0116 C.
0117 . C. ***** LOS *****
0118 C.
```

Jul 14, 10 12:00

## XRT\_OGLIST\_0374.chk

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\*\*\* OP Sequence for XRT \*\*\*

2010/07/14	10:31:54.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/07/14	10:31:56.0	XRT_FOCUS_POSITION_401_OG [0x191]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2010/07/14	10:32:00.0	AOCS_Ore-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2010/07/14	10:32:16.0	XRT_FLD_DIS_402_OG [0x192]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2010/07/14	10:32:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2010/07/14	10:32:20.0	XRT_ARS_DIS_404_OG [0x194]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/07/14	10:34:58.0	XRT_QT_PROG_SET_442_OG [0x1ba]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09			
2010/07/14	10:35:00.0	XRT_CTRL_AUTO_406_OG [0x196]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/07/14	10:42:00.5	XRT_CTRL_MANU_440_OG [0x1b8]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/07/14	10:42:30.5	XRT_TCIB_XRT_S_HTR_A_ENA_417_OG [0x1a1]						
		TCIB_XRT_S_HTR_A_ENA	0	04-BC				
2010/07/14	11:41:00.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/07/14	12:42:30.5	XRT_Custom_414_OG [0x19e]						
2010/07/14	14:42:30.5	XRT_Custom_414_OG [0x19e]						
2010/07/14	16:32:00.0	AOCS_Ore-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	01 00 00 00 00			
2010/07/14	16:42:30.5	XRT_Custom_414_OG [0x19e]						
2010/07/14	18:42:30.5	XRT_Custom_434_OG [0x1b2]						
2010/07/14	18:42:40.5	XRT_Custom_414_OG [0x19e]						
2010/07/14	20:42:40.5	XRT_Custom_425_OG [0x1a9]						
2010/07/14	22:42:18.5	XRT_Custom_434_OG [0x1b2]						
2010/07/14	22:42:28.5	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/07/14	22:42:30.5	XRT_TCIB_XRT_S_HTR_A_DIS_433_OG [0x1b1]						
		TCIB_XRT_S_HTR_A_DIS	0	04-C0				
2010/07/14	22:43:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/07/14	22:43:02.0	XRT_QT_PROG_SET_415_OG [0x19f]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b			
2010/07/14	22:43:04.0	XRT_ARS_DIS_421_OG [0x1a5]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/07/14	22:43:06.0	XRT_FLD_DIS_427_OG [0x1ab]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2010/07/14	22:43:08.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2010/07/14	22:43:10.0	XRT_CTRL_AUTO_406_OG [0x196]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/07/15	04:44:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/07/15	04:44:02.0	XRT_FOCUS_POSITION_409_OG [0x199]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2010/07/15	04:44:22.0	XRT_FLD_RESET_412_OG [0x19c]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2010/07/15	04:44:24.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2010/07/15	04:44:26.0	XRT_FLRCTRL_ENA_413_OG [0x19d]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2010/07/15	04:44:28.0	XRT_ARS_DIS_446_OG [0x1be]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/07/15	04:44:30.0	XRT_QT_PROG_SET_443_OG [0x1bb]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12			
2010/07/15	04:44:32.0	XRT_FL_PROG_SET_416_OG [0x1a0]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01			
2010/07/15	04:44:34.0	XRT_CTRL_AUTO_406_OG [0x196]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/07/15	05:35:00.0	XRT_CTRL_MANU_408_OG [0x198]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/07/15	06:13:30.0	XRT_Custom_418_OG [0x1a2]						
2010/07/15	06:14:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/07/15	06:18:54.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/07/15	06:18:56.0	XRT_FOCUS_POSITION_401_OG [0x191]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2010/07/15	06:19:00.0	AOCS_Ore-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2010/07/15	06:19:16.0	XRT_FLD_DIS_402_OG [0x192]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2010/07/15	06:19:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2010/07/15	06:19:20.0	XRT_ARS_DIS_404_OG [0x194]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2010/07/15	06:21:58.0	XRT_QT_PROG_SET_442_OG [0x1ba]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09			
2010/07/15	06:22:00.0	XRT_CTRL_AUTO_406_OG [0x196]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2010/07/15	06:28:54.0	XRT_CTRL_MANU_424_OG [0x1a8]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2010/07/15	06:29:00.0	AOCS_Ore-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	00 2e f9 2e f9			
2010/07/15	06:31:32.0	XRT_FOCUS_POSITION_429_OG [0x1ad]						

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2010/07/15	06:31:52.0	XRT_QT_PROG_SET_435_OG [0x1b3]	4	07-F8	22 ff aa	00
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f	
2010/07/15	06:31:54.0	XRT_FLD_DIS_402_OG [0x192]				
		MDP_XRT_FLD_DIS	1	07-F0	d9	
2010/07/15	06:31:56.0	XRT_FLRCTRL_DIS_403_OG [0x193]				
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2010/07/15	06:31:58.0	XRT_ARS_DIS_446_OG [0x1be]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/07/15	06:32:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/07/15	06:38:54.0	XRT_CTRL_MANU_424_OG [0x1a8]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/07/15	06:39:00.0	AOCS_ORe-point_Start_4_OG [0x09a]				
		AOCU_NM	5	02-76	00 2e f9 d1	07
2010/07/15	06:41:32.0	XRT_FOCUS_POSITION_429_OG [0x1ad]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2010/07/15	06:41:52.0	XRT_QT_PROG_SET_436_OG [0x1b4]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2010/07/15	06:41:54.0	XRT_FLD_DIS_402_OG [0x192]				
		MDP_XRT_FLD_DIS	1	07-F0	d9	
2010/07/15	06:41:56.0	XRT_FLRCTRL_DIS_403_OG [0x193]				
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2010/07/15	06:41:58.0	XRT_ARS_DIS_446_OG [0x1be]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/07/15	06:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/07/15	06:48:54.0	XRT_CTRL_MANU_424_OG [0x1a8]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/07/15	06:49:00.0	AOCS_ORe-point_Start_5_OG [0x09b]				
		AOCU_NM	5	02-76	00 d1 07 d1	07
2010/07/15	06:51:32.0	XRT_FOCUS_POSITION_429_OG [0x1ad]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2010/07/15	06:51:52.0	XRT_QT_PROG_SET_444_OG [0x1bc]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c	
2010/07/15	06:51:54.0	XRT_FLD_DIS_402_OG [0x192]				
		MDP_XRT_FLD_DIS	1	07-F0	d9	
2010/07/15	06:51:56.0	XRT_FLRCTRL_DIS_403_OG [0x193]				
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2010/07/15	06:51:58.0	XRT_ARS_DIS_446_OG [0x1be]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/07/15	06:52:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/07/15	06:58:54.0	XRT_CTRL_MANU_424_OG [0x1a8]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/07/15	06:59:00.0	AOCS_ORe-point_Start_6_OG [0x09c]				
		AOCU_NM	5	02-76	00 d1 07 2e	f9
2010/07/15	07:01:32.0	XRT_FOCUS_POSITION_429_OG [0x1ad]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2010/07/15	07:01:52.0	XRT_QT_PROG_SET_448_OG [0x1c0]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2010/07/15	07:01:54.0	XRT_FLD_DIS_402_OG [0x192]				
		MDP_XRT_FLD_DIS	1	07-F0	d9	
2010/07/15	07:01:56.0	XRT_FLRCTRL_DIS_403_OG [0x193]				
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2010/07/15	07:01:58.0	XRT_ARS_DIS_446_OG [0x1be]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/07/15	07:02:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/07/15	07:08:54.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/07/15	07:08:56.0	XRT_FOCUS_POSITION_409_OG [0x199]				
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2010/07/15	07:09:00.0	AOCS_ORe-point_Start_2_OG [0x098]				
		AOCU_NM	5	02-76	01 00 00 00	00
2010/07/15	07:09:16.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2010/07/15	07:09:18.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2010/07/15	07:09:20.0	XRT_FLRCTRL_ENA_413_OG [0x19d]				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2010/07/15	07:09:22.0	XRT_ARS_DIS_420_OG [0x1a4]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/07/15	07:11:56.0	XRT_QT_PROG_SET_410_OG [0x19a]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02	
2010/07/15	07:11:58.0	XRT_FL_PROG_SET_416_OG [0x1a0]				
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01	
2010/07/15	07:12:00.0	XRT_CTRL_AUTO_406_OG [0x196]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/07/15	07:15:30.0	XRT_CTRL_MANU_408_OG [0x198]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/07/15	07:52:00.0	XRT_Custom_418_OG [0x1a2]				
2010/07/15	07:53:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/07/15	08:08:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/07/15	08:21:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/07/15	08:21:02.0	XRT_FOCUS_POSITION_409_OG [0x199]				
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2010/07/15	08:21:22.0	XRT_FLD_RESET_412_OG [0x19c]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2010/07/15	08:21:24.0	XRT_FLD_ENA_411_OG [0x19b]				

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2010/07/15	08:21:26.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8	
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2010/07/15	08:21:28.0	XRT_ARS_DIS_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/07/15	08:21:30.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02
2010/07/15	08:21:32.0	XRT_FL_PROG_SET_416_OG [0x1a0]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	01
2010/07/15	08:21:34.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/07/15	08:55:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/07/15	09:30:30.0	XRT_Custom_418_OG [0x1a2]					
2010/07/15	09:31:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/07/15	10:37:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/07/15	10:52:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/07/15	11:09:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00	00 00 00 00 00