

XRT Timeline to be uploaded on 2010/07/27

Period: 2010/07/27 10:46:00 - 2010/07/31 10:32:00

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Normal mode

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XOB #1775: HOP130 Al/mesh short(128ms)/long(2048ms) + Ti/poly short(256ms)/long(4096ms) FOV 512x512 - bin 1x1												
Term	Pointing (x, y)		Comment									
07/27 10:59:02 - 07/27 11:11:54	Fixed (936.0, 0.0)	# OP start + 10min HOP130 1/15									
07/27 11:15:02 - 07/27 12:02:54	Fixed (468.0, 0.0)	2/15									
07/27 12:06:02 - 07/27 12:18:54	Fixed (0.0, 0.0)	3/15									
07/27 12:22:02 - 07/27 12:34:54	Fixed (-	468.0, 0.0)	4/15									
07/27 12:38:02 - 07/27 12:50:54	Fixed (-	936.0, 0.0)	5/15									
07/27 12:54:02 - 07/27 13:41:54	Fixed (815.0, 470.0)	6/15									
07/27 13:45:02 - 07/27 13:57:54	Fixed (411.0, 847.0)	7/15									
07/27 14:01:02 - 07/27 14:13:54	Fixed (0.0, 941.0)	8/15									
07/27 14:17:02 - 07/27 14:29:54	Fixed (-	411.0, 847.0)	9/15									
07/27 14:33:02 - 07/27 15:35:30	Fixed (-	815.0, 470.0)	10/15									
07/27 15:41:00 - 07/27 15:53:54	Fixed (815.0, -470.0)	11/15									
07/27 15:57:02 - 07/27 16:09:54	Fixed (411.0, -847.0)	12/15									
07/27 16:13:02 - 07/27 16:29:30	Fixed (0.0, -941.0)	13/15									
07/27 17:18:00 - 07/27 17:30:54	Fixed (-	411.0, -847.0)	14/15									
07/27 17:34:02 - 07/27 17:54:54	Fixed (-	815.0, -471.0)	15/15									
PROG= 08 1-time(s)												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 62 1-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	125ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 60 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	4.00s	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 61 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	512x512 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)												
Term	Pointing (x, y)		Comment									
07/27 17:58:00 - 07/27 18:08:00	Fixed (0.0, 0.0)	synoptic, shifted -5.0 min									
07/28 06:11:30 - 07/28 06:18:24	Fixed (0.0, 0.0)	synoptic, shifted 8.5 min									
07/28 18:10:00 - 07/28 18:16:54	Fixed (0.0, 0.0)	synoptic, shifted 7.0 min									
07/29 05:41:30 - 07/29 08:47:54	Fixed (0.0, 0.0)	synoptic, shifted -22.0 min +3 hours for SOT flat field obs.									
PROG= 03 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 7 1-time(s) 4.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 8 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 4 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #17EE: AR Standard-B(Morphology) for FW1=Open, 384x384 at 1064 1048, 10sec-cad												
Term	Pointing (x, y)		Comment									
07/28 00:09:30 - 07/28 04:29:54	Track (477.3, -469.7)	# Track AR11089									
PROG= 16 Inf.-time(s)												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 76 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 95 4-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 25 240-time(s) 10.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #17ED: AR Standard-A(Filter-Ratio) for FW1=Open, 384x384 at 1064 1048, 2min-cad												
Term	Pointing (x, y)		Comment									

	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subbr= 2	1-time(s)	2.0sec											
	Seqn= 90	1-time(s)	2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subbr= 3	30-time(s)	60.0sec											
	Seqn= 87	1-time(s)	2.0sec										
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 88	1-time(s)	2.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subbr= 2	1-time(s)	2.0sec											
	Seqn= 90	1-time(s)	2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subbr= 3	30-time(s)	60.0sec											
	Seqn= 87	1-time(s)	2.0sec										
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 88	1-time(s)	2.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subbr= 4	24-time(s)	600.0sec											
	Seqn= 89	1-time(s)	2.0sec										
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

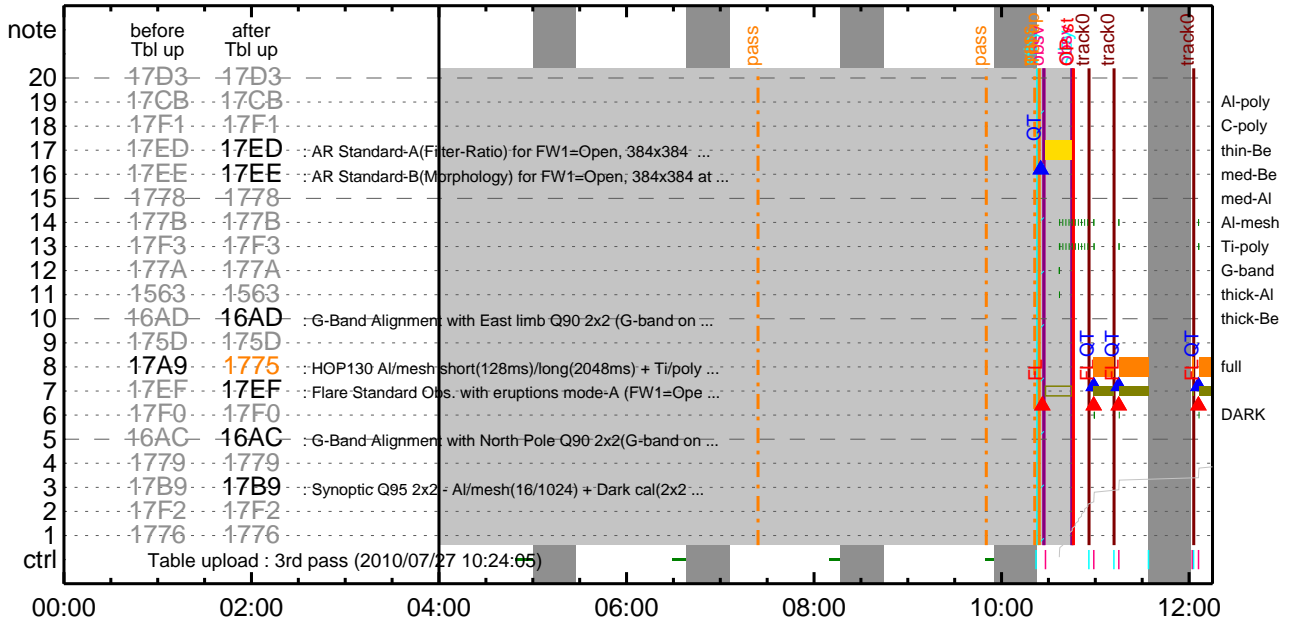
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Flare Detection

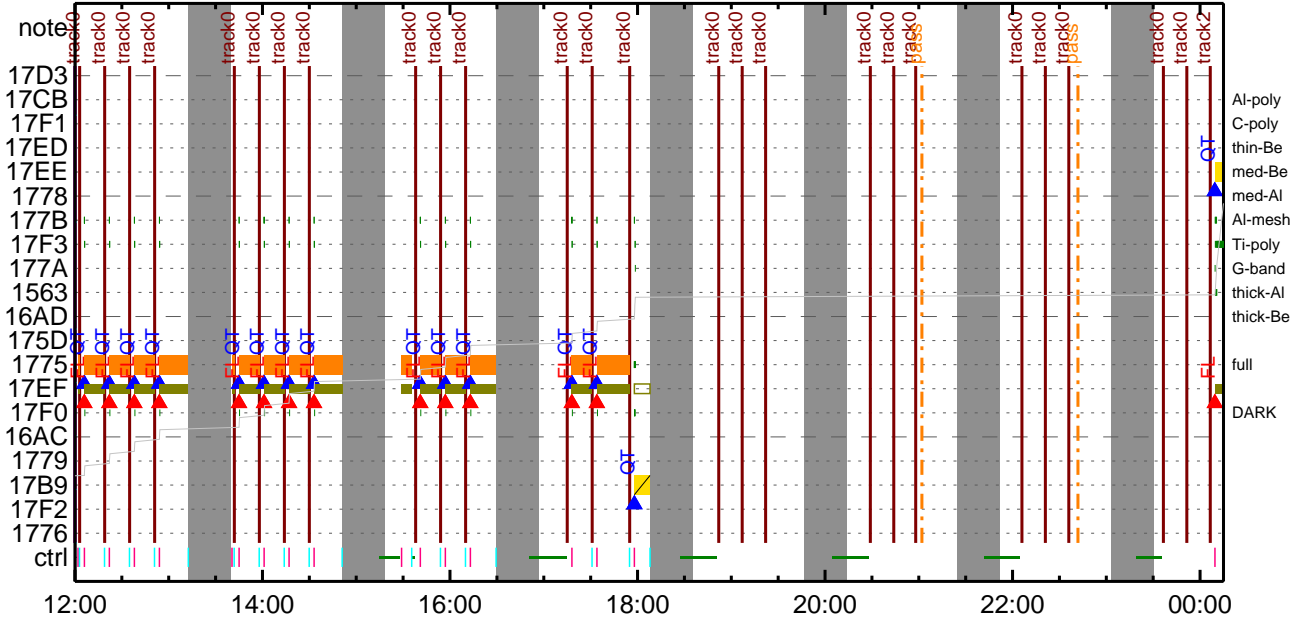
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FLD Patrol												
Term	Pointing (x, y)								Comment			
07/27 10:56:16 - 07/27 17:55:16	Fixed (936.0, 0.0)								# OP start + 10min HOP130 1/15			
07/28 00:06:46 - 07/28 06:08:46	Track (477.3, -469.7) ^{Ⓢ 07/28 00:06:30}								# Track AR11089			
07/28 06:18:46 - 07/28 18:07:16	Track (517.8, -467.2) ^{Ⓢ 07/28 06:18:30}								# Track AR11089			
07/28 22:17:16 - 07/29 05:38:46	Track (613.7, -459.4) ^{Ⓢ 07/28 22:17:00}								# Track AR11089			
07/29 08:48:16 - 07/31 10:32:00	Track (669.4, -453.5) ^{Ⓢ 07/29 08:48:00}								# Track AR11089			
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

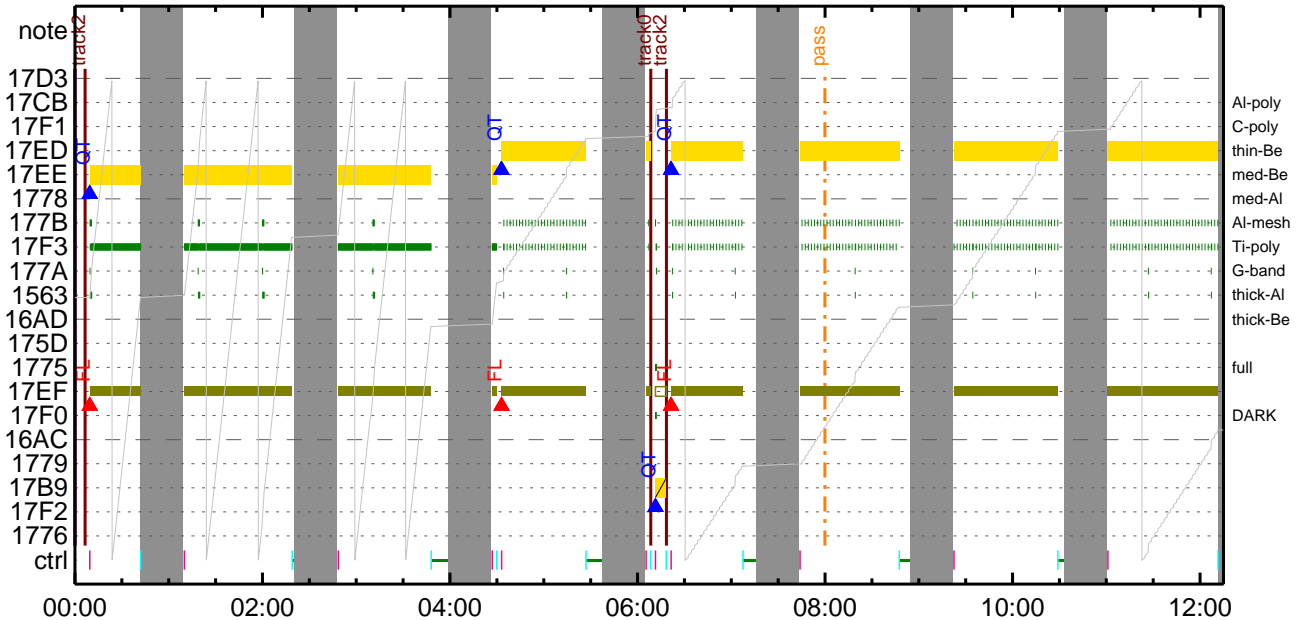
CMDI #0396 2010/07/27



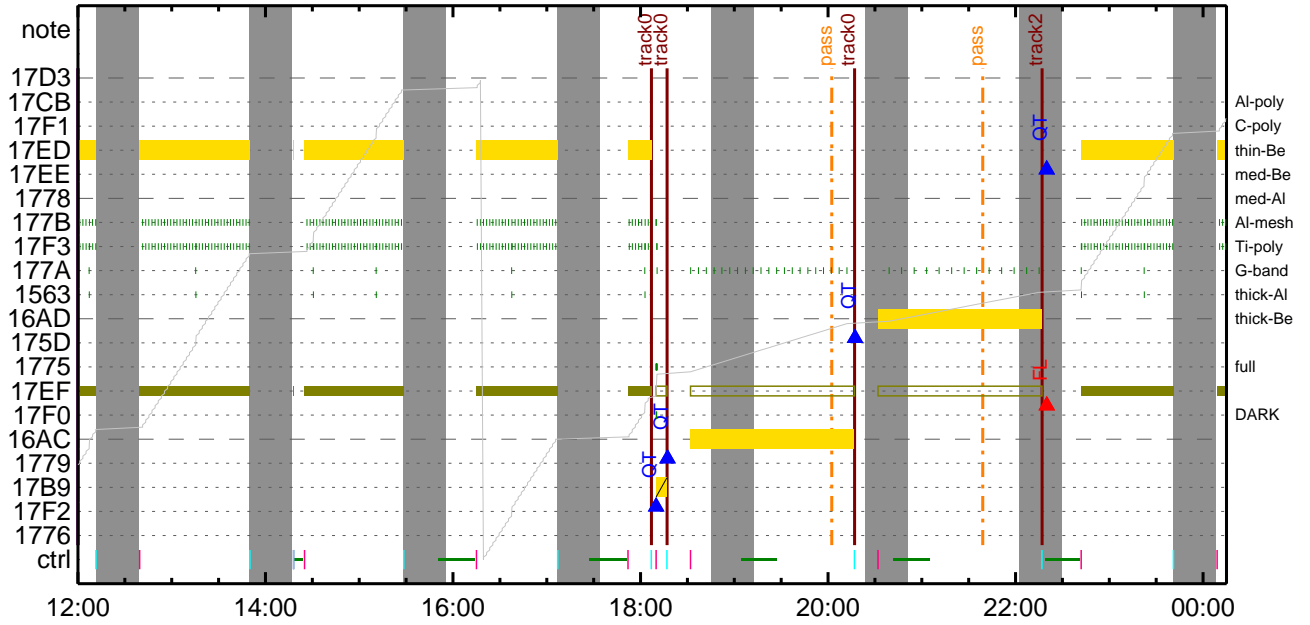
CMDI #0396 2010/07/27



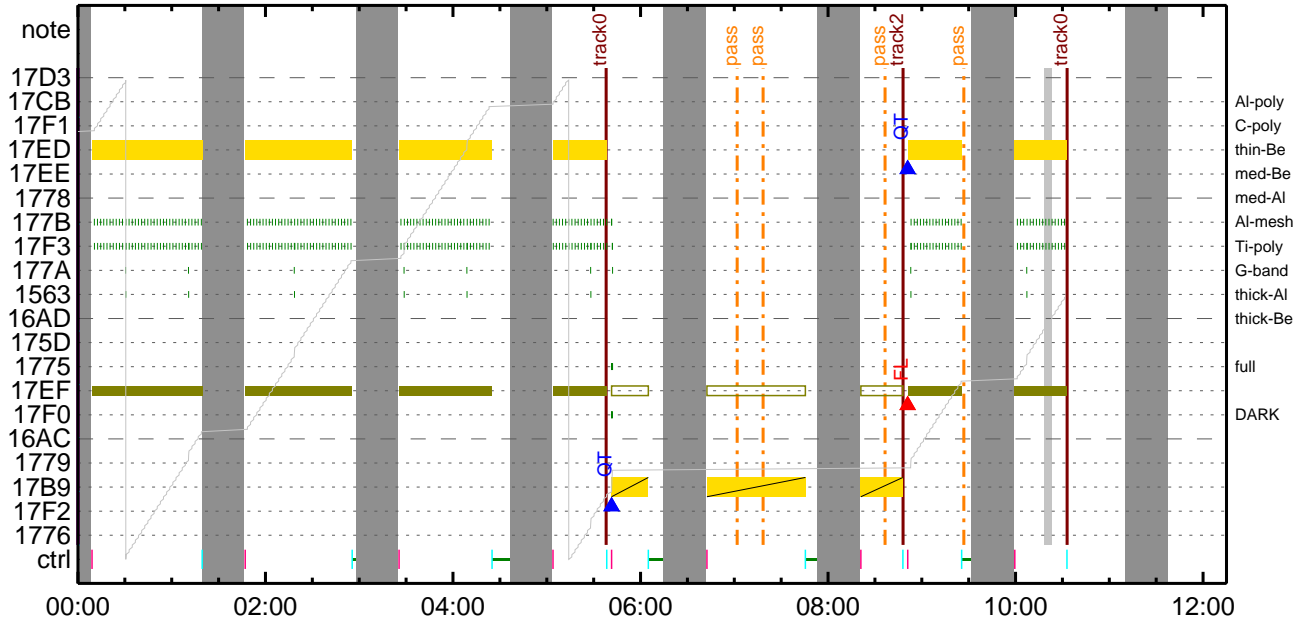
CMDI #0396 2010/07/28



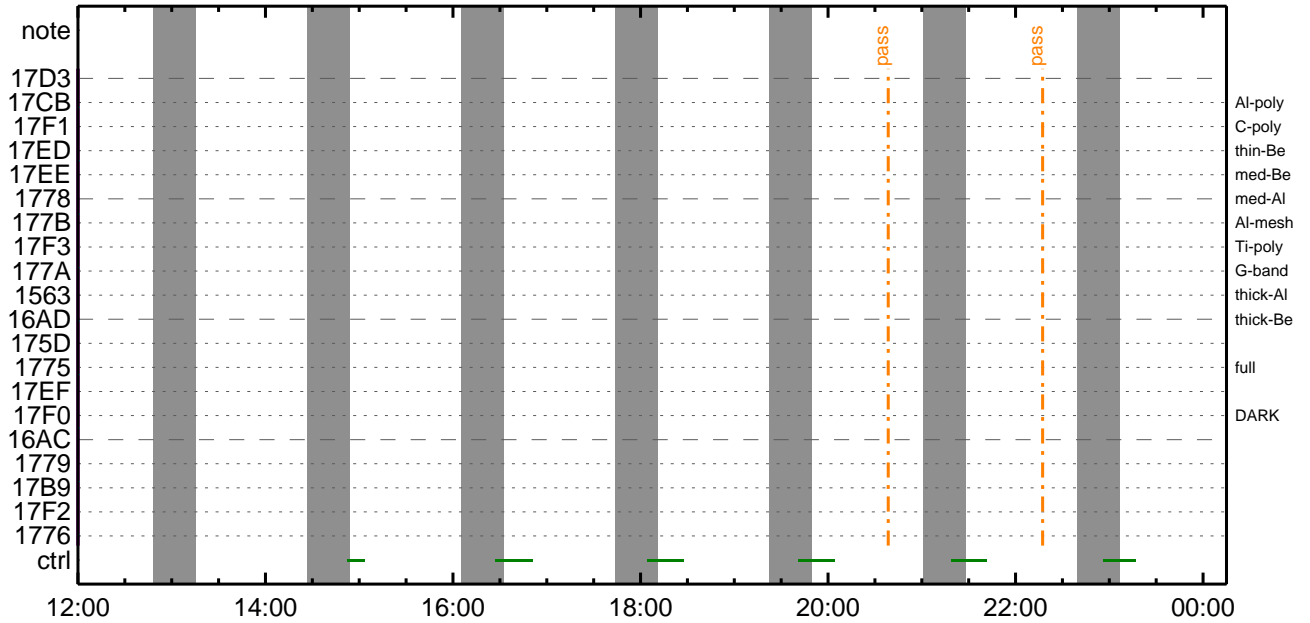
CMDI #0396 2010/07/28



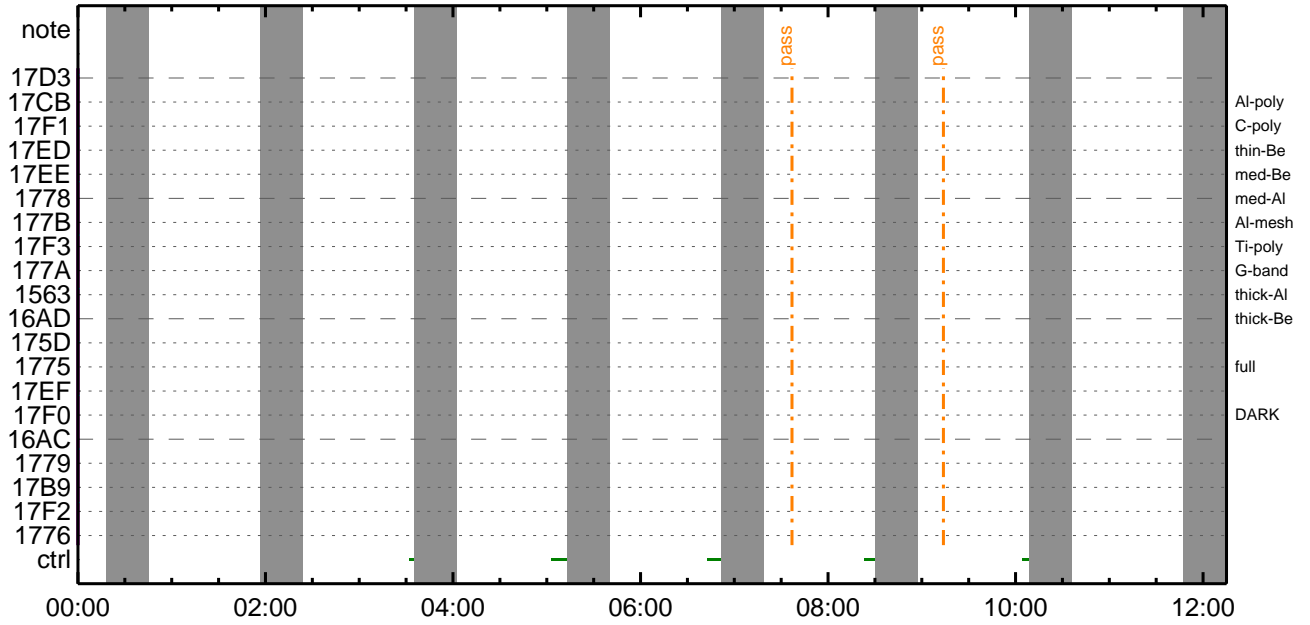
CMDI #0396 2010/07/29



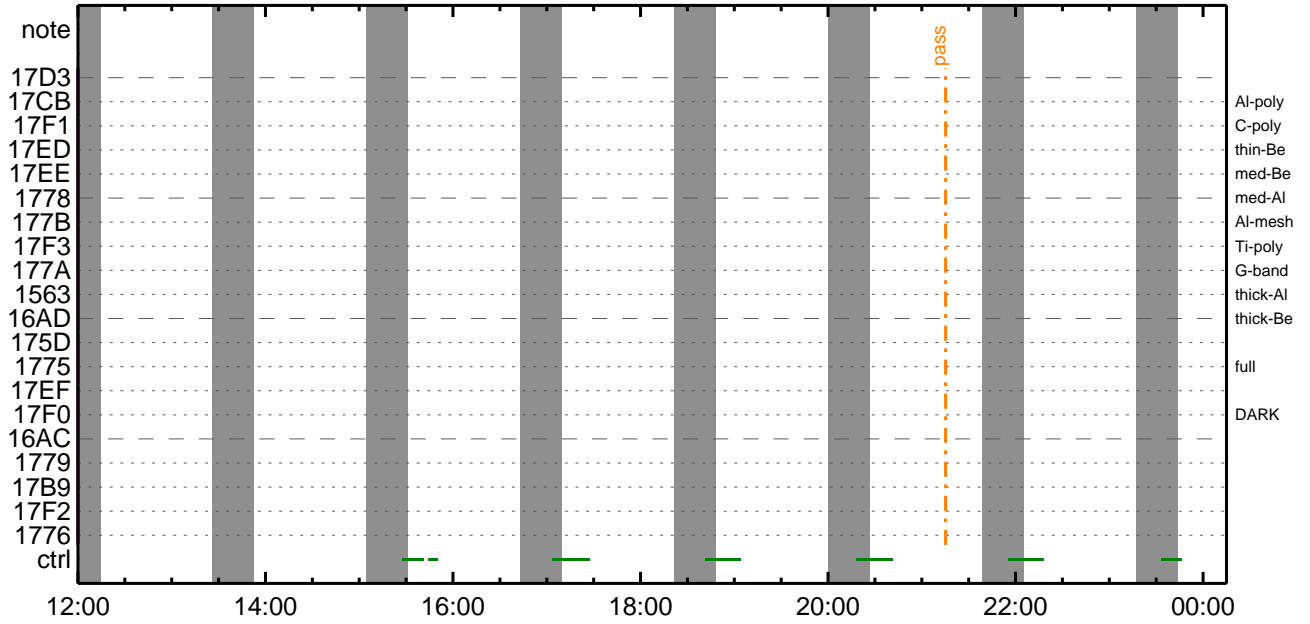
CMDI #0396 2010/07/29



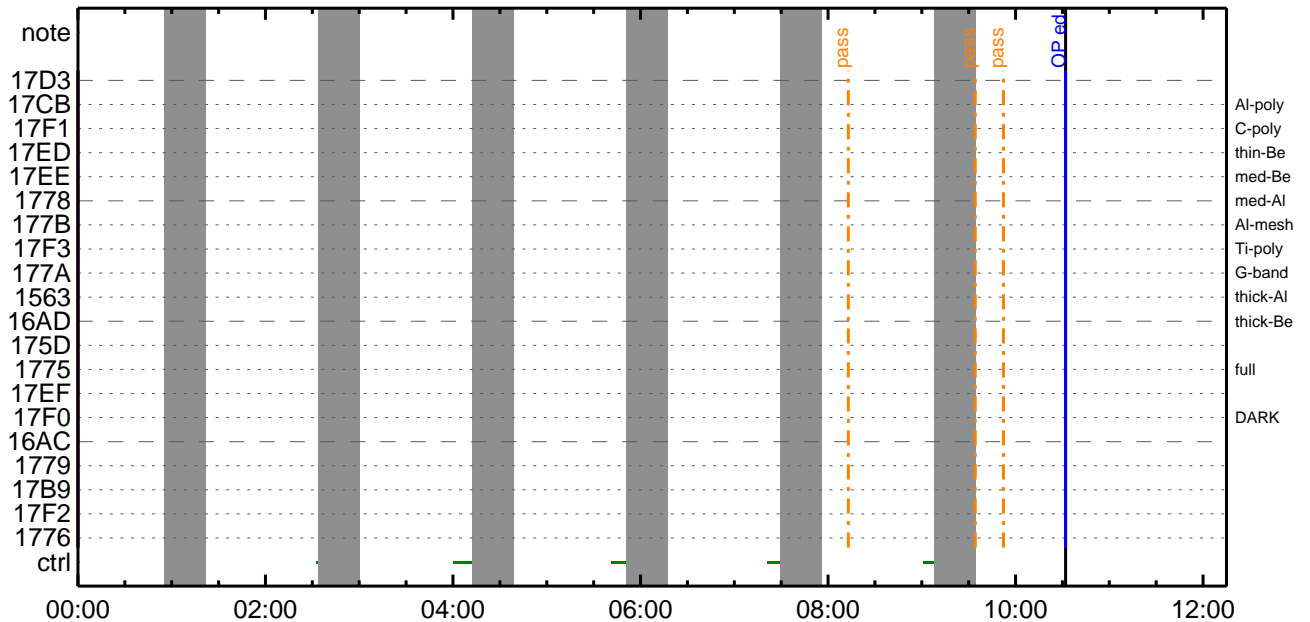
CMDI #0396 2010/07/30



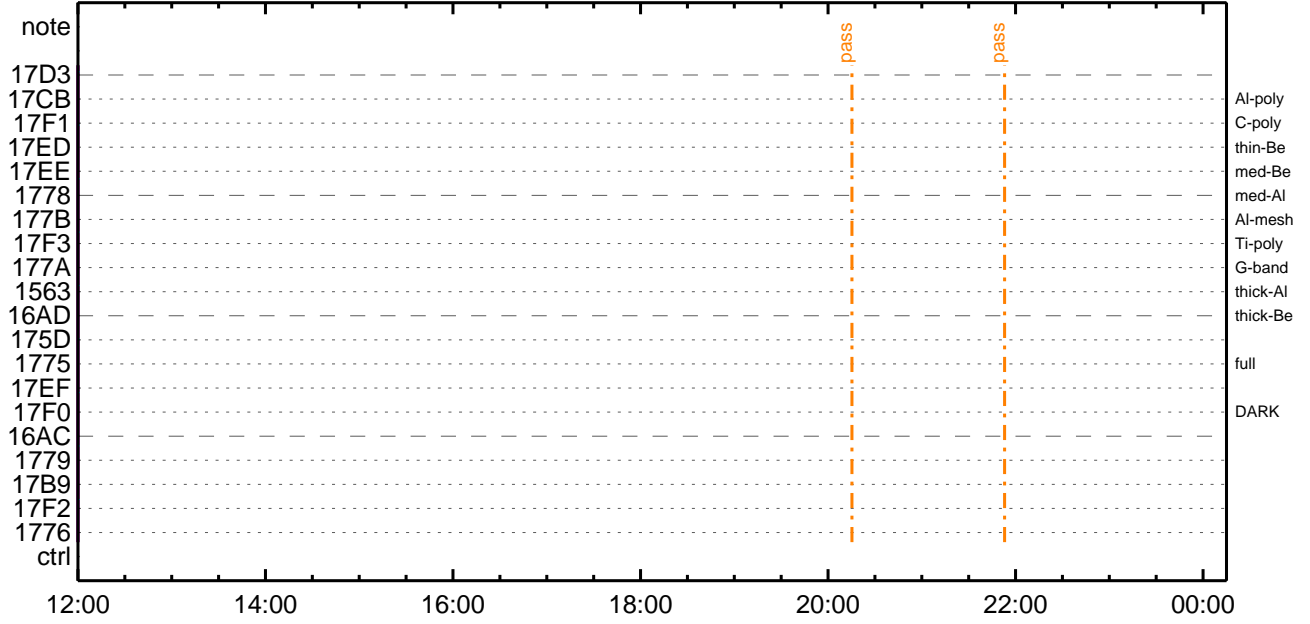
CMDI #0396 2010/07/30



CMDI #0396 2010/07/31



CMDI #0396 2010/07/31




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-491:OP
0104 ( )
0105 S. OG og-491:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¼E¹ç•è²îOK²³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¼E¹ç•è²îOK²³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î¼E¹ç•è²îOK²³îÇ§
0165 C.
0166 C. ***** °E²¼²î¼A´¶Á°EÉ-²°A÷¿@ (¼âµ-YAYOYx½ªî»ò³îÇ§²°AÖAæ²ç¼ª°²°E¼¹¹ç²ç²â) *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;E²òîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î¼¹¹ç;ç°E²¼²î¼TI-CMDÁ÷¿@²î¼¹¹Ô²°²E²²²³²E;f
0180 C. ²²²¿;çSET²E²DUMP²î¼E²±²î¼Y¹¹ç¹¹Ô²²³²E;f
0181 C.
0182 C. TIY³Y²Y³Y²Y³E²²òÁDî¿(UT)
0183 +. TI 2010-07-27 10:41:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2010-07-27 10:41:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2010-07-27 10:41:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
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0194 C.
0195 +. TI 2010-07-27 10:45:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0198 C.
0199 C. °È²¼ªíÄè%ííñªîŷÄŷ§ŷÄŷ-¹àîŷ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ          ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ          4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ          DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]        EQ          0xB3
0204 C.
0205 C. *****
0206 C. TIîŷ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC          (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ          07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ          2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ          3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ          0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC          (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ          7
0220 C.          çç[HK1_PKT_GEN_TIME]        EQ          0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]     EQ          32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]     EQ          4M
0223 C.          çç[HK1_DMP_CHK_FLG]        EQ          EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»ª³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]        EQ          NON
0227 C.
0228 C. RAM ID=TI_TBLªîŷÈ¹çªè²îOKª³îç§
0229 C.
0230 C. DHUŷª;¼ŷÈ;È¼ŷ¼. ŷî;¼ŷÈ;Èª³îª¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC          (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ          2
0234 C.          çç[HK1_PKT_GEN_TIME]        EQ          0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]     EQ          32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]     EQ          4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2010-07-27 10:45:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC          (21 02)
0247 +. TI 2010-07-27 10:45:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC          (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]      EQ          2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C. *****
0255 C. SOT TI command set
0256 C. *****
0257 C. Execute, after the success of OP upload.
0258 +. TI 2010-07-27 10:45:16.0
0259 DC 07-F0 MDP_SOT_MODE_STBY
0260 BC          (41)
0261 C. -----
0262 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0263 C. -----
0264 C. ***** SOT END *****
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2010-07-27 10:45:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC          (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîªî»ö¼ŷªÈÄª¹ªèDCBCª²è *****
0276 C. (¼ªªîŷÖŷÄŷÈŷŷŷÄŷçŷèÈ¼ªªªª»Ûª¹ªè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷÐŷ¹.î Daily±çîñªÈ'ªª¹ªèDCBCª²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼ª»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-493 2010-07-27 14:15:06 165 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;äAOSYÄY$YÄY-¼Ä»Ü;ä
0005 C.
0006 C. YÄY$;¼Y³YF¥ÖYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Äí;Ë¿¿ªÄª•µ°Æ»Í×ÄÇªÍY¿YÄY×Yí;¼YÉ;ËÈ¿µ•íÉ;ËªÈ¼°ÇÖª•ª¿¼i¹¿ªÍ;¿Ä®; ùª¹ªËªªªªÇÄ+¿®ª•ªËªªªªªªË; f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-267:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Upload DPL table >
0036 C.
0037 C. Y¿YÄY×Yí;¼YÉªíÄ°ªËSTS_CHKªðOFFªËª¹ªË
0038 C.
0039 . S. RAM ram-271:MDP_DPL
0040 ( )
0041 C.
0042 . C. < Dump RAMID=MDP_DPL >
0043 +. DC 07-F0 MDP_DUMP_FGTBL
0044 BC (82 07 00 38 b8 00 40)
0045 C. -----
0046 C. MDP_DPL verify = OK [ ]
0047 C. -----
0048 C.
0049 C. STS_CHKªðONªËª¹ªË
0050 C.
0051 . C. < Update MDP DSC PAR1 >
0052 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0053 BC (4c)
0054 C. MDP_CMD_CODE = F04C0700[ ]
0055 C. MDP_CMD_CNT (count-up 1) [ ]
0056 C. -----
0057 C.
0058 . C.
0059 . C. < Resume FG table (auto mode) >
0060 +. DC 07-F0 MDP_FG_CTRL_AUTO
0061 BC (50)
0062 . C. -----
0063 C. MDP_FG_CTRL_MODE = AUTO [ ]
0064 C. -----
0065 C.
0066 C. *****
0067 C. SOT TI command set
0068 C. *****
0069 C. Execute, after the success of TBL upload.
0070 +. TI 2010-07-27 10:45:18.0
0071 DC 07-F0 MDP_SOT_MODE_OBSV
0072 BC (40)
0073 . C. -----
0074 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0075 C. -----
0076 C.
0077 C. Only when FG_CTRL_AUTO is used in RT.
0078 +. TI 2010-07-27 10:45:20.0
0079 DC 07-F0 MDP_FG_CTRL_AUTO
0080 BC (50)
0081 . C. -----
0082 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0083 C. -----
0084 C. ***** SOT END *****
0085 C.
0086 C. ***** XRT START *****
0087 C.
0088 +. DC 07-F0 MDP_XRT_CTRL_MANU
0089 BC (c1)
0090 +. DC 07-F0 MDP_XRT_MODE_STBY
0091 BC (c3)
0092 . C. ----- Success Verify ? OK / NG_____
0093 C.
0094 C. XRT Obs. Table Upload
0095 . S. RAM ram-291:MDP_OBS_X
```

```

0096      ( )
0097      C.
0098 +. DC 07-F0 MDP_DUMP_XRTTBL
0099      BC      (84 07 00 00 00 3a d4)
0100 . C. ----- Comparison Check ?                    OK / ERR ____
0101      C.
0102      C.
0103 +. DC 07-F0 MDP_XRT_ROI_SET
0104      BC      (cd 01 b1 b1 04 04)
0105 + DC 07-F0 MDP_XRT_ROI_SET
0106      BC      (cd 02 b1 b1 08 08)
0107 + DC 07-F0 MDP_XRT_ROI_SET
0108      BC      (cd 03 b1 b1 08 08)
0109 + DC 07-F0 MDP_XRT_ROI_SET
0110      BC      (cd 04 b1 b1 06 06)
0111 + DC 07-F0 MDP_XRT_ROI_SET
0112      BC      (cd 06 85 83 06 06)
0113 + DC 07-F0 MDP_XRT_ROI_SET
0114      BC      (cd 07 80 80 20 20)
0115 + DC 07-F0 MDP_XRT_ROI_SET
0116      BC      (cd 08 80 80 20 08)
0117 + DC 07-F0 MDP_XRT_ROI_SET
0118      BC      (cd 09 80 80 08 20)
0119 + DC 07-F0 MDP_XRT_ROI_SET
0120      BC      (cd 0a 80 80 08 08)
0121 + DC 07-F0 MDP_XRT_ROI_SET
0122      BC      (cd 0b 80 60 20 18)
0123 + DC 07-F0 MDP_XRT_ROI_SET
0124      BC      (cd 0c a0 80 18 20)
0125 + DC 07-F0 MDP_XRT_ROI_SET
0126      BC      (cd 0f 80 80 06 06)
0127 + DC 07-F0 MDP_XRT_ROI_SET
0128      BC      (cd 10 80 80 08 08)
0129 + DC 07-F0 MDP_XRT_AEC_RESET
0130      BC      (d0)
0131 . C. ----- Success Verify ?                    OK / NG ____
0132      C.
0133 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0134      BC      (c4 11)
0135 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0136      BC      (c5 07)
0137      C.
0138 . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0139      C.
0140 +. DC 07-F0 MDP_XRT_MODE_OBSV
0141      BC      (c2)
0142 +. DC 07-F0 MDP_XRT_CTRL_AUTO
0143      BC      (c0)
0144 +. TI 2010-07-27 10:45:02.0
0145      DC 07-F0 MDP_XRT_MODE_OBSV
0146      BC      (c2)
0147 . C. ----- Success Verify ?                    OK / NG ____
0148      C.
0149      C. ***** XRT END *****
0150      C.
0151 . C. ***** MDP `0ÃÃîî»ô%ÿòÈÂð¹æèDCBC•x²è *****
0152 . C. (%ã°îÿÓÿÃÿÈÿPÿËÿÿáÿçÿèøE%¼ã¼Ã»Û¹æè)
0153 . S. DC-BC dcbc-402:DCBC
0154      (MDP_known_event)
0155      C.
0156      C.
0157 . C. ***** ÿÐÿ¹•Ï Daily±¿îÑøÈ´ø¹æèDCBC•x²è *****
0158 . S. DC-BC dcbc-153:DCBC
0159      (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0160      C.
0161      C.
0162 . C. ;ãLOSÿÃÿSÿÿÿ¼Ã»Û¿ã
0163      C.
0164 . C. ***** LOS *****
0165      C.

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*** OP Sequence for XRT ***

2010/07/27	10:55:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/07/27	10:55:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/07/27	10:56:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 00 00 ac cd		
2010/07/27	10:56:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/07/27	10:56:18.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/07/27	10:56:20.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/07/27	10:56:22.0	XRT_ARS_DIS_424_OG [0x1a8]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/07/27	10:58:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08		
2010/07/27	10:59:00.0	XRT_FL_PROG_SET_405_OG [0x195]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2010/07/27	10:59:02.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/07/27	11:11:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/07/27	11:11:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/07/27	11:12:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 d6 67		
2010/07/27	11:12:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/07/27	11:12:18.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/07/27	11:12:20.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/07/27	11:12:22.0	XRT_ARS_DIS_424_OG [0x1a8]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/07/27	11:14:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08		
2010/07/27	11:15:00.0	XRT_FL_PROG_SET_405_OG [0x195]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2010/07/27	11:15:02.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/07/27	11:34:00.0	XRT_CTRL_MANU_408_OG [0x198]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/07/27	12:01:00.0	XRT_Custom_418_OG [0x1a2]					
2010/07/27	12:02:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/07/27	12:02:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/07/27	12:02:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/07/27	12:03:00.0	AOCS_Ore-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2010/07/27	12:03:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/07/27	12:03:18.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/07/27	12:03:20.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/07/27	12:03:22.0	XRT_ARS_DIS_424_OG [0x1a8]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/07/27	12:05:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08		
2010/07/27	12:06:00.0	XRT_FL_PROG_SET_405_OG [0x195]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2010/07/27	12:06:02.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/07/27	12:18:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/07/27	12:18:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/07/27	12:19:00.0	AOCS_Ore-point_Start_4_OG [0x09a]					
		AOCU_NM	5	02-76	00 00 00 29 99		
2010/07/27	12:19:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2010/07/27	12:19:18.0	XRT_FLD_RESET_412_OG [0x19c]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2010/07/27	12:19:20.0	XRT_FLRCTRL_ENA_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2010/07/27	12:19:22.0	XRT_ARS_DIS_424_OG [0x1a8]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2010/07/27	12:21:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08		
2010/07/27	12:22:00.0	XRT_FL_PROG_SET_405_OG [0x195]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2010/07/27	12:22:02.0	XRT_CTRL_AUTO_406_OG [0x196]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2010/07/27	12:34:54.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2010/07/27	12:34:56.0	XRT_FOCUS_POSITION_409_OG [0x199]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2010/07/27	12:35:00.0	AOCS_Ore-point_Start_5_OG [0x09b]					
		AOCU_NM	5	02-76	00 00 00 53 33		

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2010/07/27	12:35:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/07/27	12:35:18.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/07/27	12:35:20.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/07/27	12:35:22.0	XRT_ARS_DIS_424_OG [0x1a8]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/07/27	12:37:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2010/07/27	12:38:00.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2010/07/27	12:38:02.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/07/27	12:50:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/07/27	12:50:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/07/27	12:51:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 d6 36 b7 8e				
2010/07/27	12:51:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/07/27	12:51:18.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/07/27	12:51:20.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/07/27	12:51:22.0	XRT_ARS_DIS_424_OG [0x1a8]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/07/27	12:53:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2010/07/27	12:54:00.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2010/07/27	12:54:02.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/07/27	13:12:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/07/27	13:39:30.0	XRT_Custom_418_OG [0x1a2]							
2010/07/27	13:40:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/07/27	13:41:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/07/27	13:41:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/07/27	13:42:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00 b4 b5 db 75				
2010/07/27	13:42:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/07/27	13:42:18.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/07/27	13:42:20.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/07/27	13:42:22.0	XRT_ARS_DIS_424_OG [0x1a8]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/07/27	13:44:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2010/07/27	13:45:00.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2010/07/27	13:45:02.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/07/27	13:57:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/07/27	13:57:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/07/27	13:58:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00 ac 5b 00 00				
2010/07/27	13:58:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/07/27	13:58:18.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/07/27	13:58:20.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/07/27	13:58:22.0	XRT_ARS_DIS_424_OG [0x1a8]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/07/27	14:00:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2010/07/27	14:01:00.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2010/07/27	14:01:02.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/07/27	14:13:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/07/27	14:13:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/07/27	14:14:00.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00 b4 b5 24 8b				
2010/07/27	14:14:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/07/27	14:14:18.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/07/27	14:14:20.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/07/27	14:14:22.0	XRT_ARS_DIS_424_OG [0x1a8]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				

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2010/07/27	14:16:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08
2010/07/27	14:17:00.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2010/07/27	14:17:02.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/07/27	14:29:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/07/27	14:29:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2010/07/27	14:30:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	00	d6 36 48 72
2010/07/27	14:30:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2010/07/27	14:30:18.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/07/27	14:30:20.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2010/07/27	14:30:22.0	XRT_ARS_DIS_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/07/27	14:32:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08
2010/07/27	14:33:00.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2010/07/27	14:33:02.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/07/27	14:51:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/07/27	15:28:00.0	XRT_Custom_418_OG [0x1a2]					
2010/07/27	15:29:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/07/27	15:35:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/07/27	15:37:54.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2010/07/27	15:38:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	00	29 ca b7 8e
2010/07/27	15:38:14.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2010/07/27	15:38:16.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/07/27	15:38:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2010/07/27	15:38:20.0	XRT_ARS_DIS_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/07/27	15:40:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08
2010/07/27	15:40:58.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2010/07/27	15:41:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/07/27	15:53:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/07/27	15:53:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2010/07/27	15:54:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00	4b 4b db 75
2010/07/27	15:54:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2010/07/27	15:54:18.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/07/27	15:54:20.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2010/07/27	15:54:22.0	XRT_ARS_DIS_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/07/27	15:56:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08
2010/07/27	15:57:00.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2010/07/27	15:57:02.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/07/27	16:09:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/07/27	16:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2010/07/27	16:10:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	00	53 a5 00 00
2010/07/27	16:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2010/07/27	16:10:18.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/07/27	16:10:20.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2010/07/27	16:10:22.0	XRT_ARS_DIS_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/07/27	16:12:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08
2010/07/27	16:13:00.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2010/07/27	16:13:02.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/07/27	16:29:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2010/07/27	17:14:54.0	XRT_FOCUS_POSITION_409_OG [0x199] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/07/27	17:15:00.0	AOCS_ORe-point_Start_14_OG [0x0a4] AOCU_NM	5	02-76	00 4b 4b 24 8b
2010/07/27	17:15:14.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2010/07/27	17:15:16.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da
2010/07/27	17:15:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/07/27	17:15:20.0	XRT_ARS_DIS_424_OG [0x1a8] MDP_XRT_ARS_DIS	1	07-F0	d5
2010/07/27	17:17:56.0	XRT_QT_PROG_SET_425_OG [0x1a9] MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2010/07/27	17:17:58.0	XRT_FL_PROG_SET_405_OG [0x195] MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2010/07/27	17:18:00.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/07/27	17:30:54.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/07/27	17:30:56.0	XRT_FOCUS_POSITION_409_OG [0x199] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/07/27	17:31:00.0	AOCS_ORe-point_Start_15_OG [0x0a5] AOCU_NM	5	02-76	00 29 db 48 72
2010/07/27	17:31:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2010/07/27	17:31:18.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da
2010/07/27	17:31:20.0	XRT_FLRCTRL_ENA_413_OG [0x19d] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/07/27	17:31:22.0	XRT_ARS_DIS_424_OG [0x1a8] MDP_XRT_ARS_DIS	1	07-F0	d5
2010/07/27	17:33:58.0	XRT_QT_PROG_SET_425_OG [0x1a9] MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2010/07/27	17:34:00.0	XRT_FL_PROG_SET_405_OG [0x195] MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2010/07/27	17:34:02.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/07/27	17:54:54.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/07/27	17:54:56.0	XRT_FOCUS_POSITION_401_OG [0x191] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/07/27	17:55:00.0	AOCS_ORe-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 00 00 00 00
2010/07/27	17:55:16.0	XRT_FLD_DIS_402_OG [0x192] MDP_XRT_FLD_DIS	1	07-F0	d9
2010/07/27	17:55:18.0	XRT_FLRCTRL_DIS_403_OG [0x193] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2010/07/27	17:55:20.0	XRT_ARS_DIS_404_OG [0x194] MDP_XRT_ARS_DIS	1	07-F0	d5
2010/07/27	17:57:58.0	XRT_QT_PROG_SET_407_OG [0x197] MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2010/07/27	17:58:00.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/07/27	18:08:00.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/07/27	18:52:00.0	AOCS_ORe-point_Start_16_OG [0x0a6] AOCU_NM	5	02-76	00 ed 59 51 68
2010/07/27	19:07:00.0	AOCS_ORe-point_Start_17_OG [0x0a7] AOCU_NM	5	02-76	00 f1 82 42 a7
2010/07/27	19:22:00.0	AOCS_ORe-point_Start_18_OG [0x0a8] AOCU_NM	5	02-76	00 f2 f2 32 d9
2010/07/27	20:29:00.0	AOCS_ORe-point_Start_19_OG [0x0a9] AOCU_NM	5	02-76	00 f3 e8 22 66
2010/07/27	20:44:00.0	AOCS_ORe-point_Start_20_OG [0x0aa] AOCU_NM	5	02-76	00 f4 5b 11 81
2010/07/27	20:58:00.0	AOCS_ORe-point_Start_21_OG [0x0ab] AOCU_NM	5	02-76	00 f4 8c 00 41
2010/07/27	22:06:00.0	AOCS_ORe-point_Start_22_OG [0x0ac] AOCU_NM	5	02-76	00 f4 5b ef 65
2010/07/27	22:21:00.0	AOCS_ORe-point_Start_23_OG [0x0ad] AOCU_NM	5	02-76	00 f3 e8 de 7f
2010/07/27	22:36:00.0	AOCS_ORe-point_Start_24_OG [0x0ae] AOCU_NM	5	02-76	00 f2 f2 ce 0d
2010/07/27	23:36:30.0	AOCS_ORe-point_Start_25_OG [0x0af] AOCU_NM	5	02-76	00 f1 82 be 36
2010/07/27	23:51:30.0	AOCS_ORe-point_Start_26_OG [0x0b0] AOCU_NM	5	02-76	00 ed 59 af 75
2010/07/28	00:06:24.0	XRT_FOCUS_POSITION_409_OG [0x199] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/07/28	00:06:30.0	AOCS_ORe-point_Start_27_OG [0x0b1] AOCU_NM	5	02-76	02 00 00 00 00
2010/07/28	00:06:44.0	XRT_FLD_RESET_449_OG [0x1c1] MDP_XRT_FLD_RESET	1	07-F0	da
2010/07/28	00:06:46.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2010/07/28	00:06:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/07/28	00:06:50.0	XRT_AEC_RESET_441_OG [0x1b9] MDP_XRT_AEC_RESET	1	07-F0	d0
2010/07/28	00:06:52.0	XRT_ARS_DIS_420_OG [0x1a4] MDP_XRT_ARS_DIS	1	07-F0	d5
2010/07/28	00:09:26.0	XRT_QT_PROG_SET_439_OG [0x1b7]			

2010/07/28	00:09:28.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2010/07/28	00:09:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/07/28	00:42:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/07/28	01:09:00.0	XRT_Custom_418_OG [0x1a2]					
2010/07/28	01:10:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/07/28	02:19:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/07/28	02:47:30.0	XRT_Custom_418_OG [0x1a2]					
2010/07/28	02:48:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/07/28	03:48:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/07/28	04:26:00.0	XRT_Custom_418_OG [0x1a2]					
2010/07/28	04:27:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/07/28	04:29:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/07/28	04:29:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2010/07/28	04:30:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2010/07/28	04:30:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2010/07/28	04:30:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2010/07/28	04:30:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/07/28	04:32:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2010/07/28	04:32:58.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2010/07/28	04:33:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/07/28	05:27:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/07/28	06:04:30.0	XRT_Custom_418_OG [0x1a2]					
2010/07/28	06:05:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/07/28	06:08:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/07/28	06:08:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2010/07/28	06:08:30.0	AOCs_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00	
2010/07/28	06:08:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2010/07/28	06:08:48.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2010/07/28	06:08:50.0	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/07/28	06:11:28.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2010/07/28	06:11:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/07/28	06:18:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/07/28	06:18:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2010/07/28	06:18:30.0	AOCs_OrE-point_Start_27_OG [0x0b1]	AOCU_NM	5	02-76	02 00 00 00 00	
2010/07/28	06:18:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2010/07/28	06:18:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2010/07/28	06:18:50.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2010/07/28	06:18:52.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/07/28	06:21:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2010/07/28	06:21:28.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2010/07/28	06:21:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/07/28	07:07:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/07/28	07:43:00.0	XRT_Custom_418_OG [0x1a2]					
2010/07/28	07:44:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/07/28	08:47:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/07/28	09:21:30.0	XRT_Custom_418_OG [0x1a2]					
2010/07/28	09:22:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/07/28	10:29:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/07/28	11:00:00.0	XRT_Custom_418_OG [0x1a2]					
2010/07/28	11:01:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					

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2010/07/28	12:11:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/07/28	12:38:30.0	XRT_Custom_418_OG [0x1a2]								
2010/07/28	12:39:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/07/28	13:50:00.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/07/28	14:17:00.0	XRT_Custom_418_OG [0x1a2]								
2010/07/28	14:18:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/07/28	14:18:00.5	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/07/28	14:24:00.0	XRT_Custom_418_OG [0x1a2]								
2010/07/28	14:25:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/07/28	15:28:30.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/07/28	16:14:00.0	XRT_Custom_418_OG [0x1a2]								
2010/07/28	16:15:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/07/28	17:07:00.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/07/28	17:51:00.0	XRT_Custom_418_OG [0x1a2]								
2010/07/28	17:52:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/07/28	18:06:54.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/07/28	18:06:56.0	XRT_FOCUS_POSITION_401_OG [0x191]								
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/07/28	18:07:00.0	AOCS_ORe-point_Start_3_OG [0x099]								
			AOCU_NM	5	02-76	00 00 00 00 00				
2010/07/28	18:07:16.0	XRT_FLD_DIS_402_OG [0x192]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/07/28	18:07:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/07/28	18:07:20.0	XRT_ARS_DIS_404_OG [0x194]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/07/28	18:09:58.0	XRT_QT_PROG_SET_407_OG [0x197]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/07/28	18:10:00.0	XRT_CTRL_AUTO_406_OG [0x196]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/07/28	18:16:54.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/07/28	18:16:56.0	XRT_FOCUS_POSITION_426_OG [0x1aa]								
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/07/28	18:17:00.0	AOCS_ORe-point_Start_28_OG [0x0b2]								
			AOCU_NM	5	02-76	00 ac 00 00 00				
2010/07/28	18:17:16.0	XRT_QT_PROG_SET_432_OG [0x1b0]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2010/07/28	18:17:18.0	XRT_FLD_DIS_427_OG [0x1ab]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/07/28	18:17:20.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/07/28	18:17:22.0	XRT_ARS_DIS_430_OG [0x1ae]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/07/28	18:32:00.0	XRT_CTRL_AUTO_406_OG [0x196]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/07/28	20:16:54.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/07/28	20:16:56.0	XRT_FOCUS_POSITION_426_OG [0x1aa]								
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/07/28	20:17:00.0	AOCS_ORe-point_Start_29_OG [0x0b3]								
			AOCU_NM	5	02-76	00 00 00 54 00				
2010/07/28	20:17:16.0	XRT_QT_PROG_SET_431_OG [0x1af]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2010/07/28	20:17:18.0	XRT_FLD_DIS_427_OG [0x1ab]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/07/28	20:17:20.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/07/28	20:17:22.0	XRT_ARS_DIS_430_OG [0x1ae]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/07/28	20:32:00.0	XRT_CTRL_AUTO_406_OG [0x196]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/07/28	22:16:54.0	XRT_CTRL_MANU_408_OG [0x198]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/07/28	22:16:56.0	XRT_FOCUS_POSITION_409_OG [0x199]								
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/07/28	22:17:00.0	AOCS_ORe-point_Start_27_OG [0x0b1]								
			AOCU_NM	5	02-76	02 00 00 00 00				
2010/07/28	22:17:16.0	XRT_FLD_ENA_411_OG [0x19b]								
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/07/28	22:17:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/07/28	22:17:20.0	XRT_AEC_RESET_441_OG [0x1b9]								
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/07/28	22:17:22.0	XRT_ARS_DIS_420_OG [0x1a4]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/07/28	22:19:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2010/07/28	22:19:58.0	XRT_FL_PROG_SET_405_OG [0x195]								
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2010/07/28	22:41:00.0	XRT_Custom_418_OG [0x1a2]								

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2010/07/28	22:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/07/28	23:41:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/07/29	00:08:00.0	XRT_Custom_418_OG [0x1a2]							
2010/07/29	00:09:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/07/29	01:19:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/07/29	01:46:00.0	XRT_Custom_418_OG [0x1a2]							
2010/07/29	01:47:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/07/29	02:55:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/07/29	03:24:30.0	XRT_Custom_418_OG [0x1a2]							
2010/07/29	03:25:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/07/29	04:25:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/07/29	05:03:00.0	XRT_Custom_418_OG [0x1a2]							
2010/07/29	05:04:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/07/29	05:38:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/07/29	05:38:24.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/07/29	05:38:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/07/29	05:38:46.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/07/29	05:38:48.0	XRT_FLRCTRL_DIS_403_OG [0x193]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/07/29	05:38:50.0	XRT_ARS_DIS_404_OG [0x194]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/07/29	05:41:28.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/07/29	05:41:30.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/07/29	06:05:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/07/29	06:41:30.0	XRT_Custom_418_OG [0x1a2]							
2010/07/29	06:42:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/07/29	07:45:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/07/29	08:20:00.0	XRT_Custom_418_OG [0x1a2]							
2010/07/29	08:21:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/07/29	08:47:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/07/29	08:47:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/07/29	08:48:00.0	AOCS_ORe-point_Start_27_OG [0x0b1]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2010/07/29	08:48:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/07/29	08:48:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/07/29	08:48:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/07/29	08:48:22.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/07/29	08:50:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2010/07/29	08:50:58.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2010/07/29	08:51:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/07/29	09:25:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/07/29	09:58:30.0	XRT_Custom_418_OG [0x1a2]							
2010/07/29	09:59:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/07/29	10:32:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/07/29	10:33:00.5	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				