

# XRT Timeline to be uploaded on 2010/08/07

Period: 2010/08/07 10:10:00 - 2010/08/12 10:52:00

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Normal mode

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XOB #17F9: HOP146-shortexp- Ti/poly 2s, Al/mesh 1s, G-band - 512FOV 10min cadence - Q95													
Term	Pointing (x, y)	Comment											
08/07 10:40:08 - 08/07 18:01:24	Fixed ( 0.0, 860.0)	# OP start + 10min, HOP 146, SW source regions, N.											
<b>PROG= 19 Inf.-time(s)</b>													
┌ Subr= 1 1-time(s) 2.0sec													
└ Seqn= 3 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
┌ Subr= 2 4-time(s) 600.0sec													
└ Seqn= 33 2-time(s) 30.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)													
Term	Pointing (x, y)	Comment											
08/07 18:04:32 - 08/07 19:07:00	Fixed ( 0.0, 0.0)	synoptic, shifted 1.5 min											
08/08 06:23:02 - 08/08 08:29:54	Fixed ( 0.0, 0.0)	synoptic, shifted 20.0 min, extended for SOT flat fields.											
08/08 18:10:02 - 08/08 18:16:54	Fixed ( 0.0, 0.0)	synoptic, shifted 7.0 min											
08/09 05:57:02 - 08/09 06:03:54	Fixed ( 0.0, 0.0)	synoptic, shifted -6.0 min											
08/09 17:54:32 - 08/09 18:01:30	Fixed ( 0.0, 0.0)	synoptic, shifted -8.5 min											
08/10 06:03:02 - 08/10 06:09:54	Fixed ( 0.0, 0.0)	synoptic											
<b>PROG= 03 1-time(s)</b>													
┌ Subr= 1 1-time(s) 12.0sec													
└ Seqn= 7 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└ Seqn= 8 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Seqn= 4 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #17F6: AR Standard-A(Filter-Ratio) for FW1=Open, 384x384 at 1064 1048, 2min-cad													
Term	Pointing (x, y)	Comment											
08/07 19:10:08 - 08/07 20:04:00	Track ( -309.6, -398.1) <sup>@ 08/07 18:11:30</sup>	# Track AR 11095, HOP 160 test.											
08/08 18:20:02 - 08/09 05:53:54	Track ( 868.8, 135.0) <sup>@ 08/08 18:17:00</sup>	# Track AR 11092.											
<b>PROG= 14 Inf.-time(s)</b>													
┌ Subr= 1 1-time(s) 2.0sec													
└ Seqn= 19 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└ Seqn= 20 4-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
┌ Subr= 2 1-time(s) 2.0sec													
└ Seqn= 78 20-time(s) 120.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #17F7: AR Standard-A(Filter-Ratio) for FW1=Open, 512x512 at 1064 1048, 4min-cad													
Term	Pointing (x, y)	Comment											
08/07 20:47:38 - 08/08 05:44:00	Track ( -479.9, 62.9) <sup>@ 08/07 20:11:30</sup>	* Track AR 11093.											
08/08 12:56:08 - 08/08 17:24:30	Track ( -347.8, 55.2) <sup>@ 08/08 12:30:00</sup>	* Track AR 11093											
08/09 06:11:02 - 08/09 17:51:24	Track ( -195.7, 49.4) <sup>@ 08/09 06:04:00</sup>	# Track AR 11093											
08/09 18:47:38 - 08/10 05:59:54	Track ( -88.7, 47.1) <sup>@ 08/09 18:01:30</sup>	# Cont.											
08/10 06:13:02 - 08/10 10:19:00	Track ( 21.2, 46.2) <sup>@ 08/10 06:10:00</sup>	# Cont.											
<b>PROG= 06 Inf.-time(s)</b>													
┌ Subr= 1 1-time(s) 2.0sec													
└ Seqn= 17 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
└ Seqn= 6 4-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec

Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 86 10-time(s) 240.0sec</b>												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #16AC: G-Band Alignment with North Pole Q90 2x2(G-band only) - 5min cadence - Partial Sun-wNGT**

Term	Pointing (x, y)	Comment
08/08 08:45:00 - 08/08 10:29:54	Fixed ( 0.0, 945.0)	# Co-alignment offset, N.
<b>PROG= 05 1-time(s)</b>		
<b>Subr= 1 1-time(s) 360.0sec</b>		
<b>Seqn= 21 24-time(s) 300.0sec</b>		
Open/G-band	Open/G-band	open Safe Norm 16ms Obs 2x2 2048x1536 (1024, 768) Q=90 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #16AD: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8 min cadence-wNGT**

Term	Pointing (x, y)	Comment
08/08 10:45:00 - 08/08 12:28:59	Fixed ( -945.0, 0.0)	* Co-alignment offset, E.
<b>PROG= 10 1-time(s)</b>		
<b>Subr= 1 1-time(s) 360.0sec</b>		
<b>Seqn= 22 15-time(s) 480.0sec</b>		
Open/G-band	Open/G-band	open Safe Norm 16ms Obs 2x2 1536x2048 (1280, 1024) Q=90 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Flare mode**

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**XOB #17EF: Flare Standard Obs. with eruptions mode-A (FW1=Open)**

Term	Pointing (x, y)	Comment
08/07 10:40:08 - 08/07 18:01:24	Fixed ( 0.0, 860.0)	# OP start + 10min, HOP 146, SW source regions, N.
08/07 19:10:08 - 08/07 20:04:00	Track ( -309.6, -398.1) @ 08/07 18:11:30	# Track AR 11095, HOP 160 test.
08/07 20:47:38 - 08/08 05:44:00	Track ( -479.9, 62.9) @ 08/07 20:11:30	* Track AR 11093.
08/08 12:56:08 - 08/08 17:24:30	Track ( -347.8, 55.2) @ 08/08 12:30:00	* Track AR 11093
08/08 18:20:02 - 08/09 05:53:54	Track ( 868.8, 135.0) @ 08/08 18:17:00	# Track AR 11092.
08/09 06:11:02 - 08/09 17:51:24	Track ( -195.7, 49.4) @ 08/09 06:04:00	# Track AR 11093
08/09 18:47:38 - 08/10 05:59:54	Track ( -88.7, 47.1) @ 08/09 18:01:30	# Cont.
08/10 06:13:02 - 08/10 10:19:00	Track ( 21.2, 46.2) @ 08/10 06:10:00	# Cont.

<b>PROG= 07 1-time(s)</b>												
<b>Subr= 1 30-time(s) 20.0sec</b>												
<b>Seqn= 87 1-time(s) 2.0sec</b>												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 91 1-time(s) 2.0sec</b>												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	0	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 90 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 3 30-time(s) 60.0sec</b>												
<b>Seqn= 87 1-time(s) 2.0sec</b>												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 88 1-time(s) 2.0sec</b>												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 90 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 3 30-time(s) 60.0sec</b>												
<b>Seqn= 87 1-time(s) 2.0sec</b>												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 88 1-time(s) 2.0sec</b>												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 90 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 3 30-time(s) 60.0sec</b>												
<b>Seqn= 87 1-time(s) 2.0sec</b>												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs 1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 88 1-time(s) 2.0sec</b>											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs 2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs 2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 4 24-time(s) 600.0sec</b>											
<b>Seqn= 89 1-time(s) 2.0sec</b>											
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs 1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs 1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs 1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs 1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

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### Active Region Search

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NOT USED

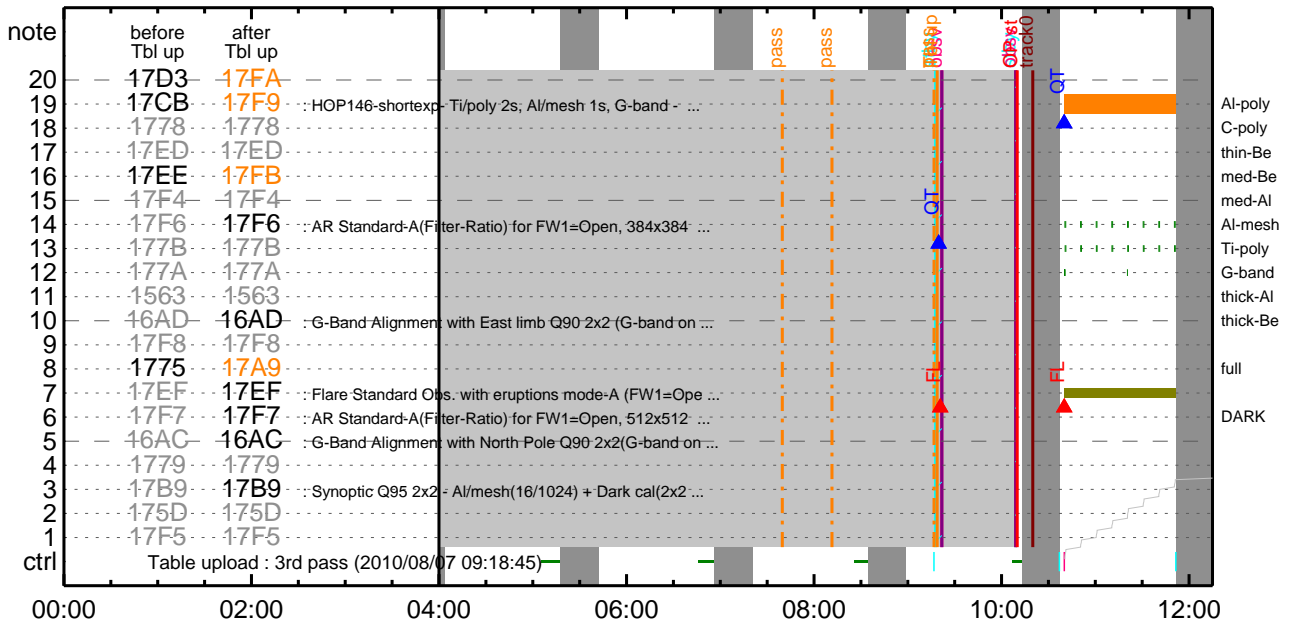
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### Flare Detection

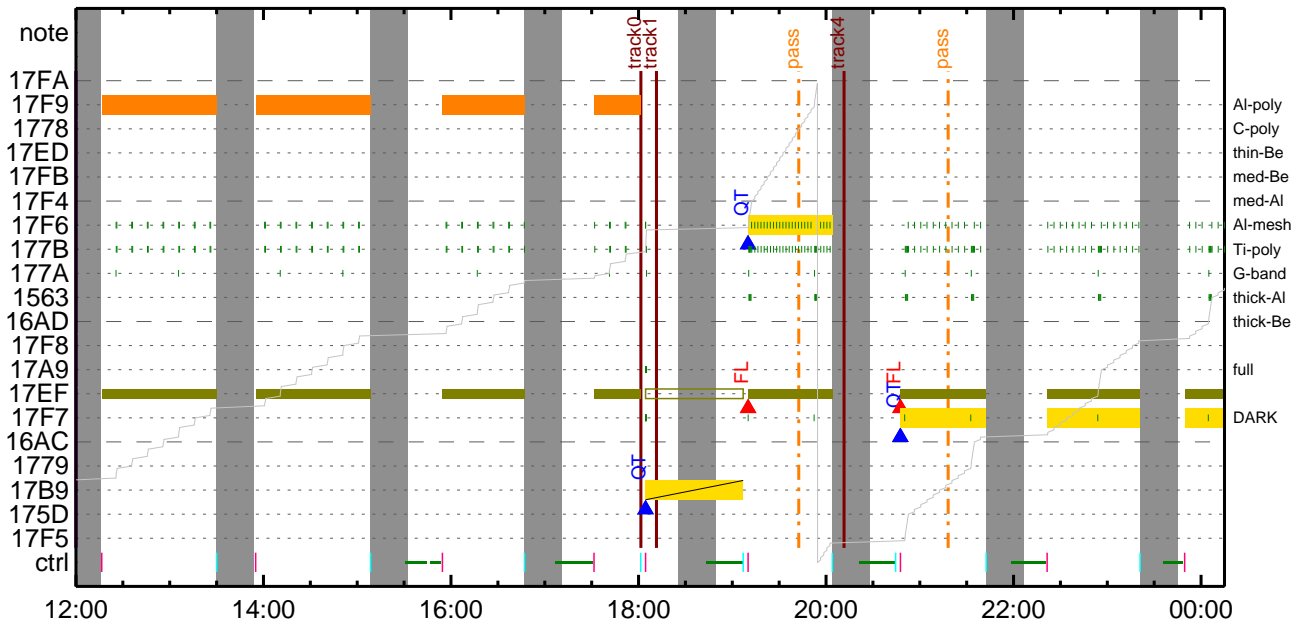
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<b>FLD Patrol</b>											
Term	Pointing (x, y)		Comment								
08/07 10:37:22 - 08/07 18:01:46	Fixed (	0.0, 860.0)	# OP start + 10min, HOP 146, SW source regions, N.								
08/07 19:07:22 - 08/08 06:20:16	Track (	-309.6, -398.1)	# Track AR 11095, HOP 160 test.								
08/08 12:53:22 - 08/08 18:07:16	Track (	-347.8, 55.2)	* Track AR 11093								
08/08 18:17:16 - 08/09 05:54:16	Track (	868.8, 135.0)	# Track AR 11092.								
08/09 06:08:16 - 08/09 17:51:46	Track (	-195.7, 49.4)	# Track AR 11093								
08/09 18:44:52 - 08/10 06:00:16	Track (	-88.7, 47.1)	# Cont.								
08/10 06:10:16 - 08/12 10:52:00	Track (	21.2, 46.2)	# Cont.								
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs 8x8	Q=50		30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

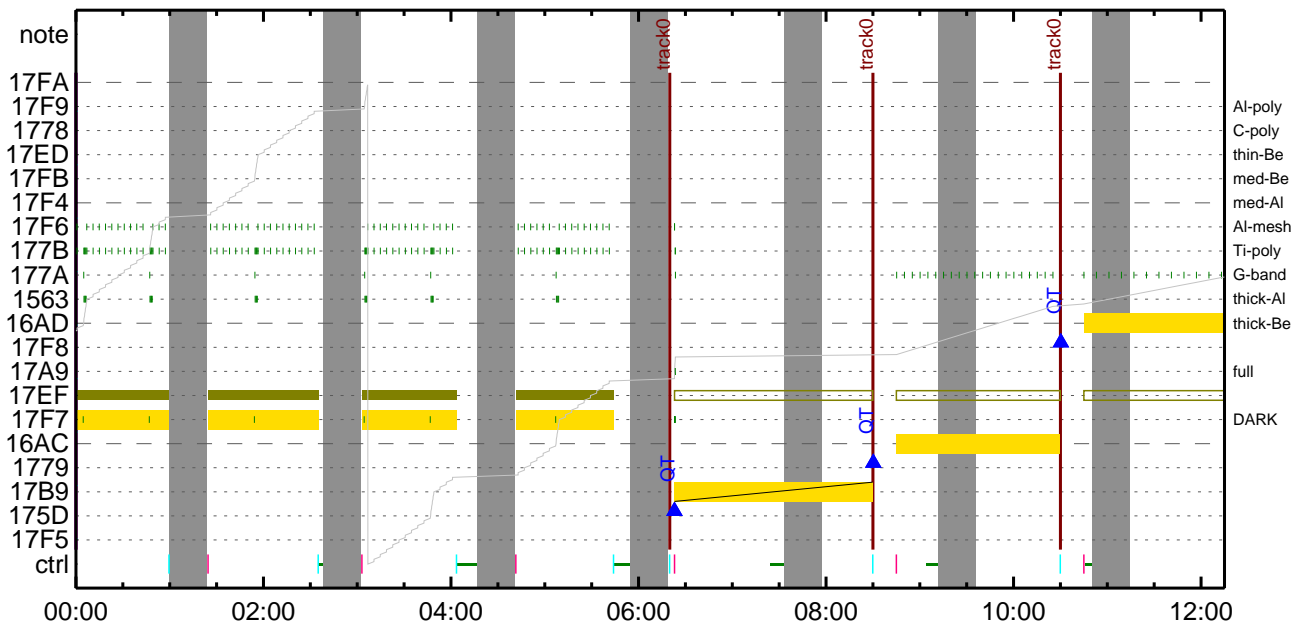
### CMDI #0416 2010/08/07



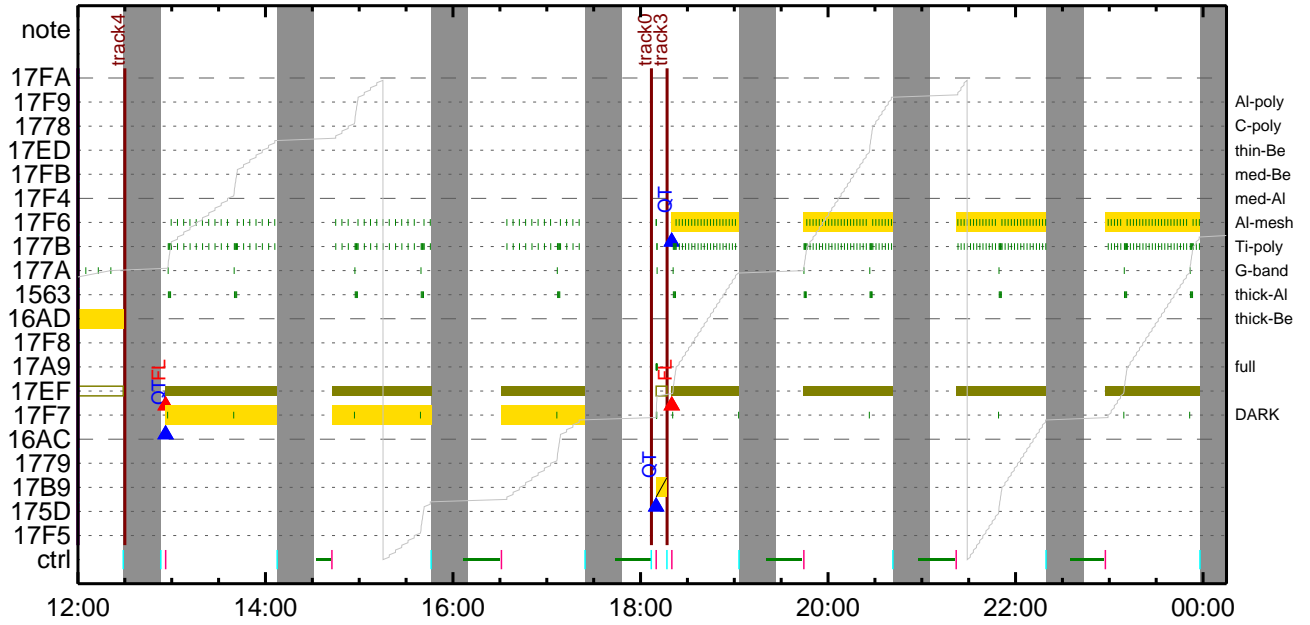
### CMDI #0416 2010/08/07



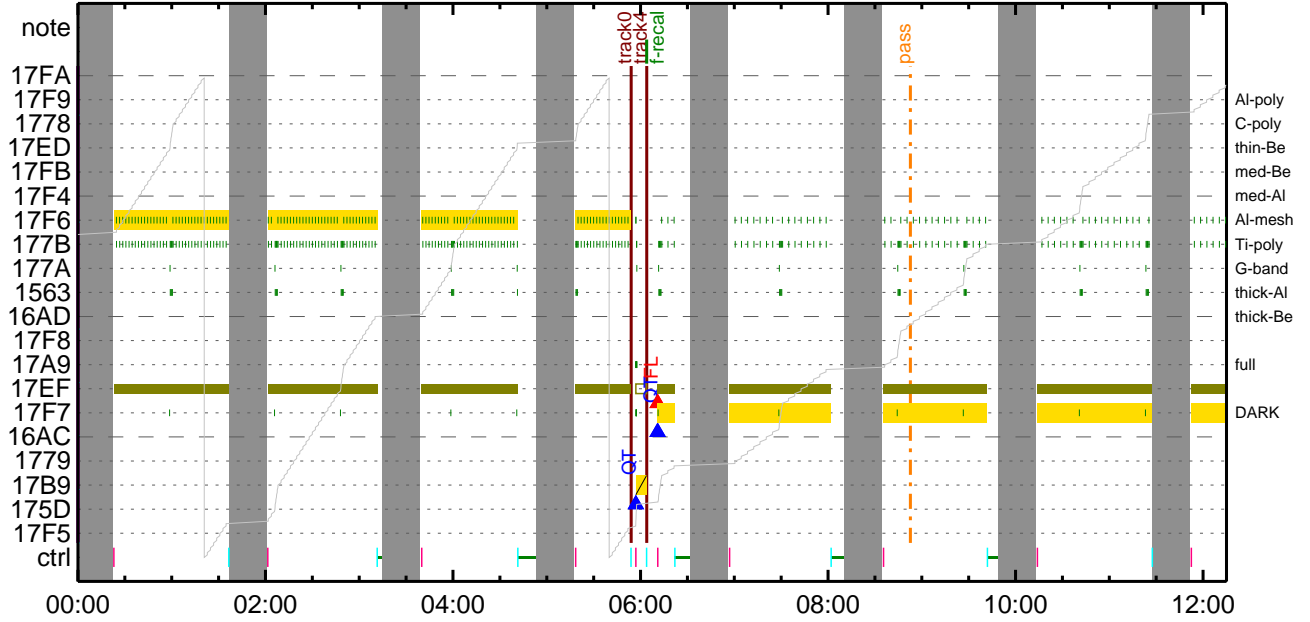
### CMDI #0416 2010/08/08



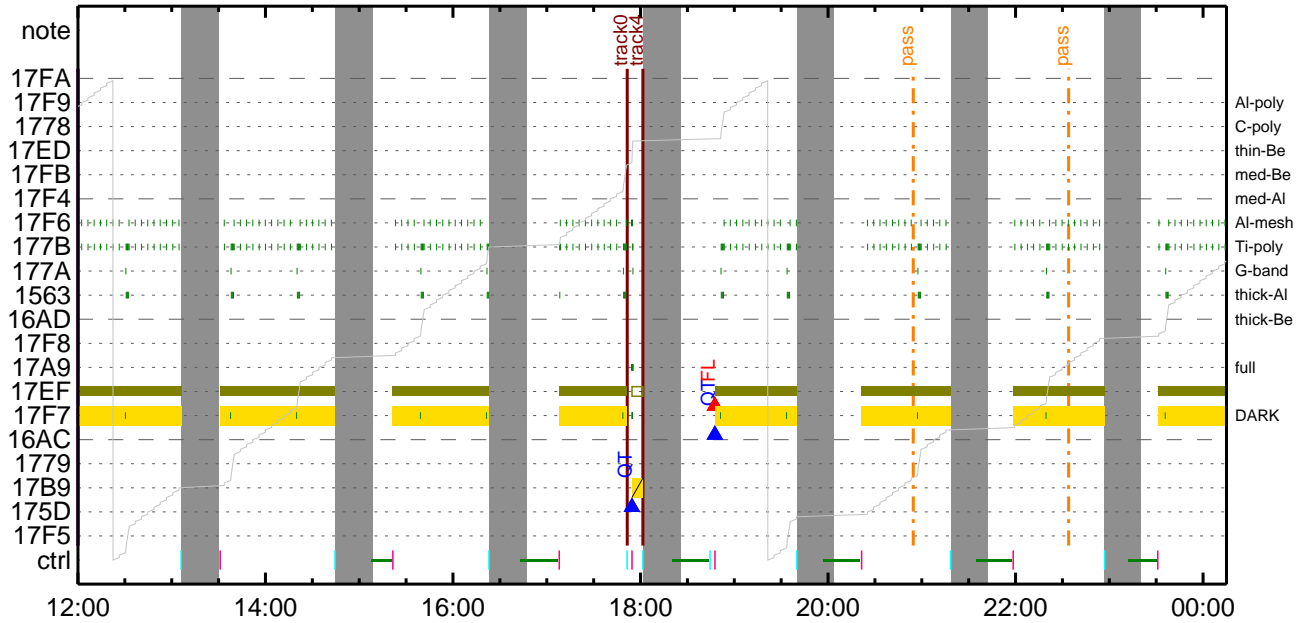
CMDI #0416 2010/08/08



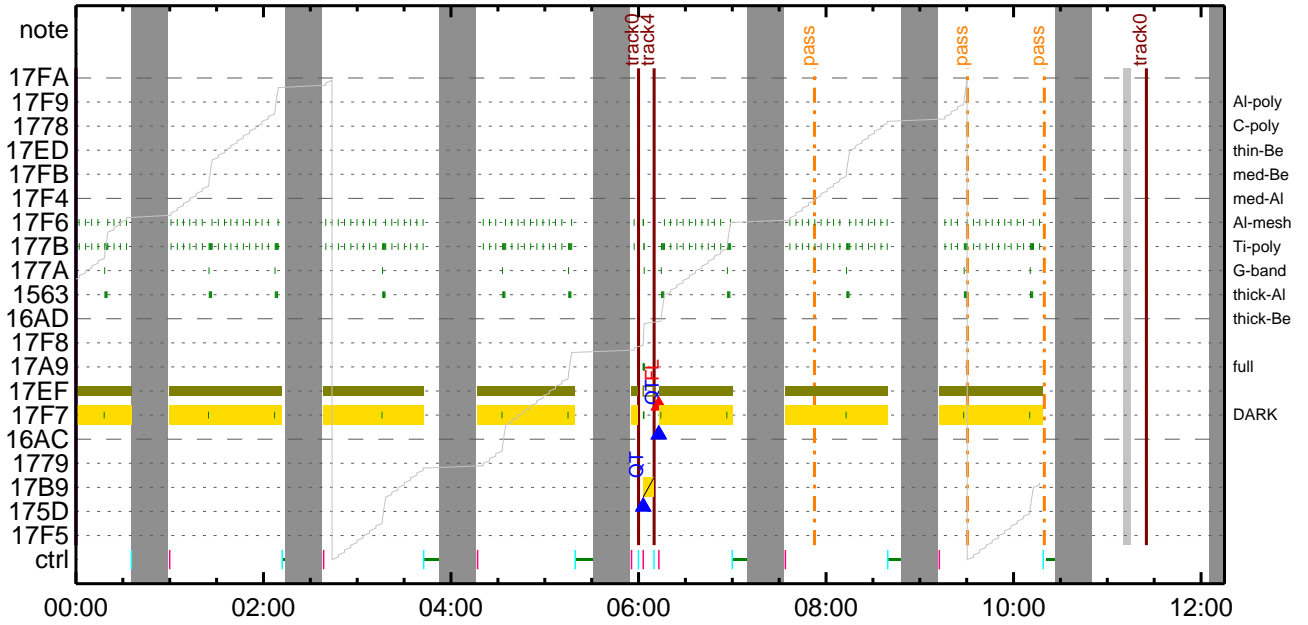
CMDI #0416 2010/08/09



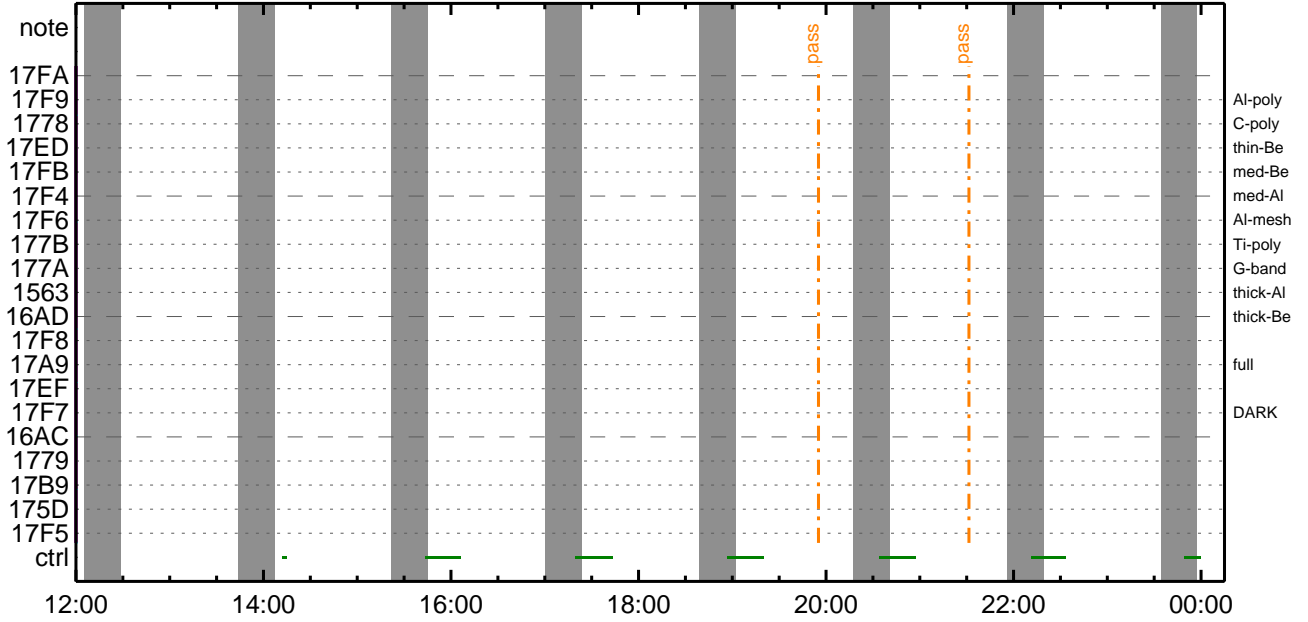
CMDI #0416 2010/08/09



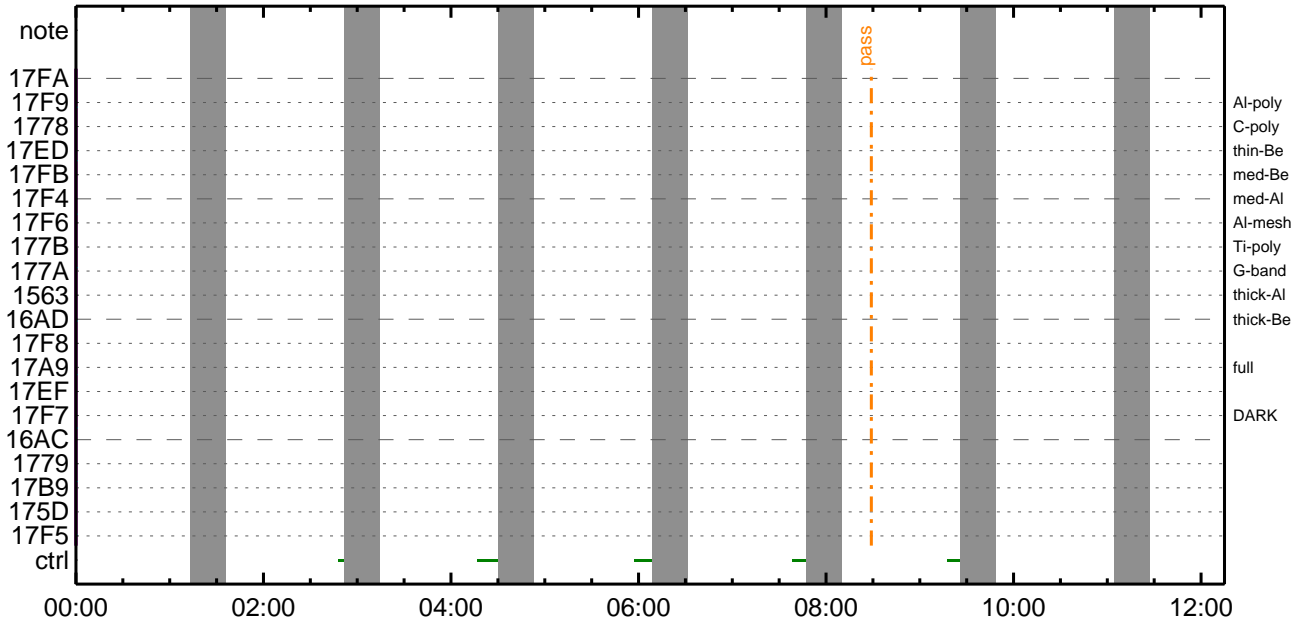
CMDI #0416 2010/08/10



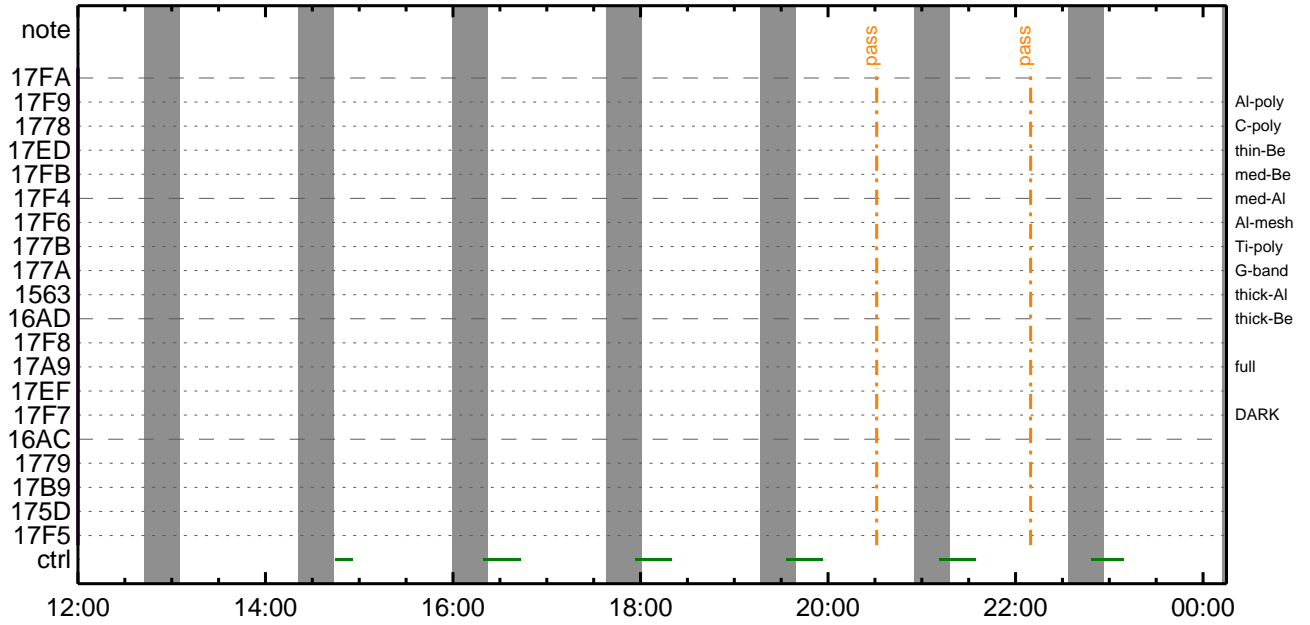
CMDI #0416 2010/08/10



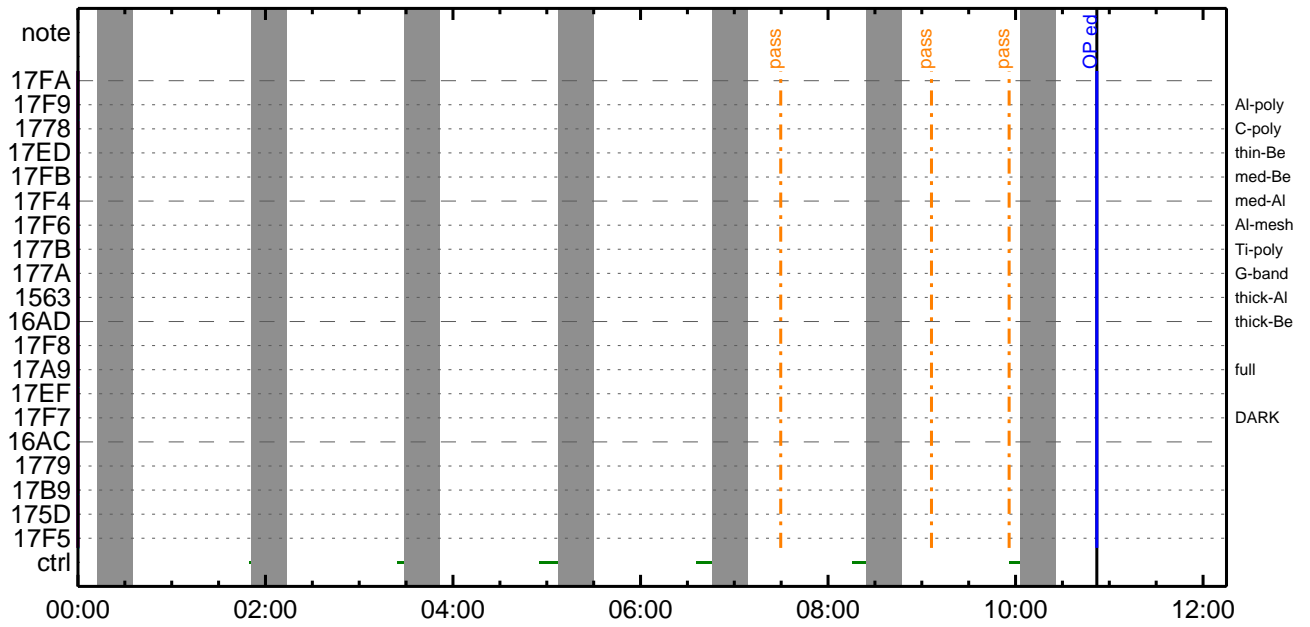
CMDI #0416 2010/08/11



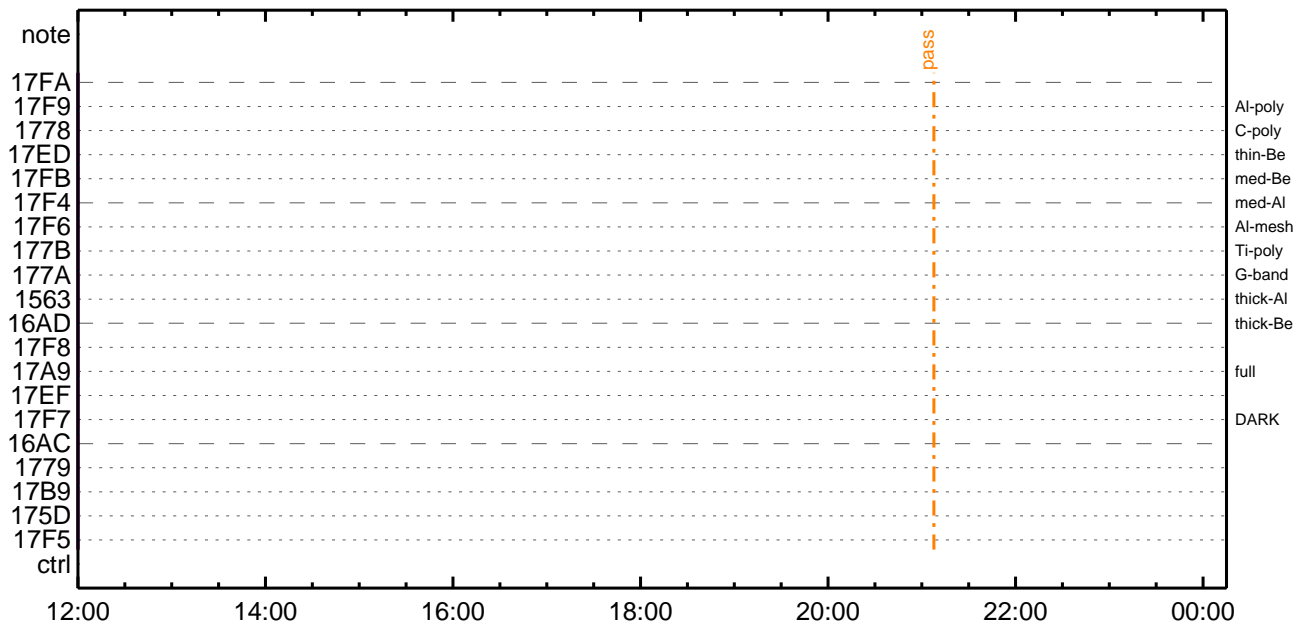
CMDI #0416 2010/08/11



CMDI #0416 2010/08/12



CMDI #0416 2010/08/12







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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;aOP/OGY1;4YE;a
0103 S. OP op-513:OP
0104 ( )
0105 S. OG og-513:OG
0106 ( )
0107 C.
0108 C. ;aNMOG&OPf^°eYAYOX;a
0109 C. NMOG(0x200000-0x207FFF;s 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C.          c{[HK1_DMP_TOP_ADRS_1]          EQ      40
0113 C.          c{[HK1_DMP_TOP_ADRS_0]          EQ      0
0114 C.          c{[HK1_DMP_BLOCK_NUM]           EQ     127
0115 C.          c{[HK1_DMP_REPEAT_NUM]          EQ      0
0116 C.          c{[HK1_DMA_DMP_PIM]             EQ     DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C.          c{[HK1_PKT_FORM_NO]             EQ      7
0120 C.          c{[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121 C.          c{[HK1_S_TLM_BIT_RATE]          EQ     32k
0122 C.          c{[HK1_X_TLM_BIT_RATE]          EQ      4M
0123 C.          c{[HK1_DMP_CHK_FLG]            EQ     EXEC
0124 C. YAYOXx1/2^i»oð³iÇ§
0125 C.          c{[HK1_DMP_CHK_FLG]             EQ     NON
0126 C. RAM ID=NMOGqifE¹ç•ë²iOKoð³iÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;s 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C.          c{[HK1_DMP_TOP_ADRS_1]          EQ     41
0132 C.          c{[HK1_DMP_TOP_ADRS_0]          EQ      0
0133 C.          c{[HK1_DMP_BLOCK_NUM]           EQ     127
0134 C.          c{[HK1_DMP_REPEAT_NUM]          EQ      0
0135 C.          c{[HK1_DMA_DMP_PIM]             EQ     DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C.          c{[HK1_PKT_FORM_NO]             EQ      7
0139 C.          c{[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140 C.          c{[HK1_S_TLM_BIT_RATE]          EQ     32k
0141 C.          c{[HK1_X_TLM_BIT_RATE]          EQ      4M
0142 C.          c{[HK1_DMP_CHK_FLG]            EQ     EXEC
0143 C. YAYOXx1/2^i»oð³iÇ§
0144 C.          c{[HK1_DMP_CHK_FLG]             EQ     NON
0145 C. RAM ID=NMOGqifE¹ç•ë²iOKoð³iÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;s 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C.          c{[HK1_DMP_TOP_ADRS_1]          EQ     42
0151 C.          c{[HK1_DMP_TOP_ADRS_0]          EQ      0
0152 C.          c{[HK1_DMP_BLOCK_NUM]           EQ     65
0153 C.          c{[HK1_DMP_REPEAT_NUM]          EQ      0
0154 C.          c{[HK1_DMA_DMP_PIM]             EQ     DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C.          c{[HK1_PKT_FORM_NO]             EQ      7
0158 C.          c{[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159 C.          c{[HK1_S_TLM_BIT_RATE]          EQ     32k
0160 C.          c{[HK1_X_TLM_BIT_RATE]          EQ      4M
0161 C.          c{[HK1_DMP_CHK_FLG]            EQ     EXEC
0162 C. YAYOXx1/2^i»oð³iÇ§
0163 C.          c{[HK1_DMP_CHK_FLG]             EQ     NON
0164 C. RAM ID=NMOG, RAM ID=OPqifE¹ç•ë²iOKoð³iÇ§
0165 C.
0166 C. ***** oE²¼oI3A´¶A°oEÉ-°oA÷¿@ (¼âµ-YAYOXx1/2^e¼çoðAÓAæoÇ¼^a°°oE¼i¹çoÇoâ) *****
0167 C. DHUÿâ;4YE;E¼Y¼, Y¼;4YE;EoðIâ¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C.          c{[HK1_PKT_FORM_NO]             EQ      2
0171 C.          c{[HK1_PKT_GEN_TIME]            EQ     0.5S
0172 C.          c{[HK1_S_TLM_BIT_RATE]          EQ     32K
0173 C.          c{[HK1_X_TLM_BIT_RATE]          EQ      4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE |s OPOG UPLOADo-Á÷¿@NGuifE¹ç;ç°E²¼oI¹TI-CMDÁ÷¿@oI¼A¹Ôo•oEoð³oE;f
0180 C.          oBp¿;çSEToEDUMPaIÆ±°iYÑY¹ç¹Ôo|o³oE;f
0181 C.
0182 C. TIY³YpYóYÉoðAÐI¿(UT)
0183 +. TI 2010-08-07 10:05:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C.          c{[HK1_TI_CMD_NUM]               EQ     1COUNTUP
0186 C.
0187 +. TI 2010-08-07 10:05:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C.          c{[HK1_TI_CMD_NUM]               EQ     1COUNTUP
0190 C.
0191 +. TI 2010-08-07 10:05:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C.          c{[HK1_TI_CMD_NUM]               EQ     1COUNTUP

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0194 C.
0195 +. TI 2010-08-07 10:09:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.      çç[HK1_TI_CMD_NUM]              EQ      1COUNTUP
0198 C.
0199 C. °È²¼ºíÄë%îîÑºîî¥Ä¥§¥Ä¥-¹àîü
0200 C.      çç[HK1_TI_CMD_ENA/DIS]          EQ      ENA
0201 C.      çç[HK1_TI_CMD_NUM]              EQ      4
0202 C.      çç[HK1_NEXT_EXEC_PIM]          EQ      DHU
0203 C.      çç[HK1_NEXT_EXEC_DC]           EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°è¥Ä¥Ø¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.      çç[HK1_DMP_TOP_ADRS_1]        EQ      07
0213 C.      çç[HK1_DMP_TOP_ADRS_0]        EQ      2B
0214 C.      çç[HK1_DMP_BLOCK_NUM]         EQ      3
0215 C.      çç[HK1_DMP_REPEAT_NUM]       EQ      0
0216 C.      çç[HK1_DMA_DMP_PIM]          EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.      çç[HK1_PKT_FORM_NO]           EQ      7
0220 C.      çç[HK1_PKT_GEN_TIME]          EQ      0.25 s
0221 C.      çç[HK1_S_TLM_BIT_RATE]       EQ      32k
0222 C.      çç[HK1_X_TLM_BIT_RATE]       EQ      4M
0223 C.      çç[HK1_DMP_CHK_FLG]          EQ      EXEC
0224 C.
0225 C. ¥Ä¥Ø¥×½ªî»º³îç§
0226 C.      çç[HK1_DMP_CHK_FLG]          EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLºîî¼È¹ç•è²ìOKº³îç§
0229 C.
0230 C. DHU¥à;¼¥È;È¼½¥. ¥ì;¼¥È;Èº³îäº¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.      çç[HK1_PKT_FORM_NO]           EQ      2
0234 C.      çç[HK1_PKT_GEN_TIME]          EQ      0.5S
0235 C.      çç[HK1_S_TLM_BIT_RATE]       EQ      32K
0236 C.      çç[HK1_X_TLM_BIT_RATE]       EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2010-08-07 10:09:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2010-08-07 10:09:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.      [ ] [HK1_TI_CMD_NUM]          EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C. *****
0255 C. SOT TI command set
0256 C. *****
0257 C. Execute, after the success of OP upload.
0258 +. TI 2010-08-07 10:09:16.0
0259 DC 07-F0 MDP_SOT_MODE_STBY
0260 BC      (41)
0261 C. -----
0262 C.      HK1_TI_CMD_NUM = 1 CNTUP [ ]
0263 C. -----
0264 C. ***** SOT END *****
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2010-08-07 10:09:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.      [ ] [HK1_TI_CMD_NUM]          EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP °ûÄîäî»ö¼ÝºèÄðº¹ºèDCBC•x²è *****
0276 C. (¾åºîî¥Ä¥È¥¥¥Ä¥ç¥èºÈ¼ºº¼Ä»Üº¹ºè)
0277 C. S. DC-BC dcbc-402:DCBC
0278 C. (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ¥Ð¥¹•î Daily±çîÑºè°º¹ºèDCBC•x²è *****
0282 C. S. DC-BC dcbc-153:DCBC
0283 C. (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOS¥Ä¥§¥Ä¥-¼Ä»Ü;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 06 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 07 80 80 08 08)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 08 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 09 80 80 20 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 0a 80 80 08 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0b 85 83 08 08)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0c 80 60 20 18)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0d a0 80 18 20)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0f 80 80 06 06)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 10 80 80 08 08)
0144 + DC 07-F0 MDP_XRT_AEC_RESET
0145 BC (d0)
0146 . C. ----- Success Verify ? OK / NG ____
0147 C.
0148 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0149 BC (c4 0e)
0150 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0151 BC (c5 07)
0152 C.
0153 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0154 C.
0155 +. DC 07-F0 MDP_XRT_MODE_OBSV
0156 BC (c2)
0157 +. TI 2010-08-07 10:09:02.0
0158 DC 07-F0 MDP_XRT_MODE_OBSV
0159 BC (c2)
0160 . C. ----- Success Verify ? OK / NG ____
0161 C.
0162 C. ***** XRT END *****
0163 . C. *****
0164 C. SOT table upload
0165 C. *****
0166 . C. < Stop FG table >
0167 +. DC 07-F0 MDP_FG_CTRL_MANU
0168 BC (51)
0169 . C. -----
0170 C. MDP_FG_CTRL_MODE = MANU [ ]
0171 C. -----
0172 C.
0173 . C. <Upload FG Observation Table>
0174 . S. RAM ram-263:MDP_OBS_F
0175 ( )
0176 C.
0177 . C. < Dump RAMID=MDP_OBS_F >
0178 +. DC 07-F0 MDP_DUMP_FGTBL
0179 BC (82 07 00 00 00 38 b8)
0180 C. -----
0181 C. MDP_OBS_F verify = OK/NG [ ]
0182 C. -----
0183 C.
0184 C. *****
0185 C. SOT TI command set
0186 C. *****
0187 C. Execute, after the success of TBL upload.
0188 +. TI 2010-08-07 10:09:18.0
0189 DC 07-F0 MDP_SOT_MODE_OBSV
0190 BC (40)
0191 . C. -----
0192 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0193 C. -----

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\*\*\* OP Sequence for XRT \*\*\*

2010/08/07	10:20:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76 00 b3 8e 00 00	
2010/08/07	10:37:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2010/08/07	10:37:02.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00	
2010/08/07	10:37:22.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0 d8	
2010/08/07	10:37:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8	
2010/08/07	10:37:26.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0 d0	
2010/08/07	10:37:28.0	XRT_ARS_DIS_420_OG [0x1a4]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2010/08/07	10:40:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2010/08/07	10:40:04.0	XRT_QT_PROG_SET_442_OG [0x1ba]			
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 13	
2010/08/07	10:40:06.0	XRT_FL_PROG_SET_405_OG [0x195]			
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 07	
2010/08/07	10:40:08.0	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2010/08/07	11:51:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2010/08/07	11:51:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2010/08/07	11:51:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2010/08/07	11:54:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2010/08/07	12:15:30.0	XRT_Custom_418_OG [0x1a2]			
2010/08/07	12:16:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2010/08/07	13:30:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2010/08/07	13:30:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2010/08/07	13:30:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2010/08/07	13:33:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2010/08/07	13:54:00.0	XRT_Custom_418_OG [0x1a2]			
2010/08/07	13:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2010/08/07	15:08:30.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2010/08/07	15:08:32.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2010/08/07	15:08:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2010/08/07	15:11:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2010/08/07	15:53:30.0	XRT_Custom_418_OG [0x1a2]			
2010/08/07	15:54:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2010/08/07	16:47:00.0	XRT_CTRL_MANU_408_OG [0x198]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2010/08/07	16:47:02.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2010/08/07	16:47:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2010/08/07	16:50:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2010/08/07	17:30:30.0	XRT_Custom_418_OG [0x1a2]			
2010/08/07	17:31:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2010/08/07	18:01:24.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2010/08/07	18:01:26.0	XRT_FOCUS_POSITION_401_OG [0x191]			
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00	
2010/08/07	18:01:30.0	AOCS_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76 00 00 00 00 00	
2010/08/07	18:01:46.0	XRT_FLD_DIS_402_OG [0x192]			
		MDP_XRT_FLD_DIS	1	07-F0 d9	
2010/08/07	18:01:48.0	XRT_FLRCTRL_DIS_403_OG [0x193]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9	
2010/08/07	18:01:50.0	XRT_ARS_DIS_404_OG [0x194]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2010/08/07	18:04:28.0	XRT_FLD_RESET_412_OG [0x19c]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2010/08/07	18:04:30.0	XRT_QT_PROG_SET_407_OG [0x197]			
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 03	
2010/08/07	18:04:32.5	XRT_CTRL_AUTO_406_OG [0x196]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2010/08/07	18:11:30.0	AOCS_Ore-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76 01 00 00 00 00	
2010/08/07	19:07:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2010/08/07	19:07:02.0	XRT_FOCUS_POSITION_409_OG [0x199]			
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00	
2010/08/07	19:07:22.0	XRT_FLD_ENA_411_OG [0x19b]			

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2010/08/07	19:07:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/08/07	19:07:26.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/08/07	19:07:28.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/08/07	19:10:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/07	19:10:04.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2010/08/07	19:10:06.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2010/08/07	19:10:08.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/07	20:04:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/07	20:04:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/07	20:04:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/07	20:07:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/07	20:11:30.0	AOCS_Orе-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	04	00	00	00	00
2010/08/07	20:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/07	20:44:32.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2010/08/07	20:44:52.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/08/07	20:44:54.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/08/07	20:44:56.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/08/07	20:44:58.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/08/07	20:47:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/07	20:47:34.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2010/08/07	20:47:36.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2010/08/07	20:47:38.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/07	21:42:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/07	21:42:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/07	21:42:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/07	21:45:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/07	22:20:30.0	XRT_Custom_418_OG [0x1a2]								
2010/08/07	22:21:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/07	23:21:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/07	23:21:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/07	23:21:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/07	23:24:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/07	23:48:30.0	XRT_Custom_418_OG [0x1a2]								
2010/08/07	23:49:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/08	00:59:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/08	00:59:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/08	00:59:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/08	01:02:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/08	01:23:30.0	XRT_Custom_418_OG [0x1a2]								
2010/08/08	01:24:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/08	02:35:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/08	02:35:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/08	02:35:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/08	02:38:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/08	03:02:00.0	XRT_Custom_418_OG [0x1a2]								
2010/08/08	03:03:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/08	04:03:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/08	04:03:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				



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2010/08/08	04:03:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/08	04:06:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/08	04:40:30.0	XRT_Custom_418_OG [0x1a2]							
2010/08/08	04:41:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/08	05:44:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/08	05:44:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/08	05:44:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/08	05:47:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/08	06:19:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/08	06:19:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/08/08	06:20:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/08/08	06:20:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/08/08	06:20:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/08/08	06:20:20.0	XRT_ARS_DIS_404_OG [0x194]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/08/08	06:22:58.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/08	06:23:00.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/08/08	06:23:02.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/08	08:29:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/08	08:29:56.0	XRT_FOCUS_POSITION_426_OG [0x1aa]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/08/08	08:30:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 ac 00 00 00				
2010/08/08	08:30:16.0	XRT_QT_PROG_SET_432_OG [0x1b0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2010/08/08	08:30:18.0	XRT_FLD_DIS_427_OG [0x1ab]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/08/08	08:30:20.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/08/08	08:30:22.0	XRT_ARS_DIS_430_OG [0x1ae]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/08/08	08:45:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/08	10:29:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/08	10:29:56.0	XRT_FOCUS_POSITION_426_OG [0x1aa]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/08/08	10:30:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 00 00 54 00				
2010/08/08	10:30:16.0	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2010/08/08	10:30:18.0	XRT_FLD_DIS_427_OG [0x1ab]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/08/08	10:30:20.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/08/08	10:30:22.0	XRT_ARS_DIS_430_OG [0x1ae]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/08/08	10:45:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/08	12:28:59.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/08	12:30:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2010/08/08	12:53:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/08	12:53:02.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/08/08	12:53:22.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/08/08	12:53:24.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/08/08	12:53:26.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/08/08	12:53:28.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/08/08	12:56:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/08	12:56:04.0	XRT_QT_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2010/08/08	12:56:06.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2010/08/08	12:56:08.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/08	14:07:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/08	14:07:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

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2010/08/08	14:07:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/08	14:10:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/08	14:41:30.0	XRT_Custom_418_OG [0x1a2]							
2010/08/08	14:42:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/08	15:46:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/08	15:46:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/08	15:46:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/08	15:49:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/08	16:30:00.0	XRT_Custom_418_OG [0x1a2]							
2010/08/08	16:31:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/08	17:24:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/08	17:24:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/08	17:24:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/08	17:27:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/08	18:06:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/08	18:06:56.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/08/08	18:07:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2010/08/08	18:07:16.0	XRT_FLD_DIS_402_OG [0x192]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/08/08	18:07:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/08/08	18:07:20.0	XRT_ARS_DIS_404_OG [0x194]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/08/08	18:09:58.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/08	18:10:00.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/08/08	18:10:02.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/08	18:16:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/08	18:16:56.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/08/08	18:17:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2010/08/08	18:17:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/08/08	18:17:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/08/08	18:17:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/08/08	18:17:22.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/08/08	18:19:56.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/08	18:19:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2010/08/08	18:20:00.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2010/08/08	18:20:02.5	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/08	19:03:00.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/08	19:03:02.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/08	19:03:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/08	19:06:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/08	19:43:30.0	XRT_Custom_418_OG [0x1a2]							
2010/08/08	19:44:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/08	20:41:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/08	20:41:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/08	20:41:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/08	20:44:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/08	21:21:00.0	XRT_Custom_418_OG [0x1a2]							
2010/08/08	21:22:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/08	22:19:30.0	XRT_CTRL_MANU_408_OG [0x198]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/08	22:19:32.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/08	22:19:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]							

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2010/08/08	22:22:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e8				
2010/08/08	22:56:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/08	22:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/08	23:58:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/08	23:58:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/08	23:58:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/09	00:01:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/09	00:22:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/09	00:23:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/09	01:36:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/09	01:36:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/09	01:36:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/09	01:39:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/09	02:00:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/09	02:01:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/09	03:11:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/09	03:11:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/09	03:11:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/09	03:14:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/09	03:39:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/09	03:40:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/09	04:41:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/09	04:41:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/09	04:41:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2010/08/09	04:44:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2010/08/09	05:17:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/09	05:18:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/09	05:53:54.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2010/08/09	05:53:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00 00 00 00 00				
2010/08/09	05:54:00.0	AOCs_Or-e-point_Start_2_OG [0x098]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2010/08/09	05:54:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2010/08/09	05:54:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/08/09	05:54:20.0	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/09	05:56:58.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2010/08/09	05:57:00.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/09	05:57:02.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/09	06:03:54.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_FOCUS_RECALIBRATE_449_OG [0x1c1]	2	07-F8	78 00				
2010/08/09	06:03:56.0	XRT_FOCUS_RECALIBRATE_449_OG [0x1c1]	AOCU_NM	5	02-76	04 00 00 00 00				
2010/08/09	06:04:00.0	AOCs_Or-e-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2010/08/09	06:07:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2010/08/09	06:08:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2010/08/09	06:08:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2010/08/09	06:08:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2010/08/09	06:08:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da				
2010/08/09	06:10:56.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2010/08/09	06:10:58.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2010/08/09	06:11:00.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2010/08/09	06:11:02.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2010/08/09	06:22:00.0	XRT_CTRL_MANU_408_OG [0x198]								

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2010/08/09	06:22:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/09	06:22:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/09	06:25:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/09	06:56:00.0	XRT_Custom_418_OG [0x1a2]				
2010/08/09	06:57:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/09	08:02:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/09	08:02:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/09	08:02:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/09	08:05:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/09	08:34:30.0	XRT_Custom_418_OG [0x1a2]				
2010/08/09	08:35:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/09	09:42:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/09	09:42:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/09	09:42:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/09	09:45:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/09	10:13:00.0	XRT_Custom_418_OG [0x1a2]				
2010/08/09	10:14:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/09	11:27:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/09	11:27:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/09	11:27:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/09	11:30:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/09	11:51:30.0	XRT_Custom_418_OG [0x1a2]				
2010/08/09	11:52:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/09	13:06:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/09	13:06:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/09	13:06:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/09	13:09:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/09	13:30:00.0	XRT_Custom_418_OG [0x1a2]				
2010/08/09	13:31:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/09	14:44:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/09	14:44:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/09	14:44:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/09	14:47:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/09	15:20:30.0	XRT_Custom_418_OG [0x1a2]				
2010/08/09	15:21:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/09	16:23:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/09	16:23:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/09	16:23:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/09	16:26:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/09	17:07:00.0	XRT_Custom_418_OG [0x1a2]				
2010/08/09	17:08:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/09	17:51:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/09	17:51:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2010/08/09	17:51:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2010/08/09	17:51:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2010/08/09	17:51:48.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2010/08/09	17:51:50.0	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/08/09	17:54:28.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/09	17:54:30.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2010/08/09	17:54:32.5	XRT_CTRL_AUTO_406_OG [0x196]				

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2010/08/09	18:01:30.0	AOCS_OrE-point_Start_4_OG [0x09a]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
		AOCU_NM		5	02-76	04 00 00 00 00
2010/08/09	18:01:30.5	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/09	18:01:32.5	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	da
2010/08/09	18:01:34.5	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/09	18:04:44.5	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/09	18:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/09	18:44:32.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2010/08/09	18:44:52.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
			MDP_XRT_FLD_ENA	1	07-F0	d8
2010/08/09	18:44:54.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2010/08/09	18:44:56.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
			MDP_XRT_AEC_RESET	1	07-F0	d0
2010/08/09	18:44:58.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5
2010/08/09	18:47:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5
			MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/09	18:47:34.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2010/08/09	18:47:36.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2010/08/09	18:47:38.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/09	19:40:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/09	19:40:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/09	19:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	da
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/09	19:40:04.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/09	20:20:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/09	20:21:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]	1	07-F0	c0
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/09	21:18:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/09	21:18:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/09	21:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	da
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/09	21:18:34.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/09	21:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/09	21:57:30.0	XRT_Custom_418_OG [0x1a2]	XRT_Custom_418_OG [0x1a2]	1	07-F0	c0
2010/08/09	21:58:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/09	22:57:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/09	22:57:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/09	22:57:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/09	23:00:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/09	23:30:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/09	23:31:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]	1	07-F0	c0
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/10	00:35:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/10	00:35:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/10	00:35:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/10	00:38:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/10	00:59:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/10	01:00:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]	1	07-F0	c0
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/10	02:12:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/10	02:12:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/10	02:12:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/10	02:15:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/10	02:37:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2010/08/10	02:38:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	XRT_Custom_418_OG [0x1a2]	1	07-F0	c0
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2010/08/10	03:42:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2010/08/10	03:42:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_FLD_RESET	1	07-F0	da
2010/08/10	03:42:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2010/08/10	03:45:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e8

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2010/08/10	04:16:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/08/10	04:17:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
2010/08/10	05:19:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/08/10	05:19:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/08/10	05:19:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/08/10	05:22:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/08/10	05:54:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/08/10	05:55:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]					
2010/08/10	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/08/10	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/08/10	06:00:00.0	AOCS_Or-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2010/08/10	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00 00	
2010/08/10	06:00:18.0	XRT_FLRCTRL_DIS_403_OG [0x193]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2010/08/10	06:00:20.0	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2010/08/10	06:02:58.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/08/10	06:03:00.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/08/10	06:03:02.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2010/08/10	06:09:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/08/10	06:09:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/08/10	06:10:00.0	AOCS_Or-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2010/08/10	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04 00 00 00 00	
2010/08/10	06:10:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2010/08/10	06:10:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2010/08/10	06:10:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2010/08/10	06:12:56.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2010/08/10	06:12:58.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/08/10	06:13:00.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06	
2010/08/10	06:13:02.5	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2010/08/10	07:00:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/08/10	07:00:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/08/10	07:00:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/08/10	07:03:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/08/10	07:33:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/08/10	07:34:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
2010/08/10	08:39:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/08/10	08:39:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2010/08/10	08:39:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2010/08/10	08:42:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2010/08/10	09:11:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2010/08/10	09:12:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]					
2010/08/10	10:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2010/08/10	11:25:00.0	AOCS_Or-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
		AOCU_NM		5	02-76	00 00 00 00 00	