XRT Timeline to be uploaded on 2010/08/12

Period: 2010/08/12 10:52:00 - 2010/08/17 10:41:00

* * * * * **Normal mode** * * * * *

XOB #16AC: G-Band Alignment with North Pole Q90 2x2(G-band only) - 5min cadence - Partial Sun-wNGT

Term	Pointing (x, y)	Comment
08/12 11:17:00 - 08/12 13:01:54	Fixed (0.0, 945.0)	# OP start + 10min, alignment offset pointing, N.

PROG= 05 1-time(s)
Subr= 1 1-time(s) 360.0sec
Seqn= 21 24-time(s) 300.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec

XOB #16AD: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8 min cadence-wNGT

Term	Pointing (x, y)	Comment
08/12 13:17:00 - 08/12 18:59:54	Fixed (-945.0, 0.0)	* Alignment offset pointing, E.

PROG= 10 1-time(s)
Subr= 1 1-time(s) 360.0sec
Seqn= 22 15-time(s) 480.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec

XOB #17FB: Synoptic_Full-disk_Full-Res Al/Mesh- Ti/Poly- Thick/Al

Term	Pointing (x, y)	Comment
08/12 19:03:02 - 08/12 19:54:00	Fixed (0.0, 0.0)	synoptic, extended for SOT flat fields.

PROG= 16 1-time(s)
Subr= 1 4-time(s) 600.0sec
Seqn= 13 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/thick-Al	Open/thick-Al	close	Safe	Norm	22.6s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 12 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 11 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec
Seqn= 14 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Dark	1.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

XOB #17E7: PFB-AR-Thermal-Evolution-Al/Mesh-Ti/Poly-FOV384-AEC3-1x1

Term	Pointing (x, y)	Comment
08/12 21:13:02 - 08/13 05:33:00	Track (-260.5, 107.6) @ 08/12 21:10:00	# Track AR 11096.

PROG= 11 Inf.-time(s)
Subr= 1 1-time(s) 2.0sec
Seqn= 49 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	1	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	2	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	3	2.0sec

XOB #17B9: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + G-band(16)

Term	Pointing (x, y)	Comment
08/13 06:12:32 - 08/13 06:19:24	Fixed (0.0, 0.0)	synoptic, shifted 9.5 min
08/13 18:01:32 - 08/13 18:08:24	Fixed (0.0, 0.0)	synoptic, shifted -1.5 min
08/14 05:46:32 - 08/14 05:53:24	Fixed (0.0, 0.0)	synoptic, shifted -16.5 min

PROG= 03 1-time(s)
Subr= 1 1-time(s) 12.0sec
Seqn= 7 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 5 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 8 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 4 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #17F6: AR Standard-A(Filter-Ratio) for FW1=Open, 384x384 at 1064 1048, 2min-cad

Term	Pointing (x, y)	Comment
08/13 06:22:32 - 08/13 17:14:30	Track (618.1, 69.9) @ 08/13 06:19:30	# Track AR 11093.
08/13 18:11:32 - 08/14 05:43:24	Track (694.2, 78.1) @ 08/13 18:08:30	# Cont.
08/14 05:56:32 - 08/14 07:51:30	Track (760.9, 87.3) @ 08/14 05:53:30	# Cont.

	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
PROG= 14 Inf.-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 19 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 20 4-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 78 20-time(s) 120.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

✲ ✲ ✲ ✲ ✲ Flare mode ✲ ✲ ✲ ✲ ✲

XOB #17EF: Flare Standard Obs. with eruptions mode-A (FW1=Open)

Term	Pointing (x, y)	Comment
08/12 21:13:02 - 08/13 05:33:00	Track (-260.5, 107.6) @ 08/12 21:10:00	# Track AR 11096.
08/13 06:22:32 - 08/13 17:14:30	Track (618.1, 69.9) @ 08/13 06:19:30	# Track AR 11093.
08/13 18:11:32 - 08/14 05:43:24	Track (694.2, 78.1) @ 08/13 18:08:30	# Cont.
08/14 05:56:32 - 08/14 07:51:30	Track (760.9, 87.3) @ 08/14 05:53:30	# Cont.

	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
PROG= 07 1-time(s)													
Subr= 1 30-time(s) 20.0sec													
Seqn= 87 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 91 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 90 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec													
Seqn= 87 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 90 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec													
Seqn= 87 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 90 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec													
Seqn= 87 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 88 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4 24-time(s) 600.0sec													
Seqn= 89 1-time(s) 2.0sec													

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec

Active Region Search

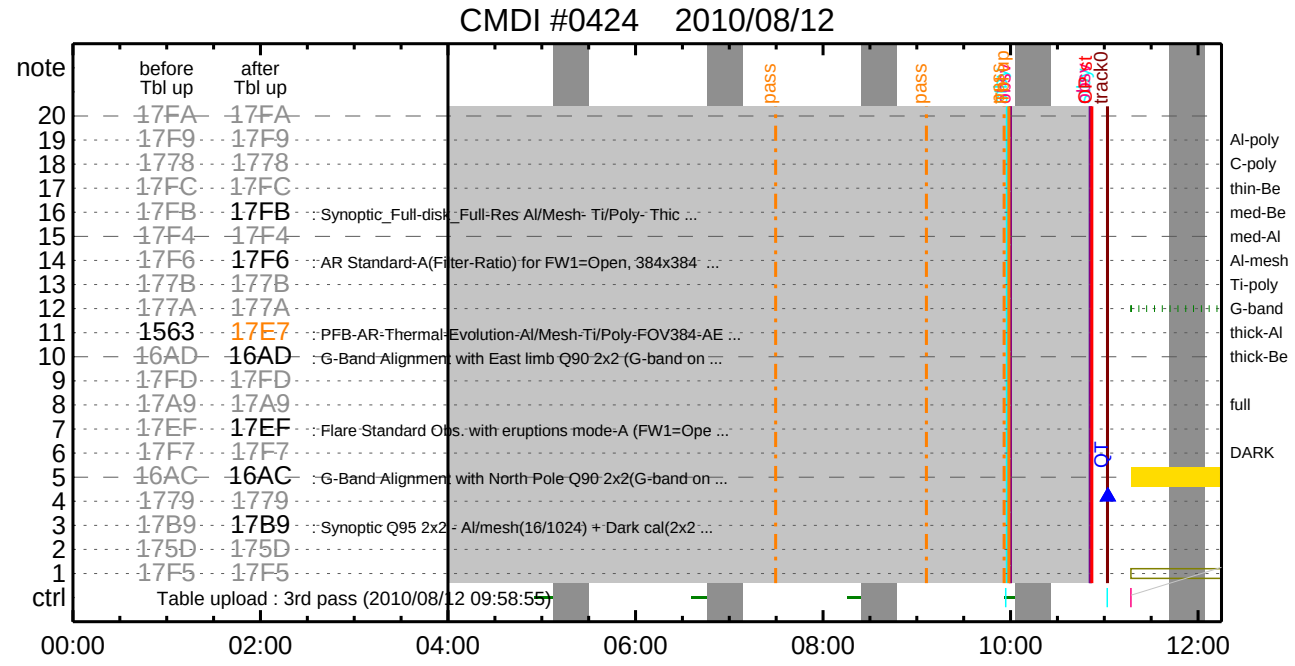
NOT USED

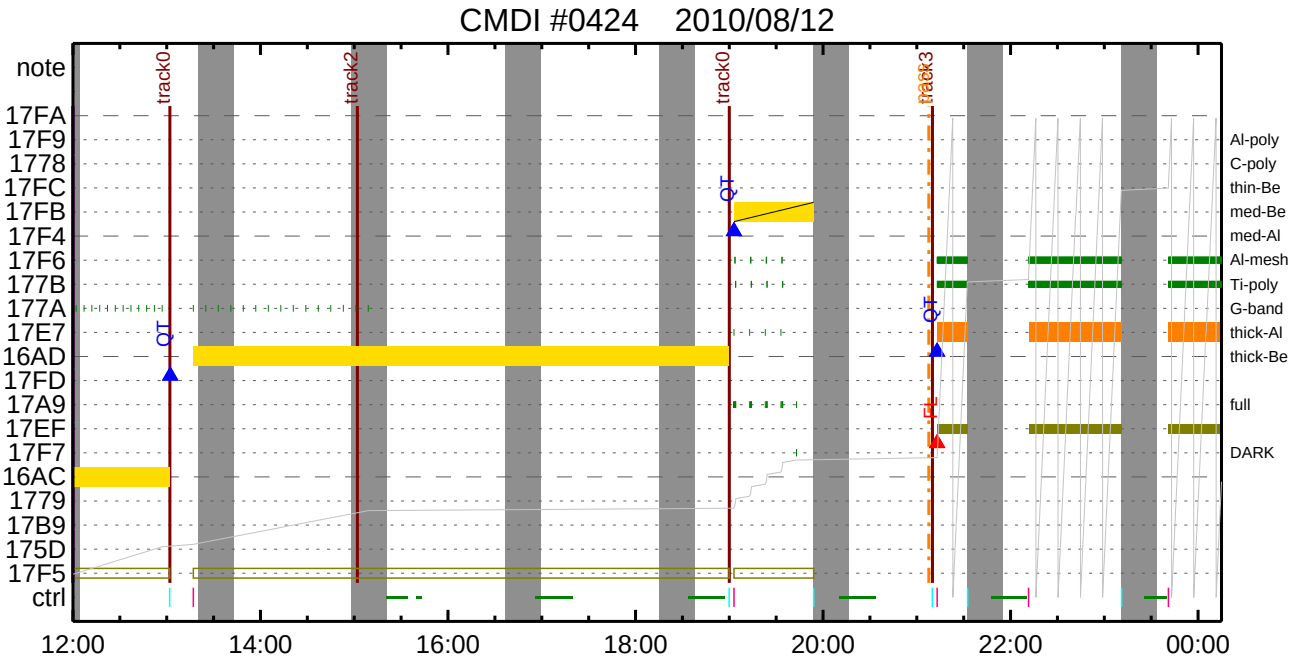
Flare Detection

FLD Patrol

Term	Pointing (x, y)	Comment
08/12 21:10:16 - 08/13 06:09:46	Track (-260.5, 107.6) @ 08/12 21:10:00	# Track AR 11096.
08/13 06:19:46 - 08/13 17:58:46	Track (618.1, 69.9) @ 08/13 06:19:30	# Track AR 11093.
08/13 18:08:46 - 08/14 05:43:46	Track (694.2, 78.1) @ 08/13 18:08:30	# Cont.
08/14 05:53:46 - 08/17 10:41:00	Track (760.9, 87.3) @ 08/14 05:53:30	# Cont.

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec


CMDI #0424 2010/08/12
note
before Tbl up
after Tbl up
20 17FA 17FA
19 17F9 17F9
18 1778 1778
17 17FC 17FC
16 17FB 17FB : Synoptic_Full-disk_Full-Res Al/Mesh- Ti/Poly- Thic ...
15 17F4 17F4
14 17F6 17F6 : AR Standard-A(Filter-Ratio) for FW1=Open, 384x384 ...
13 177B 177B
12 177A 177A
11 1563 17E7 : PFB-AR-Thermal-Evolution-Al/Mesh-Ti/Poly-FOV384-AE ...
10 16AD 16AD : G-Band Alignment with East limb Q90 2x2 (G-band on ...
9 17FD 17FD
8 17A9 17A9
7 17EF 17EF : Flare Standard Obs. with eruptions mode-A (FW1=Ope ...
6 17F7 17F7
5 16AC 16AC : G-Band Alignment with North Pole Q90 2x2(G-band on ...
4 1779 1779
3 17B9 17B9 : Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 ...
2 175D 175D
1 17F5 17F5
ctrl Table upload : 3rd pass (2010/08/12 09:58:55)
pass
pass
track0
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0424 2010/08/12
note
track0
track2
track0
track3
QT
QT
QT
FL
17FA
17F9
1778
17FC
17FB
17F4
17F6
177B
177A
17E7
16AD
17FD
17A9
17EF
17F7
16AC
1779
17B9
175D
17F5
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
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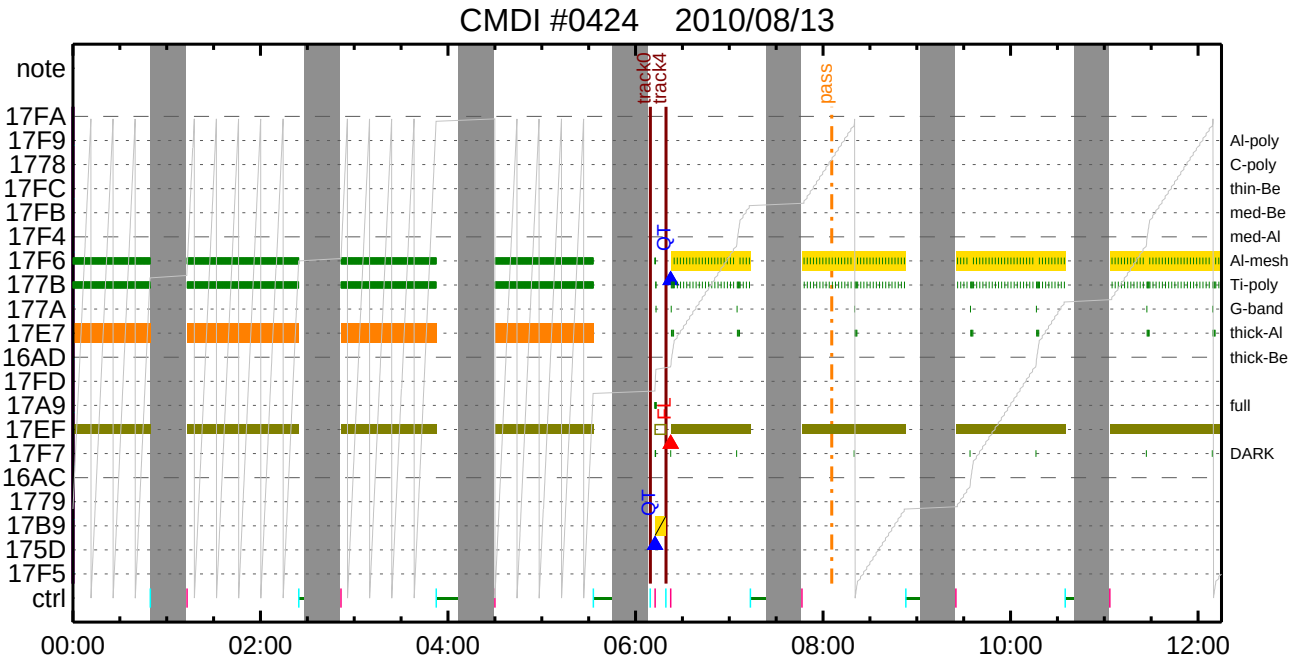
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0424 2010/08/13
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track0
track4
pass
QT
FL
QT
17FA
17F9
1778
17FC
17FB
17F4
17F6
177B
177A
17E7
16AD
17FD
17A9
17EF
17F7
16AC
1779
17B9
175D
17F5
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
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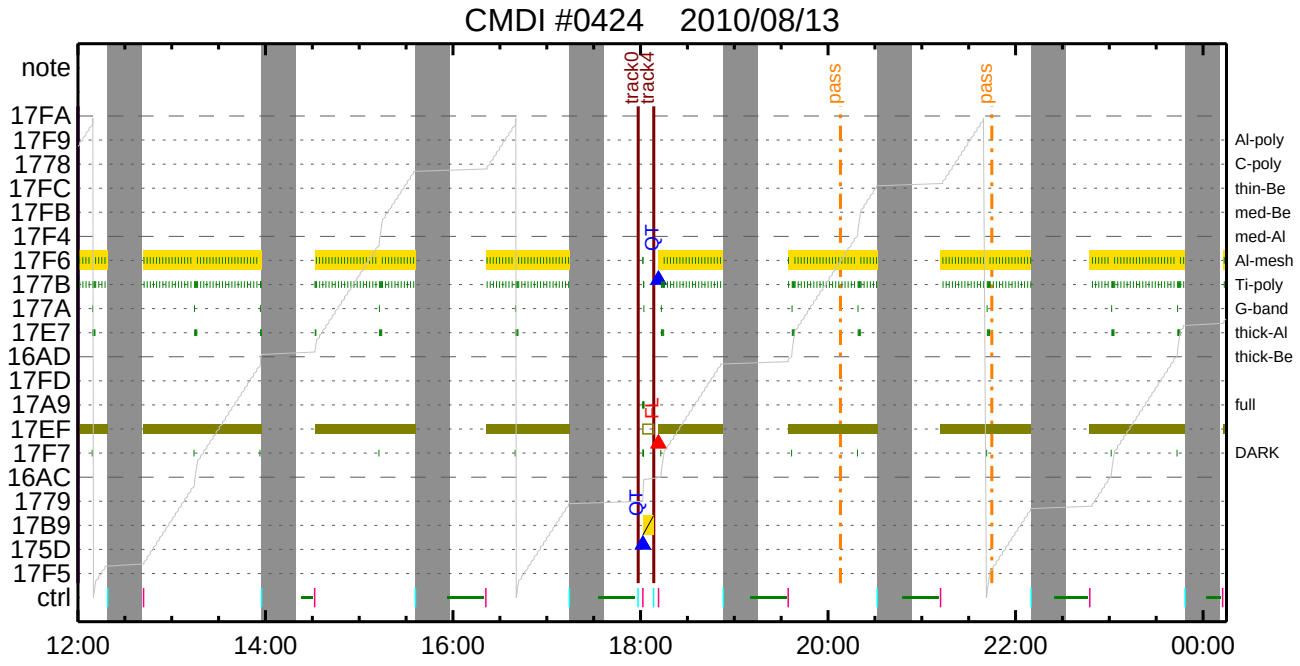
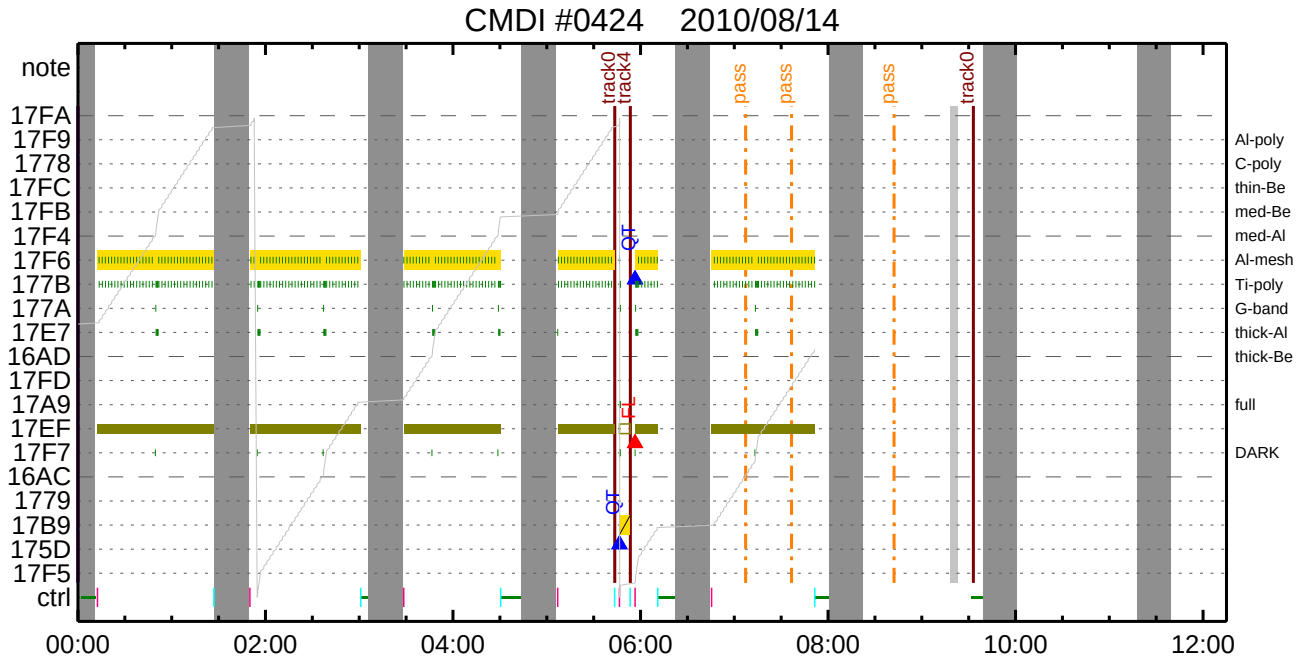
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0424 2010/08/13
note
17FA
17F9
1778
17FC
17FB
17F4
17F6
177B
177A
17E7
16AD
17FD
17A9
17EF
17F7
16AC
1779
17B9
175D
17F5
ctrl
track0
track4
pass
pass
QT
QT
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK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note
17FA
17F9
1778
17FC
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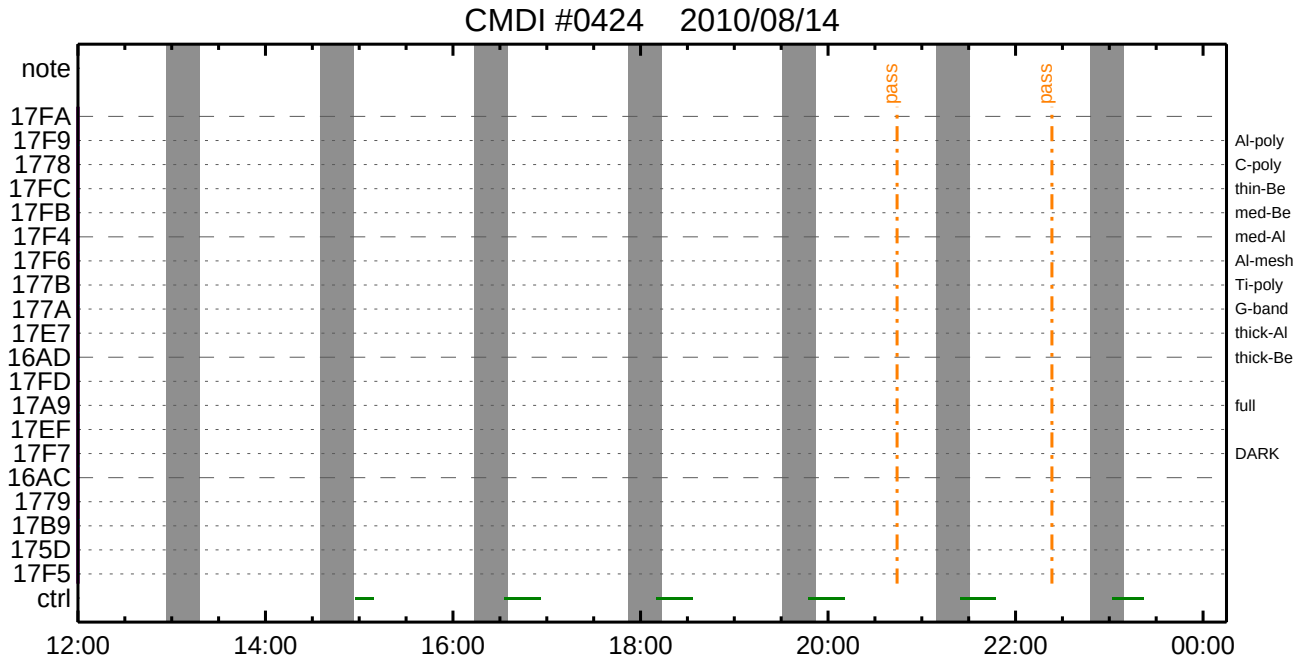
177B
177A
17E7
16AD
17FD
17A9
17EF
17F7
16AC
1779
17B9
175D
17F5
ctrl
track0
track4
pass
pass
pass
track0
QT
QT
00:00
02:00
04:00
06:00
08:00
10:00
12:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0424 2010/08/14
note
17FA
17F9
1778
17FC
17FB
17F4
17F6
177B
177A
17E7
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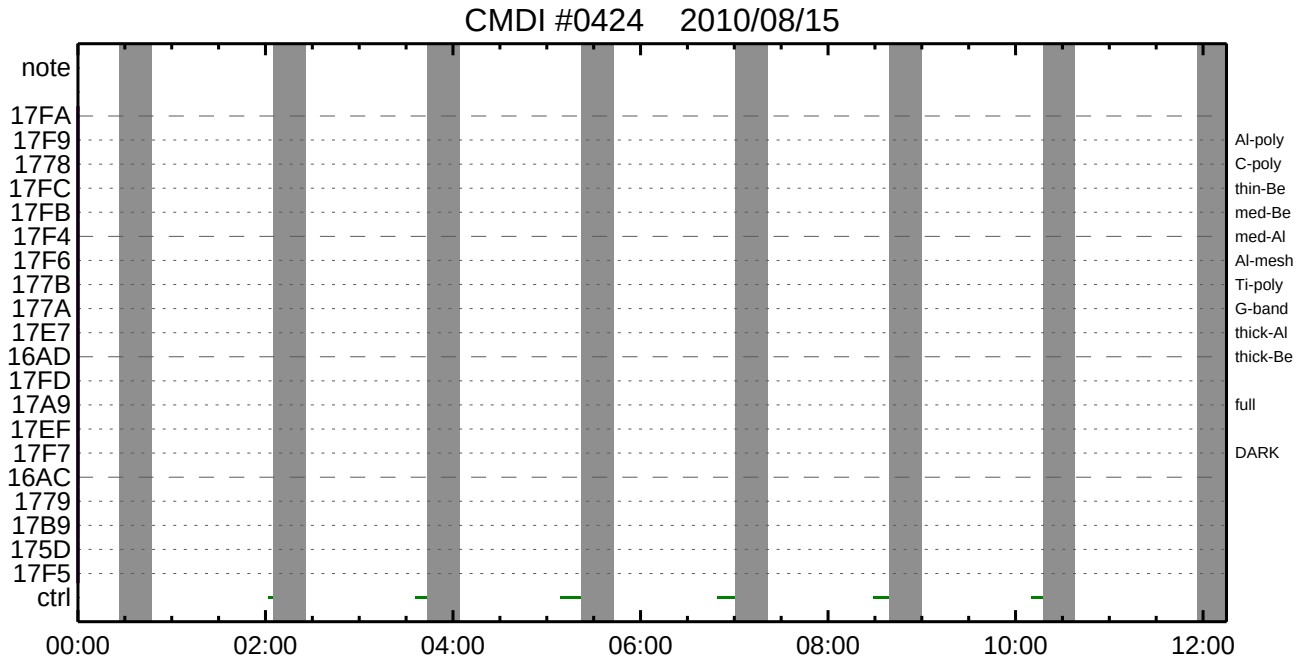
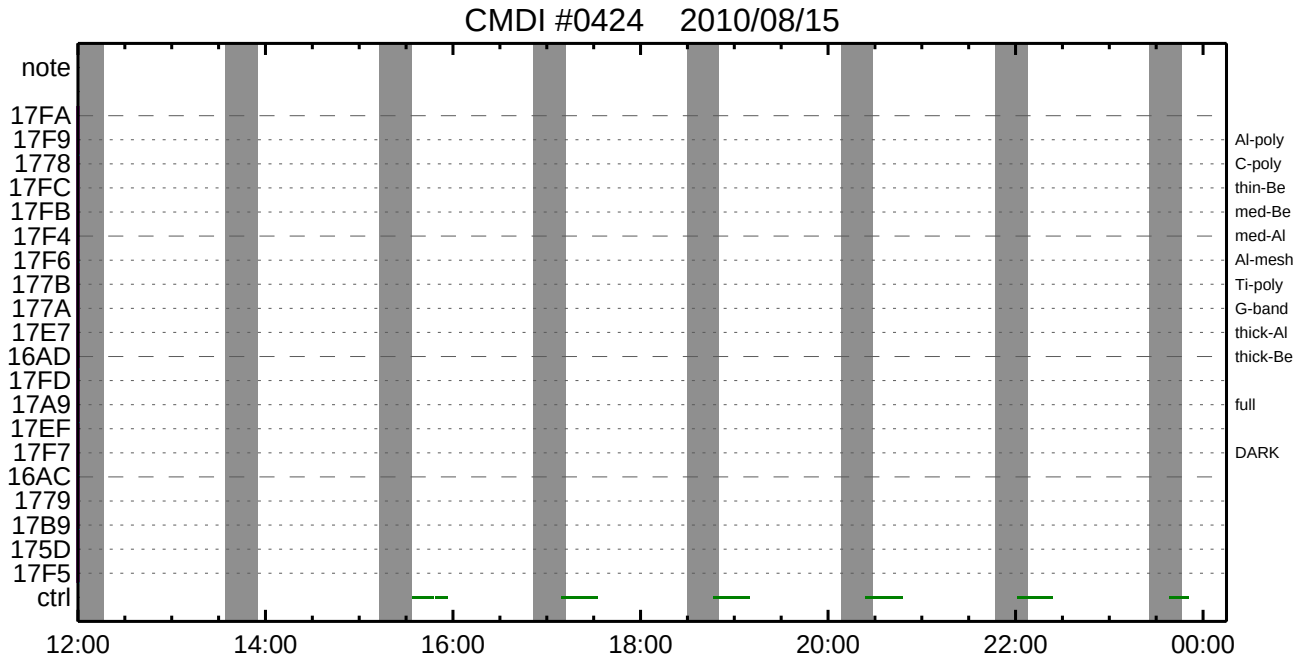
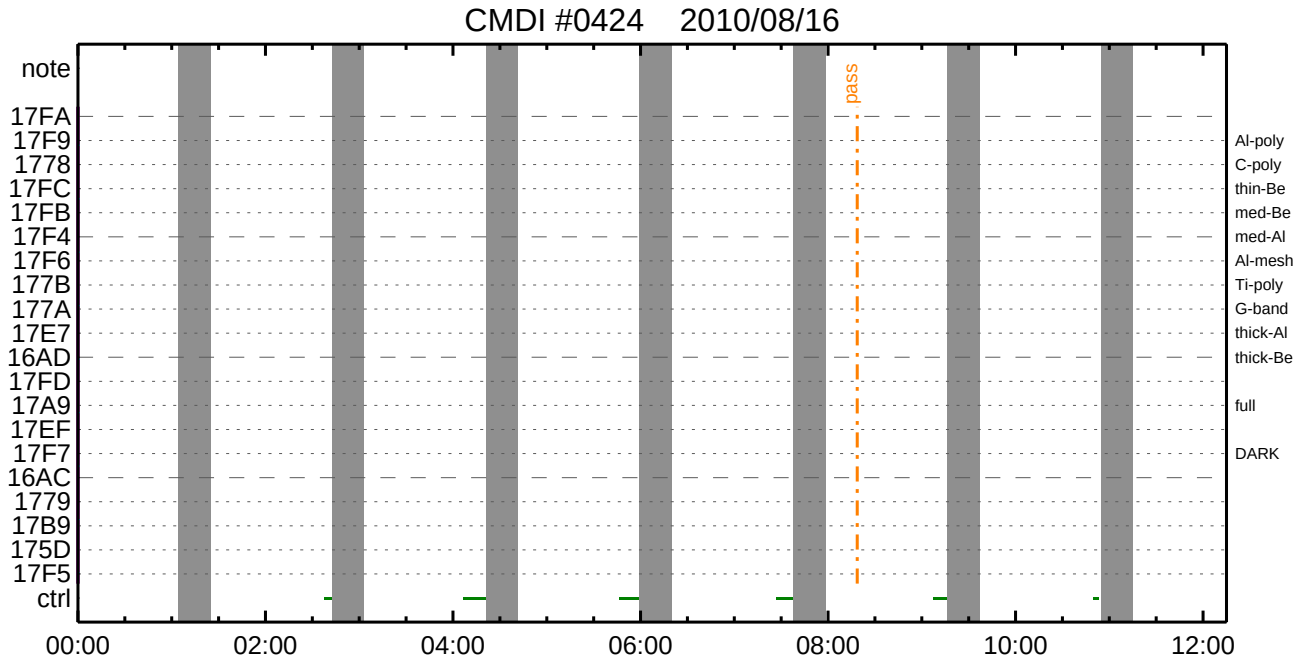
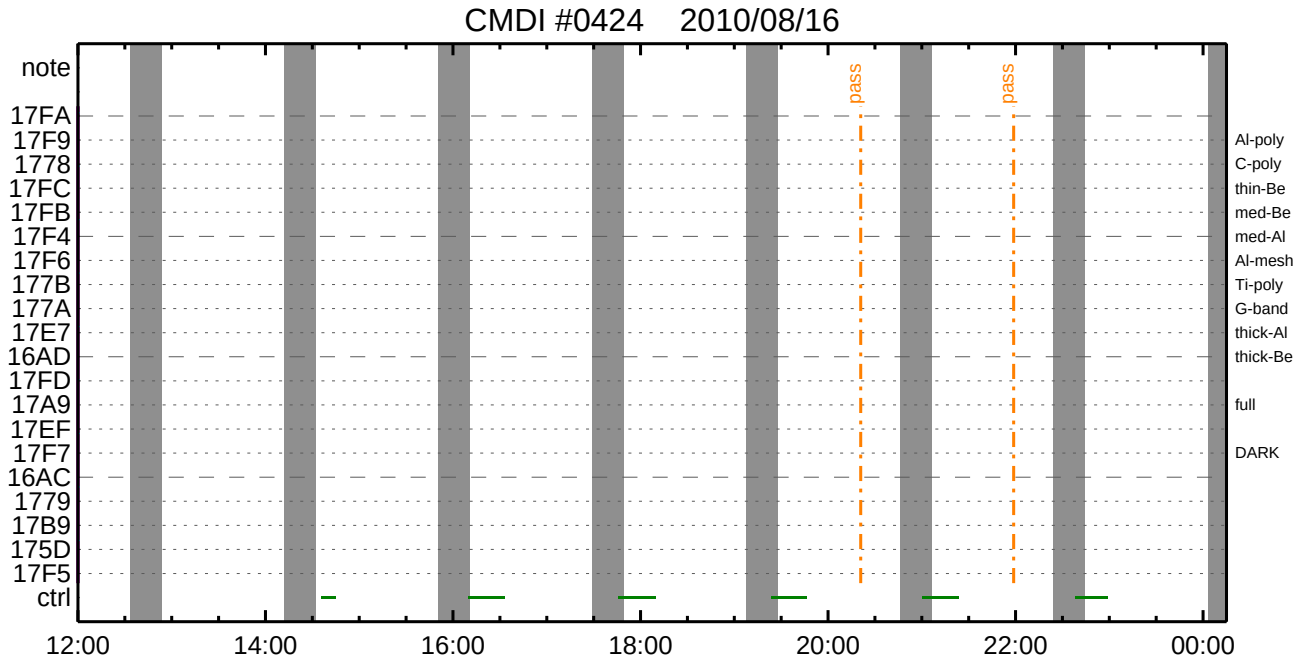
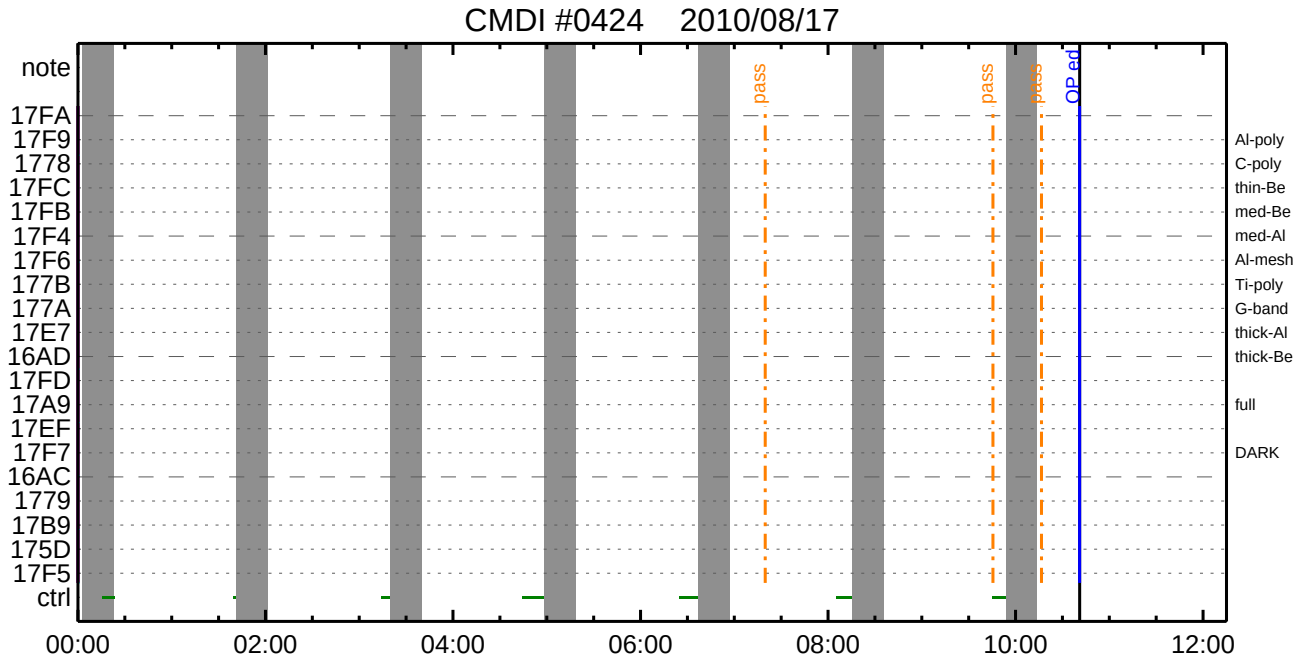
17FD
17A9
17EF
17F7
16AC
1779
17B9
175D
17F5
ctrl
pass
pass
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK

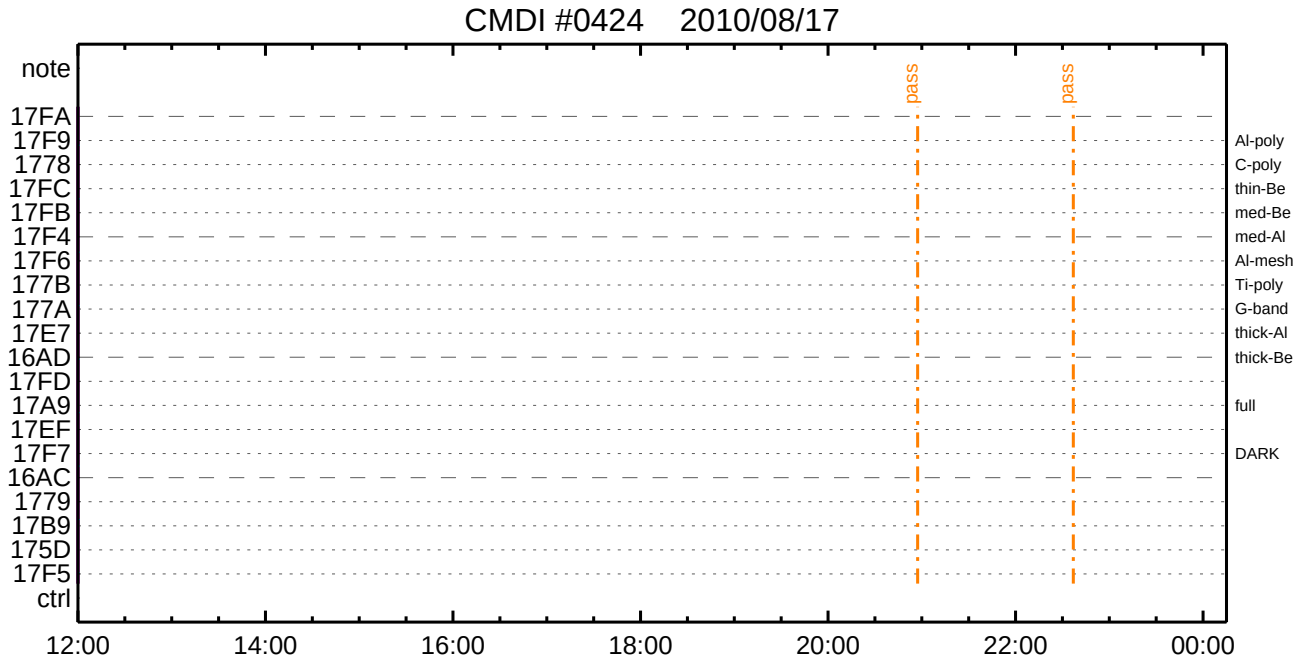

CMDI #0424 2010/08/15
note
17FA
17F9
1778
17FC
17FB
17F4
17F6
177B
177A
17E7
16AD
17FD
17A9
17EF
17F7
16AC
1779
17B9
175D
17F5
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0424 2010/08/15
note
17FA
17F9
1778
17FC
17FB
17F4
17F6
177B
177A
17E7
16AD
17FD
17A9
17EF
17F7
16AC
1779
17B9
175D
17F5
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0424 2010/08/16
note
pass
17FA
17F9
1778
17FC
17FB
17F4
17F6
177B
177A
17E7
16AD
17FD
17A9
17EF
17F7
16AC
1779
17B9
175D
17F5
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0424 2010/08/16
note
17FA
17F9
1778
17FC
17FB
17F4
17F6
177B
177A
17E7
16AD
17FD
17A9
17EF
17F7
16AC
1779
17B9
175D
17F5
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0424 2010/08/17
note
17FA
17F9
1778
17FC
17FB
17F4
17F6
177B
177A
17E7
16AD
17FD
17A9
17EF
17F7
16AC
1779
17B9
175D
17F5
ctrl
pass
pass
pass
OP ed
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0424 2010/08/17
note
17FA
17F9
1778
17FC
17FB
17F4
17F6
177B
177A
17E7
16AD
17FD
17A9
17EF
17F7
16AC
1779
17B9
175D
17F5
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-521 2010-08-12 13:12:48 289 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ****************************
0099     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0100     C. ****************************
0101     C.
0102   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0103   . S. OP    op-521:OP
0104     ()
0105   . S. OG    og-521:OG
0106     ()
0107     C.
0108   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0109     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0110  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111     BC       (20 00 7f 01 02)
0112     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0113     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0114     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0115     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0116     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0117  +. DC 01-22 DHU_MODE_CHNG
0118     BC       (07 0b f8)
0119     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0120     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0122     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0123     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0124   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0125     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0126   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0127     C.
0128     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0129  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130     BC       (20 80 7f 01 02)
0131     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0132     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0133     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0134     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0135     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0136  +. DC 01-22 DHU_MODE_CHNG
0137     BC       (07 0b f8)
0138     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0139     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0141     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0142     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0143   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0144     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0145   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0146     C.
0147     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149     BC       (21 00 41 01 02)
0150     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0151     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0152     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0153     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0154     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0155  +. DC 01-22 DHU_MODE_CHNG
0156     BC       (07 0b f8)
0157     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0158     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0160     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0161     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0162   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0163     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0164   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0165     C.
0166   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0167     C. DHU¥â¡¼¥É¡Ê¼ý¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0168  +. DC 01-22 DHU_MODE_CHNG
0169     BC       (02 0a f8)
0170     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0171     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0172     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0173     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0174     C.
0175   . C. ********************************
0176     C. TI-CMD SET (OPOG STOP/COPY/START)
0177     C. ********************************
0178     C.
0179   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
0180     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¿¼Ç¹Ô¤¦¤³¤È¡£
0181     C.
0182   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0183  +. TI 2010-08-12 10:47:00.0
0184     DC 01-B3 DHU_OP_STOP
0185     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186     C.
0187  +. TI 2010-08-12 10:47:01.0
0188     DC 01-B4 DHU_OP_COPY
0189     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190     C.
0191  +. TI 2010-08-12 10:47:01.0
0192     DC 01-B5 DHU_OPOG_COPY
0193     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195   +. TI 2010-08-12 10:51:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ***************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ***************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238    . C. Stop EIS observation and temporarily disable EIS mode changes
0239     C.
0240     C.
0241     C. ********** Start EIS operation (TI set) **********
0242     C. Execute, after the success of OP upload.
0243     C. Set EIS TI-commands
0244   +. TI 2010-08-12 10:51:30.0
0245     DC 07-FC EIS_MODE_MANU
0246     BC       (21 02)
0247   +. TI 2010-08-12 10:51:40.0
0248     DC 07-FC EIS_MODE_CHG_DIS
0249     BC       (22)
0250    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0251     C. ************ End EIS operation (TI set) ************
0252     C.
0253     C.
0254     C. ********************************
0255     C.  SOT TI command set
0256     C. ********************************
0257     C. Execute, after the success of OP upload.
0258   +. TI 2010-08-12 10:51:16.0
0259     DC 07-F0 MDP_SOT_MODE_STBY
0260     BC       (41)
0261    . C. ------------------------------------
0262     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0263     C. ------------------------------------
0264     C. ************ SOT END ************
0265     C.
0266     C. ************ XRT START ************
0267     C. Execute, after the success of OP upload.
0268   +. TI 2010-08-12 10:51:00.0
0269     DC 07-F0 MDP_XRT_MODE_STBY
0270     BC       (c3)
0271    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0272     C.
0273     C. ************ XRT END ************
0274     C.
0275    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0276     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0277    . S. DC-BC dcbc-402:DCBC
0278     (MDP_known_event)
0279     C.
0280     C.
0281    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0282    . S. DC-BC dcbc-153:DCBC
0283     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284     C.
0285     C.
0286    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0287     C.
0288    . C. ********** LOS **********
0289     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-522 2010-08-12 13:12:48 219 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Äº²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106   +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113   +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                      Result = OK [ ]
0121     C.
0122   +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                         <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127   +. DC 02-8E AOCU_ORB_UPD
0128   . C.
0129   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130   +. DC 07-FC EIS_MODE_MANU
0131     BC       (21 02)
0132   . C. Verify EIS in MANUAL mode
0133   . C. Estimated OBSTBL upload time is 15s
0134     C. *********************************
0135     C. EIS START OBSTBL LOAD
0136     C. *********************************
0137   . S. RAM   ram-820:EIS_OBSTBL
0138     ()
0139   +. DC 07-FC EIS_DUMP_OBSTBL
0140     BC       (07 07 07 00 00 70 00)
0141     C.
0142     C. Execute, after the success of OBSTBL upload.
0143     C. Set EIS TI-commands
0144   +. TI 2010-08-12 10:51:50.0
0145     DC 07-FC EIS_MODE_CHG_ENA
0146     BC       (20)
0147   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0148     C. *********************************
0149     C. EIS END OBSTBL LOAD
0150     C. *********************************
0151   . C. *********************************
0152     C.  OTA CLU OP HTR DIS
0153     C. *********************************
0154     C.
0155     C. < Check initial temperatures >
0156     C. -----------------------------------
0157     C.  HK1_OTA_CLU_HTR_TMP (BChtr)= [     ]
0158     C.  HK1_OTA_M2_HTR_TMP (M2htr) = [     ]
0159     C.  HK1_OTA-13_TEMP (MC)       = [     ]
0160     C.  HK1_OTA-29_TEMP (CLU)      = [     ]
0161     C.  HK1_OTA-30_TEMP (CTM-TM)   = [     ]
0162     C.  HK1_FPP_PMU_TEMP           = [     ]
0163     C. -----------------------------------
0164     C.
0165   . C. < Disable Heaters >
0166   +. DC 07-56 CTM_OTA_HTR_ALL_DIS
0167   . C. -----------------------------------
0168     C.  CTM_CLU_HTR_OP1_E/D = DIS     [ ]
0169     C.  CTM_CLU_HTR_OP2_E/D = DIS     [ ]
0170     C.  CTM_M2_HTR_OP1_E/D  = DIS     [ ]
0171     C.  CTM_M2_HTR_OP2_E/D  = DIS     [ ]
0172     C. -----------------------------------
0173     C.
0174   . C. < Enable M2 OP1 Heater >
0175   +. DC 07-50 CTM_M2_HTR_OP1_ENA
0176   . C. -----------------------------------
0177     C.  CTM_M2_HTR_OP1_E/D  = ENA     [ ]
0178     C.  CTM_OTA_HTR_VOLT    = NORM    [ ]
0179     C.  HK1_OTA_M2_HTR_TMP  increase  [ ]
0180     C. -----------------------------------
0181     C.
0182   . C. -----------------------------------
0183     C. °Ê²¼¤Ï¥Ð¥Ã¥ ¯¥¢¥Ã¥×ÍÑ¡£ÌäÂê¤¬Ìµ¤±¤ì¤Ð»ÈÍÑ¤•¤Ê¤¤
0184   . C. < Enable Back-up Heaters >
0185   +. DC 07-52 CTM_CLU_HTR_OP1_ENA
0186   . C. -----------------------------------
0187     C.  CTM_CLU_HTR_OP1_E/D = ENA     [ ]
0188     C.  CTM_OTA_HTR_VOLT    = NORM    [ ]
0189     C. -----------------------------------
0190     C.
0191   +. DC 07-53 CTM_CLU_HTR_OP2_ENA
0192   . C. -----------------------------------
0193     C.  CTM_CLU_HTR_OP2_E/D = ENA     [ ]
```

```
0194     C.  CTM_OTA_HTR_VOLT    = NORM    [ ]
0195     C. ------------------------------------
0196     C.
0197  +. DC 07-51 CTM_M2_HTR_OP2_ENA
0198   . C. ------------------------------------
0199     C.  CTM_M2_HTR_OP2_E/D  = ENA     [ ]
0200     C.  CTM_OTA_HTR_VOLT    = NORM    [ ]
0201     C. ------------------------------------
0202     C.
0203     C. ********** SOT END ***********
0204     C.
0205   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0206     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0207   . S. DC-BC dcbc-402:DCBC
0208     (MDP_known_event)
0209     C.
0210     C.
0211   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0212   . S. DC-BC dcbc-153:DCBC
0213     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0214     C.
0215     C.
0216   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0217     C.
0218   . C. ********** LOS **********
0219     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-523 2010-08-12 13:12:48 122 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014     C.
0015     C. ************ XRT START ************
0016     C.
0017   +. DC 07-F0 MDP_XRT_CTRL_MANU
0018     BC       (c1)
0019   +  DC 07-F0 MDP_XRT_MODE_STBY
0020     BC       (c3)
0021   . C. ----------- Success Verify ?              OK / NG____
0022     C.
0023     C. XRT Obs. Table Upload
0024   . S. RAM   ram-291:MDP_OBS_X
0025     ()
0026     C.
0027   +. DC 07-F0 MDP_DUMP_XRTTBL
0028     BC       (84 07 00 00 00 3a d4)
0029   . C. ----------- Comparison Check ?                        OK / ERR ____
0030     C.
0031     C.
0032   +. DC 07-F0 MDP_XRT_ROI_SET
0033     BC       (cd 01 b1 b1 04 04)
0034   +  DC 07-F0 MDP_XRT_ROI_SET
0035     BC       (cd 02 b1 b1 08 08)
0036   +  DC 07-F0 MDP_XRT_ROI_SET
0037     BC       (cd 03 b1 b1 08 08)
0038   +  DC 07-F0 MDP_XRT_ROI_SET
0039     BC       (cd 04 b1 b1 06 06)
0040   +  DC 07-F0 MDP_XRT_ROI_SET
0041     BC       (cd 05 80 80 06 06)
0042   +  DC 07-F0 MDP_XRT_ROI_SET
0043     BC       (cd 06 80 60 20 18)
0044   +  DC 07-F0 MDP_XRT_ROI_SET
0045     BC       (cd 07 a0 80 18 20)
0046   +  DC 07-F0 MDP_XRT_ROI_SET
0047     BC       (cd 08 80 80 20 20)
0048   +  DC 07-F0 MDP_XRT_ROI_SET
0049     BC       (cd 09 80 80 06 06)
0050   +  DC 07-F0 MDP_XRT_ROI_SET
0051     BC       (cd 0a 80 80 20 08)
0052   +  DC 07-F0 MDP_XRT_ROI_SET
0053     BC       (cd 0b 80 80 08 20)
0054   +  DC 07-F0 MDP_XRT_ROI_SET
0055     BC       (cd 0c 85 83 06 06)
0056   +  DC 07-F0 MDP_XRT_ROI_SET
0057     BC       (cd 0f 80 80 06 06)
0058   +  DC 07-F0 MDP_XRT_ROI_SET
0059     BC       (cd 10 80 80 08 08)
0060   +  DC 07-F0 MDP_XRT_AEC_RESET
0061     BC       (d0)
0062   . C. ---------- Success Verify ?           OK / NG ____
0063     C.
0064     C.
0065   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0066     C.
0067   +. DC 07-F0 MDP_XRT_MODE_OBSV
0068     BC       (c2)
0069   +. TI 2010-08-12 10:51:02.0
0070     DC 07-F0 MDP_XRT_MODE_OBSV
0071     BC       (c2)
0072   . C. ---------- Success Verify ?           OK / NG ____
0073     C.
0074     C. ************ XRT END ***********
0075   . C. ********************************
0076     C.  SOT table upload
0077     C. ********************************
0078   . C. < Stop FG table >
0079   +. DC 07-F0 MDP_FG_CTRL_MANU
0080     BC       (51)
0081   . C. ------------------------------------
0082     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0083     C. ------------------------------------
0084     C.
0085   . C. <Upload FG Observation Table>
0086   . S. RAM   ram-265:MDP_OBS_F
0087     ()
0088     C.
0089   . C. < Dump RAMID=MDP_OBS_F >
0090   +. DC 07-F0 MDP_DUMP_FGTBL
0091     BC       (82 07 00 00 00 38 b8)
0092     C. ------------------------------------
0093     C.  MDP_OBS_F verify = OK/NG        [ ]
0094     C. ------------------------------------
0095     C.
```

```
0096     C. ******************************
0097     C.  SOT TI command set
0098     C. ******************************
0099     C. Execute, after the success of TBL upload.
0100  +. TI 2010-08-12 10:51:18.0
0101     DC 07-F0 MDP_SOT_MODE_OBSV
0102     BC       (40)
0103   . C. ------------------------------------
0104     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0105     C. ------------------------------------
0106     C.
0107     C.
0108   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0109     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0110   . S. DC-BC dcbc-402:DCBC
0111     (MDP_known_event)
0112     C.
0113     C.
0114   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0115   . S. DC-BC dcbc-153:DCBC
0116     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0117     C.
0118     C.
0119   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0120     C.
0121   . C. ********** LOS **********
0122     C.
```

```
*** OP Sequence for XRT ***

2010/08/12  11:01:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/12  11:01:56.0  XRT_FOCUS_POSITION_426_OG [0x1aa]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/08/12  11:02:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 ac 00 00 00
2010/08/12  11:02:16.0  XRT_QT_PROG_SET_432_OG [0x1b0]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 05
2010/08/12  11:02:18.0  XRT_FLD_DIS_427_OG [0x1ab]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2010/08/12  11:02:20.0  XRT_FLRCTRL_DIS_428_OG [0x1ac]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/08/12  11:02:22.0  XRT_ARS_DIS_430_OG [0x1ae]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/08/12  11:17:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/12  13:01:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/12  13:01:56.0  XRT_FOCUS_POSITION_426_OG [0x1aa]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/08/12  13:02:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 54 00
2010/08/12  13:02:16.0  XRT_QT_PROG_SET_431_OG [0x1af]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0a
2010/08/12  13:02:18.0  XRT_FLD_DIS_427_OG [0x1ab]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2010/08/12  13:02:20.0  XRT_FLRCTRL_DIS_428_OG [0x1ac]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/08/12  13:02:22.0  XRT_ARS_DIS_430_OG [0x1ae]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/08/12  13:17:00.0  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/12  15:02:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 02 00 00 00 00
2010/08/12  18:59:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/12  18:59:56.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/08/12  19:00:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 00 00 00 00
2010/08/12  19:00:16.0  XRT_FLD_DIS_402_OG [0x192]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2010/08/12  19:00:18.0  XRT_FLRCTRL_DIS_403_OG [0x193]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/08/12  19:00:20.0  XRT_ARS_DIS_404_OG [0x194]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/08/12  19:02:58.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/12  19:03:00.0  XRT_QT_PROG_SET_437_OG [0x1b5]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 10
2010/08/12  19:03:02.5  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/12  19:54:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/12  19:54:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/12  19:54:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/08/12  19:57:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/08/12  21:09:54.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/12  21:09:56.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/08/12  21:10:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM              5 02-76 03 00 00 00 00
2010/08/12  21:10:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/08/12  21:10:18.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/08/12  21:10:20.0  XRT_AEC_RESET_441_OG [0x1b9]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2010/08/12  21:10:22.0  XRT_ARS_DIS_420_OG [0x1a4]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/08/12  21:12:56.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/12  21:12:58.0  XRT_QT_PROG_SET_446_OG [0x1be]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2010/08/12  21:13:00.0  XRT_FL_PROG_SET_405_OG [0x195]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2010/08/12  21:13:02.5  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/12  21:32:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/12  21:32:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/12  21:32:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/08/12  21:35:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/08/12  22:10:30.0  XRT_Custom_418_OG [0x1a2]
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2010/08/12  22:11:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/12  23:11:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/12  23:11:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/12  23:11:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/08/12  23:14:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/08/12  23:40:00.0  XRT_Custom_418_OG [0x1a2]
2010/08/12  23:41:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/13  00:49:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/13  00:49:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/13  00:49:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/08/13  00:52:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/08/13  01:12:00.0  XRT_Custom_418_OG [0x1a2]
2010/08/13  01:13:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/13  02:24:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/13  02:24:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/13  02:24:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/08/13  02:27:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/08/13  02:50:30.0  XRT_Custom_418_OG [0x1a2]
2010/08/13  02:51:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/13  03:52:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/13  03:52:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/13  03:52:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/08/13  03:55:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/08/13  04:29:00.0  XRT_Custom_418_OG [0x1a2]
2010/08/13  04:30:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/13  05:33:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/13  05:33:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/13  05:33:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/08/13  05:36:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/08/13  06:09:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/13  06:09:26.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/08/13  06:09:30.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 00 00 00 00
2010/08/13  06:09:46.0  XRT_FLD_DIS_402_OG [0x192]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2010/08/13  06:09:48.0  XRT_FLRCTRL_DIS_403_OG [0x193]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/08/13  06:09:50.0  XRT_ARS_DIS_404_OG [0x194]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/08/13  06:12:28.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/13  06:12:30.0  XRT_QT_PROG_SET_407_OG [0x197]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2010/08/13  06:12:32.5  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/13  06:19:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/13  06:19:26.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/08/13  06:19:30.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM              5 02-76 04 00 00 00 00
2010/08/13  06:19:46.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/08/13  06:19:48.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/08/13  06:19:50.0  XRT_AEC_RESET_441_OG [0x1b9]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2010/08/13  06:19:52.0  XRT_ARS_DIS_420_OG [0x1a4]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/08/13  06:22:26.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/13  06:22:28.0  XRT_QT_PROG_SET_448_OG [0x1c0]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0e
2010/08/13  06:22:30.0  XRT_FL_PROG_SET_405_OG [0x195]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2010/08/13  06:22:32.5  XRT_CTRL_AUTO_406_OG [0x196]
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                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/13  07:13:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/13  07:13:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/13  07:13:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/08/13  07:16:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/08/13  07:45:30.0  XRT_Custom_418_OG [0x1a2]
2010/08/13  07:46:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/13  08:53:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/13  08:53:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/13  08:53:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/08/13  08:56:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/08/13  09:24:00.0  XRT_Custom_418_OG [0x1a2]
2010/08/13  09:25:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/13  10:35:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/13  10:35:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/13  10:35:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/08/13  10:38:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/08/13  11:02:30.0  XRT_Custom_418_OG [0x1a2]
2010/08/13  11:03:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/13  12:19:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/13  12:19:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/13  12:19:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/08/13  12:22:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/08/13  12:41:00.0  XRT_Custom_418_OG [0x1a2]
2010/08/13  12:42:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/13  13:57:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/13  13:57:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/13  13:57:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/08/13  14:00:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/08/13  14:30:30.0  XRT_Custom_418_OG [0x1a2]
2010/08/13  14:31:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/13  15:36:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/13  15:36:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/13  15:36:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/08/13  15:39:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/08/13  16:20:00.0  XRT_Custom_418_OG [0x1a2]
2010/08/13  16:21:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/13  17:14:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/13  17:14:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/13  17:14:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/08/13  17:17:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/08/13  17:58:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/13  17:58:26.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/08/13  17:58:30.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2010/08/13  17:58:46.0  XRT_FLD_DIS_402_OG [0x192]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2010/08/13  17:58:48.0  XRT_FLRCTRL_DIS_403_OG [0x193]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/08/13  17:58:50.0  XRT_ARS_DIS_404_OG [0x194]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/08/13  18:01:28.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/13  18:01:30.0  XRT_QT_PROG_SET_407_OG [0x197]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2010/08/13  18:01:32.5  XRT_CTRL_AUTO_406_OG [0x196]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
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2010/08/13  18:08:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/13  18:08:26.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/08/13  18:08:30.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM              5 02-76 04 00 00 00 00
2010/08/13  18:08:46.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2010/08/13  18:08:48.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/08/13  18:08:50.0  XRT_AEC_RESET_441_OG [0x1b9]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2010/08/13  18:08:52.0  XRT_ARS_DIS_420_OG [0x1a4]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2010/08/13  18:11:26.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/13  18:11:28.0  XRT_QT_PROG_SET_448_OG [0x1c0]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0e
2010/08/13  18:11:30.0  XRT_FL_PROG_SET_405_OG [0x195]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2010/08/13  18:11:32.5  XRT_CTRL_AUTO_406_OG [0x196]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/13  18:53:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/13  18:53:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/13  18:53:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/08/13  18:56:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/08/13  19:33:30.0  XRT_Custom_418_OG [0x1a2]
2010/08/13  19:34:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/13  20:31:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/13  20:31:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/13  20:31:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/08/13  20:34:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/08/13  21:11:00.0  XRT_Custom_418_OG [0x1a2]
2010/08/13  21:12:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/13  22:10:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/13  22:10:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/13  22:10:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/08/13  22:13:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/08/13  22:46:30.0  XRT_Custom_418_OG [0x1a2]
2010/08/13  22:47:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/13  23:48:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/13  23:48:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/13  23:48:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/08/13  23:51:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/08/14  00:11:30.0  XRT_Custom_418_OG [0x1a2]
2010/08/14  00:12:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/14  01:27:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/14  01:27:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/14  01:27:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/08/14  01:30:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/08/14  01:49:00.0  XRT_Custom_418_OG [0x1a2]
2010/08/14  01:50:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/14  03:01:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/14  03:01:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/14  03:01:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/08/14  03:04:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/08/14  03:27:30.0  XRT_Custom_418_OG [0x1a2]
2010/08/14  03:28:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/14  04:30:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/14  04:30:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/14  04:30:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
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                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/08/14  04:33:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/08/14  05:06:00.0  XRT_Custom_418_OG [0x1a2]
2010/08/14  05:07:00.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/14  05:43:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/14  05:43:26.0  XRT_FOCUS_POSITION_401_OG [0x191]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2010/08/14  05:43:30.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2010/08/14  05:43:46.0  XRT_FLD_DIS_402_OG [0x192]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2010/08/14  05:43:48.0  XRT_FLRCTRL_DIS_403_OG [0x193]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2010/08/14  05:43:50.0  XRT_ARS_DIS_404_OG [0x194]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/08/14  05:46:28.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/14  05:46:30.0  XRT_QT_PROG_SET_407_OG [0x197]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2010/08/14  05:46:32.5  XRT_CTRL_AUTO_406_OG [0x196]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/14  05:53:24.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/14  05:53:26.0  XRT_FOCUS_POSITION_409_OG [0x199]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2010/08/14  05:53:30.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                        AOCU_NM              5 02-76 04 00 00 00 00
2010/08/14  05:53:46.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2010/08/14  05:53:48.0  XRT_FLRCTRL_ENA_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2010/08/14  05:53:50.0  XRT_AEC_RESET_441_OG [0x1b9]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2010/08/14  05:53:52.0  XRT_ARS_DIS_420_OG [0x1a4]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2010/08/14  05:56:26.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/14  05:56:28.0  XRT_QT_PROG_SET_448_OG [0x1c0]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0e
2010/08/14  05:56:30.0  XRT_FL_PROG_SET_405_OG [0x195]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2010/08/14  05:56:32.5  XRT_CTRL_AUTO_406_OG [0x196]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/14  06:11:00.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/14  06:11:02.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/14  06:11:04.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/08/14  06:14:14.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/08/14  06:44:30.0  XRT_Custom_418_OG [0x1a2]
2010/08/14  06:45:30.0  XRT_CTRL_AUTO_419_OG [0x1a3]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2010/08/14  07:51:30.0  XRT_CTRL_MANU_408_OG [0x198]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2010/08/14  07:51:32.0  XRT_FLD_RESET_412_OG [0x19c]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2010/08/14  07:51:34.0  XRT_PREFLR_STRT_422_OG [0x1a6]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2010/08/14  07:54:44.0  XRT_PREFLR_STOP_424_OG [0x1a8]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2010/08/14  09:33:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                        AOCU_NM              5 02-76 00 00 00 00 00
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